Assil Ksiksi

Resume

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2016– Ph.D. in Electrical and Computer Engineering, Georgia Institute of Technology. Research interests: hardware/software security, hardware integrity verification

2011–2015 **B.Sc. in Electrical Engineering**, *United Arab Emirates University*, Al Ain, UAE. **Senior project:** A Real-time Traffic Information System for Vehicle Drivers (**GPA:** 3.86)

Experience

08/2016- Graduate Research Assistant, Georgia Tech, Atlanta, GA.

Working in Dr. Vincent Mooney's group, *Hardware/Software Codesign for Security*. My current research is on securing remote update delivery to reconfigurable hardware/software systems.

01-06/2016 Research Assistant, Masdar Institute of Science and Technology, Abu Dhabi, UAE.

Implemented a number of projects in digital circuit design, including a full-custom 16-bit carry lookahead adder operating at ~ 300 ps.

09-12/2014 Drilling and Measurements Intern, Schlumberger, Abu Dhabi, UAE.

Coursework

Graduate Advanced Computer Architecture, Digital Systems Test, Computer Network Security, Applied Cryptography, Digital IC Design

Undergraduate Embedded Systems, Computer Architecture, Electronics, Signal Processing

Languages and Tools

Skilled: Python, Scala, Java

 ${\bf Moderate:} \ \ {\rm C, \, C++, \, JavaScript/Node, \, Go}$

Basics: MATLAB, Verilog, VHDL

Libraries: Flask, SQLAlchemy, OpenCV, protobuf, Pandas, Numpy, jQuery, Play (Scala), Android

Tools: zsh, make, sbt, Git, LaTeX, QGIS, MongoDB

Selected Projects

procsim Tomasulo pipeline simulator jadawil Course scheduler for UAEU students

csim Digital circuit and fault simulator roadomatic Road safety alert system

Publications

2015 A. Ksiksi, S. A. Shehhi, and R. Ramzan, *Intelligent Traffic Alert System for Smart Cities*, IEEE SmartCity 2015, Chengdu, China

Awards & Honors

2016,2017 Recipient of the Georgia Tech President's Fellowship

2014,2015 5th place in Gulf Programming Competition