Assignment DCO

Assignment NO	Topics	Resources
1	1. Draw a block diagram of digital computers 2. Important Intel microprocessor 3. Make a logic table diagram 4. Half adder and full adder	B ram Book , Morris Mano
2	1. Convert binary to decimal (3 questions compulsory) 2. Convert binary to octal (3 questions) 3. Convert binary to hexadecimal (3 questions) 4. Convert decimal to binary(3 questions) 5. Convert hexadecimal to binary(3 questions) 6. Convert decimal to hexadecimal (3 questions) 7. Convert hexadecimal to decimal (3 questions) 8. Convert octal to binary(3 questions)	
3	1. Draw 8085 block diagram 2. Study and write the basics features of 8085 microprocessor in detail 3. Pin configuration	Bram book page 3.2 Bram book page 3.2 Bram book page 3.5
4	1. Program example 1 to 6 b ram	Bram book 6.1 - 6.3
5	1. Binary addition and subtraction from ppt1 2. Binary codes (BCD and Gray codes)(ppt1) 3. Boolean algebra (Laws)(ppt1) 4. Standard representation for logic function (pos and sop)(ppt2) 5. Solving pos and sop problem using k map(ppt2) 6. Half subtractor and full subtractor (ppt2) 7. Binary parallel adder and binary parallel subtractor (ppt2)	PPT 1 https://drive.google.com/file/d/1asQM58UXjgn-hf5uCqCyQYBB5Fks8alH/view?usp=sharing PPT2 https://drive.google.com/file/d/1O5AHC8XVpjRqljCQ3yEzH2NJlCv3ep9l/view?usp=sharing
6	1. Mux and demux (ppt2) 2. Sequential circuits and systems (SR , JK ,D, T flip flop)(ppt3)	Ppt 3 https://drive.google.com/file/d/1ZcQOas8ifxZaOI1zKcP4Ss1NPYJYV_Mh/view?usp=sharing
7	1. Program addition of two 8 bit numbers; sum 8 bit(b ram page no 6.3) 2. Program subtraction of two 8 bit numbers(b ram page no 6.4) 3. Program addition of two bit numbers; sum 16 bit(b ram page no 6.5 and 6.6) 4. sum of series of 8 bit numbers ,sum ; 8 bit (bram page no 6.33) also draw fig 6.4 flowchart to find sum of 8 bit numbers ; sum :8 bit(bram book page no 6.34) 5. sum of series of 8 bit numbers ,sum ; 16 bit (bram page no 6.34) also draw fig 6.5 flowchart to find sum of 8 bit numbers ; sum :16 bit(bram book page no 6.34)	**B** ram page no 6.3 B ram page no 6.4 B ram page no 6.5 and 6.6 B ram page no 6.33 and 6.34 B ram page no 6.34 and 6.35

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8	1. General register organization draw figure 8.2 and table 8.1 8.2 and 8.3 (morris mano)	figure 8.2 and table 8.1 8.2 and 8.3 (morris mano)
9	1. Do questions 8.7 8.8 and 8.9 (page no 292) morris mano	(page no 292) morris mano
10	1. Construction and working i. Hard drive ii. Pen drive iii. RAM iv. ROM v. Cache memory	B ram Chapter 1 page no 1.13 Section 1.9 Memory