

The diagram shows a USB-to-serial adapter circuit. A USB connector (CN1) is connected to a CH340C chip (U2). The chip's VCC pin is connected to the +5V supply line, and its GND pin is connected to the common ground. The chip's TXD pin is connected to the PC's TX pin, its RXD pin is connected to the PC's RX pin, and its RTS pin is connected to the PC's DTR pin. The chip's V3, UD+, UD-, XI, and XO pins are connected to the common ground. A 0.1uF capacitor is connected between the chip's GND pin and the +5V supply line. The chip's ID pin is connected to the +5V supply line. The chip's D- and D+ pins are connected to the common ground. The chip's GND pin is connected to the common ground. The chip's VCC pin is connected to the +5V supply line. The chip's TXD pin is connected to the PC's TX pin. The chip's RXD pin is connected to the PC's RX pin. The chip's RTS pin is connected to the PC's DTR pin. The chip's V3 pin is connected to the common ground. The chip's UD+ pin is connected to the common ground. The chip's UD- pin is connected to the common ground. The chip's XI pin is connected to the common ground. The chip's XO pin is connected to the common ground. A 0.1uF capacitor is connected between the chip's GND pin and the +5V supply line.

The diagram shows the RT9013-33GB voltage converter circuit. The input (IN) is connected to a +5V supply through a 1kΩ resistor. A 1.6V, 220μF capacitor (C10) is connected between the input and ground. The ground (GND) pin is connected to ground. The output (OUT) is connected to a +3V3 supply through a 5Ω resistor. A 10μF capacitor (C14) is connected between the output and ground.

[illegible]

Sheet: 1/1