CA Lab: Homework 9

student: Dimitri Tabatadze

May 26, 2023

Task Description

Immediate Extension Unit

Solution

The module:

```
module IEU #(
parameter N = 16, M = 32

) (

input U,
input [N-1:0] imm,
output [M-1:0] res

);

assign res = U
? imm[15:0]
: { {(M-N){imm[15]}}, imm[15:0] };

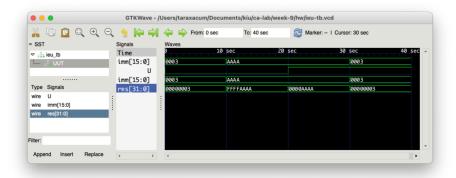
endmodule
```

The testbench:

```
1 module ieu_tb();
      reg U;
      reg [15:0] imm;
wire [31:0] res;
4
6
    IEU #(.N(16), .M(32)) UUT (
         .U(U),
           .imm(imm),
9
           .res(res)
10
    );
11
12
    initial begin
13
       $dumpfile("ieu-tb.vcd");
14
       $dumpvars(0, ieu_tb);
15
          U = 0;
16
           imm = 3;
17
18
19
           imm = 16'b1010101010101010;
           #10;
20
```

Simulation and Testing

The timing diagrams:



Reference

• me