Home Work 5

Create the model a 3-bit register with the signals: reset, set and load. Develop a testbench and simulate the design.

1. Draw the circuits of 3-bit register. 10 points

2. Create next state table for 3-bit register. 10 points

3. Write the equations for the next states. 10 points

4. Write the code of 3-bit register in Verilog. 60 points

Please, solve the problems (write the comments) 5 points, take the clear screenshots and combine all your solutions in one pdf file, then upload in teams. 5 point