

Lab 6.

Clock divider

Design of Clock divider, where the input clock is divided by an odd integer.

1. Draw the circuits. **10 points**
2. Write the equation. **10 points**
3. Write and test the code. **70 points** (main file 40 points, run to the board- 30 points)

Please, solve the problems (write the comments) **5 points**, take the clear screenshots, take the video for board results and combine all your solutions in one pdf file, then upload in teams. **5 points**