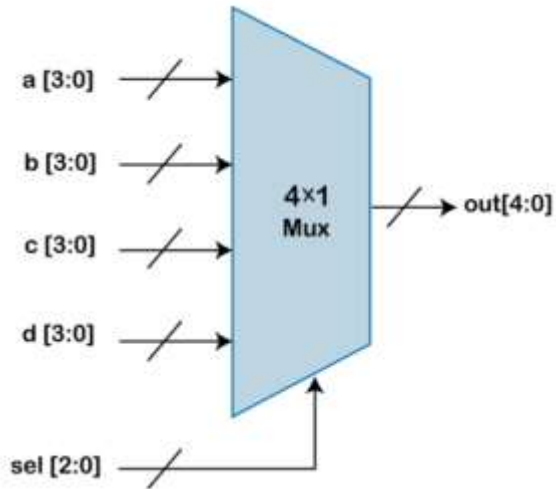


## HW 4.

The following 4X1 Mux is given:



- a) Write the code of 4X1 MUX in Verilog. 50 points
- b) Show the timing diagram of MUX in Quartus. 40 points

**!!!**Please, solve the problems (write the comments) 5 points, take the clear screenshots and combine all your solutions in one pdf file, then upload in teams. 5 points