

# BPOS: Excercises for week 7

student: Dimitri Tabatadze

May 6, 2023

## Solutions

1. The code in question:

```
1  int b;  
2  int y;  
3  
4  int main() {  
5      b = 5;  
6      y = 11;  
7      if b > 1 {  
8          b = f1(b, b - 1)  
9      };  
10     return 0  
11 };  
12  
13 int f1(int y, int z) {  
14     while b > 0 {  
15         if y > 12 {  
16             b = (y - 1) * f1(y - 1, y)  
17         } else {  
18             if y > 9 {  
19                 y = y - 1;  
20                 b = 5  
21             } else {  
22                 b = 5;  
23                 y = y - 1  
24             }  
25         }  
26     };  
27     y = y - 1;  
28     return b  
29 }
```

- $c'.pr = b=5;ft(f).body;return\ 0$
- $c'.pr = ft(f).body;return\ 0$
- $c'.pr = return\ b;return\ 0$

2. The expression to translate:  $z + x[z]$

$$\begin{aligned} \text{displ}(z, \$gm) &= \text{size}(\text{vec}) = (6 \cdot \text{size}(\text{int}))_{32} = (6 \cdot 4)_{32} = 24_{32} \\ \text{displ}(x, \$gm) &= 0_{32} \end{aligned}$$

The register `bpt`, where the pointer to the start of the region where global memory is contained, is the register number 28 which is called `$gp` in MIPS.

The generated MIPS code:

- Load `x[z]` into `$t0`

```
1 addi $t0 bpt displ(x, $gm)
2 deref($t0)
3 addi $t1 bpt displ(z, $gm)
4 deref($t1)
5 addi 23 0 size(int)
6 mul($t1, $t1, 23)
7 add $t0 $t0 $t1
8 deref($t0)
```

- Load `z` into `$t1`

```
9 addi $t1 bpt displ(z, $gm)
10 deref($t1)
```

- perform addition

```
11 add $t0 $t0 $t1
```

After expanding the macros:

```
1 addi $t0 $gp 0
2 lw $t0 $t0 0
3 addi $t1 $gp 24
4 lw $t1 $t1 0
5 addi $s7 $zero 4
6 mul $t1 $t1 $s7
7 add $t0 $t0 $t1
8 lw $t0 $t0 0
9 addi $t1 $gp 24
10 lw $t1 $t1 0
11 add $t0 $t0 $t1
```

3. The expression to translate: `while z >0 { z = z - 2 }`

$$\text{displ}(z, \$gm) = \text{size}(\text{vec}) = (6 \cdot \text{size}(\text{int}))_{32} = (6 \cdot 4)_{32} = 24_{32}$$

The register `bpt`, where the pointer to the start of the region where global memory is contained, is the register number 28 which is called `$gp` in MIPS.

The generated MIPS code:

```
• code(n1)
1 addi $t0 bpt displ(z, $gm)
2 deref($t0)
3 addi $t1 0 0
4 slt $t0 $t0 $t1

• beqz j |code(n3)|+2
5 beqz $t0 5+2

• code(n3)
6 addi $t1 bpt displ(z, $gm)
7 deref($t1)
8 addi $t2 0 2
9 sub $t1 $t1 $t2
10 sw $t1 $t2 0

• blez 0 -(|code(n1)|+|code(n3)|+1)
11 blez 0 -(4+5+1)
```

After expanding the macros:

```
1 addi $t0 28 24
2 lw $t0 $t0 0
3 addi $t1 $zero 0
4 slt $t0 $t0 $t1
5 beqz $t0 7
6 addi $t1 28 24
7 lw $t1 $t1 0
8 addi $t2 $zero 2
9 sub $t1 $t1 $t2
10 sw $t1 $t2 0
11 blez $zero -10
```