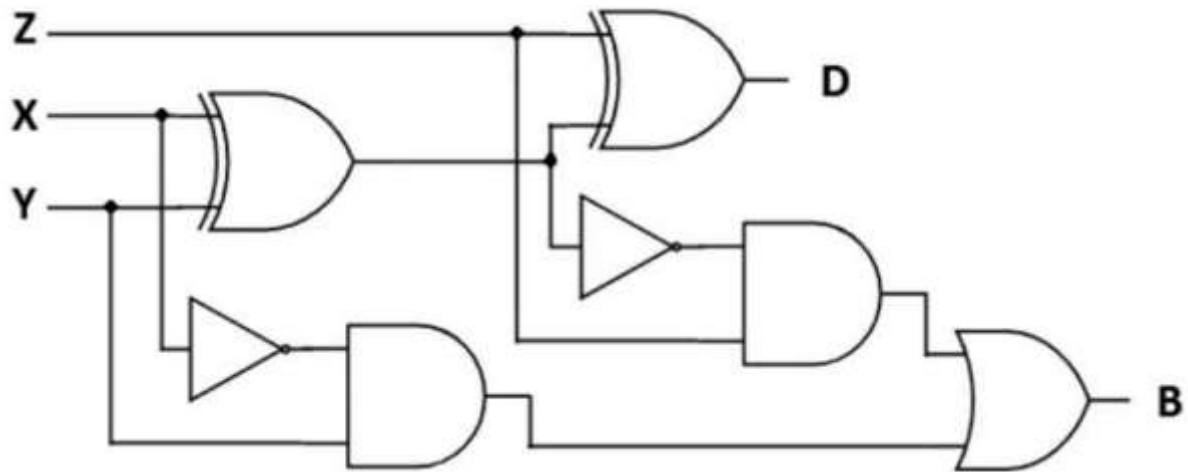


### Lab 4.

The following logic diagram is given:



a) Write the equations for the outputs. 15 points

b) Create truth table for the inputs X, Y, Z. Fill out the columns of the outputs. 10 points

c) Please, write what does this logic circuit represents? 15 points

d) Write Verilog HDL code of this logic diagram. 50 points

**!!!!**Please, solve the problems (write the comments) 5 points, take the clear screenshots, and combine all your solutions in one pdf file, then upload in teams. 5 points