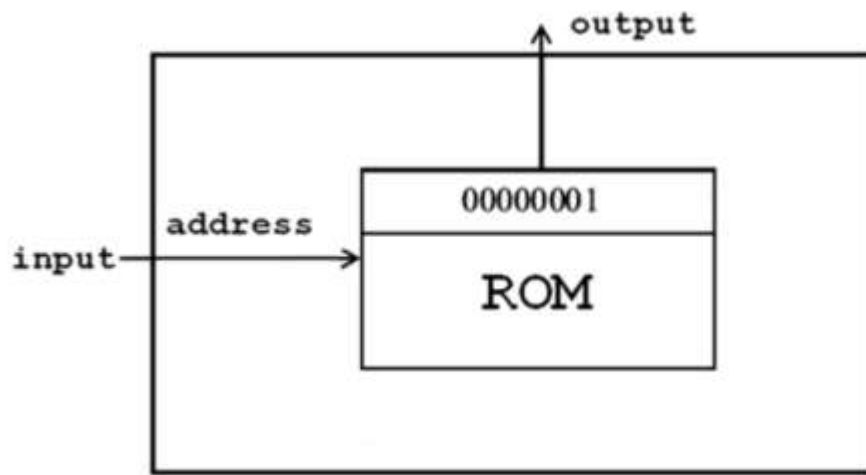


Home Work 6. Design of ROM



Write the code for ROM which has 3-bit inputs and 8-bit outputs.

Use Case block for the address with the following conditions: for each input, the output will be from 1 to 64. **50 points.**

Write the testbench and make the analyze of timing diagram. **40 points**

Please, solve the problems (write the comments) **5 points**, take the clear screenshots and combine all your solutions in one pdf file, then upload in teams. **5 points**