









Design Rules Verification Report

Filename : F:\Altium\STM_Project\STM_BLUEPILL\STM_BLUEPILL.PcbDoc

Warnings 0
Rule Violations 0

Rule Violations	
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Clearance Constraint (Gap=5mil) (All),(All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) (All)	0
Width Constraint (Min=10mil) (Max=50mil) (Preferred=10mil) (All)	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Hole Size Constraint (Min=1mil) (Max=100mil) (All)	0
Hole To Hole Clearance (Gap=10mil) (All),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Width Constraint (Min=8mil) (Max=8mil) (Preferred=8mil) (InDifferentialPair('D'))	0
Matched Net Lengths(Tolerance=1000mil) (InDifferentialPair ('D'))	0