

LEVEL 2

BRAIN GAME

LEVEL 3

LEVEL 1
UELLMAN

LEVEL 4

PMK12'1



LD2

LD3

T1

LD1

LD4

ת

C1

12F508
2123

LD1

LD2

LD3

LD4

LD1

LD2

LD3

LD4

LD1

LD2

LD3

LD4

LD1

LD2

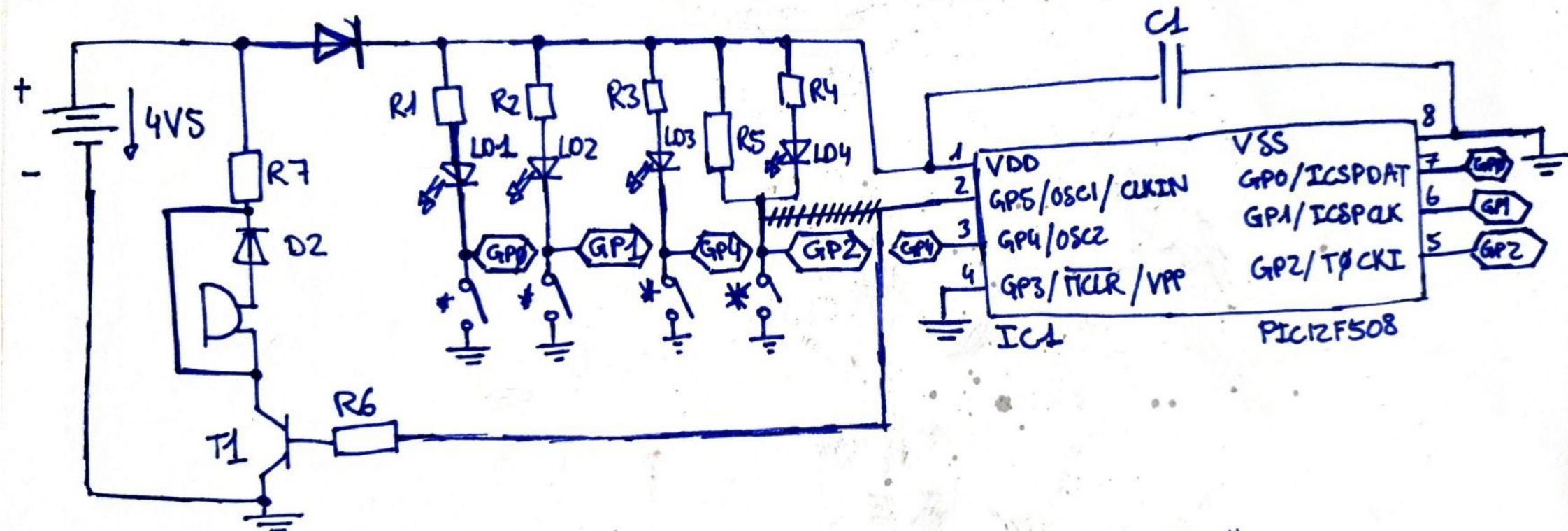
LD3

LD4

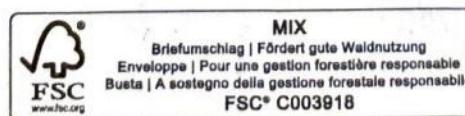
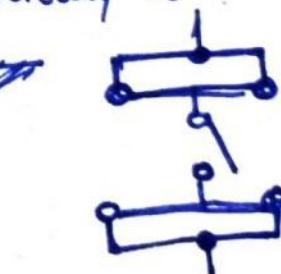
VELLEMAN

VELLEMAN

本体



* Actually is



PDIP, SOIC, MSOP

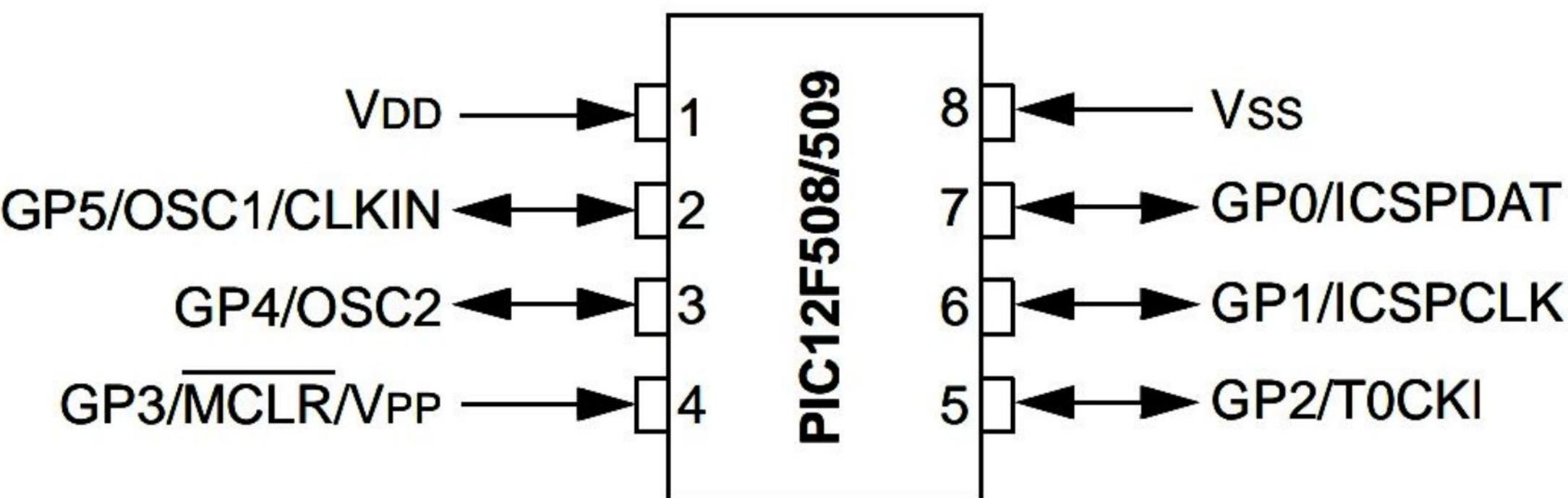


FIGURE 7-15: TYPICAL IN-CIRCUIT SERIAL PROGRAMMING CONNECTION

