

Program information

Author : Author Project name : Title Version : 0.0

Module: SR3B261BD

Cycle time in the module : 8 x 2 ms WATCHDOG action : Inactive

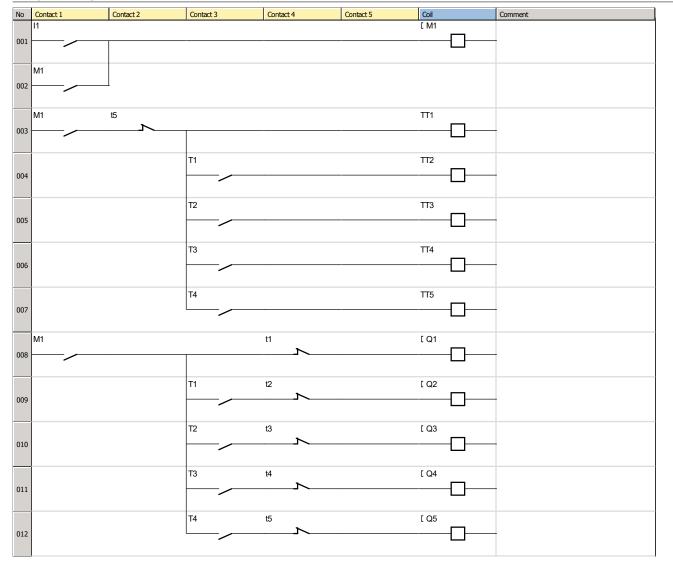
Type of Hardware Input Filtering : Slow (3ms)
☐ Zx keys inactive

Date format : dd/mm/yyyy

☐ Daylight Saving Time change activated
Zone : Europe
Change to Daylight Saving Time : March, Last Sunday
Return to winter time : October, Last Sunday



Program diagram





Physical inputs

No	Symbol	Function	Lock	Parameters	Location of (L/C)	Comment
I 1		Discrete inputs		No parameters	(1/1)	

Physical outputs

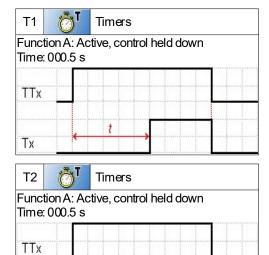
No	Symbol	Function	Latching	Location of (L/C)	Comment
Q1	φo	Discrete outputs	No	(8/6)	
Q2	φo	Discrete outputs	No	(9/6)	
Q3	φo	Discrete outputs	No	(10/6)	
Q4	φo	Discrete outputs	No	(11/6)	
Q5	φo	Discrete outputs	No	(12/6)	

Configurable functions

No	Symbol	Function	Lock	Latching	Parameters	Location of (L/C)	Comment
M1	□ M	Auxiliary relays		No	No parameters	(1/6) (2/1) (3/1) (8/1)	
T1	Ö	Timers	No	No	See details below	(3/6) (4/3) (8/4) (9/3)	
T2	Ö	Timers	No	No	See details below	(4/6) (5/3) (9/4) (10/3)	
Т3	Ö	Timers	No	No	See details below	(5/6) (6/3) (10/4) (11/3)	
T4	Ö	Timers	No	No	See details below	(6/6) (7/3) (11/4) (12/3)	
T5	Ö	Timers	No	No	See details below	(3/2) (7/6) (12/4)	

Timer

Τx





Title

