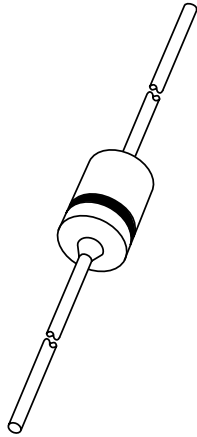


DATA SHEET



1N4148; 1N4448 High-speed diodes

Product data sheet
Supersedes data of 2002 Jan 23

2004 Aug 10

High-speed diodes

1N4148; 1N4448

FEATURES

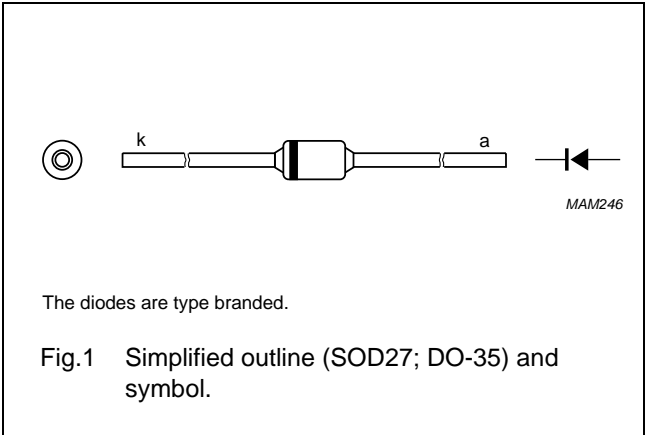
- Hermetically sealed leaded glass SOD27 (DO-35) package
- High switching speed: max. 4 ns
- General application
- Continuous reverse voltage: max. 100 V
- Repetitive peak reverse voltage: max. 100 V
- Repetitive peak forward current: max. 450 mA.

APPLICATIONS

- High-speed switching.

DESCRIPTION

The 1N4148 and 1N4448 are high-speed switching diodes fabricated in planar technology, and encapsulated in hermetically sealed leaded glass SOD27 (DO-35) packages.



MARKING

TYPE NUMBER	MARKING CODE
1N4148	1N4148PH or 4148PH
1N4448	1N4448

ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
1N4148	—	hermetically sealed glass package; axial leaded; 2 leads	SOD27
1N4448			

High-speed diodes

1N4148; 1N4448

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{RRM}	repetitive peak reverse voltage		–	100	V
V_R	continuous reverse voltage		–	100	V
I_F	continuous forward current	see Fig.2; note 1	–	200	mA
I_{FRM}	repetitive peak forward current		–	450	mA
I_{FSM}	non-repetitive peak forward current	square wave; $T_j = 25\text{ °C}$ prior to surge; see Fig.4			
		$t = 1\text{ }\mu\text{s}$	–	4	A
		$t = 1\text{ ms}$	–	1	A
		$t = 1\text{ s}$	–	0.5	A
P_{tot}	total power dissipation	$T_{amb} = 25\text{ °C}$; note 1	–	500	mW
T_{stg}	storage temperature		–65	+200	°C
T_j	junction temperature		–	200	°C

Note

1. Device mounted on an FR4 printed-circuit board; lead length 10 mm.

ELECTRICAL CHARACTERISTICS

 $T_j = 25\text{ °C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_F	forward voltage	see Fig.3			
	1N4148	$I_F = 10\text{ mA}$	–	1	V
	1N4448	$I_F = 5\text{ mA}$	0.62	0.72	V
I_R	reverse current	$V_R = 20\text{ V}$; see Fig.5		25	nA
		$V_R = 20\text{ V}$; $T_j = 150\text{ °C}$; see Fig.5	–	50	μA
I_R	reverse current; 1N4448	$V_R = 20\text{ V}$; $T_j = 100\text{ °C}$; see Fig.5	–	3	μA
C_d	diode capacitance	$f = 1\text{ MHz}$; $V_R = 0\text{ V}$; see Fig.6	–	4	pF
t_{rr}	reverse recovery time	when switched from $I_F = 10\text{ mA}$ to $I_R = 60\text{ mA}$; $R_L = 100\text{ }\Omega$; measured at $I_R = 1\text{ mA}$; see Fig.7	–	4	ns
V_{fr}	forward recovery voltage	when switched from $I_F = 50\text{ mA}$; $t_r = 20\text{ ns}$; see Fig.8	–	2.5	V

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-tp)}$	thermal resistance from junction to tie-point	lead length 10 mm	240	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	lead length 10 mm; note 1	350	K/W

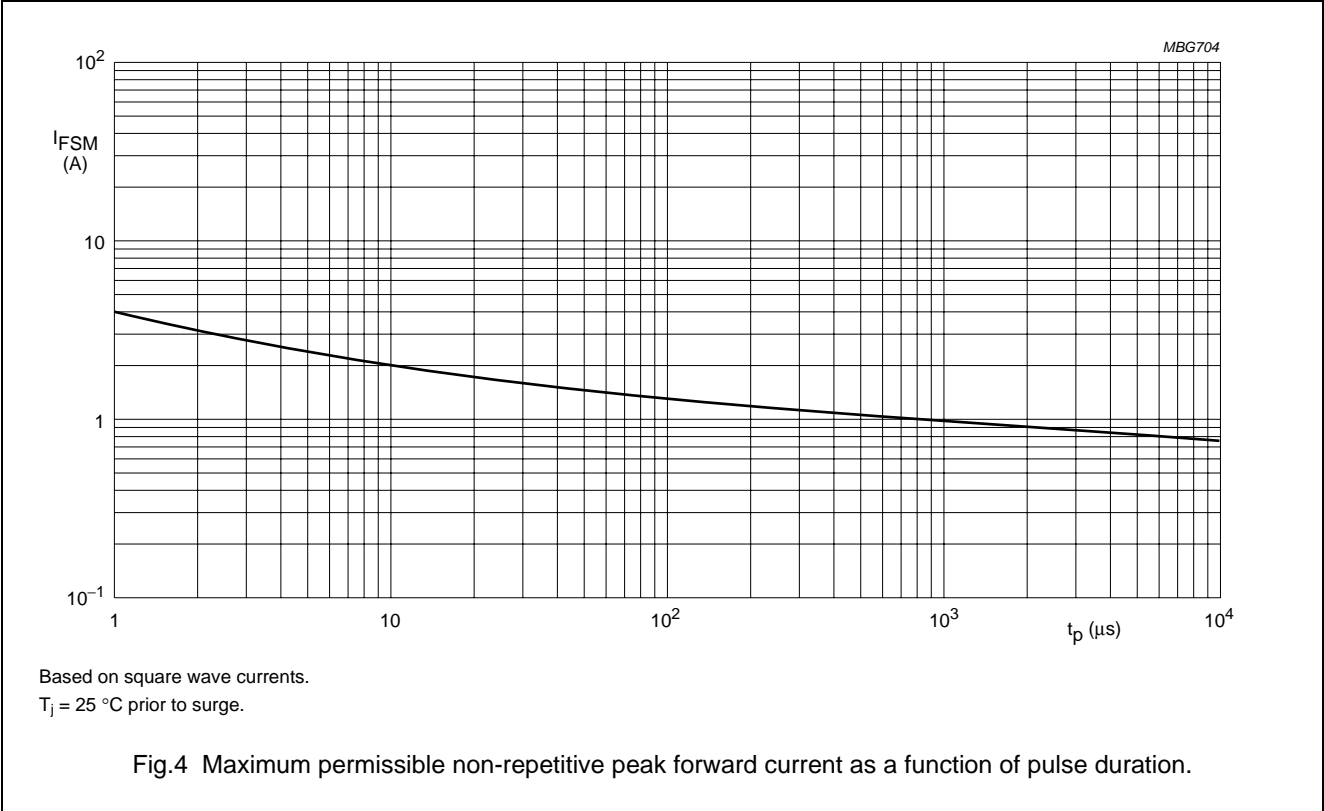
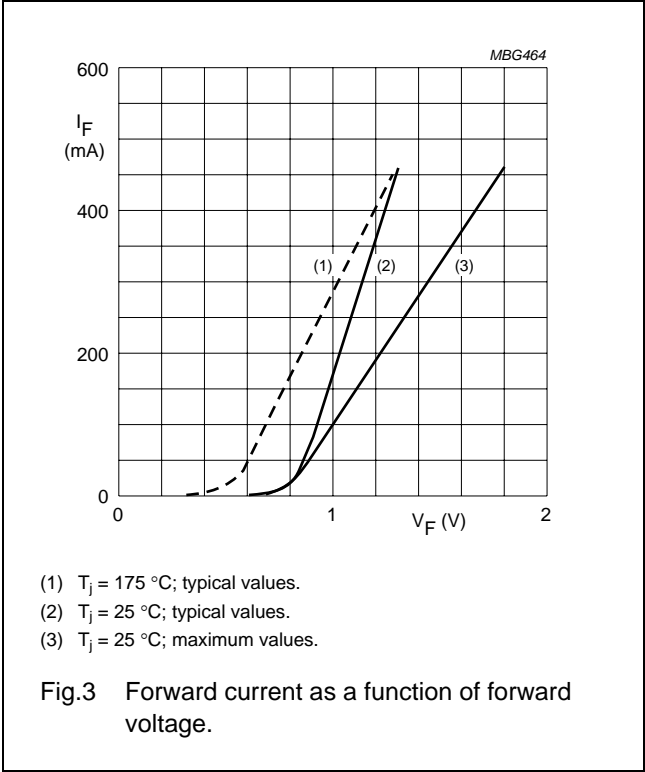
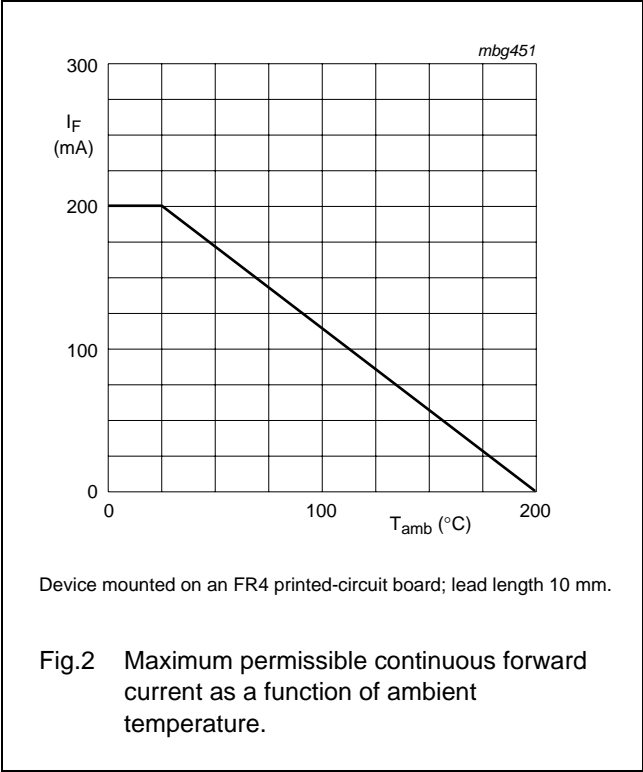
Note

1. Device mounted on a printed-circuit board without metallization pad.

High-speed diodes

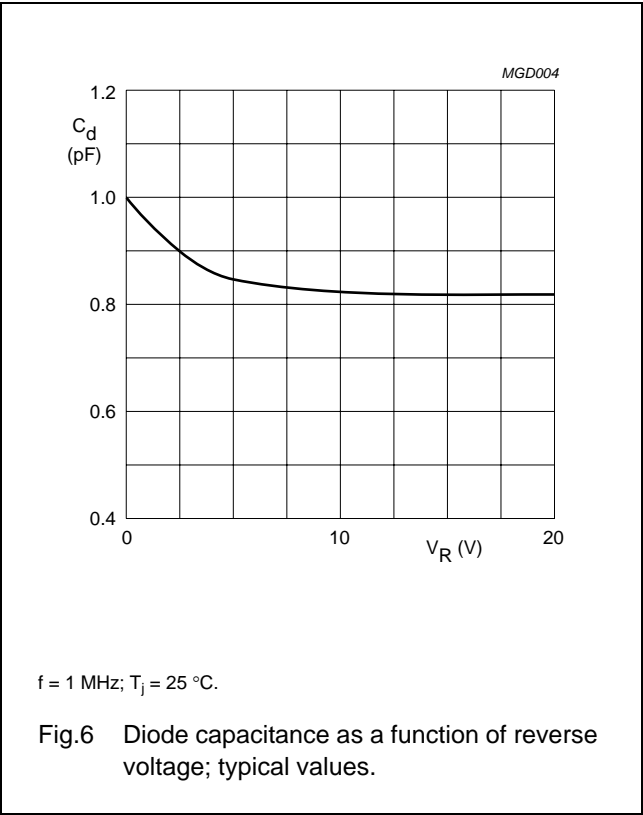
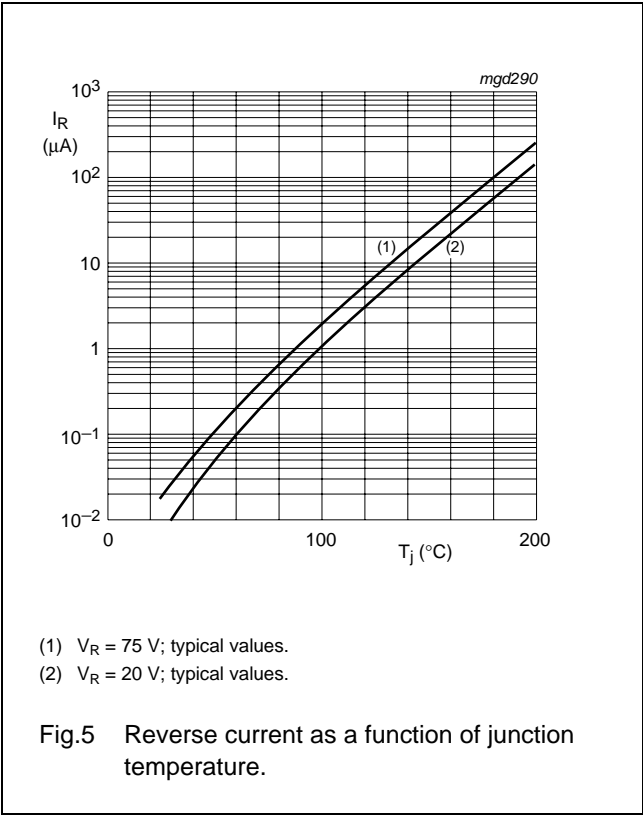
1N4148; 1N4448

GRAPHICAL DATA



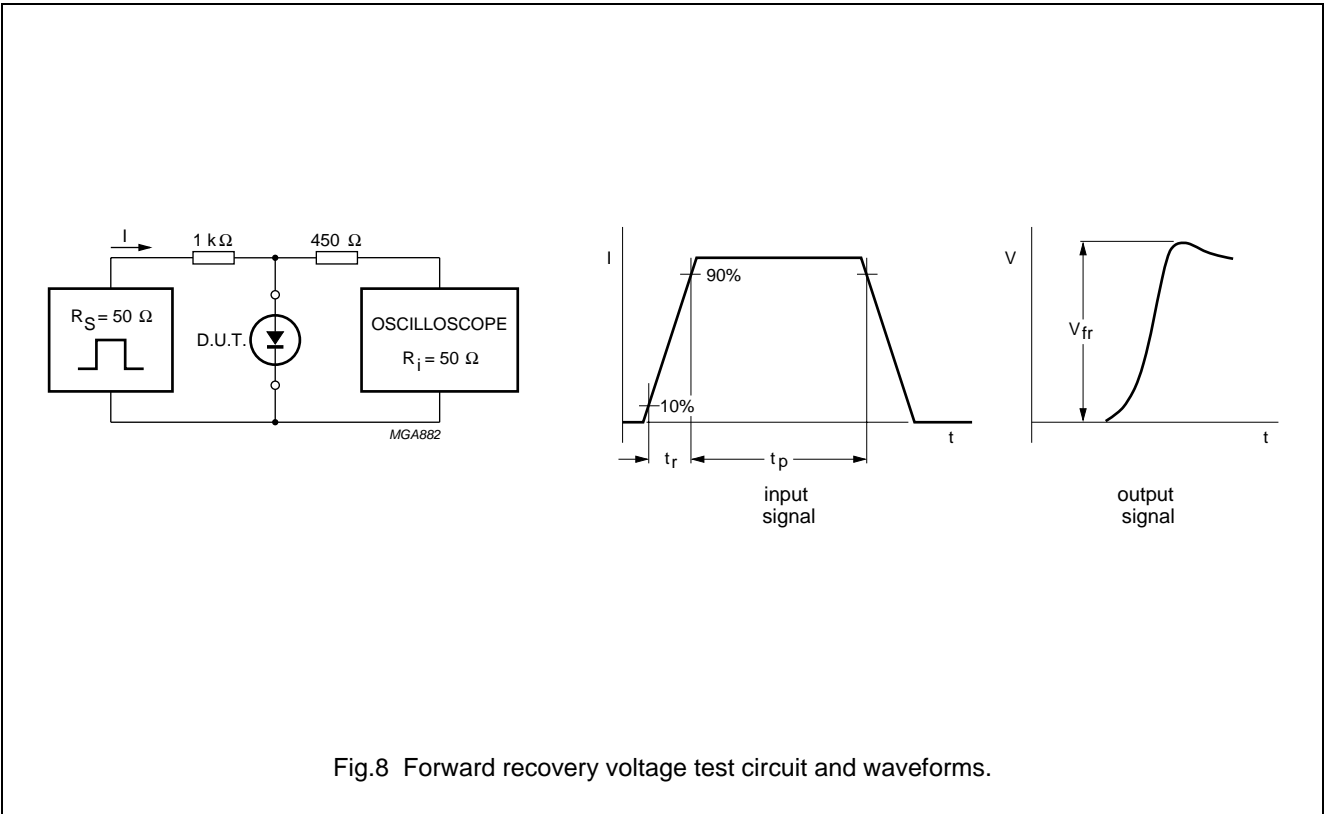
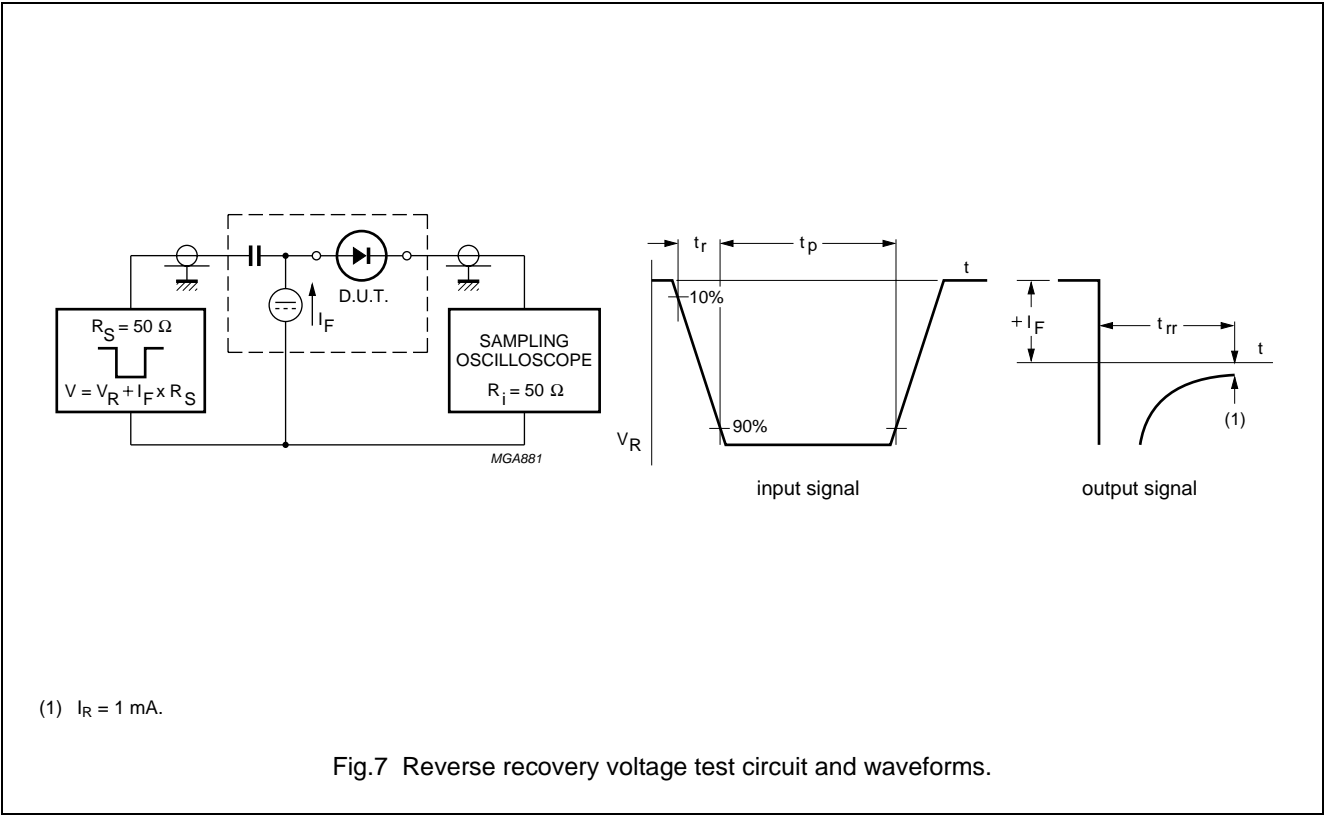
High-speed diodes

1N4148; 1N4448



High-speed diodes

1N4148; 1N4448



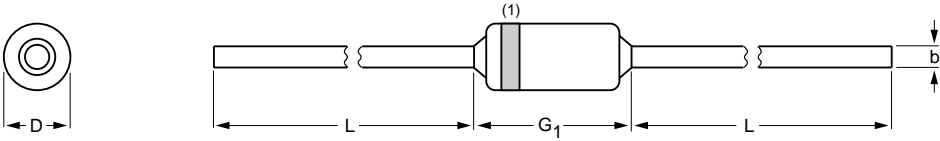
High-speed diodes

1N4148; 1N4448

PACKAGE OUTLINE

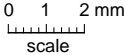
Hermetically sealed glass package; axial leaded; 2 leads

SOD27



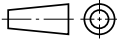
DIMENSIONS (mm are the original dimensions)

UNIT	b max.	D max.	G ₁ max.	L min.
mm	0.56	1.85	4.25	25.4



Note

1. The marking band indicates the cathode.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOD27	A24	DO-35	SC-40			97-06-09- 05-12-22

High-speed diodes

1N4148; 1N4448

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

1. Please consult the most recently issued document before initiating or completing a design.
2. The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

DISCLAIMERS

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions

above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors. No changes were made to the content, except for the legal definitions and disclaimers.

Contact information

For additional information please visit: **<http://www.nxp.com>**

For sales offices addresses send e-mail to: **salesaddresses@nxp.com**

© NXP B.V. 2009

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands

R76/05/pp9

Date of release: 2004 Aug 10

Document order number: 9397 750 13541

founded by

PHILIPS