JH7110 NOTEBOOK

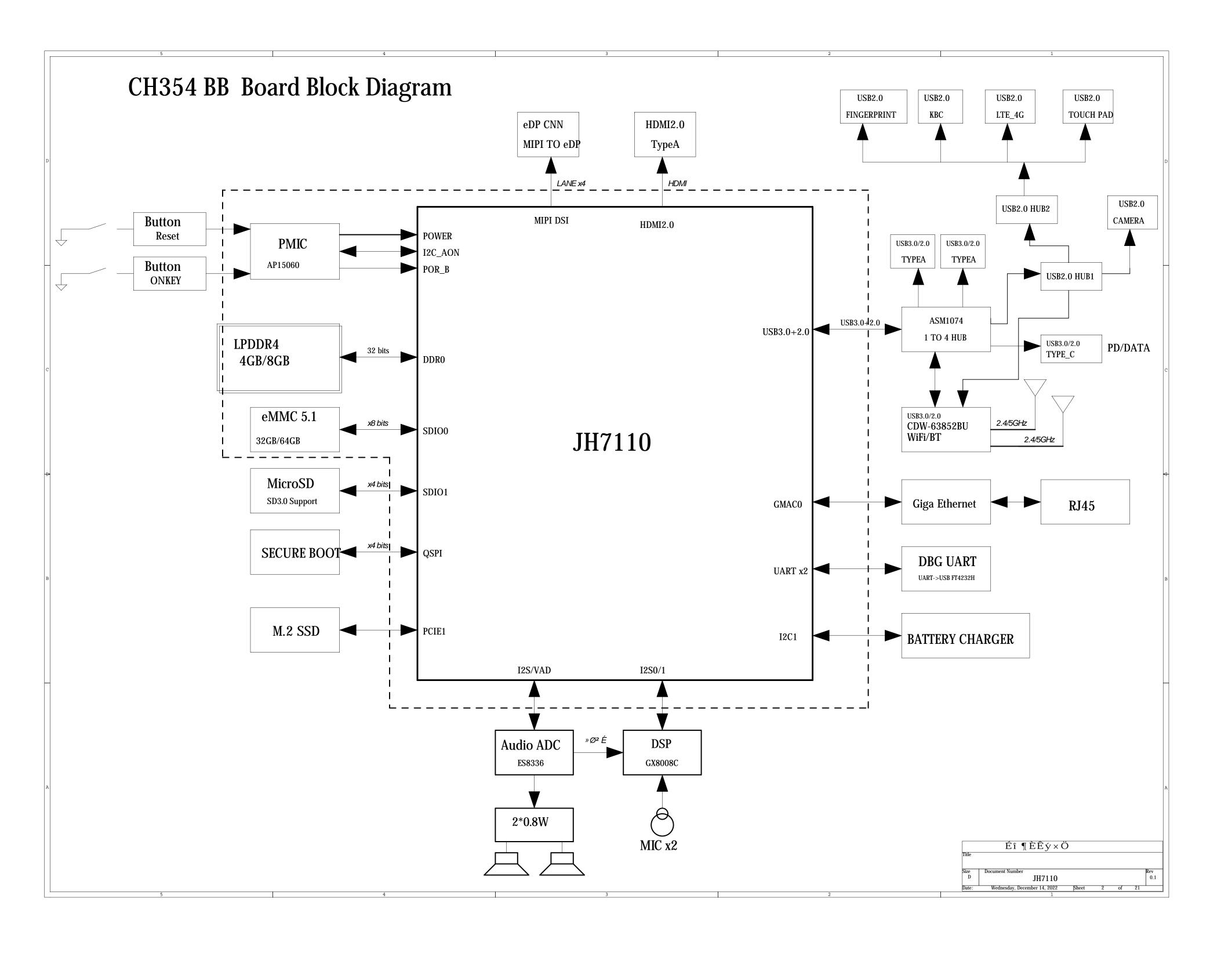
Table of Content

	of Content
Page 1	Cover Page
Page 2	Block Diagram
Page 3	Power Diagram and Sequence
Page 4	Interface
Page 5	MIPI to eDP
Page 6	TYPC-1-ANX7447
Page 7	TYPE-C CONN1
Page 8	Touch pad&USB3.0 CON&Finge
Page 9	DSP GX8008C
Page 10	EDP PANEL
Page 11	DB_CONN
Page 12	USB3.0 HUB(1 to 4)
Page 13	Thermal sensor&FAN&LED
Page 14	MCU EC KBC
Page 15	HDMI CON
Page 16	PWR IN Selector
Page 17	PWR_Charger
Page 18	WIFI NVME & LTE
Page 19	PWR_1.1V1.2V/1.8A/3V3/VDD5
Page 20	PWR_+5V/+3V/4V/2.5VADC
Page 21	ETHERNET&MECHNICAL

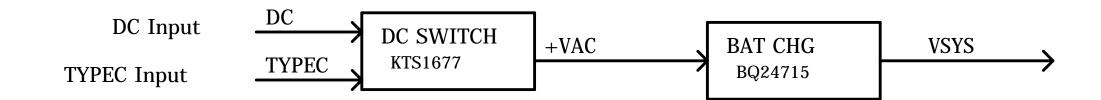
Revision History

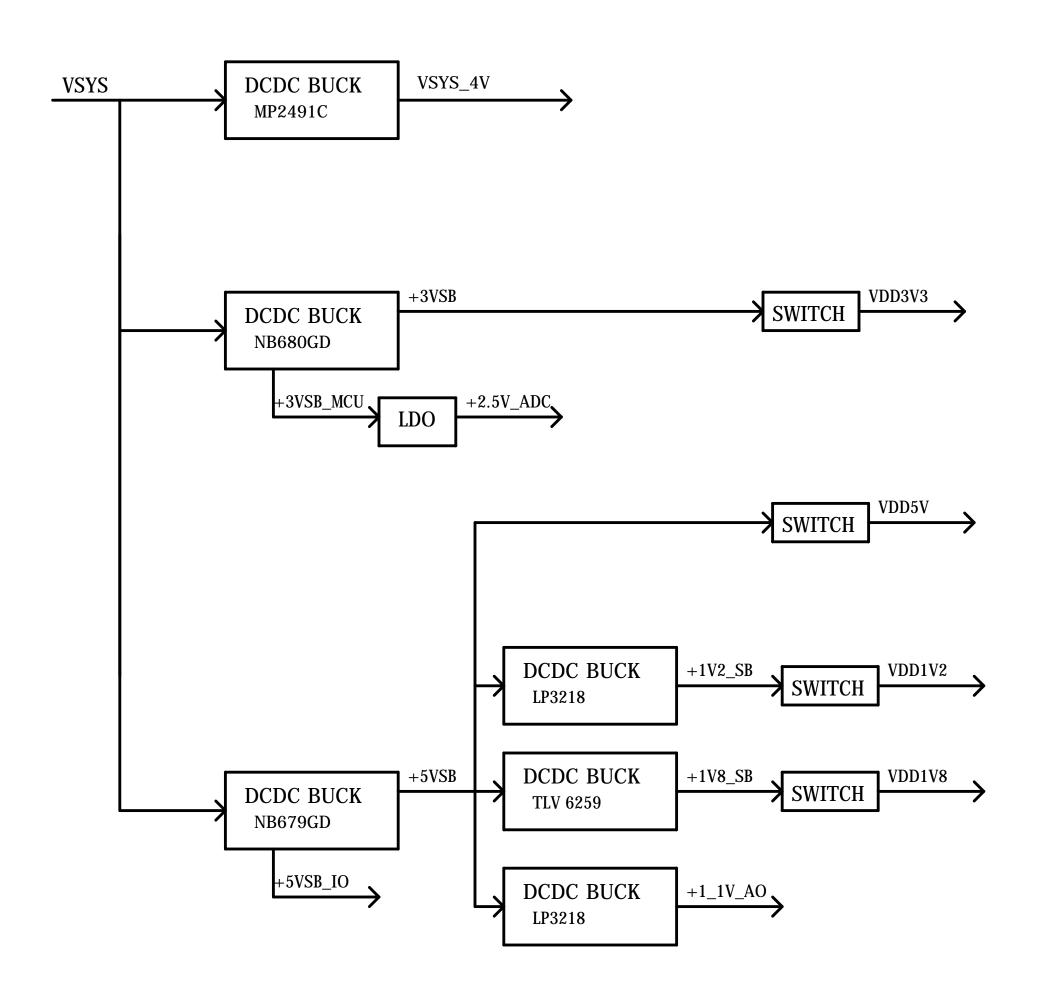
Rev. Code	Date	Ву	Description
V1.0	2023-04-21		
V2.0	2023-06-21		
V3.0	2023-068-30		1,MCU¸´Î»Á¬μ½¸´Î»°´¼ü 2,2.5VLDOÊäÈëμçÔ´¸ÄΪ+3VSB

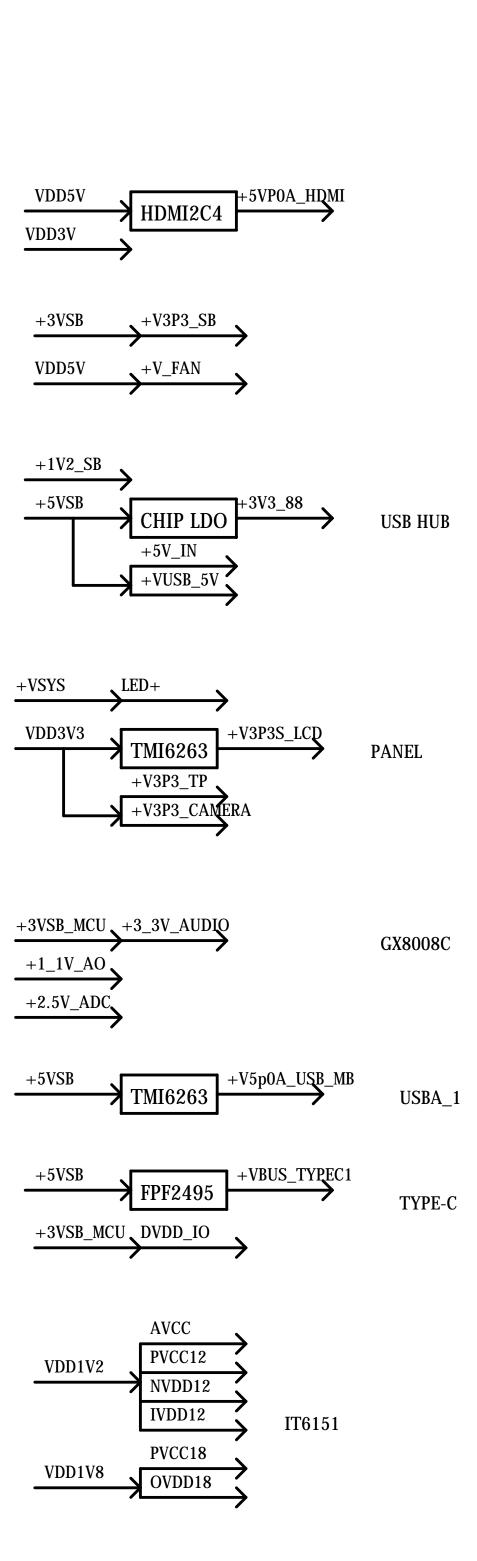
	Éî ¶ÈÊý×Ö		
Title			
Size	Document Number		Rev
Size D	Document Number JH7110		Rev 0.



Power Diagram and Sequence







JH7110 IO	名称	说明
GPIO0	HDMI_SCL	HDMI 12C SCL
GPIO1	HDMI_SDA	HDMI I2C SDA
GPIO2	MIPI_AUD_SDA	MIPI AUD I2C SDA
GPIO3	MIPI_AUD_SCL	MIPI AUD 12C SCL
GPIO4	I2S1_MCLK	ES8336 MCLK
GPIO5	UARTO_TX	调试串口TX
GPIO6	UARTO_RX	调试串口RX
GPIO7	SDIO1_D2	TF卡 D2
GPIO8	SDIO1_D3	TF卡 D3
GPIO9	SDIO1_CMD	TF卡 CMD
GPIO10	SDIO1_CLK	TF卡 CLK
GPIO11	SDIO1_D0	TF卡 D0
GPIO12	SDIO1_D1	TF+ D1
GPIO12 GPIO13		
27 14 27 27	GMAC_PHYRSTN	GMAC PHY 复位信号
GPIO14	HDMI_CEC	HDMI CEC
GPIO15	HDMI_HPD	HDMI HPD 检测
GPIO16	EC_I2C_SCL	下行 EC I2C SCL
GPIO17	EC_I2C_SDA	下行 EC I2C SDA
GPIO18	NC	
GPIO19	PMIC_SCL	PMIC 12C SCL
GPIO20	PMIC_SDA	PMIC I2C SDA
GPIO21	PCIE1_PWREN	M.2 PCIE 电源使能
GPIO22	EDP_ENPSR	MIPI 转 EDP 芯片电源使能
GPIO23	SLP_WAN	WLAN 电源开关
GPIO24	PAL_PWM	屏幕背光PWM
GPIO25	EDP_INTN	MIPI 转 EDP 芯片中断
GPIO26	TPI2C_SCL	LCD 触摸屏 I2C SCL
GPIO27	TPI2C SDA	LCD 触摸屏 I2C SDA
GPIO28	PCIE1_PERSTN	M.2 PCIE 复位
GPIO29	PCIE1_PRSN	M.2 PCIE CLKREQ
GP1029 GP1030	TP_PANEL_INT	LCD 触摸屏中断
		93 (8) (8) (8) (8) (8) (1) (1) (1) (8) (1) (1) (1)
GPIO31	TP_PANEL_RST	LCD 触摸屏复位
GPIO32	BT_HOST_WAKE_DEV	and the second s
GPIO33	PAL_BLK_EN	LCD屏背光使能
GPIO34	PAL_VDD_EN	LCD屏电源使能
GPIO35	WDO_GPIO35	JH7110 看门狗输出
GPIO36	SB_SPI_MISO	安全芯片 MISO
GPIO37	FAN_TACHO	风扇转速检测
GPIO38	I2S1_BCLK	ES8336 BCLK
GPIO39	SB_SPI_MOSI	安全芯片 MOSI
GPIO40	NC	
GPIO41	SDIO1_DET	TF卡检测
GPIO42	EDP_STANBY	MIPI 转 EDP 芯片待机
GPIO43	UART1_RX	UART1 RX
GPIO44	I2S1_DIN	ES8336 DIN
GPIO45	UART1_TX	UART1 TX
GP1045 GP1046	ES8336_INT	ES8336 中断输出
GP1046 GP1047	NOSIE DEPRES	GX8008C 消噪
		O/100000 /H*木
GPIO48	NC FC MUTE N	
GPIO49	EC_MUTE_N	关闭音频输出
GPIO50	WL_DIS#	WIFI 无线开关
GPIO51	FAN_PWMCTRL	风扇PWM输出
GPIO52	WWAN_DIS#	4G 无线开关
GPIO53	TDM_BCLK	GX8008C TDM BCLK
GPIO54	TDM_DAT	GX8008C TDM DATA
GPIO55	GX8008C_P16	GX8008C_P16
GPIO56	BT_DIS#	蓝牙无线关闭
GPIO57	SB_SPI_CSN	安全芯片 CSN
GPIO58	SB_SPI_SCLK	安全芯片SCLK
GPIO59	WWAN_ON_OFF	4G电源开关
GPIO60	TDM_WSCLK	GX8008C TDM WSCLK
GPIO61	I2S1_OUT	ES8336 DOUT
GPIO62	EMMC_RSTN	没有引出来
01 1002	FINING I/OLIA	スロコ田小
GPIO63	I2S1_LRCK	ES8336 LRCK

	Éî ¶ÈÊý×Ċ)				
Title						
Size	Document Number					Rev
D	JH7110					0.1
Date:	Wednesday, December 14, 2022	Sheet	3	of	21	•
	1					

INTERFACE Boot Mode Setting MIPI_DSIO_CLKN [4,5] // MIPI_DSIO_CLKN MIPI_DSIO_CLKP R49 R45 4.7K 4.7K MIPI_DSI0_DATAN0[4,5] MIPI_DSI0_DATAN0 MIPI_DSI0_DATAP0[4,5] MIPI_DSI0_DATAP0 FAN_PWMCTRL [4,13] >> FAN_PWMCTRL LE8911EXB ES8336 µç³Ø FAN MIPI_DSI0_DATAN1[4,5] MIPI_DSI0_DATAN1 MIPI_DSI0_DATAP1[4,5] MIPI_DSI0_DATAP1 MIPI AUD 12C 0x10(7bits) S1804 Switch_2 0x0B(7bits) 0x29(7bits) SB_SPI_SCLK[4,13] SB_SPI_CSN [4,13] SB_SPI_MISO [4,13] SB_SPI_CSN SB_SPI_MISO MIPI DSIO DATAP2[4.5] MIPI DSIO DATAP2 MIPI_DSIO_DATAN3[4,5] ____MIPI_DSIO_DATAN3 MIPI_DSIO_DATAP3[4,5] ___MIPI_DSIO_DATAP3 LCD´¥ÃþÆÁ GX8008C MIPI_AUD_SCL [4,5,11,17] MIPI_AUD_SDA [4,5,11,17] MIPI_AUD_SDA SECURE BOOT TP I2C EDP_STANDBY[4,5] EDP_INTN[4,5] EDP_RSTN [5] Ô¤Áô Ô¤Áô MIPI2EDP-IT6151 HDMI_DATAN0[4,15] HDMI_DATAN0 HDMI_DATAP0 HDMI_DATAN1[4,15] HDMI_DATAN1 HDMI_DATAP1[4,15] HDMI_DATAP1 UARTO_RXD UART1_TXD UART1_RXD WWAN HDMI_DATAN2[4,15] HDMI_DATAN2 HDMI_DATAP2 HDMI_SCL [4,15] HDMI_SCL HDMI_SDA I2S1_MCLK [4,11] //___ I2S1_MCLK 12S1_LRCK [4,11] | 12S1_LRCK 12S1_BCLK [4,11] | 12S1_BCLK HDMI_CEC [4,15] \ \ \ \ HDMI_CEC HDMI_HPD [4,15] HDMI_HPD I2S1_DIN[4,11] **HDMI** ES8336_INT_N[4,11] >> ____ES8336_INT_N GNR MPITY_L2P 50R_MPITY_L3P 50R_MPITY_L3P 50R_MPITY_L3P 50R_MPITY_L3P 50R_MPITY_L3P 50R_MPITY_L3P 50R_MPITY_L4P 50R_MPITY_L4P 50R_MPITY_L4P 50R_MPITY_L4P 50R_MPITY_L4P 50R_MPITY_L4P 50R_MPITY_L4F 50R_MPITY_L4F 50R_MPITY_L4F 50R_MPITY_L4F 50R_MPITY_L4F 50R_MPITY_L4F 50R_MPITY_L4F 50R_MPITY_L4F 50R_MPITY_LAP 60R_MPITY_LAP 60R_MP EC_MUTE_N [4,11] >> ____EC_MUTE_N DAC&SPK PCIE1_PERSTN_GPIO28 [4,18] PCIE1_PRSN_GPIO29 [4,18] USB_VBUS18[4,8] >> USB_VBUS18 PCIE1_PWREN_H_GPIO21 [4,18] PCIE SSD WIFI_RST [4,18] << WIFI_RST SLP_WLAN_N [4,18] >> SLP_WLAN_N **USB SWITCH** X7110CV2 BOOT MODE RGPIO1[4.14] WIFI&BT SYSTEM_SLEEP_WAKE [4] GMAC_PHYRSTN_GPIO13 [4,21] GMACO_TXEN [4,21] GMACO_TXDC [4,21] GMACO_TXD0 [4,21] ____ +1V8_IO_SOM SYS_RSTN [4,13,18]\ SYS_RSTN GMAC0 TXD2 [4.21] GMACO_RXC [4,21] GMACO_RXD0 [4,21] GMACO_RXD1 [4,21] GMACO_RXD2 [4,21] GMACO_RXD3 [4,21] GMACO_MDC [4,21] SOM_PWRON [4,11,14] SOM_PWRON SOM_PWROK [4,14] SOM_PWROK VCC5V0_SYS VCC5V0_SYS VCC5V0_SYS VCC_3V3_SYS_OUT VCC_3V3_SYS_OUT VCC_3V3_SYS_OUT VCC_3V3_SYS_OUT R9 88 88 87 87 **GETH** GND 87 LCD PANEL MIC-GX8008C | SDIO1_DAT30 | 4,11 | SDIO1_DAT30 | SDIO1_CLKO | 4,11 | SDIO1_CMDO | 4,11 | SDIO1_CMDO | 4,11 | SDIO1_CMDO SDIO1_DETNO [4,11] TF CARD VSYS_4V $+1V8_SB$ +3V3_IO_SOM +3V3_IO_SOM +VDD_POR R105 NC/4.7K R101 NC U12 ME6212C33M5G VDD_POR EC_WAKEUP [14] SOM PWROK R98 2K +1V8_IO_SOM R104 OR 0.1uF/16V -C60 0.1uF/16V ✓ SYS_RSTN [4,13,18] 0.1uF/16V ______WL_WAKE_HOS[4,18] R48 NC/100K < 5 SENSE CT 4 0.1uF/16V MAX809SD_3 SENSE GND C51 C51 C51 ME083A263X/CE8809C263MA/LN809C2641MR BV-FA05ZCD R69 4.7K BT_WAKE_HOST[4,18] +3V3_IO_SOM ______ R106 NC +1V8_IO_SOM ______ C46 NC/4.7uF/6.3V SYSTEM_SLEEP_WAKE C30 R70 10K TV8 BV-FA05ZCD R53 OR SW3 PQ7 SOT723 PNM723T30V01 ✓ MR_RESET [14] —>> WDO_GPIO35 [4] RESET KEY R51 1K SW_4P_5D2_5D2 TP-1152S-LS1316 R52 4.7K Éî ¶ÈÊý×Ö

JH7110

Wednesday, December 14, 2022 Sheet 4 of 21

