

## 1. Description

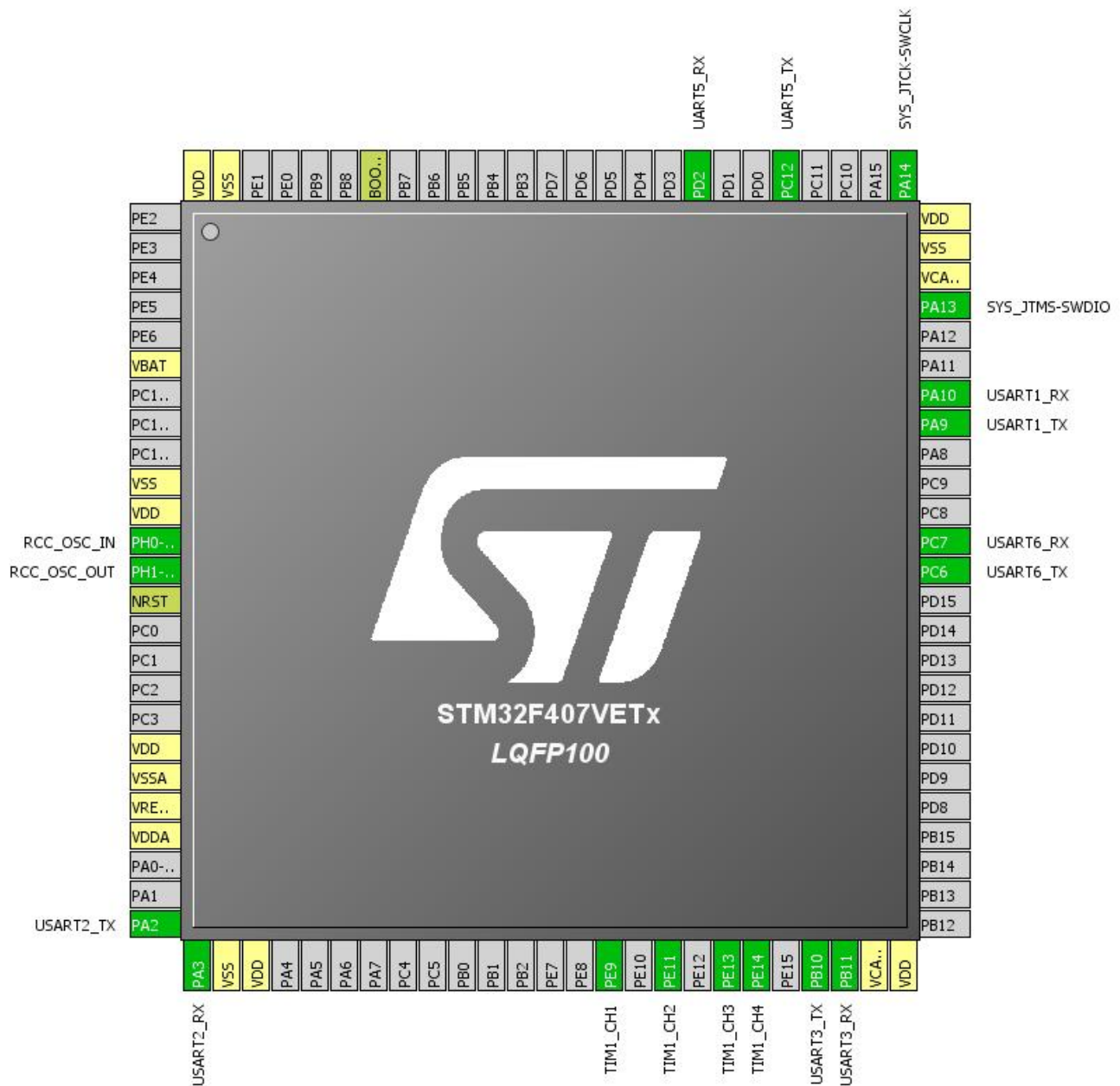
### 1.1. Project

Project Name	MTi3_test
Board Name	MTi3_test
Generated with:	STM32CubeMX 4.22.1
Date	11/01/2017

### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F407/417
MCU name	STM32F407VETx
MCU Package	LQFP100
MCU Pin number	100

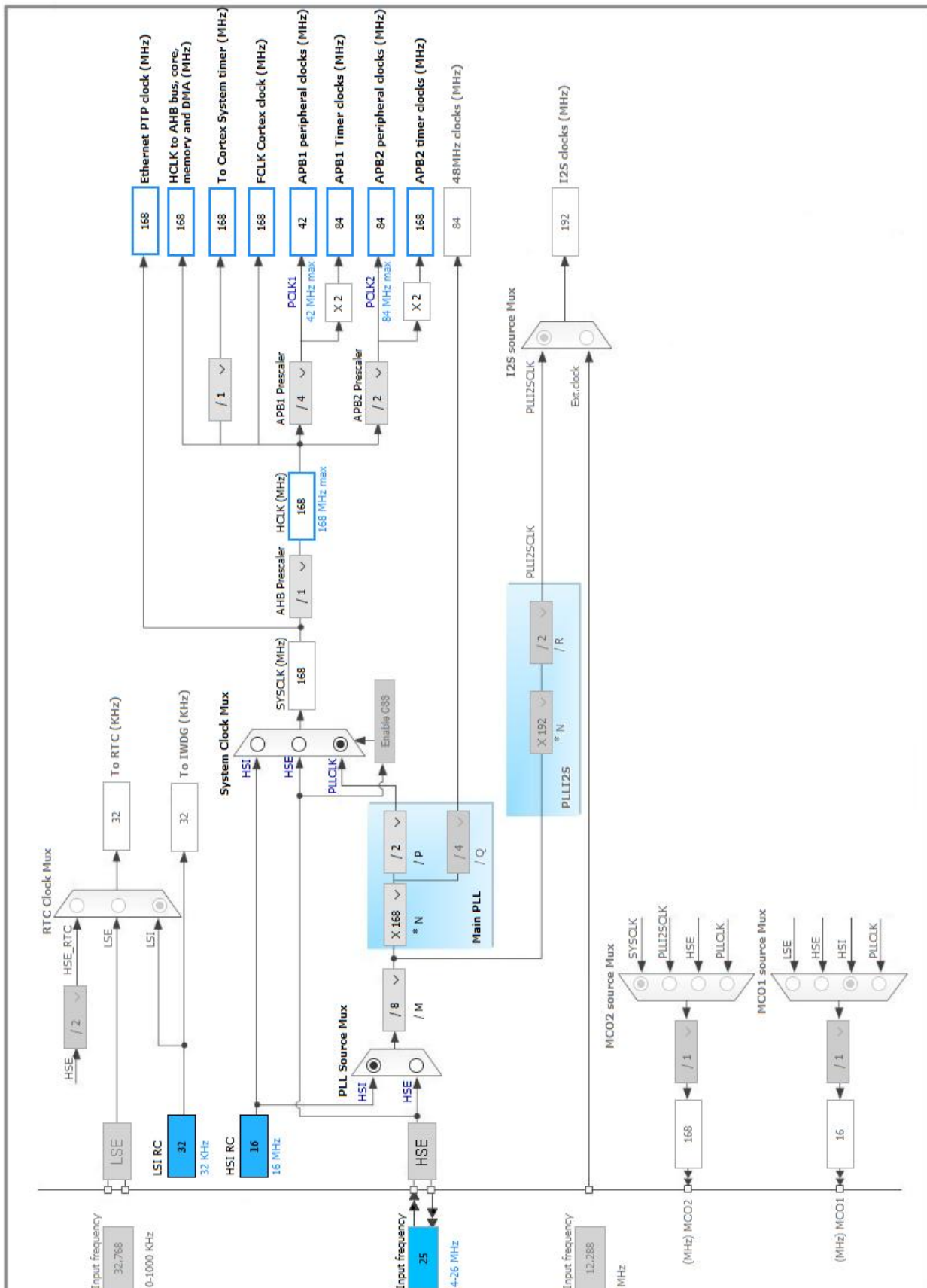
## 2. Pinout Configuration



### 3. Pins Configuration

Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
6	VBAT	Power		
10	VSS	Power		
11	VDD	Power		
12	PH0-OSC_IN	I/O	RCC_OSC_IN	
13	PH1-OSC_OUT	I/O	RCC_OSC_OUT	
14	NRST	Reset		
19	VDD	Power		
20	VSSA	Power		
21	VREF+	Power		
22	VDDA	Power		
25	PA2	I/O	USART2_TX	
26	PA3	I/O	USART2_RX	
27	VSS	Power		
28	VDD	Power		
40	PE9	I/O	TIM1_CH1	
42	PE11	I/O	TIM1_CH2	
44	PE13	I/O	TIM1_CH3	
45	PE14	I/O	TIM1_CH4	
47	PB10	I/O	USART3_TX	
48	PB11	I/O	USART3_RX	
49	VCAP_1	Power		
50	VDD	Power		
63	PC6	I/O	USART6_TX	
64	PC7	I/O	USART6_RX	
68	PA9	I/O	USART1_TX	
69	PA10	I/O	USART1_RX	
72	PA13	I/O	SYS_JTMS-SWDIO	
73	VCAP_2	Power		
74	VSS	Power		
75	VDD	Power		
76	PA14	I/O	SYS_JTCK-SWCLK	
80	PC12	I/O	UART5_TX	
83	PD2	I/O	UART5_RX	
94	BOOT0	Boot		
99	VSS	Power		
100	VDD	Power		

## 4. Clock Tree Configuration



## 5. IPs and Middleware Configuration

### 5.1. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

##### 5.1.1. Parameter Settings:

###### System Parameters:

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled
Flash Latency(WS)	5 WS (6 CPU cycle)

###### RCC Parameters:

HSI Calibration Value	16
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

###### Power Parameters:

Power Regulator Voltage Scale	Power Regulator Voltage Scale 1
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### 5.2. SYS

#### Debug: Serial Wire

#### Timebase Source: SysTick

### 5.3. TIM1

#### Clock Source : Internal Clock

#### Channel1: PWM Generation CH1

#### Channel2: PWM Generation CH2

#### Channel3: PWM Generation CH3

#### Channel4: PWM Generation CH4

##### 5.3.1. Parameter Settings:

###### Counter Settings:

Prescaler (PSC - 16 bits value)	167 *
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Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value )	<b>2499 *</b>
Internal Clock Division (CKD)	No Division
Repetition Counter (RCR - 8 bits value)	0

**Trigger Output (TRGO) Parameters:**

Master/Slave Mode	Disable (no sync between this TIM (Master) and its Slaves)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

**Break And Dead Time management - BRK Configuration:**

BRK State	Disable
BRK Polarity	High

**Break And Dead Time management - Output Configuration:**

Automatic Output State	Disable
Off State Selection for Run Mode (OSSR)	Disable
Off State Selection for Idle Mode (OSSl)	Disable
Lock Configuration	Off

**PWM Generation Channel 1:**

Mode	PWM mode 1
Pulse (16 bits value)	0
Fast Mode	Disable
CH Polarity	High
CH Idle State	Reset

**PWM Generation Channel 2:**

Mode	PWM mode 1
Pulse (16 bits value)	0
Fast Mode	Disable
CH Polarity	High
CH Idle State	Reset

**PWM Generation Channel 3:**

Mode	PWM mode 1
Pulse (16 bits value)	0
Fast Mode	Disable
CH Polarity	High
CH Idle State	Reset

**PWM Generation Channel 4:**

Mode	PWM mode 1
Pulse (16 bits value)	0
Fast Mode	Disable
CH Polarity	High
CH Idle State	Reset

## 5.4. TIM6

mode: Activated

### 5.4.1. Parameter Settings:

#### Counter Settings:

Prescaler (PSC - 16 bits value)	83 *
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value )	999 *

#### Trigger Output (TRGO) Parameters:

Trigger Event Selection	Reset (UG bit from TIMx_EGR)
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## 5.5. TIM7

mode: Activated

### 5.5.1. Parameter Settings:

#### Counter Settings:

Prescaler (PSC - 16 bits value)	8399 *
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value )	9999 *

#### Trigger Output (TRGO) Parameters:

Trigger Event Selection	Reset (UG bit from TIMx_EGR)
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## 5.6. UART5

Mode: Asynchronous

### 5.6.1. Parameter Settings:

#### Basic Parameters:

Baud Rate	100000 *
Word Length	9 Bits (including Parity) *
Parity	Even *

Stop Bits 2 \*

**Advanced Parameters:**

Data Direction Receive and Transmit  
Over Sampling 16 Samples

## 5.7. USART1

**Mode: Asynchronous**

### 5.7.1. Parameter Settings:

**Basic Parameters:**

Baud Rate 115200  
Word Length 8 Bits (including Parity)  
Parity None  
Stop Bits 1

**Advanced Parameters:**

Data Direction Receive and Transmit  
Over Sampling 16 Samples

## 5.8. USART2

**Mode: Asynchronous**

### 5.8.1. Parameter Settings:

**Basic Parameters:**

Baud Rate 921600 \*  
Word Length 8 Bits (including Parity)  
Parity None  
Stop Bits 1

**Advanced Parameters:**

Data Direction Receive and Transmit  
Over Sampling 16 Samples



## 5.9. USART3

Mode: Asynchronous

### 5.9.1. Parameter Settings:

#### Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

#### Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

## 5.10. USART6

Mode: Asynchronous

### 5.10.1. Parameter Settings:

#### Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

#### Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

\* User modified value

## 6. System Configuration

### 6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PH0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
TIM1	PE9	TIM1_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PE11	TIM1_CH2	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PE13	TIM1_CH3	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PE14	TIM1_CH4	Alternate Function Push Pull	No pull-up and no pull-down	Low	
UART5	PC12	UART5_TX	Alternate Function Push Pull	Pull-up	Very High *	
	PD2	UART5_RX	Alternate Function Push Pull	Pull-up	Very High *	
USART1	PA9	USART1_TX	Alternate Function Push Pull	Pull-up	Very High *	
	PA10	USART1_RX	Alternate Function Push Pull	Pull-up	Very High *	
USART2	PA2	USART2_TX	Alternate Function Push Pull	Pull-up	Very High *	
	PA3	USART2_RX	Alternate Function Push Pull	Pull-up	Very High *	
USART3	PB10	USART3_TX	Alternate Function Push Pull	Pull-up	Very High *	
	PB11	USART3_RX	Alternate Function Push Pull	Pull-up	Very High *	
USART6	PC6	USART6_TX	Alternate Function Push Pull	Pull-up	Very High *	
	PC7	USART6_RX	Alternate Function Push Pull	Pull-up	Very High *	

## **6.2. DMA configuration**

nothing configured in DMA service

### 6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
USART1 global interrupt	true	0	0
USART2 global interrupt	true	0	0
USART3 global interrupt	true	0	0
UART5 global interrupt	true	0	0
TIM6 global interrupt, DAC1 and DAC2 underrun error interrupts	true	0	0
TIM7 global interrupt	true	0	0
USART6 global interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
TIM1 break interrupt and TIM9 global interrupt	unused		
TIM1 update interrupt and TIM10 global interrupt	unused		
TIM1 trigger and commutation interrupts and TIM11 global interrupt	unused		
TIM1 capture compare interrupt	unused		
FPU global interrupt	unused		

\* User modified value

## ***7. Power Consumption Calculator report***

### 7.1. Microcontroller Selection

Series	STM32F4
Line	STM32F407/417
MCU	STM32F407VETx
Datasheet	022152_Rev8

### 7.2. Parameter Selection

Temperature	25
Vdd	3.3

## 8. Software Project

### 8.1. Project Settings

Name	Value
Project Name	MTi3_test
Project Folder	D:\sang-min\Github\Nonamed\Drone\Source\SBUS, MTI source
Toolchain / IDE	EWARM
Firmware Package Name and Version	STM32Cube FW_F4 V1.16.0

### 8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No