1. Description

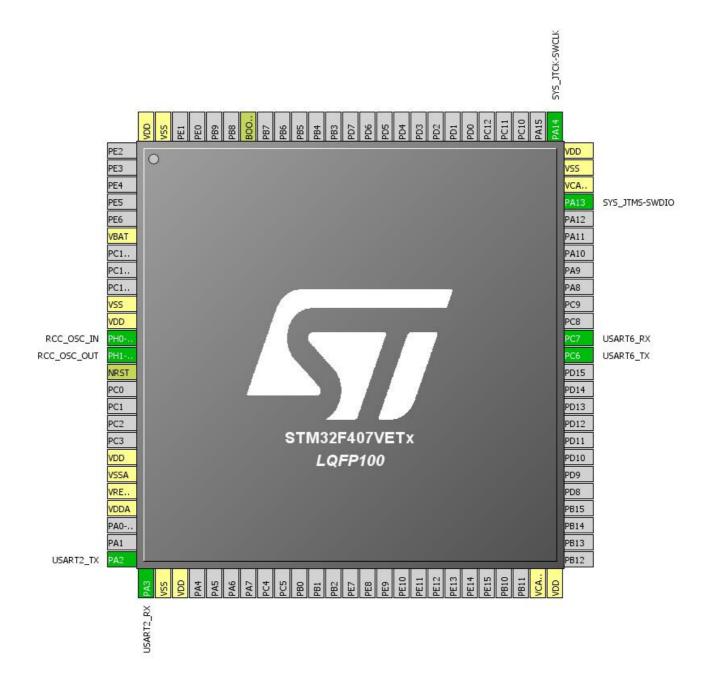
1.1. Project

| Project Name | GPS-Decoding |
|-----------------|--------------------|
| Board Name | GPS-Decoding |
| Generated with: | STM32CubeMX 4.23.0 |
| Date | 11/25/2017 |

1.2. MCU

| MCU Series | STM32F4 |
|----------------|---------------|
| MCU Line | STM32F407/417 |
| MCU name | STM32F407VETx |
| MCU Package | LQFP100 |
| MCU Pin number | 100 |

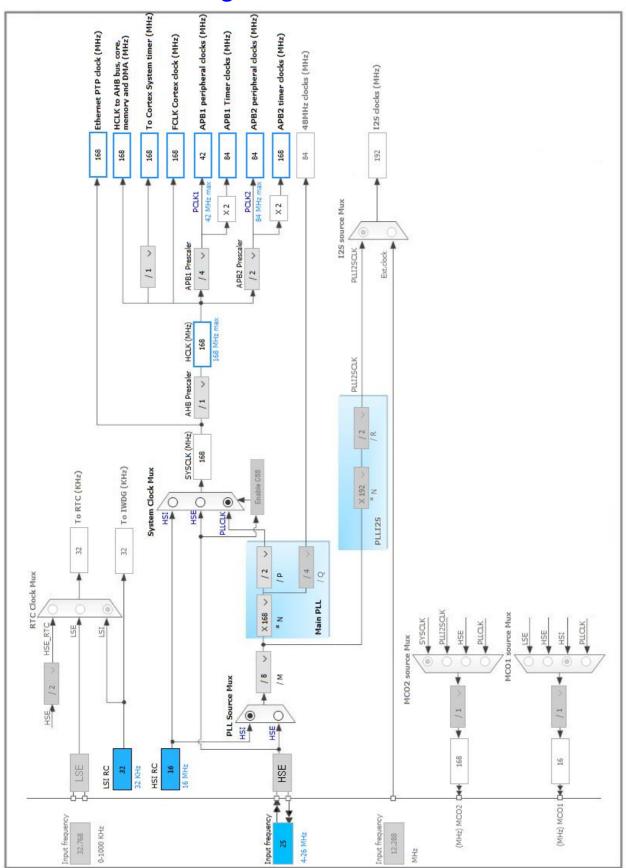
2. Pinout Configuration



3. Pins Configuration

| Pin Number LQFP100 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|-----------------------|---------------------------------------|----------|--------------------------|-------|
| 6 | VBAT | Power | | |
| 10 | VSS | Power | | |
| 11 | VDD | Power | | |
| 12 | PH0-OSC_IN | I/O | RCC_OSC_IN | |
| 13 | PH1-OSC_OUT | I/O | RCC_OSC_OUT | |
| 14 | NRST | Reset | | |
| 19 | VDD | Power | | |
| 20 | VSSA | Power | | |
| 21 | VREF+ | Power | | |
| 22 | VDDA | Power | | |
| 25 | PA2 | I/O | USART2_TX | |
| 26 | PA3 | I/O | USART2_RX | |
| 27 | VSS | Power | | |
| 28 | VDD | Power | | |
| 49 | VCAP_1 | Power | | |
| 50 | VDD | Power | | |
| 63 | PC6 | I/O | USART6_TX | |
| 64 | PC7 | I/O | USART6_RX | |
| 72 | PA13 | I/O | SYS_JTMS-SWDIO | |
| 73 | VCAP_2 | Power | | |
| 74 | VSS | Power | | |
| 75 | VDD | Power | | |
| 76 | PA14 | I/O | SYS_JTCK-SWCLK | |
| 94 | воото | Boot | | |
| 99 | VSS | Power | | |
| 100 | VDD | Power | | |

4. Clock Tree Configuration



5. IPs and Middleware Configuration

5.1. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

5.1.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 5 WS (6 CPU cycle)

RCC Parameters:

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

Power Parameters:

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

5.2. SYS

Debug: Serial Wire

Timebase Source: SysTick

5.3. TIM6

mode: Activated

5.3.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value) 83 *

Counter Mode Up

Counter Period (AutoReload Register - 16 bits value) 999 *

Trigger Output (TRGO) Parameters:

Trigger Event Selection Reset (UG bit from TIMx_EGR)

5.4. TIM7

mode: Activated

5.4.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value) 8399 *

Counter Mode Up

Counter Period (AutoReload Register - 16 bits value) 9999 *

Trigger Output (TRGO) Parameters:

Trigger Event Selection Reset (UG bit from TIMx_EGR)

5.5. USART2

Mode: Asynchronous

5.5.1. Parameter Settings:

Basic Parameters:

Baud Rate 921600 *

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

5.6. **USART6**

Mode: Asynchronous

5.6.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

^{*} User modified value

6. System Configuration

6.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|--------|-----------------|--------------------|------------------------------|---------------------------|----------------|------------|
| RCC | PH0- OSC_IN | RCC_OSC_IN | n/a | n/a | n/a | |
| | PH1- OSC_OUT | RCC_OSC_OUT | n/a | n/a | n/a | |
| SYS | PA13 | SYS_JTMS- SWDIO | n/a | n/a | n/a | |
| | PA14 | SYS_JTCK- SWCLK | n/a | n/a | n/a | |
| USART2 | PA2 | USART2_TX | Alternate Function Push Pull | Pull-up | Very High * | |
| | PA3 | USART2_RX | Alternate Function Push Pull | Pull-up | Very High | |
| USART6 | PC6 | USART6_TX | Alternate Function Push Pull | Pull-up | Very High | |
| | PC7 | USART6_RX | Alternate Function Push Pull | Pull-up | Very High | |

6.2. DMA configuration

nothing configured in DMA service

6.3. NVIC configuration

| Interrupt Table | Enable | Preenmption Priority | SubPriority |
|--|----------|----------------------|-------------|
| Non maskable interrupt | true | 0 | 0 |
| Hard fault interrupt | true | 0 | 0 |
| Memory management fault | true | 0 | 0 |
| Pre-fetch fault, memory access fault | true | 0 | 0 |
| Undefined instruction or illegal state | true | 0 | 0 |
| System service call via SWI instruction | true | 0 | 0 |
| Debug monitor | true | 0 | 0 |
| Pendable request for system service | true | 0 | 0 |
| System tick timer | true | 0 | 0 |
| USART2 global interrupt | true | 0 | 0 |
| TIM6 global interrupt, DAC1 and DAC2 underrun error interrupts | true | 0 | 0 |
| TIM7 global interrupt | true | 0 | 0 |
| USART6 global interrupt | true 0 0 | | 0 |
| PVD interrupt through EXTI line 16 | unused | | |
| Flash global interrupt | unused | | |
| RCC global interrupt | unused | | |
| FPU global interrupt | unused | | |

^{*} User modified value

7. Power Consumption Calculator report

7.1. Microcontroller Selection

| Series | STM32F4 |
|-----------|---------------|
| Line | STM32F407/417 |
| мси | STM32F407VETx |
| Datasheet | 022152_Rev8 |

7.2. Parameter Selection

| Temperature | 25 |
|-------------|-----|
| Vdd | 3.3 |

8. Software Project

8.1. Project Settings

| Name | Value | |
|-----------------------------------|--------------------------------------|--|
| Project Name | GPS-Decoding | |
| Project Folder | D:\Project_CubeMx\Drone\GPS-decoding | |
| Toolchain / IDE EWARM | | |
| Firmware Package Name and Version | STM32Cube FW_F4 V1.17.0 | |

8.2. Code Generation Settings

| Name | Value |
|---|---|
| STM32Cube Firmware Library Package | Copy all used libraries into the project folder |
| Generate peripheral initialization as a pair of '.c/.h' files | Yes |
| Backup previously generated files when re-generating | No |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power | No |
| consumption) | |