

# CS & IT ENGINEERING

## Computer Organization Architecture

Cache Organization

DPP 01 Discussion Notes



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#Q. A cache is used to reduce the effective memory access time of 200ns without cache to 65ns with cache. If cache access time is 50ns, then cache hit rate is \_\_\_\_%?

With out - Cache Memory access time = only main  
memory access time = 200 ns

Memory access time = Avg MAT

$$= 65 = H \times 50 + (1-H) \times 200$$

$$= H = 0.9 = 90\%$$

#Q. A computer system has a cache with cache access time  $T_c = 10\text{ns}$ , hit ratio of 80% and average memory access time of  $T_m = 20\text{ns}$ . The access time for physical memory  $T_p$  is \_\_\_\_\_ ns?

$$T_c = 10\text{ns}$$

$$\text{Average Memory time} = 0.8 \times 10 + 0.2 \times T_p$$

$$T_p = \underline{60\text{ns}}$$

#Q. A cache line has 128 bytes. The main memory has addressing latency 64ns and access bandwidth 1GB/s. The time required to fetch the entire cache line from the main memory is \_\_\_\_\_ ns?

$$\begin{aligned}
 &1 \text{ GB} \text{ ————— } 1 \text{ Sec} \\
 &128 \text{ byte data} = \frac{(1 \text{ Sec} \times 128 \text{ Byte})}{1 \text{ GB}} \\
 &= 128 \\
 &= \text{latency time} + \text{block access} \\
 &= 64 + 128 \\
 &= 192 \text{ ns}
 \end{aligned}$$

#Q. Consider a system using a cache. The cache is having 70% hit ratio and is 9 times faster than main memory. The average memory access time then increased due to some program execution and the new average access time becomes 40% more than older one of 340ns. The hit ratio of new cache design is 53%?

Old average MAT = 340ns

$$340 = 0.7 \times T_{cm} + 0.3 T_m$$

$$T_{cm} = 340 / 0.3 = 1000\text{ns}$$

$$T_{mm} = 9 \times 1000 = 9000\text{ns}$$

$$476 = H \times 1000 + (1-H) \times 9000$$

$$H = \frac{424}{800} = 0.53$$

$$476 = 0.53 \times 340 + 0.47 \times 9000$$

#Q. Consider a memory hierarchy which takes 500 nanoseconds for access when there is a miss in cache and takes 100 nanoseconds for access when there is a hit in cache. Assume if among all memory references 90% of the references are having a hit on cache then average memory access time is \_\_\_\_\_ nanoseconds?

Average Memory access time  $\Rightarrow$

$$0.9 \times 100 + 0.1 \times 500$$

$$= 146 \text{ ns}$$

#Q. A system has a write through cache with access time of 100ns and hit ratio of 90%. The main memory access time is 1000ns. 70% of memory references are for read operations. Average memory access time for read-write operations both and effective hit rate(in %) are?

$$AMAT_{wo} = MAT$$

**A** 433, 90%

**B** 433, 63% = 1000ns

**C** 190, 90%

**D** 190, 63%  $0.7 \times 190 + 0.3 \times 1000$

$$\begin{aligned} \text{Effective hit-Rate} &= \text{Hit-Rate} = 433 \text{ ns} \\ &\text{and } \times \% \text{ of Read op} \\ &= 0.9 \times 0.7 = 63\% \end{aligned}$$



#Q. Consider a write through cache which can provide only 63.75% of effective hit rate. If among all memory references 75% references are for read, then the hit ratio of cache for only read operations \_\_\_\_%?

Effective Hit-Rate = Hit-Rate for read %  
of Read operation

$$0.6375 = \text{Hit-Rate for Read} \times 0.75$$

$$= 0.6375 / 0.75 = \underline{\underline{85\%}}$$



#Q. Consider a write through cache which can provide only 61.92% effective hit rate. If among all memory references 28% references are for write, then the hit ratio of cache for only read operations is \_\_\_\_?

$$\% \text{ of Read operation} = 100 - 28 = 72\%$$

$$\begin{aligned} \underline{EHR} &= \text{Hit rate of end} \times \% \text{ of Read operation} \\ \underline{0.6192} &= \text{"} \times 0.72 \\ &= 0.86 \Rightarrow 86\% \end{aligned}$$



**THANK - YOU**

