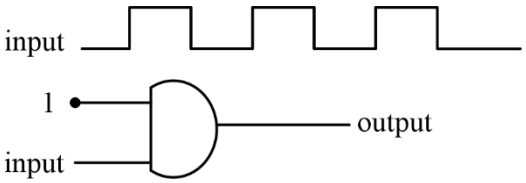
Branch:CSE & IT **English**

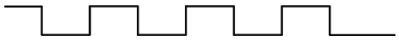
Digital Logic
Logic Gate

DPP-01

1. The input wave form is given



Draw output wave form

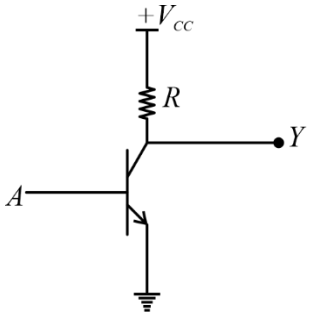
(a) 

(b) 0

(c) 1

(d) 

2.

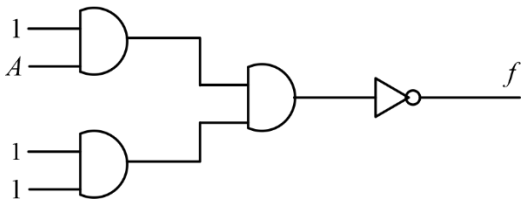


When $A = 1$ then the value of Y equal

(a) 0 (b) 1

(c) V_{CC} (d) none of the above

3.

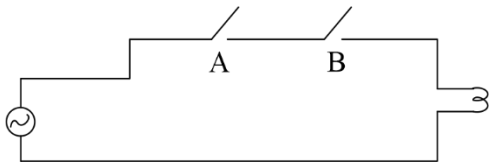


Find value of f ?

(a) 1 (b) 0

(c) A (d) $\overline{A}$

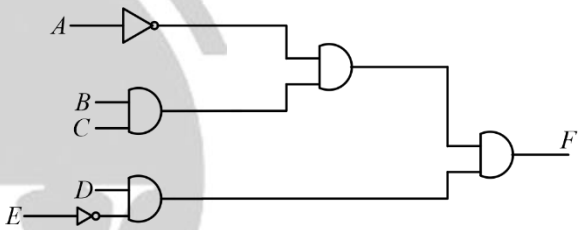
4. Bulb will glow when A and B are respectively.



(a) 00 (b) 01

(c) 10 (d) 11

5. Find expression of F



(a) $\bar{A}BC + D\bar{E}$ (b) $\bar{A}BCD\bar{E}$

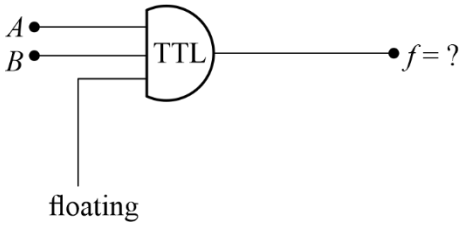
(c) $\left(\bar{A} + BC\right)D\bar{E}$ (d) $\bar{A}BC\left(D + \bar{E}\right)$

6. The inverter is _____ gate

(a) AND (b) OR

(c) NOT (d) none of the above

7.



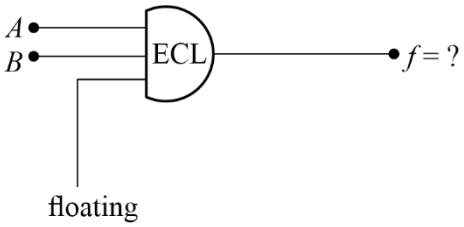
Find expression of f ?

(a) AB (b) 1

(c) $A + B$ (d) $\overline{AB}$

$\overline{AB}$

8.

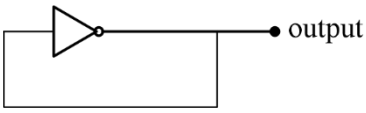


Find expression of f ?

(a) 0 (b) 1

(c) AB (d) $\overline{AB}$

09.



Find output frequency, if the propagation delay of NOT gate is 2 n sec.

(a) 125 MHz (b) 500MHz

(c) 250 MHz (d) none of the above

Answer Key

1. **(d)**
2. **(a)**
3. **(d)**
4. **(d)**
5. **(b)**
6. **(c)**
7. **(a)**
8. **(a)**
9. **(c)**