Variant:

3V3A
3V3

C10   L1   X47

PWR 5V

GND

C9   5V   GND

X41   R24
3V3A   D10

PGM   X45

Analog1   Analog0

R13   R6

Git:c1833218

GP15   SCS3   SCS1   MISO   AI3   AI1   AIO
SCS4   SCS2   SCLK   MOSI   AI2   AI0

C6   D5   +

C7   R10
VR1
USB   X44

IO CLK 3V3 Boot

3V3A   R21
R12   R3   C5   5V
USB   D8   X43

GND   R20   R23   X43
RST   C3   R22

Reset   R16   X66
R17   C14   C13
GND   R5   C12   VBUS

GP11 GP9 GP7 GP5 GP3 GP1
GP12 GP10GP8GP6 GP4 GP2

X42   L2
Boot   D9
R15

S5   C11
IC1   D7
TP3   L3   DBG   X46

TP2
B1008

USBN   GP14   SDA   RX2   CTS1   RX1
USBP   GP13   SCL   TX2   RTS1   TX1

LED1   +   LED2   +   LED3   +   LED4   +

R3   R4   R7   R9
S1   S2   S3   S4
B1   B2   B3   B4

GND

G-Base-1 Rev 0.2
erb-pcb0005
2024/11/25

3V3   R1   R2   R8   R14
3V3   C1   C2   C4   C8

Bottom Overlay

Variant:

DEV::BLOCK
Agile Hardware Design
info@dev-block.com

Design Files
https://github.com/DEV-BLOCK-IO/

Documentation:
www.dev-block.com/docs