



Centre of Excellence in VLSI

CERTIFICATE OF COMPLETION

This is to certify that

SAPPARAPU DEVI SRI PRASAD

has successfully completed

RISC-V RV32I RTL Design using Verilog HDL

Online Course on **2nd day of November 2023**

Celebrating
10 YEARS
Of Excellence



SIVAKUMAR P R
CEO, MAVEN SILICON