

Von: **Ulrich Habel** [espero7757@gmx.net](mailto:espero7757@gmx.net)  
Betreff: HFT-Core-Module V1.1 - Hochlauf  
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An: Ulrich Habel [espero7757@gmx.net](mailto:espero7757@gmx.net)



## Bootstrap - check for HFT-Core-Module PCB V1.1

Variant	Energy entry	Bootstrap circuit
1	Solar 0.3V .. 2V	VSOL, U2601, BQ_VSTOR, U2681, +5V0, BQ_VSTOR, BQ_VOUT, +3V3.
2	USB	USB_VBUS, VDDUSB, D2801, (R2801    R2802), U2801, VUSB_SW, +5V0, D2681, BQ_VSTOR, BQ_VOUT, +3V3.
3	12V in	DC_12V_IN, U2401, POW_IN_5V0, D2521, +5V0, D2681, BQ_VSTOR, BQ_VOUT, +3V3.
4	LiPo 3.7V	NO! First set register REG01[5:4] = 10 and PSEL = 1 and OTG = 1 to enable BOOST mode (OTG). Circuit: LIPO_BAT, U2501, +5V0, D2681, BQ_VSTOR, BQ_VOUT, +3V3.

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