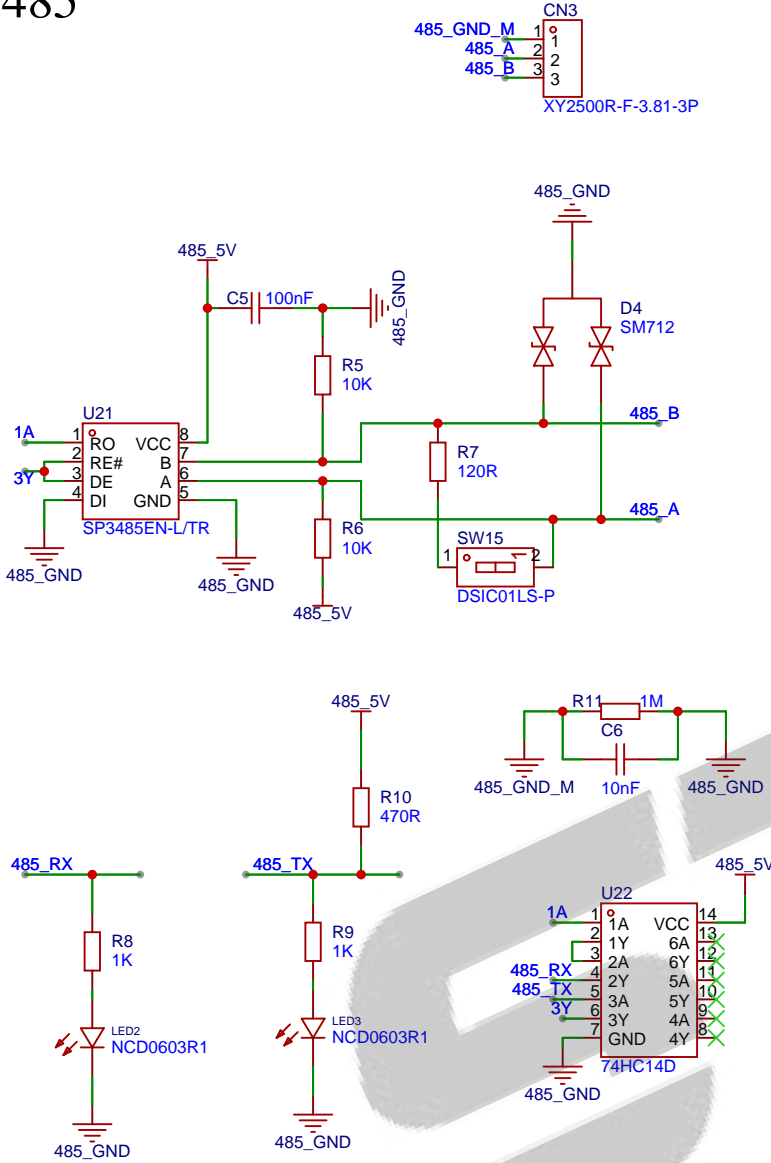
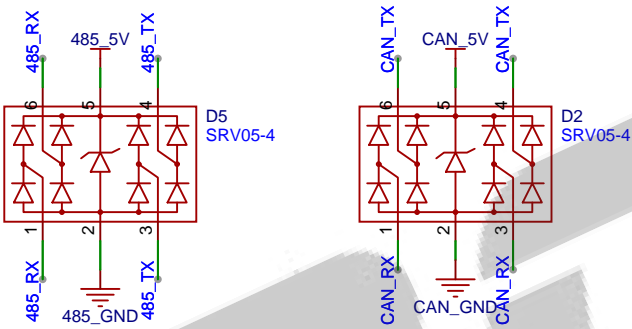


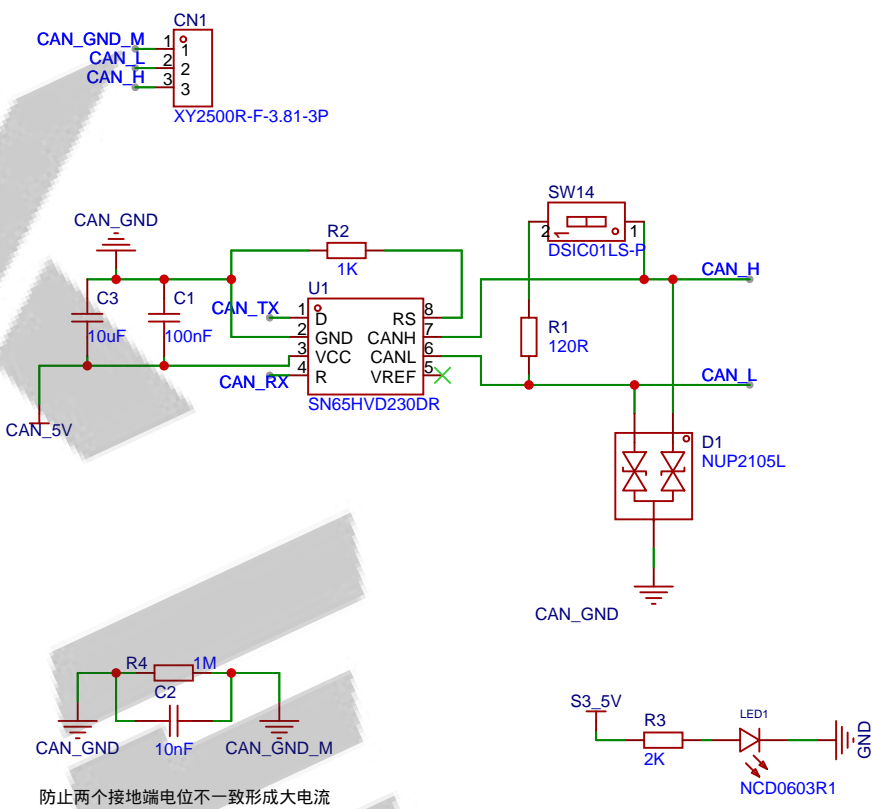
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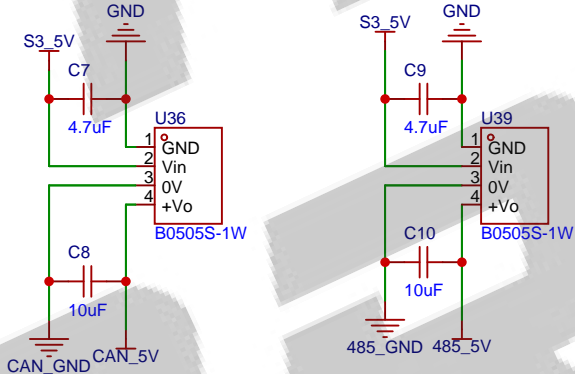
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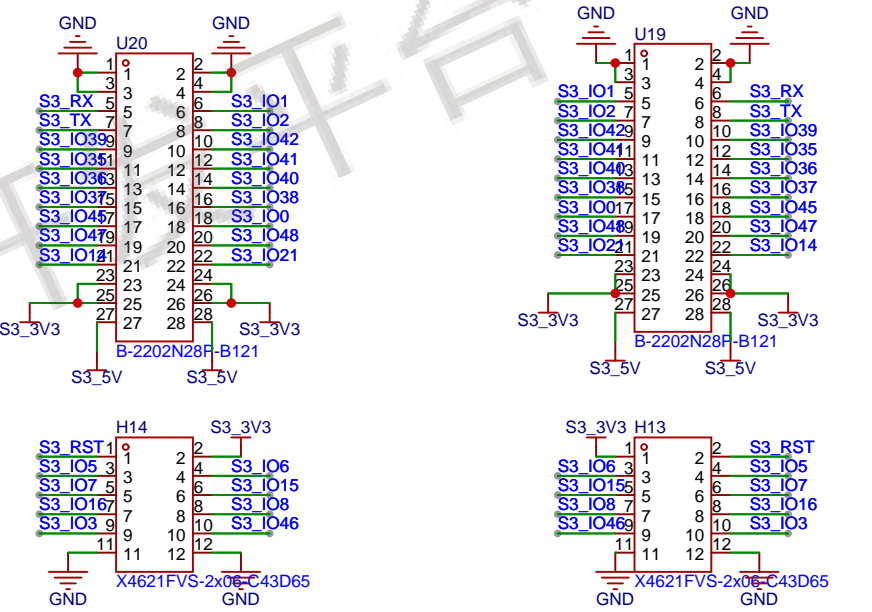
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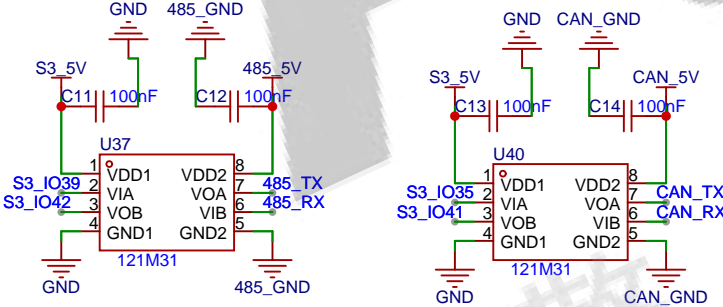
DC-ISOLATION



S3 BUS



IO-ISOLATION



原理图			更新日期	
			创建日期	
图页			物料编码	
绘制				
审阅				
		版本	尺寸	页 共
嘉立创EDA				