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This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge International will not enter into discussions about these mark schemes.

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This document consists of **11** printed pages.

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**Generic Marking Principles**

These general marking principles must be applied by all examiners when marking candidate answers. They should be applied alongside the specific content of the mark scheme or generic level descriptions for a question. Each question paper and mark scheme will also comply with these marking principles.

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| GENERIC MARKING PRINCIPLE 1:<br><br>Marks must be awarded in line with:<br><br>• the specific content of the mark scheme or the generic level descriptors for the question<br>• the specific skills defined in the mark scheme or in the generic level descriptors for the question<br>• the standard of response required by a candidate as exemplified by the standardisation scripts. |
| GENERIC MARKING PRINCIPLE 2:<br><br>Marks awarded are always **whole marks** (not half marks, or other fractions). |
| GENERIC MARKING PRINCIPLE 3:<br><br>Marks must be awarded **positively**:<br><br>• marks are awarded for correct/valid answers, as defined in the mark scheme. However, credit is given for valid answers which go beyond the scope of the syllabus and mark scheme, referring to your Team Leader as appropriate<br>• marks are awarded when candidates clearly demonstrate what they know and can do<br>• marks are not deducted for errors<br>• marks are not deducted for omissions<br>• answers should only be judged on the quality of spelling, punctuation and grammar when these features are specifically assessed by the question as indicated by the mark scheme. The meaning, however, should be unambiguous. |
| GENERIC MARKING PRINCIPLE 4:<br><br>Rules must be applied consistently, e.g. in situations where candidates have not followed instructions or in the application of generic level descriptors. |

GENERIC MARKING PRINCIPLE 5:

Marks should be awarded using the full range of marks defined in the mark scheme for the question (however; the use of the full mark range may be limited according to the quality of the candidate responses seen).

GENERIC MARKING PRINCIPLE 6:

Marks awarded are based solely on the requirements as defined in the mark scheme. Marks should not be awarded with grade thresholds or grade descriptors in mind.

| Question | Answer | Marks |
|---|---|---|
| 1(a) | **Instructions/program** that is used to operate a computer/hardware | 1 |
| 1(b) | B | 1 |
| 1(c) | Operating system // system software | 1 |

| Question | Answer | Marks |
|---|---|---|
| 2(a) | bit | 1 |
| 2(b) | 4 | 1 |
| 2(c) | Any **two** from:<br><br>• 22 016 × 8 then divided 8<br>• 22 016 × 10<br>• 220 160 / 1024<br><br>**One** mark for:<br><br>215 KiB | 3 |
| 2(d)(i) | Reducing the **size** of a file | 1 |
| 2(d)(ii) | It will take up/use less **storage** space | 1 |

| Question | Answer | Marks |
|---|---|---|
| 3(a)(i) | They are both **number systems** | 1 |
| 3(a)(ii) | • Binary is base-2 whereas hexadecimal is base-16<br>• Binary only uses numbers whereas hexadecimal also uses letters // Binary only uses 0 and 1 whereas hexadecimal uses 0 to 9/A to F | 2 |

| Question | Answer | Marks |
|---|---|---|
| 3(b) | • (0000)1111 <br> • 10110100 <br> • 11101011 | **3** |
| 3(c) | • E <br> • 64 <br> • FA | **3** |
| 3(d)(i) | Any **two** from: <br><br> • **Each/All/Every value/digit/bit** in the binary number is **shifted/moved** to the **left** <br> • The **left most/most significant** bit is lost <br> • A **0** is added as the **right most/least significant** bit | **2** |
| 3(d)(ii) | The binary integer is multiplied by 2 | **1** |
| 3(e) | Two's complement | **1** |

| Question | Answer | Marks |
|---|---|---|
| 4(a) | Any **two** from: <br><br> • Touchscreen <br> • Microphone <br> • Button <br> • (Digital) camera <br> • Sensor // by example | **2** |
| 4(b) | Any **one** from: <br><br> • Screen <br> • Speaker <br> • LED <br> • Actuator | **1** |

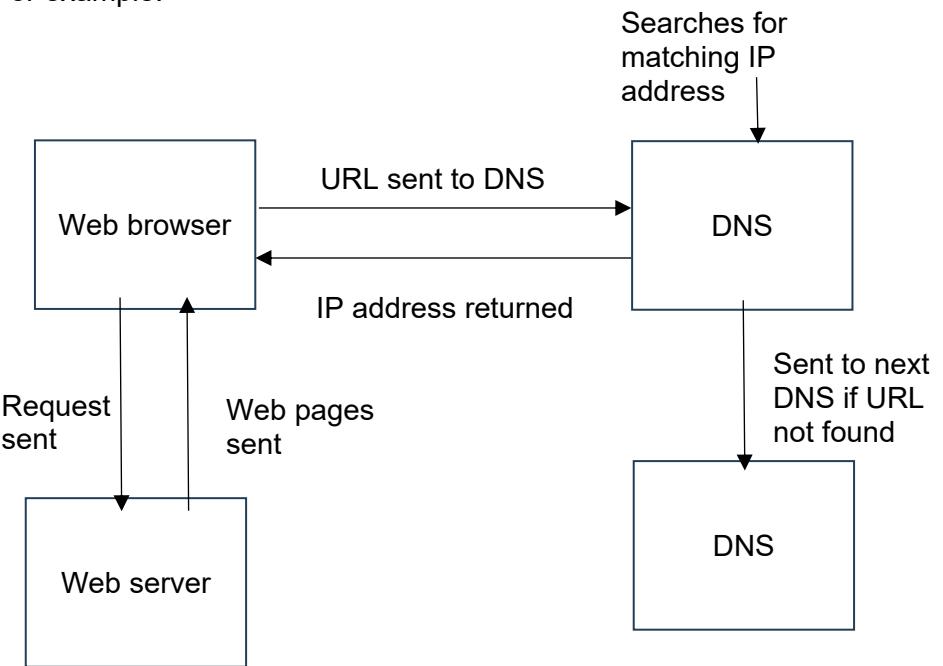| Question | Answer | Marks |
|---|---|---|
| 4(c) | Any **two** from:<br><br>• To **store** the BIOS<br>• To **store** the bootstrap<br>• To help start-up the smart watch // to **store** start-up instructions<br>• To **store** the firmware<br>• For non-volatile storage // to store data **permanently**<br>• To **store data/instructions** that **should not change** (unless needed) | **2** |
| 4(d)(i) | Any **two** from:<br><br>• A collection of servers<br>• Allows access to data remotely // Stores data in a remote location<br>• Hardware can be owned/managed by a third party<br>• Data storage that is stored/accessed using the internet | **2** |
| 4(d)(ii) | Any **four** from:<br><br>• No need to maintain the hardware<br>• … the third party is responsible for maintaining the hardware / security of the data<br><br>• Resources can be increased / decreased (on demand) // Can have unlimited storage capacity<br>• … no need to worry about running out of storage space<br>• … can save cost of **purchase** of **hardware**<br><br>• Need less/no (secondary) storage space **on the watch**<br>• … watch can remain/is small in size<br>• … costs of watch can be kept lower<br><br>• Can access the data from **other/any devices**<br>• … if the watch breaks/is lost data is still available | **4** |

| Question | Answer | Marks |
|---|---|---|
| 4(e) | Any **two** from:<br><br>• The watch can only perform a dedicated/limited functions/tasks<br>• … and a general purpose computer performs many/multiple functions<br>• … (this means that) it is an embedded system<br>• You cannot plug in peripherals<br>• You cannot reprogram the smartwatch // Cannot install other software/apps<br>• It would only have a microprocessor // It would not have a CPU | 2 |

| Question | Answer | Marks |
|---|---|---|
| 5(a) | Any **four** from:<br><br>• A check digit is **calculated** from/using the **barcode data**<br>• … using an <u>algorithm</u> // by example e.g. Modulo 11<br>• … and added to the **barcode**<br>• When/after the **barcode is scanned** the check digit is **recalculated …**<br>• … using the **same** algorithm<br>• If the **check digits** do not match an error has occurred when scanning the barcode // If the **check digits** match no error has occurred when scanning the barcode | 4 |
| 5(b)(i) | • <u>Bits</u> are sent one at a time<br>• Bits are sent down a single wire<br>• Data is sent in one direction only | 3 |

| Question | Answer | Marks |
|---|---|---|
| 5(b)(ii) | Any **three** from:<br><br>• The stock control system may be a long distance away<br>• … parallel should not be used in long distance transmission // Serial is more **reliable** for long distance transmission<br>• The data does not need to be sent quickly<br>• … the increased speed of parallel is not needed<br>• … as only **small** amounts of data need to be sent<br>• The <u>bits</u> are sent/arrived in order<br>• … the **data** will not be skewed // the **data** could be skewed if parallel was used<br>• … there will be no data collisions<br><br>• There will be less interference/crosstalk (due to single wire)<br>• … there will be fewer errors in the data<br><br>• No need for a reply/response from stock control system<br>• … **half-duplex/full-duplex** is not necessary as only one way transmission needed | 3 |
| 5(b)(iii) | Any **two** from:<br><br>• (Odd/even) Parity check // Parity byte check // parity block check<br>• Checksum<br>• Echo check<br>• (Positive/negative) ARQ // Automatic repeat query // Automatic repeat request | 2 |

| Question | Answer | Marks |
|---|---|---|
| 6 | **One** mark for each correct term or definition in the correct place:<br><br>**Components**<br>• Control unit // CU<br>• Memory address register // MAR<br>• Data bus<br>• Current instruction register // CIR<br><br>**Descriptions**<br>• (Program counter) Stores the address of the next instruction to be fetched<br>• (Accumulator) Stores the interim **result** for a calculation | **6** |

| Question | Answer | Marks |
|---|---|---|
| 7(a) | A **text-based address** for a **website**/**web page** | **1** |
| 7(b) | Any **two** from:<br><br>• Protocol<br>• Domain name<br>• File name | **2** |
| 7(c) | Web browser | **1** |

| Question | Answer | Marks |
|---|---|---|
| 7(d) | **One** mark for each correct part of the diagram.<br><br>The diagram shows:<br><br>• **URL** sent from **web browser** to the **DNS**<br>• DNS finding **matching IP address**<br>• If it cannot find the URL, it is sent to the next DNS (until found)<br>• **IP address** returned to **web browser**<br>• **Request** sent from **web browser** to **web server** (for web page)<br>• Web page/HTML files sent from **web server** to **web browser**<br>• Web browser **rendering HTML**<br><br>For example:<br><br> | 5 |

| Question | Answer | Marks |
|---|---|---|
| 7(e) | **One** mark for each correct term in the correct order:<br><br>• Web browser<br>• Web server<br>• Web server<br>• Web browser<br>• Encrypted | **5** |

| Question | Answer | Marks |
|---|---|---|
| 8(a) | A | **1** |
| 8(b) | • Knowledge base<br>• Rule base<br>• Inference engine | **3** |
| 8(c) | Any **three** from:<br><br>For example:<br><br>• It could **gather data** during **vacuuming**<br>• … and adapt its **own processes**<br>• … such as where obstacles are placed in the room<br>• … such as where dirtier areas are<br>• … such as a path through a room<br>• … such as the shape of a room<br>• … such as the most efficient route to vacuum a room<br>• … so, it knows areas to avoid/concentrate on/use different cleaning tools | **3** |