

# **DHRUV PATEL**

#### CONTACT

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### **EDUCATION**

April 2022 8.1 CGPA: Computer Engineering **HGCE**, Ahmedabad

#### PROFESSIONAL SUMMARY

#### SKILLS

PT AF

PI INTERFACE - PI OPC DA & HDA, PI MODBUS,OPC UA, PI to PI,UFL PI SMT

PI VISION PI EXTENSIBILITY PI DATALINK

## **WORK HISTORY**

April 2022 - Current

AVEVA PI Team Lead, Cerebulb (India) Private Limited, Gandhinagar , India

Experienced Software Engineer with a demonstrated history of working in the Software industry. OSIsoft PI Developer and have been working on OSISoft - PI Suite.

- PI SMT: Creation and modification of PI points, manual data upload in the archive, PI server backup, tuning parameters, analyzing errors from PI message logs.
- 2. PI Interfaces: Design and configuration of PI to PI interfaces, PI Interface for OPC DA, UFL, Modbus. OPC UA, Ping, Performance
- 3. PI Process book and PI Vision: creating and editing process book displays, publishing displays in PI vision
- 4. PI Addins: Generation of the report, data validation, PI point configuration using PI builder, PI configurator.
- PI SDK: collective configuration and management, Analysing PI SDK logs
   PIAF: AF elements, AF expression Analysis, Event frame analysis, creating and modifying, and mapping element template.
- 7. Handling service requests of PI point configuration issues, performance equations, PI analysis, event frames, and PI client issues.