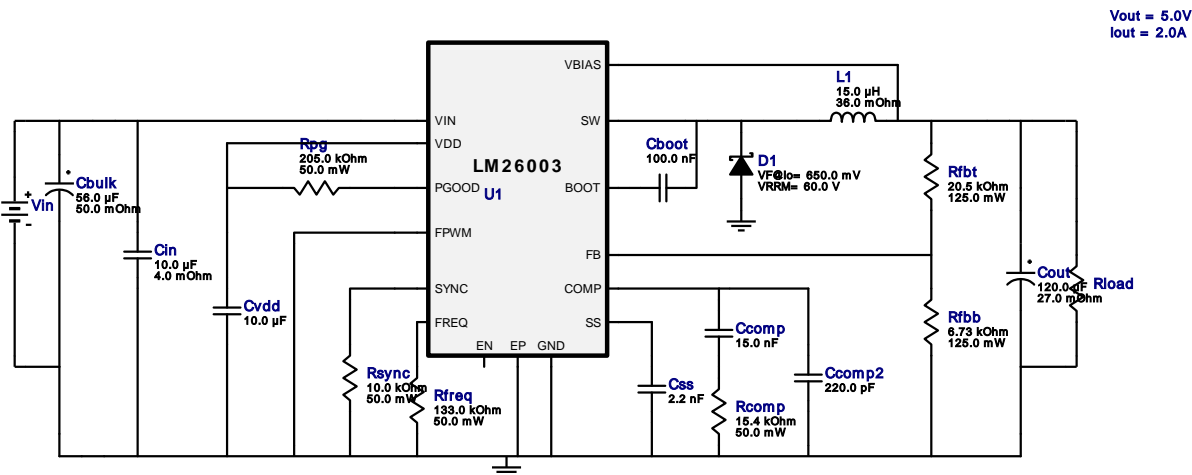


# WEBENCH® Design Report

Design : 12 LM26003MHX/NOPB  
LM26003MHX/NOPB 6V-10V to 5.00V @ 2A

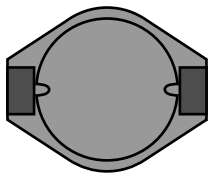







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VinMax = 10.0V  
Vout = 5.0V  
Iout = 2.0A

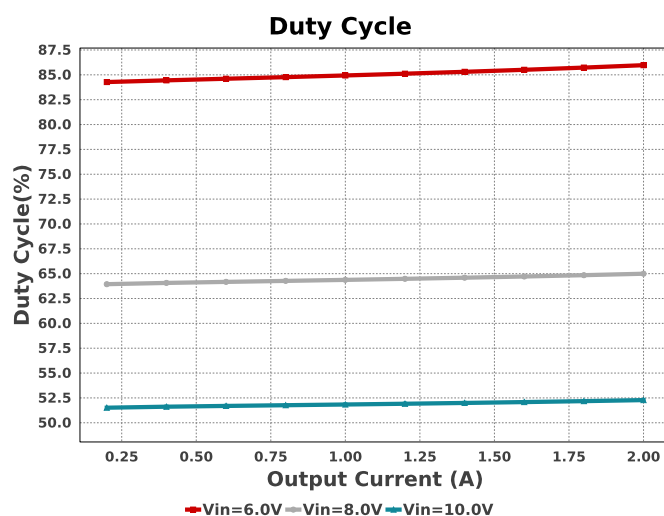
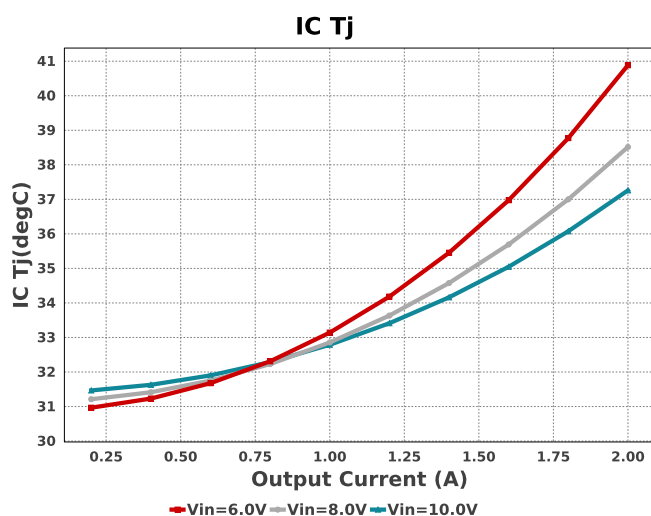
Device = LM26003MHX/NOPB  
Topology = Buck  
Created = 2023-04-21 10:11:07.004  
BOM Cost = \$2.87  
BOM Count = 17  
Total Pd = 0.77W



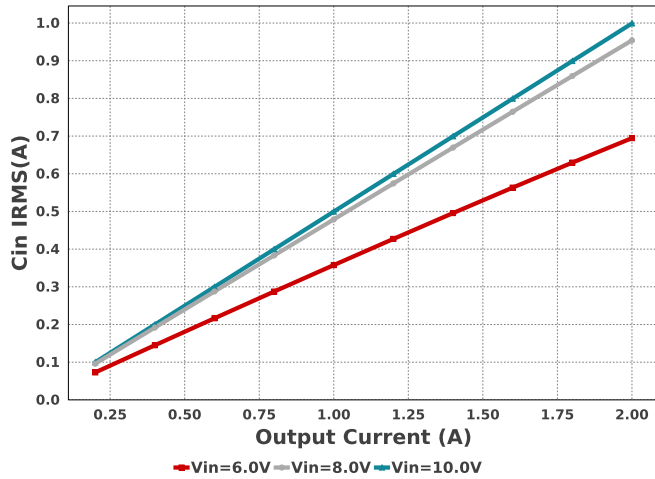
## Electrical BOM

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	AVX	08053C104JAZ2A Series= X7R	Cap= 100.0 nF VDC= 25.0 V IRMS= 0.0 A	1	\$0.07	0805 7 mm <sup>2</sup>
Cbulk	Panasonic	EEHZC1E560P Series= ZC	Cap= 56.0 uF ESR= 50.0 mOhm VDC= 25.0 V IRMS= 900.0 mA	1	\$0.29	SM_RADIAL_6.3AMM 80 mm <sup>2</sup>
Ccomp	TDK	C2012C0G1H153J085AA Series= C0G/NP0	Cap= 15.0 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.06	0805 7 mm <sup>2</sup>
Ccomp2	Samsung Electro-Mechanics	CL21C221JBANNNC Series= C0G/NP0	Cap= 220.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm <sup>2</sup>
Cin	MuRata	GRM21BR61E106MA73L Series= X5R	Cap= 10.0 uF ESR= 4.0 mOhm VDC= 25.0 V IRMS= 2.8 A	1	\$0.04	0805 7 mm <sup>2</sup>
Cout	Panasonic	10SVPC120M Series= SVPC	Cap= 120.0 uF ESR= 27.0 mOhm VDC= 10.0 V IRMS= 2.32 A	1	\$0.46	SM_RADIAL_6.3AMM 80 mm <sup>2</sup>
Css	Samsung Electro-Mechanics	CL21C222JBFNNNE Series= C0G/NP0	Cap= 2.2 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.02	0805 7 mm <sup>2</sup>
Cvdd	Samsung Electro-Mechanics	CL10A106MQ8NNNC Series= X5R	Cap= 10.0 uF VDC= 6.3 V IRMS= 0.0 A	1	\$0.02	0603 5 mm <sup>2</sup>
D1	Fairchild Semiconductor	FSV360FP	VF@Io= 650.0 mV VRRM= 60.0 V	1	\$0.13	SOD-123HE 13 mm <sup>2</sup>

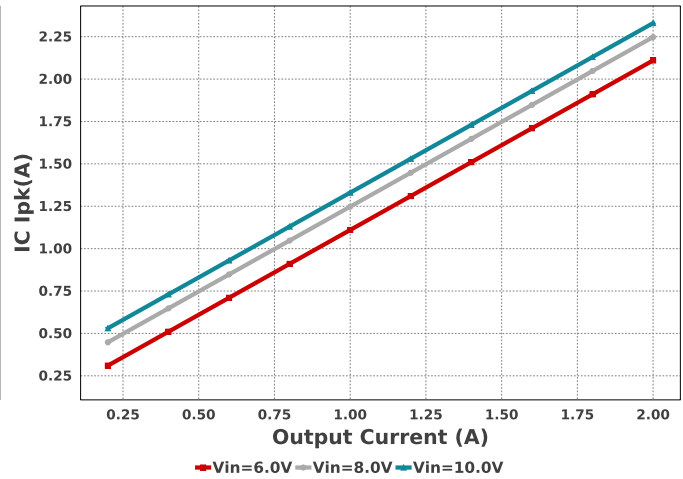
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
L1	NIC Components	NPI52W150MTRF	L= 15.0 $\mu$ H 36.0 mOhm	1	\$0.26	 IND_NPI52W 358 mm <sup>2</sup>
Rcomp	Yageo	RC0201FR-0715K4L Series= ?	Res= 15.4 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	 0201 2 mm <sup>2</sup>
Rfbb	Yageo	RT0805BRD076K73L Series= ?	Res= 6.73 kOhm Power= 125.0 mW Tolerance= 0.1%	1	\$0.05	 0805 7 mm <sup>2</sup>
Rfbt	Panasonic	ERJ-6ENF2052V Series= ERJ-6E	Res= 20.5 kOhm Power= 125.0 mW Tolerance= 1.0%	1	\$0.01	 0805 7 mm <sup>2</sup>
Rfreq	Yageo	RC0201FR-07133KL Series= ?	Res= 133.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	 0201 2 mm <sup>2</sup>
Rpg	Yageo	RC0201FR-07205KL Series= ?	Res= 205.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	 0201 2 mm <sup>2</sup>
Rsync	Yageo	RC0201FR-0710KL Series= ?	Res= 10.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	 0201 2 mm <sup>2</sup>
U1	Texas Instruments	LM26003MHX/NOPB	Switcher	1	\$1.40	 MXA20A 71 mm <sup>2</sup>



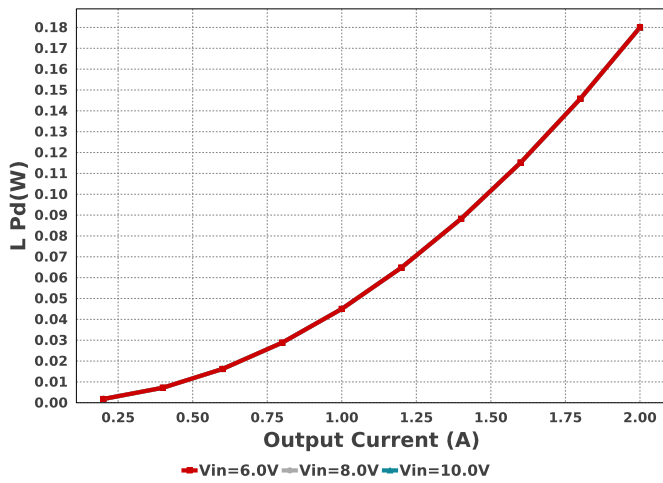
Cin IRMS



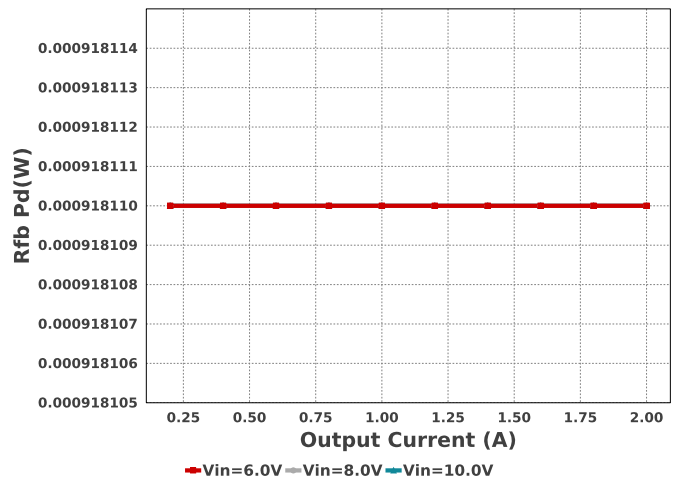
IC Ipk



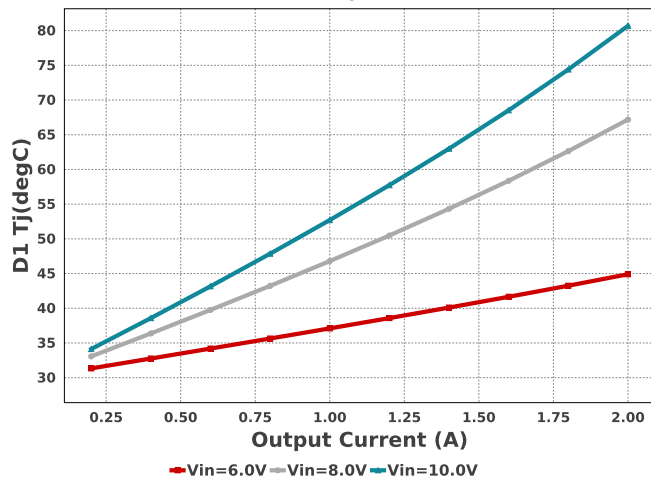
L Pd



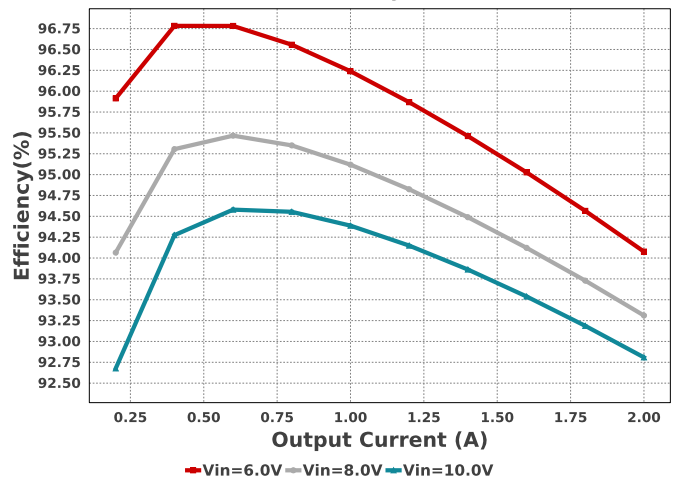
Rfb Pd

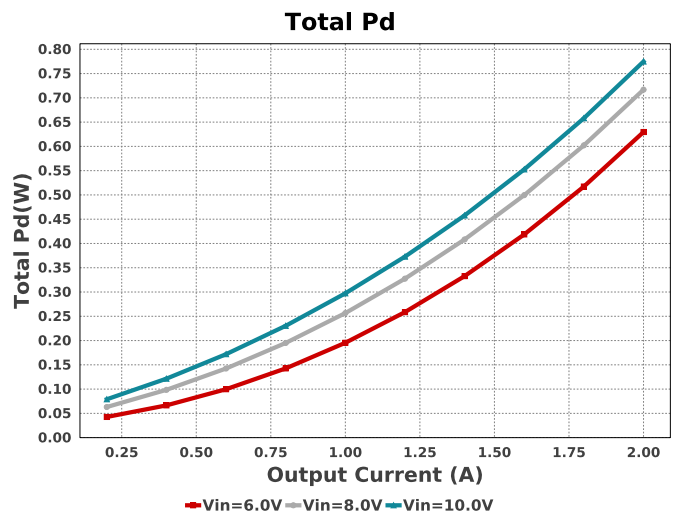
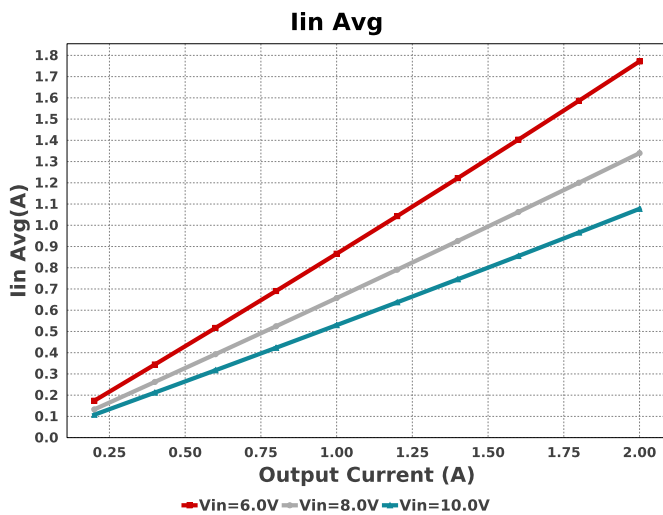
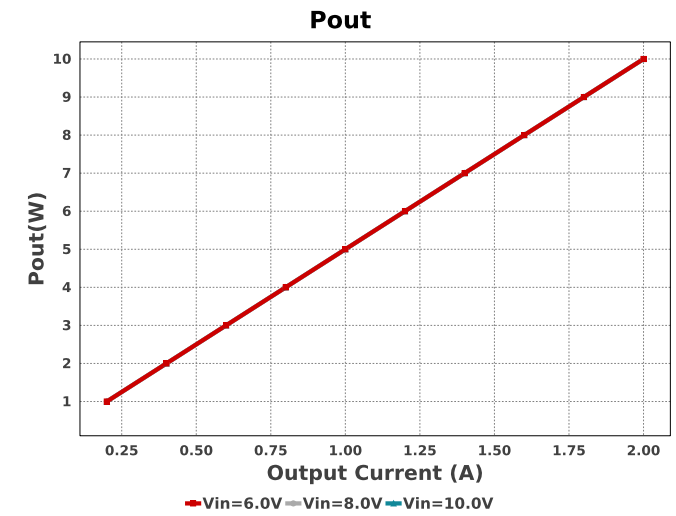
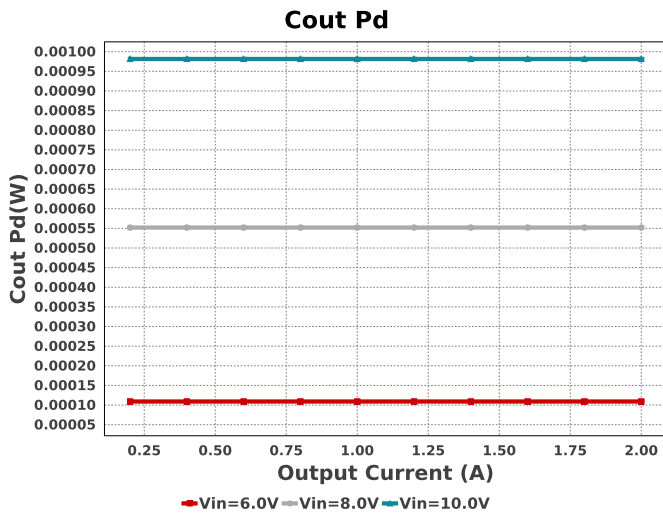
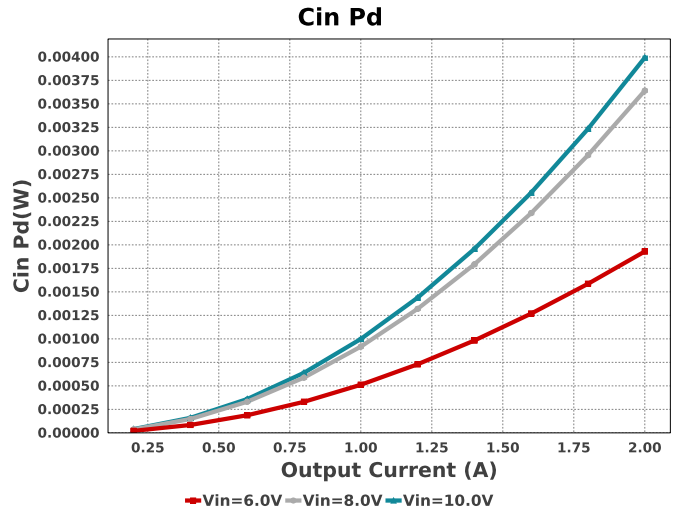
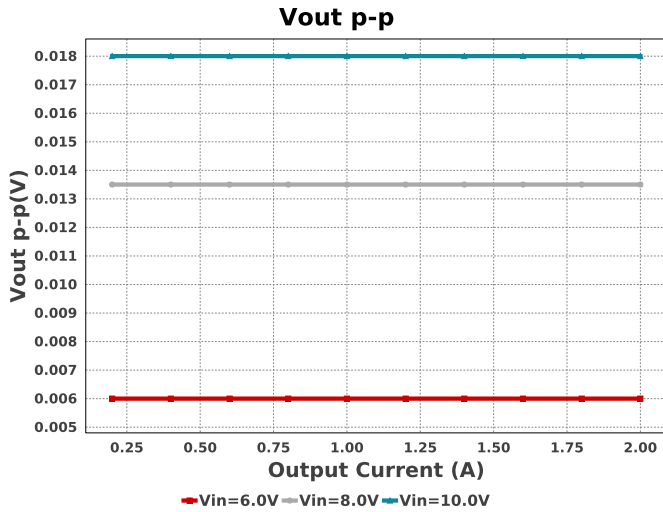


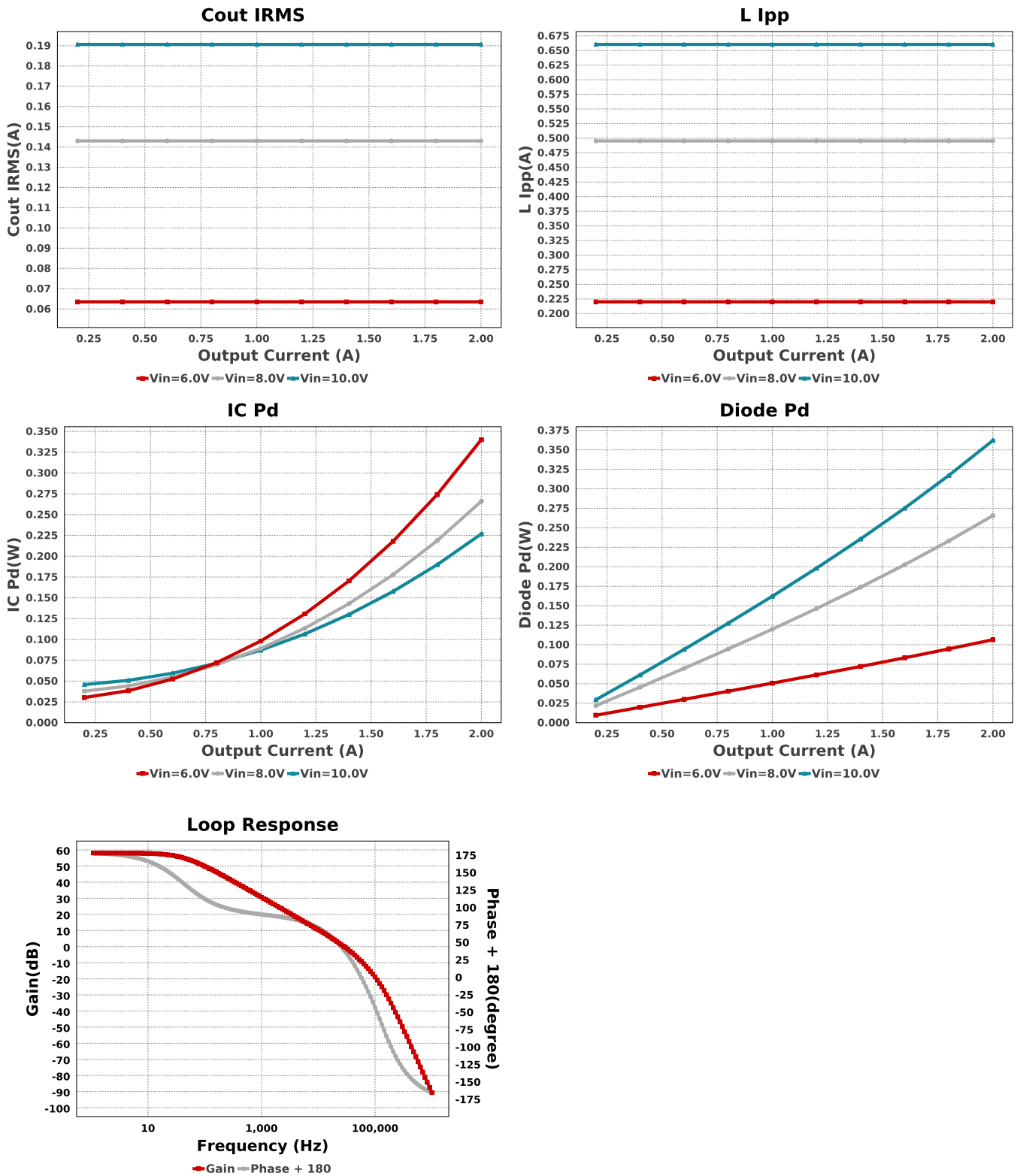
D1 Tj



Efficiency







## Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	998.959 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	3.992 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	190.654 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	981.42 $\mu$ W	Capacitor	Output capacitor power dissipation
5.	D1 Tj	80.686 degC	Diode	D1 junction temperature
6.	Diode Pd	362.04 mW	Diode	Diode power dissipation
7.	IC Ipk	2.33 A	IC	Peak switch current in IC
8.	IC Pd	226.86 mW	IC	IC power dissipation
9.	IC Tj	37.26 degC	IC	IC junction temperature
10.	IC ThetaJA	32.0 degC/W	IC	IC junction-to-ambient thermal resistance
11.	Iin Avg	1.078 A	IC	Average input current

#	Name	Value	Category	Description
12.	L Ipp	660.44 mA	Inductor	Peak-to-peak inductor ripple current
13.	L Pd	180.0 mW	Inductor	Inductor power dissipation
14.	Cin Pd	3.992 mW	Power	Input capacitor power dissipation
15.	Cout Pd	981.42 $\mu$ W	Power	Output capacitor power dissipation
16.	Diode Pd	362.04 mW	Power	Diode power dissipation
17.	IC Pd	226.86 mW	Power	IC power dissipation
18.	L Pd	180.0 mW	Power	Inductor power dissipation
19.	Rfb Pd	918.11 $\mu$ W	Power	Rfb Power Dissipation
20.	Total Pd	774.83 mW	Power	Total Power Dissipation
21.	Rfb Pd	918.11 $\mu$ W	Resistor	Rfb Power Dissipation
22.	BOM Count	17	System	Total Design BOM count
			Information	
23.	Cross Freq	28.516 kHz	System	Bode plot crossover frequency
			Information	
24.	Duty Cycle	52.281 %	System	Duty cycle
			Information	
25.	Efficiency	92.809 %	System	Steady state efficiency
			Information	
26.	FootPrint	663.0 mm <sup>2</sup>	System	Total Foot Print Area of BOM components
			Information	
27.	Frequency	277.591 kHz	System	Switching frequency
			Information	
28.	Gain Marg	-9.174 dB	System	Bode Plot Gain Margin
			Information	
29.	Iout	2.0 A	System	Iout operating point
			Information	
30.	Low Freq Gain	58.082 dB	System	Gain at 1Hz
			Information	
31.	Mode	SleepMode	System	Conduction Mode
			Information	
32.	Phase Marg	39.084 deg	System	Bode Plot Phase Margin
			Information	
33.	Pout	10.0 W	System	Total output power
			Information	
34.	Total BOM	\$2.87	System	Total BOM Cost
			Information	
35.	Vin	10.0 V	System	Vin operating point
			Information	
36.	Vout	5.0 V	System	Operational Output Voltage
			Information	
37.	Vout Actual	5.001 V	System	Vout Actual calculated based on selected voltage divider resistors
			Information	
38.	Vout Tolerance	2.379 %	System	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
			Information	
39.	Vout p-p	18.003 mV	System	Peak-to-peak output ripple voltage
			Information	

## Design Inputs

Name	Value	Description
Iout	2.0	Maximum Output Current
VinMax	10.0	Maximum input voltage
VinMin	6.0	Minimum input voltage
Vout	5.0	Output Voltage
base_pn	LM26003	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

## WEBENCH® Assembly

### Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of  $C_{in}$  and  $C_{out}$ , and the inductance and DC resistance of  $L1$  before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

### Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

### Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 6.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to  $V_{in}$  and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from  $V_{out}$  and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

### Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between  $V_{in}$  and GND, a load is connected between  $V_{out}$  and GND and a current meter is connected in series between  $V_{out}$  and the load. The load must be able to handle at least rated output power + 50% ( 7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



### Design Assistance

1. Master key : 638D447F34E559AB91B69E60416861C3[v1]
2. **LM26003** Product Folder : <http://www.ti.com/product/LM26003> : contains the data sheet and other resources.

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