


| REV | DESCRIPTION | DATE | BY |
|-----|--------------------|------------|-----|
| B | Production Release | 11/23/2015 | EPD |
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|-----|---------------------------------------|
| P01 | TITLE PAGE |
| P02 | 12V INPUT, 3.3V AND 5V SWITCHERS |
| P03 | TPS6590374 1 CONTROL |
| P04 | TPS6590374 2 RESET, & RTC |
| P05 | TPS6590374 3 MPU,DDR, & DSP SWITCHERS |
| P06 | TPS6590374 4 CORE & 1.8V |
| P07 | AM5728 CORE POWER & EEPROM |
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| P13 | AM5728 GPMC & LCD |
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| P25 | USB & SATA CONNECTOR |
| P26 | PCIe, SATA, & SERDES CLOCK |
| P27 | EXPANSION HEADERS |
| P28 | MECHANICAL |

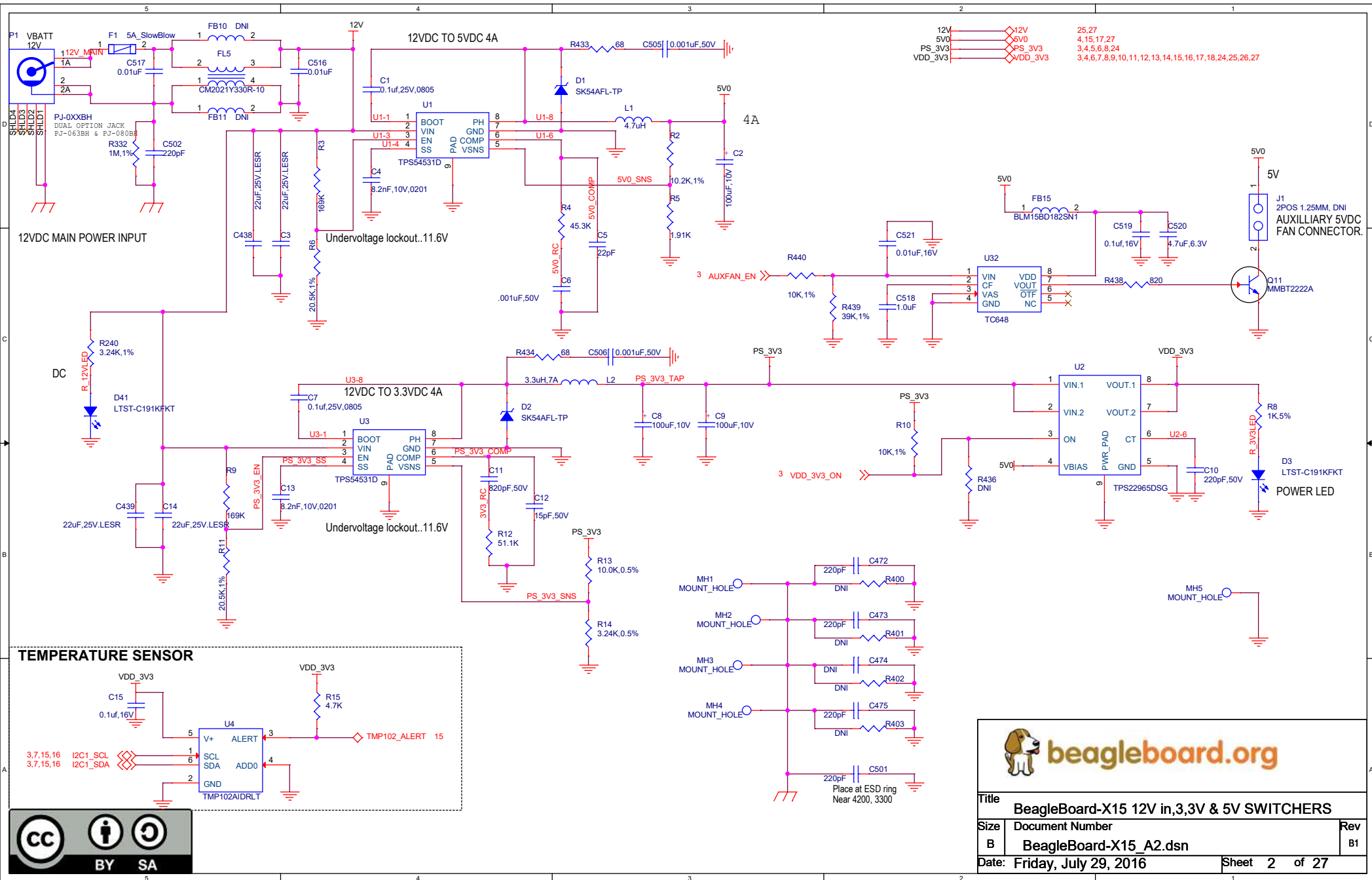
This schematic is *NOT SUPPORTED* and DOES NOT constitute a reference design. Only "community" support is allowed via resources at BeagleBoard.org/discuss.

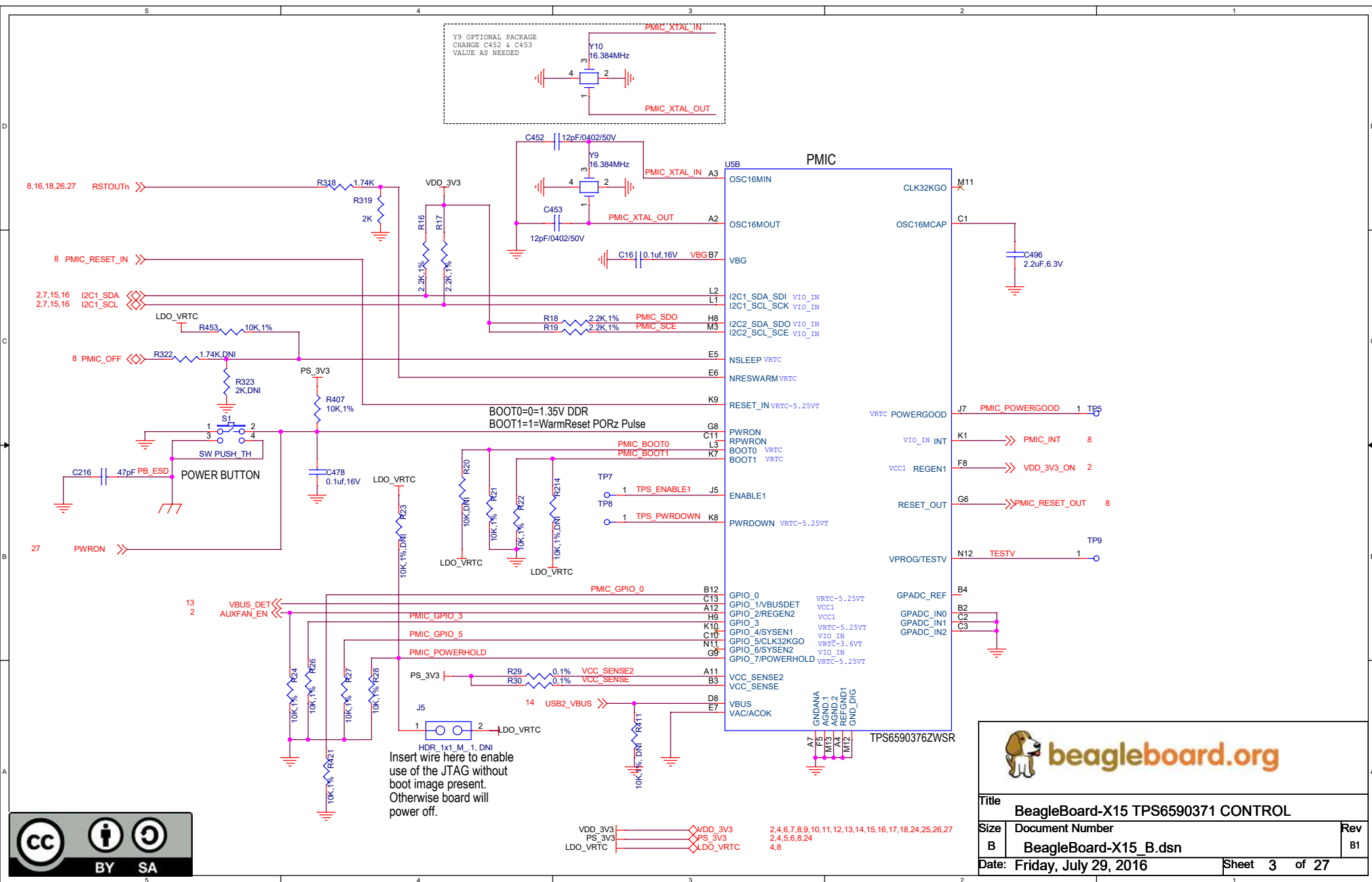
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| Title | | |
| BeagleBoard-X15 | | |
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| B | BeagleBoard-X15- B.dsn | B1 |
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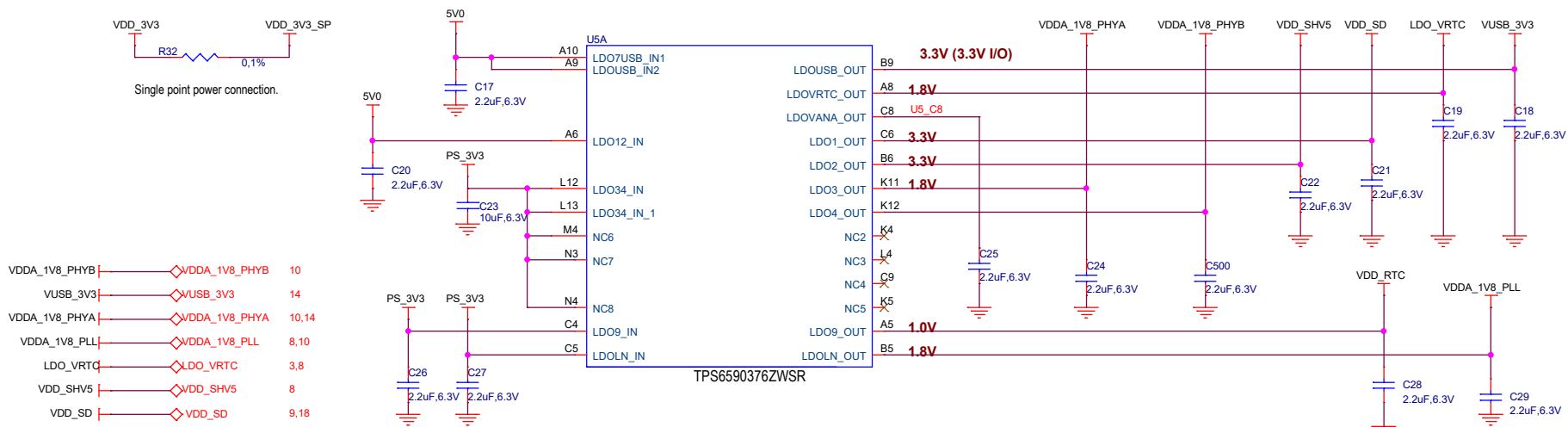






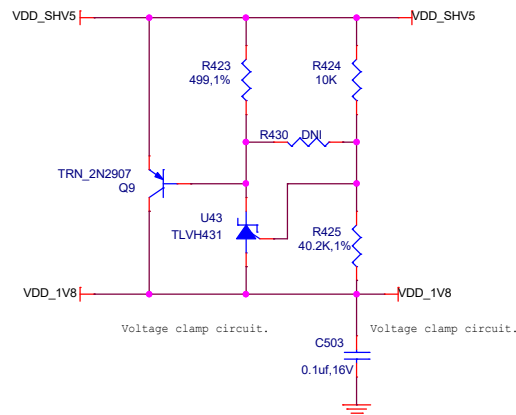
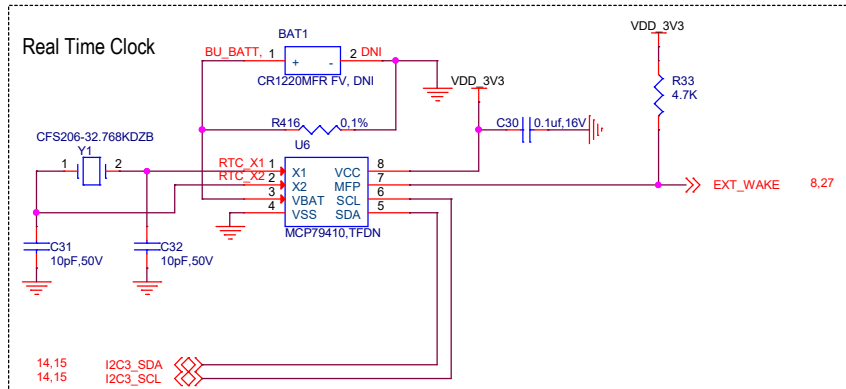
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| Title | | BeagleBoard-X15 TPS6590371 CONTROL | |
| Size | Document Number | Rev | |
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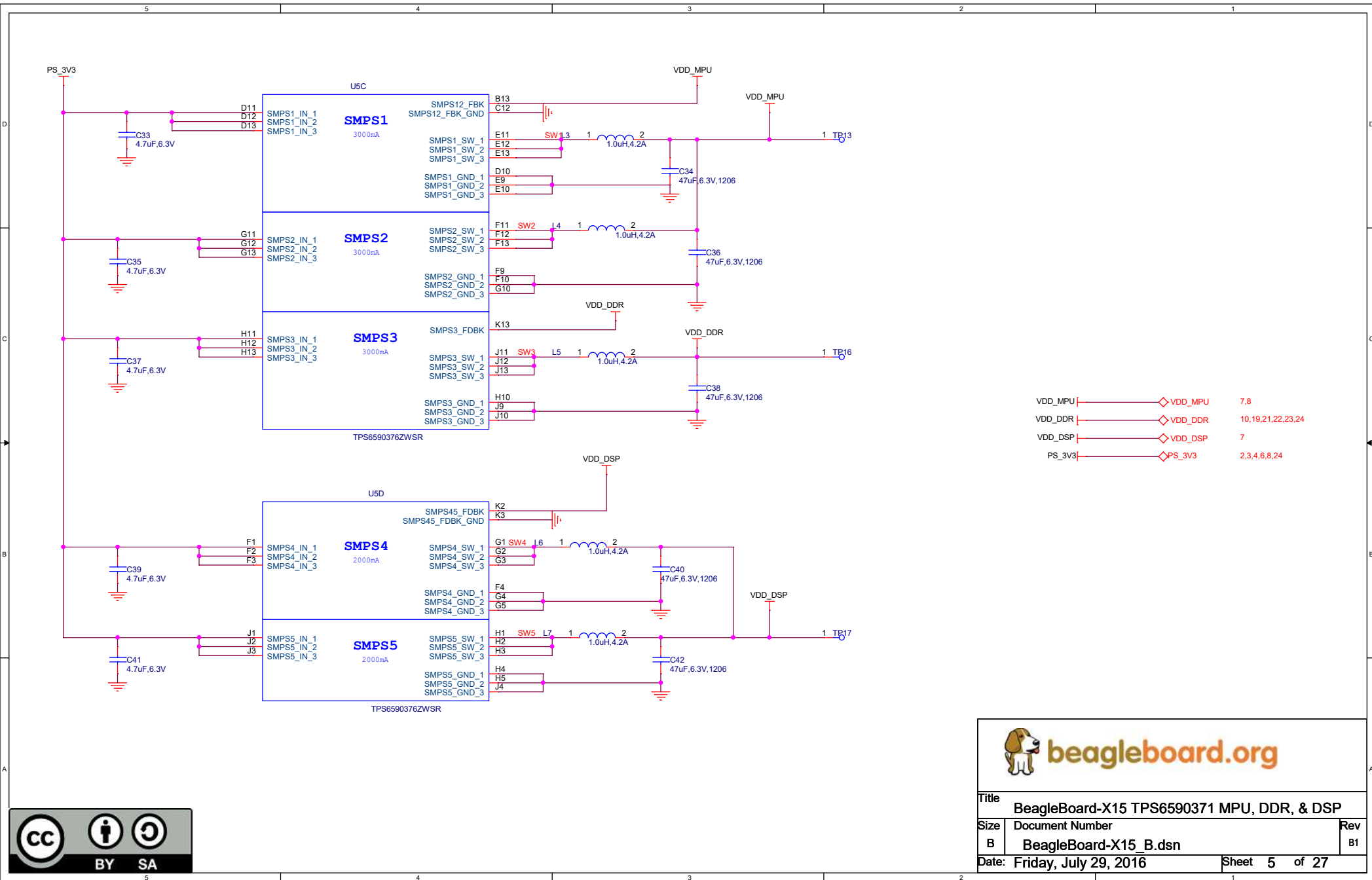
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| VDDA_1V8_PHYB | ◇VDDA_1V8_PHYB | 10 |
| VUSB_3V3 | ◇VUSB_3V3 | 14 |
| VDDA_1V8_PHYA | ◇VDDA_1V8_PHYA | 10,14 |
| VDDA_1V8_PLL | ◇VDDA_1V8_PLL | 8,10 |
| LDO_VRTC | ◇LDO_VRTC | 3,8 |
| VDD_SHV5 | ◇VDD_SHV5 | 8 |
| VDD_SD | ◇VDD_SD | 9,18 |
| VDD_3V3_SP | ◇VDD_3V3_SP | 8 |
| PS_3V3 | ◇PS_3V3 | 2,3,5,6,8,24 |
| 5V0 | ◇5V0 | 2,15,17,27 |
| VDD_3V3 | ◇VDD_3V3 | 2,3,6,7,8,9,10,11,12,13,14,15,16,17,18,24,25,26,27 |
| VDD_1V8 | ◇VDD_1V8 | 6,10,26 |

Real Time Clock



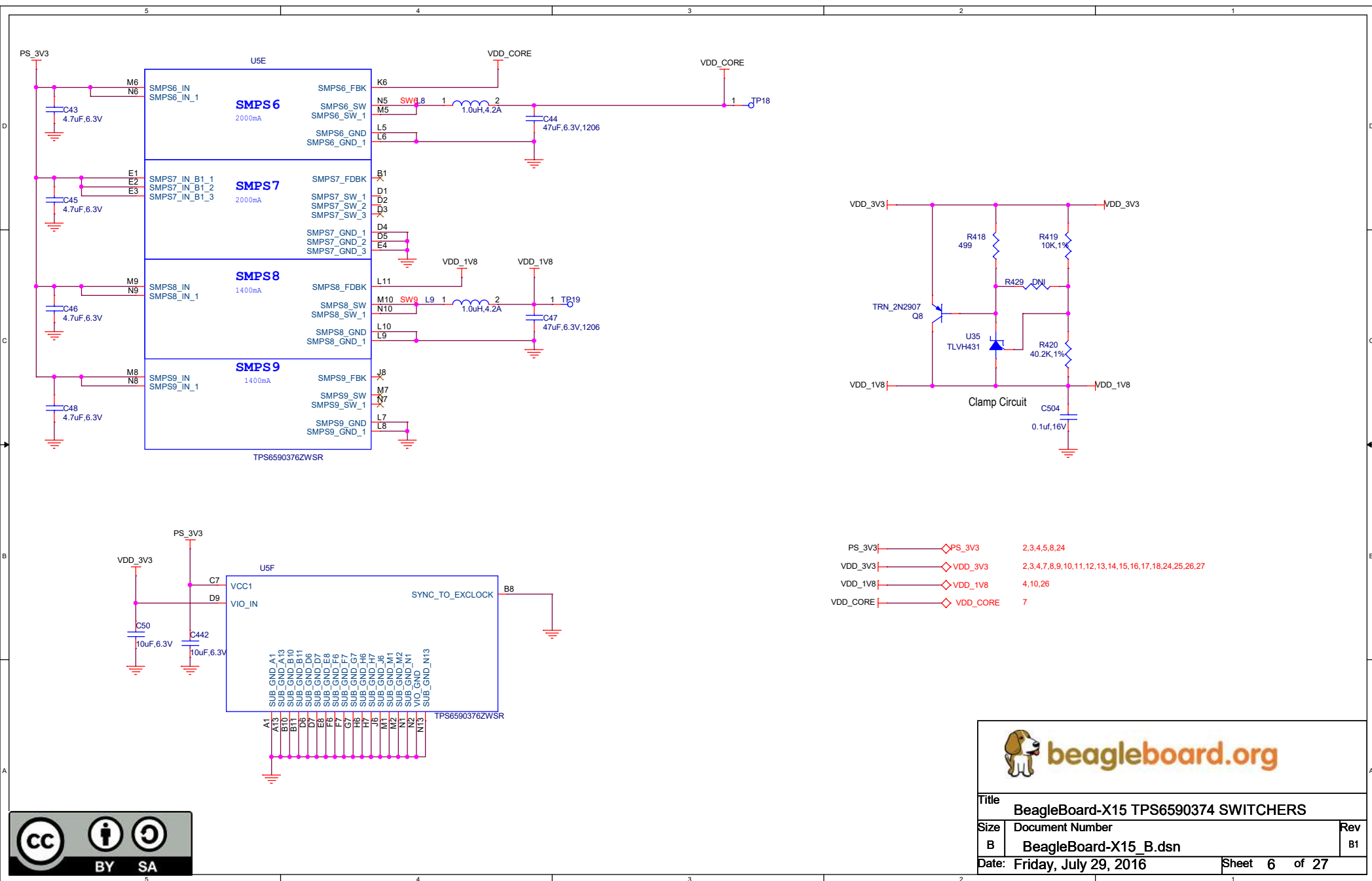
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| Title | | | BeagleBoard-X15 TPS6590371 CONTROL |
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| Title | | | BeagleBoard-X15 TPS6590371 MPU, DDR, & DSP |
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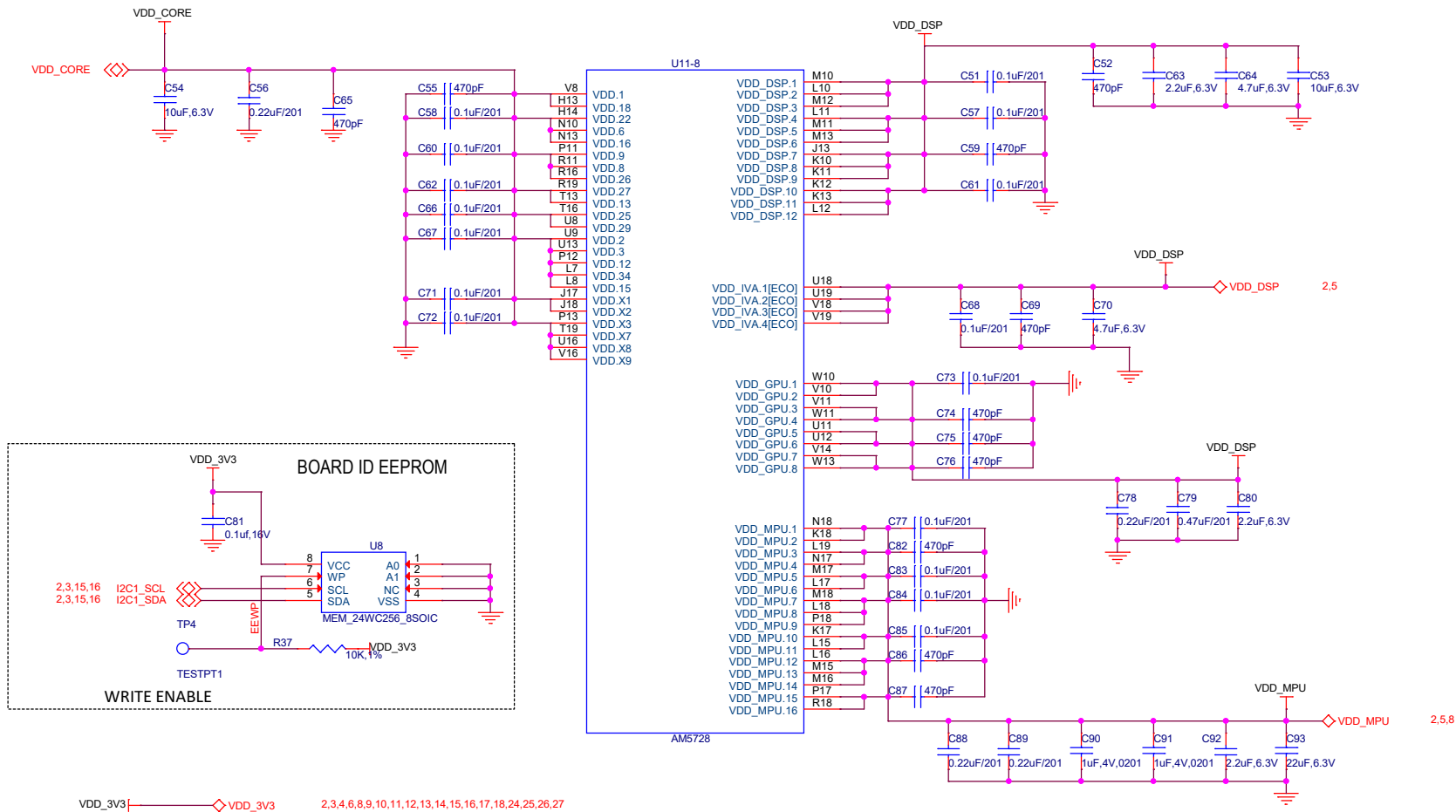




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| Title | | | BeagleBoard-X15 TPS6590374 SWITCHERS |
| Size | Document Number | | Rev |
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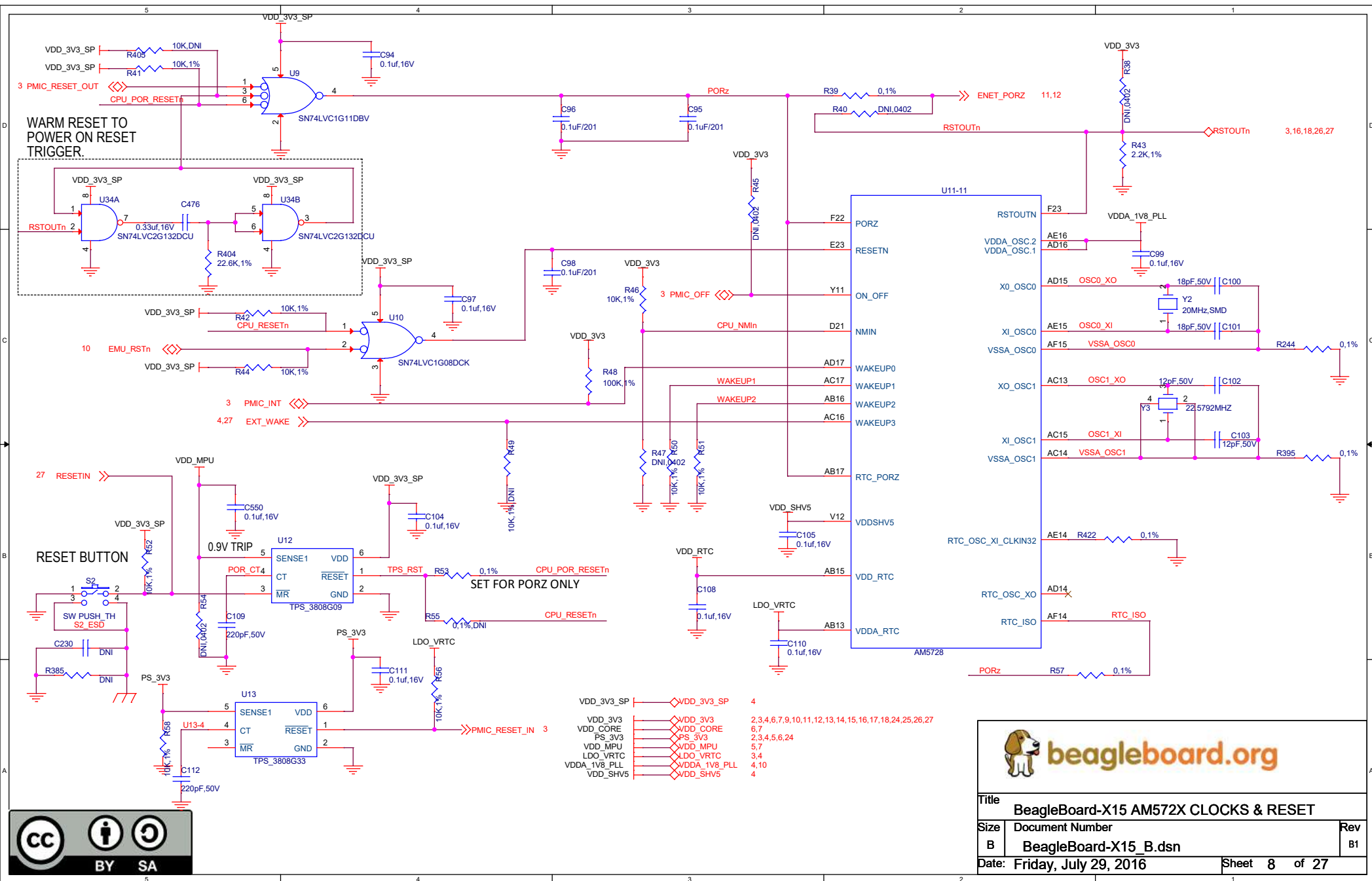




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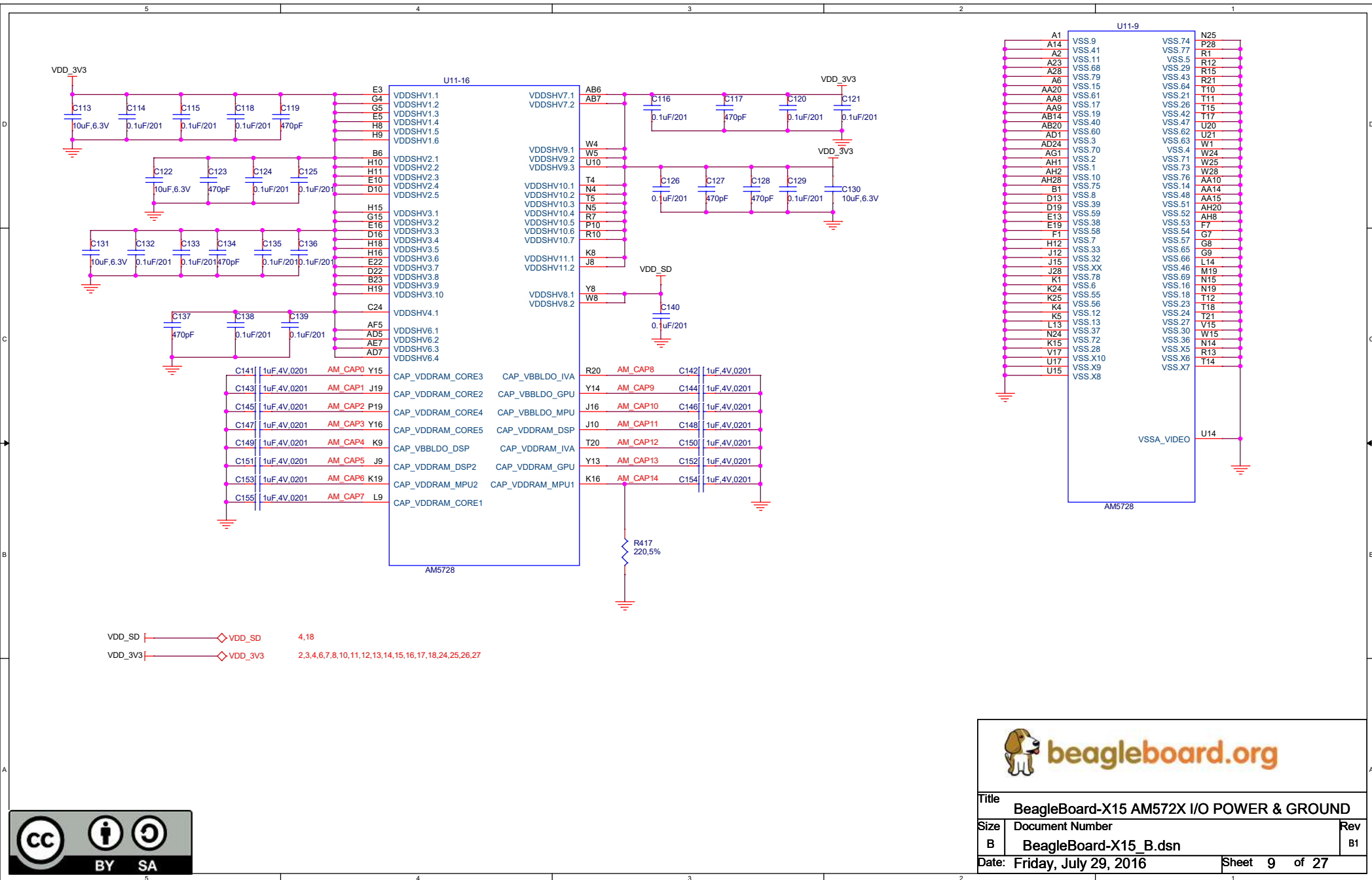
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| Size | Document Number | | Rev |
| B | BeagleBoard-X15_B.dsn | | B1 |
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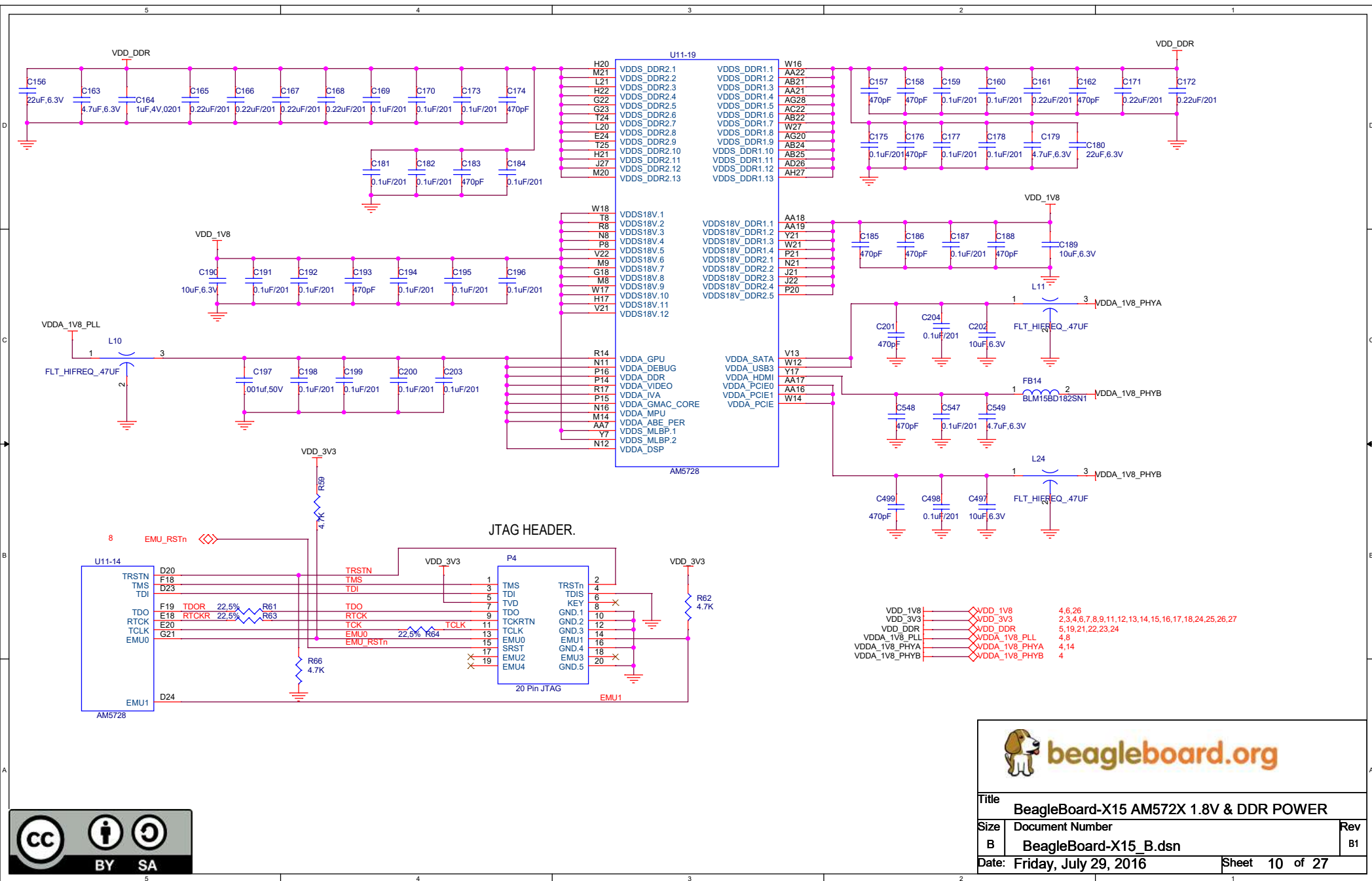


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| Title | | | |
| BeagleBoard-X15 AM572X CLOCKS & RESET | | | |
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Title BeagleBoard-X15 AM572X 1.8V & DDR POWER

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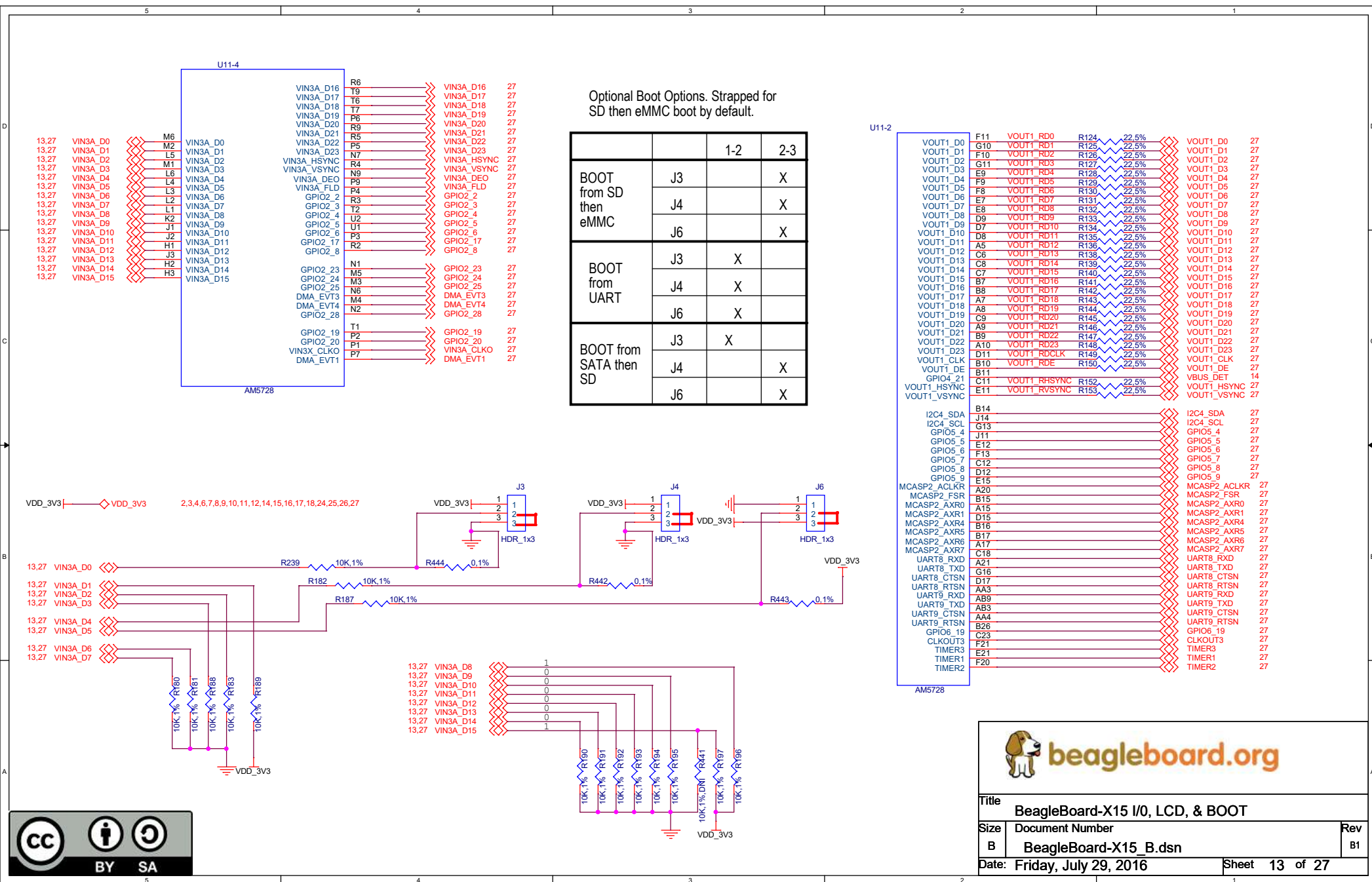
B BeagleBoard-X15_B.dsn

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| Title | BeagleBoard-X15 I/O, LCD, & BOOT |
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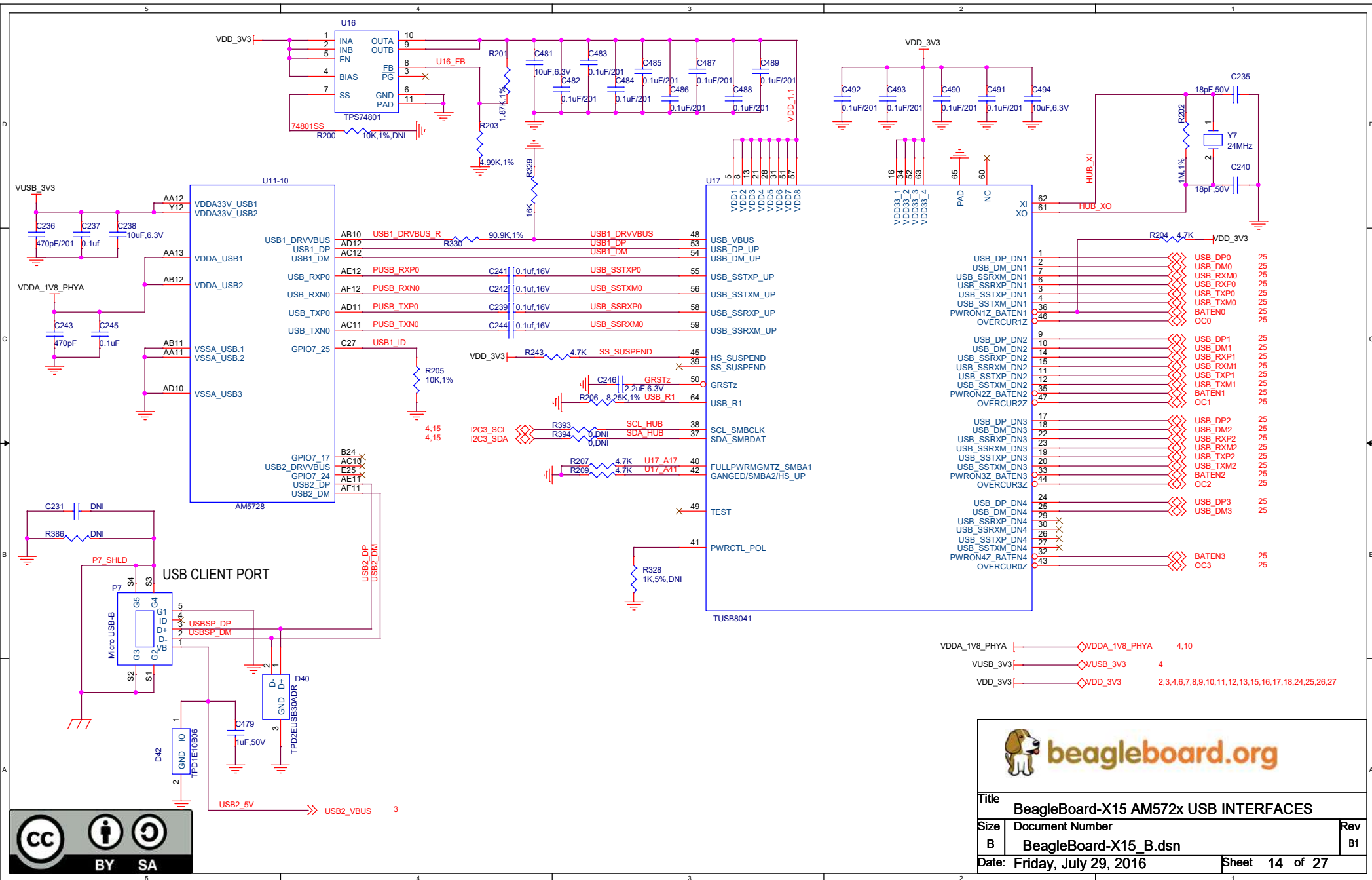
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| B | BeagleBoard-X15 B.dsn |
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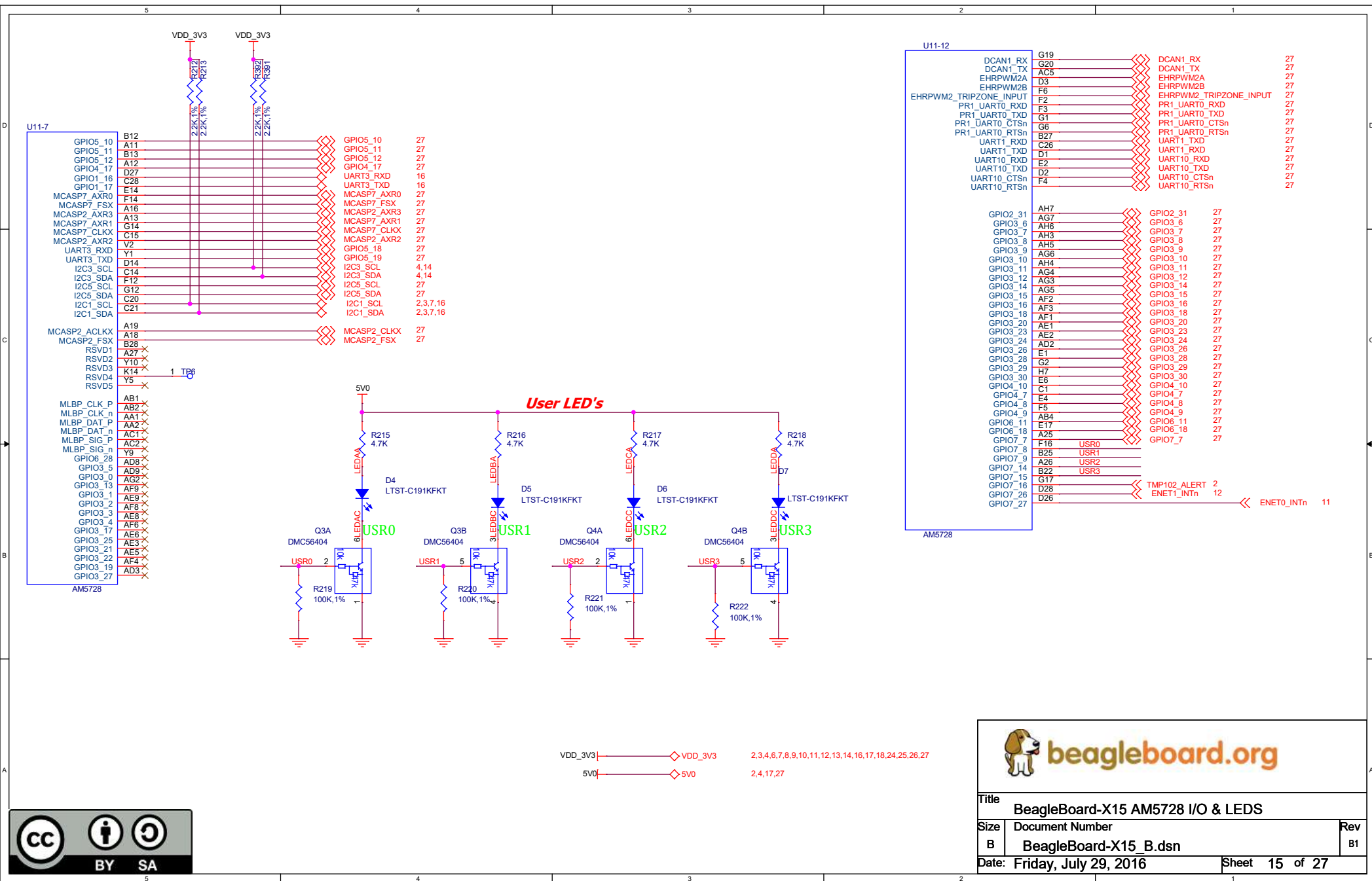
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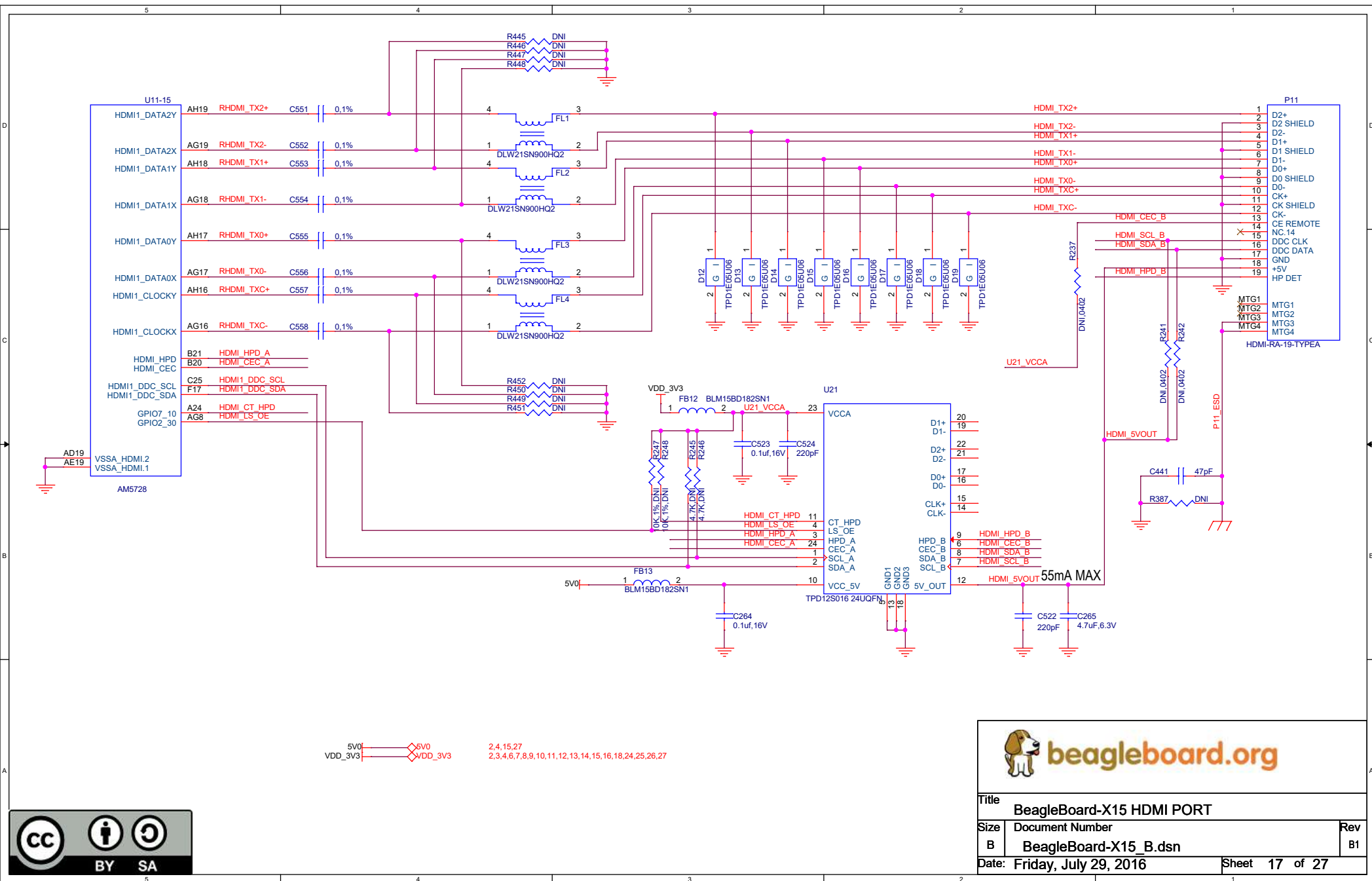
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| Size | | Document Number | | | Rev B1 |
| B | | BeagleBoard-X15_B.dsn | | | |
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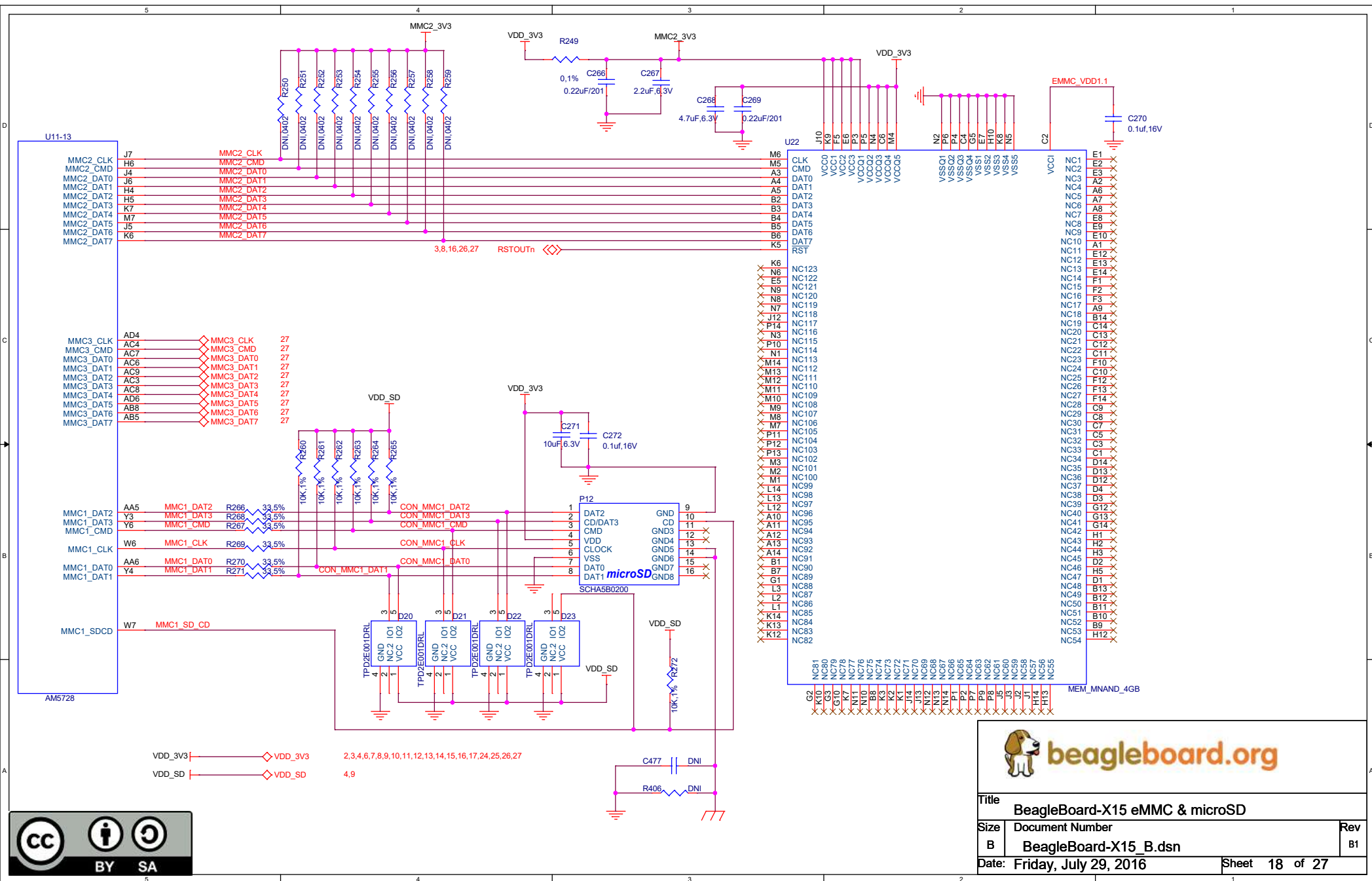


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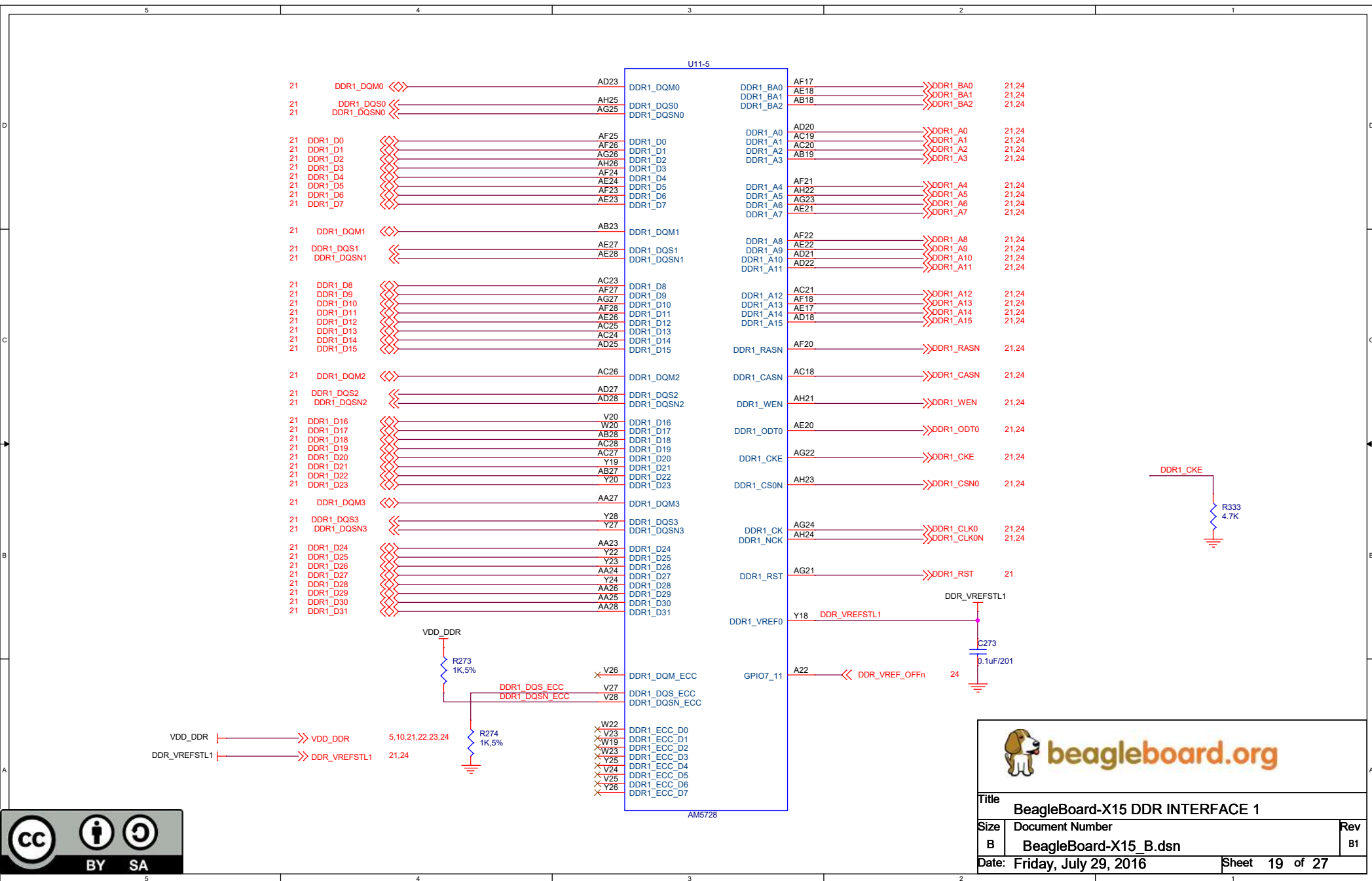
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| Title | | | BeagleBoard-X15 AM5728 I/O & LEDS |
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| B | BeagleBoard-X15_B.dsn | | B1 |
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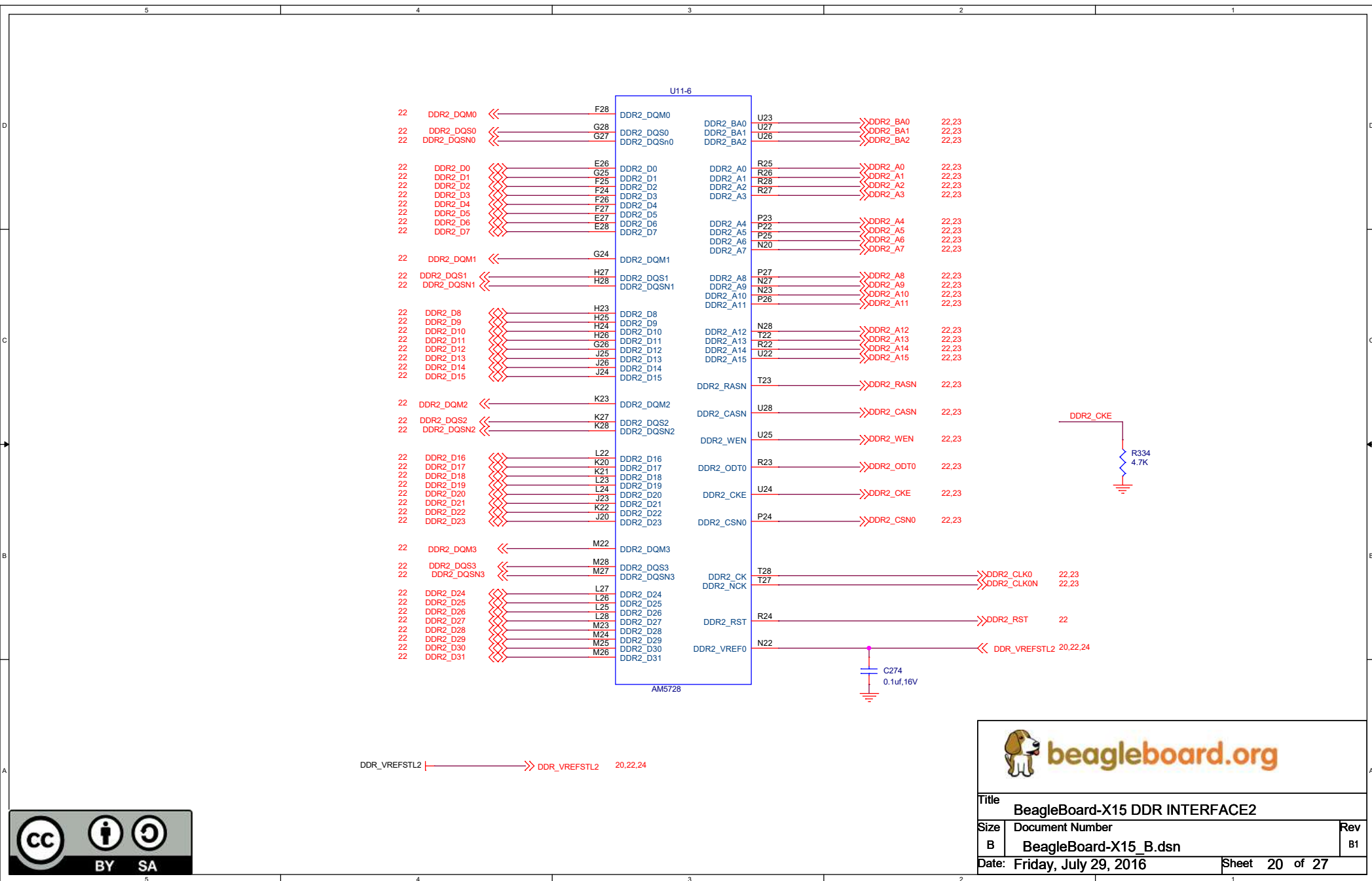






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| Title | | | BeagleBoard-X15 eMMC & microSD |
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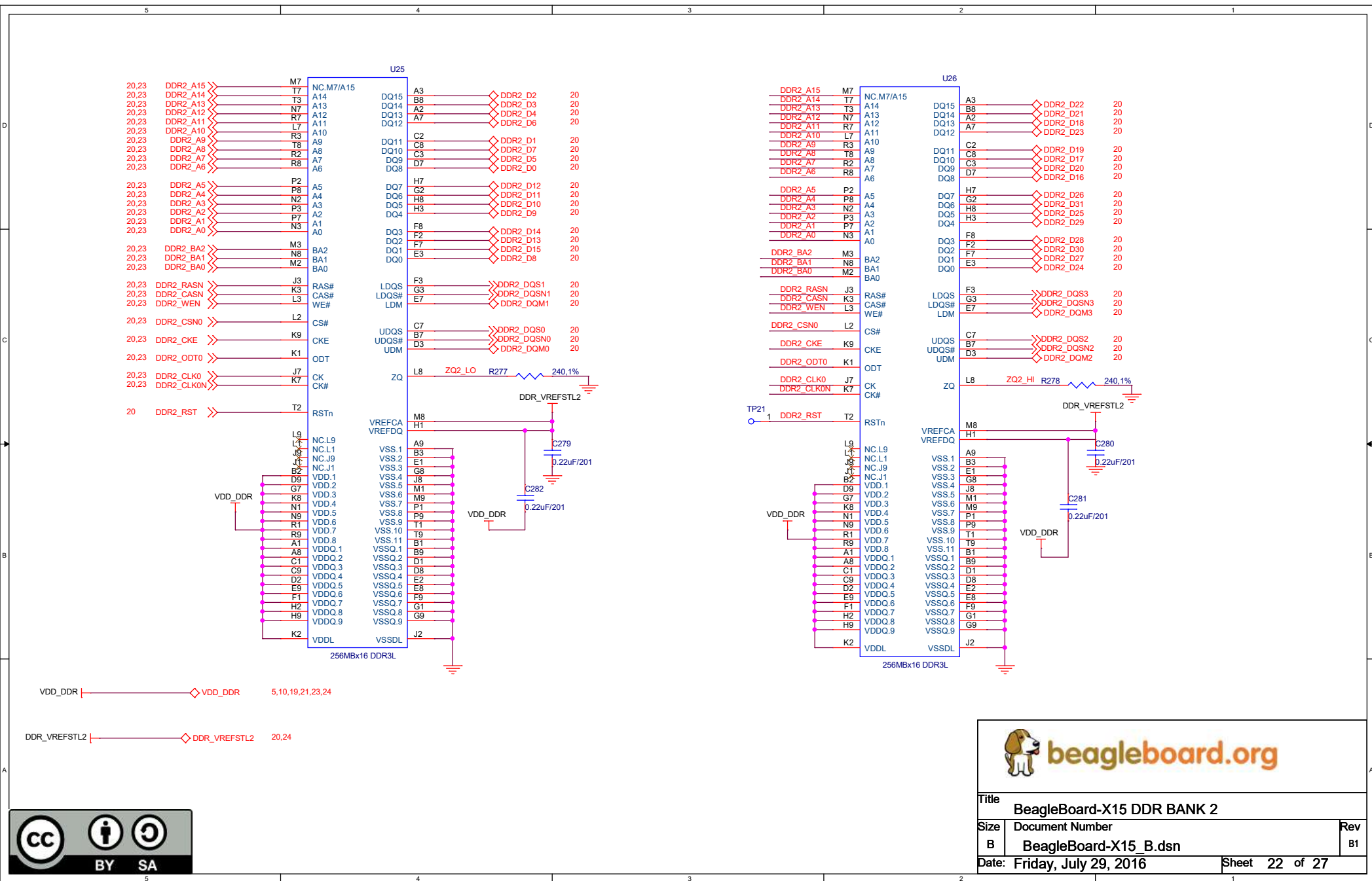




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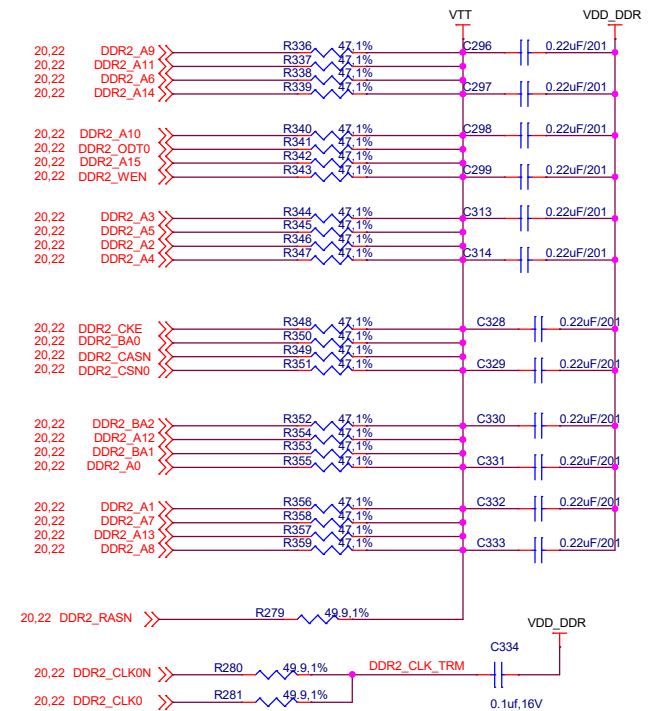
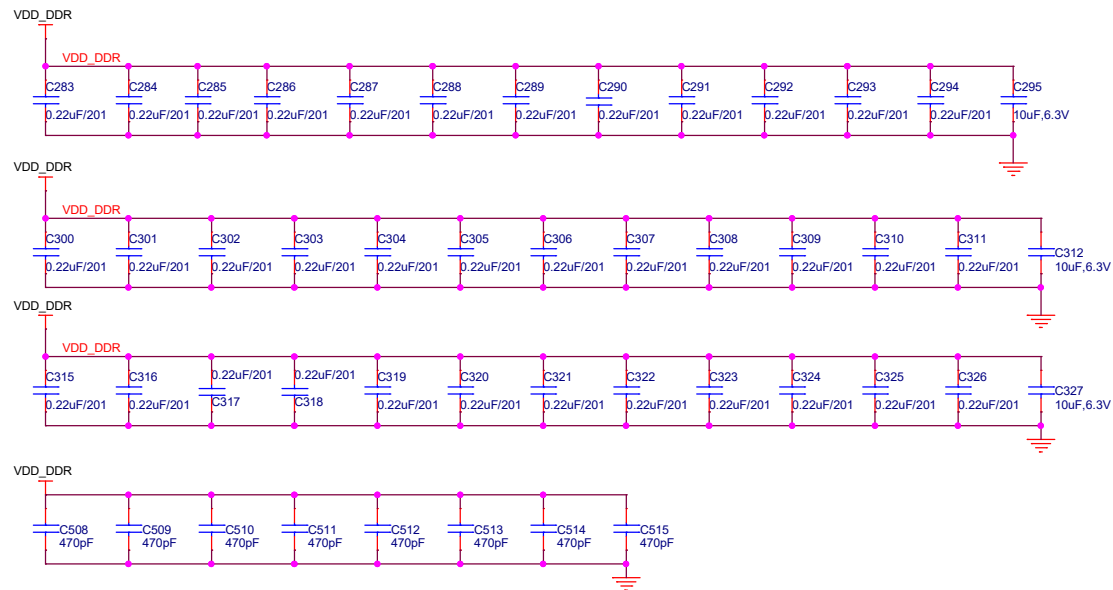
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| Title | | | BeagleBoard-X15 DDR INTERFACE2 |
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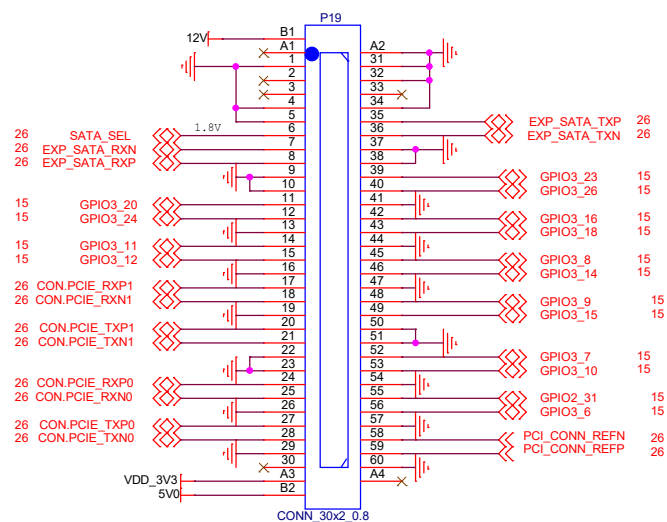
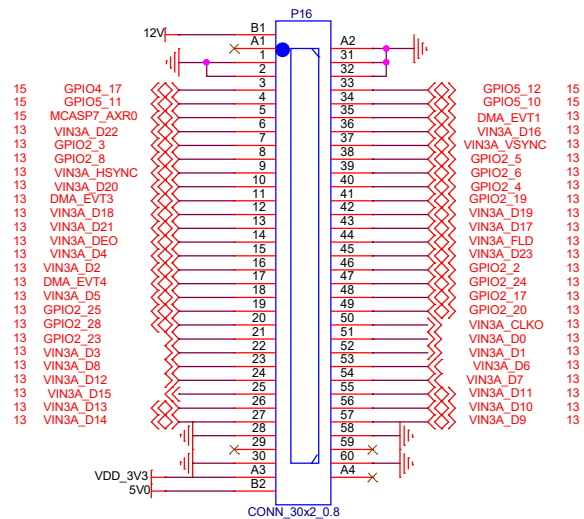


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|-------|-----------------------|--|----------------------------|
| Title | | | BeagleBoard-X15 DDR BANK 2 |
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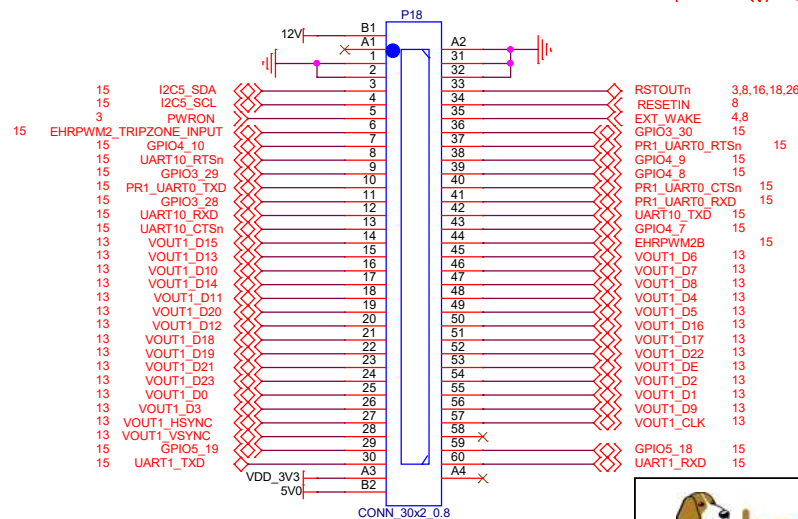
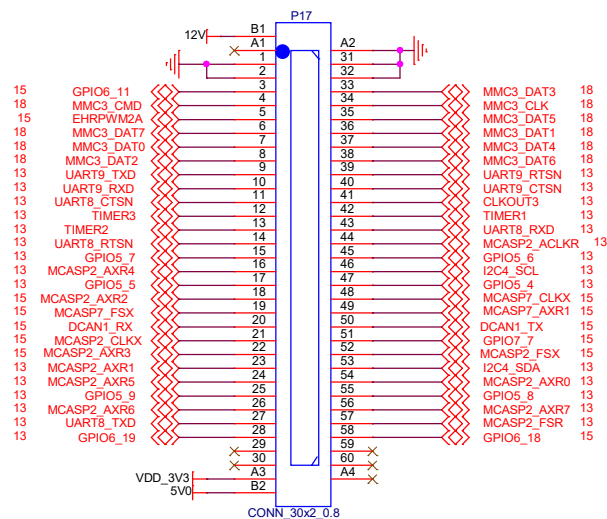
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| Title | | |
| BEAGLEBOARD-X15 DDR3L BANK 2 CAPS AND TERMINATIONS | | |
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12V
5V0
VDD_3V3

2.25
2.4,15,17
2.3,4,6,7,8,9,10,11,12,13,14,15,16,17,18,24,25,26



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| Title | | | BEAGLEBOARD-X15 EXPANSION CONNECTORS |
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