REV	DESCRIPTION	DATE	BY
A2	Production Release	8/28/2015	TI
A2b	Production Release, BOM fixes	10/09/2015	

P01 TITLE PAGE P02 12V INPUT, 3.3V AND 5V SWITCHERS P03 TPS6590371 1 CONTROL P04 TPS6590371 2 RESET, & RTC P05 TPS6590371 3 MPU, DDR, & DSP SWITCHERS P06 TPS6590371 4 CORE & 1.8V P07 AM572X CORE POWER & EEPROM P08 AM572X CLOCKS AND RESET P09 AM572X IO POWER AND GROUND P10 AM572X DDR3 POWER & JTAG P11 AM572X ETHERNET 1 P12 AM572X ETHERNET 2 P13 AM572X GPMC & LCD P14 AM572X USB INTERFACES P15 AM572X I/O, LEDs, AND GPIO P16 AM572X AUDIO & DEBUG SERIAL P17 AM572X HDMI PORT P18 AM572X eMMC, MMC3, & uSD P19 AM572X DDR INTERFACE 1 P20 AM572X DDR INTERFACE 2 P21 DDR3 BANK 1 P22 DDR3 BANK 2 P23 DDR3 BANK 2 CAPS & TERM P24 DDR3 BANK 1 CAPS & TERM P25 USB & SATA CONNECTOR P26 PCIe, SATA, & SERDES CLOCK

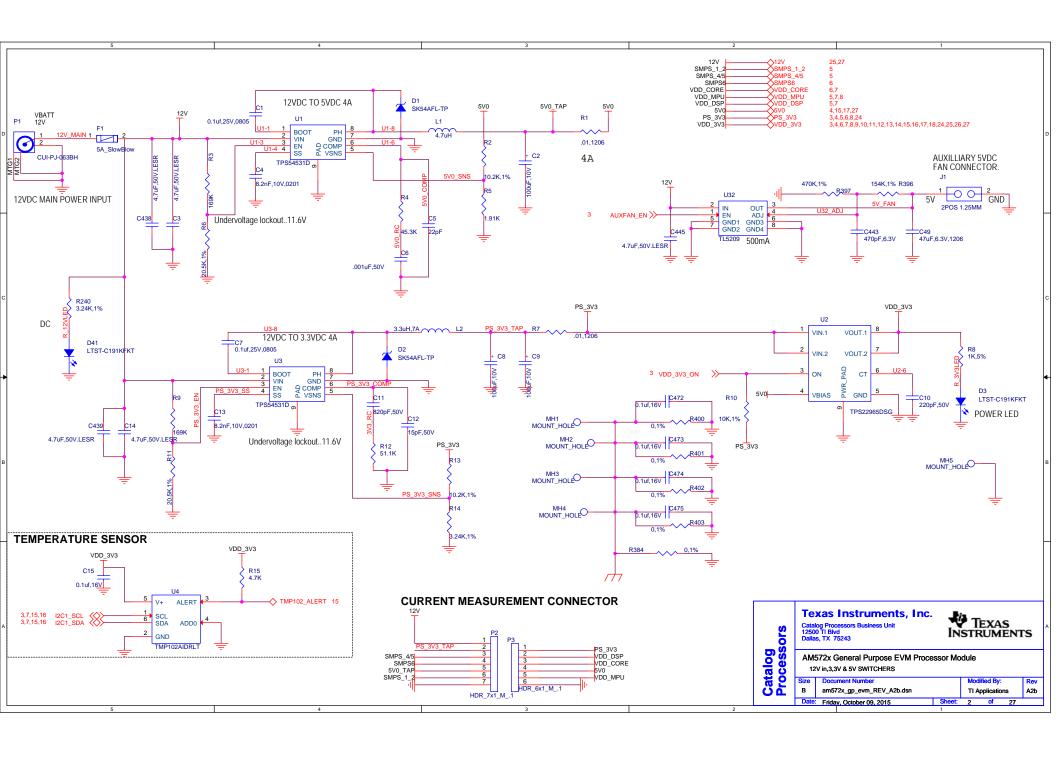
P27 EXPANSION HEADERS

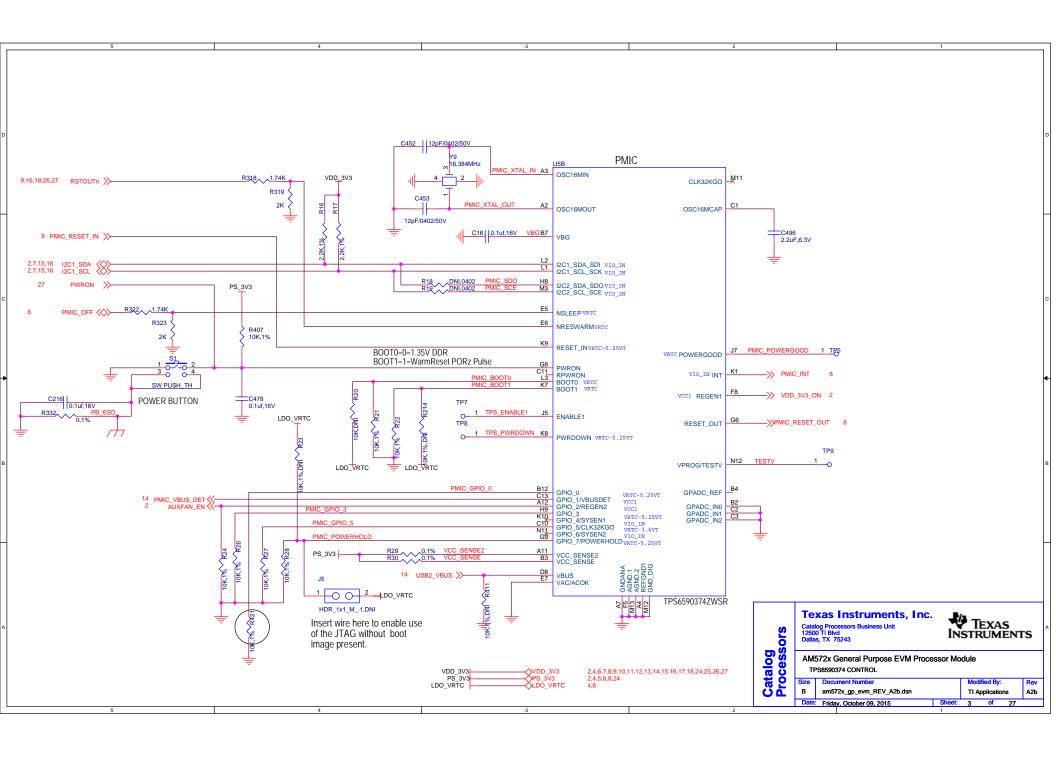
P28 MECHANICAL

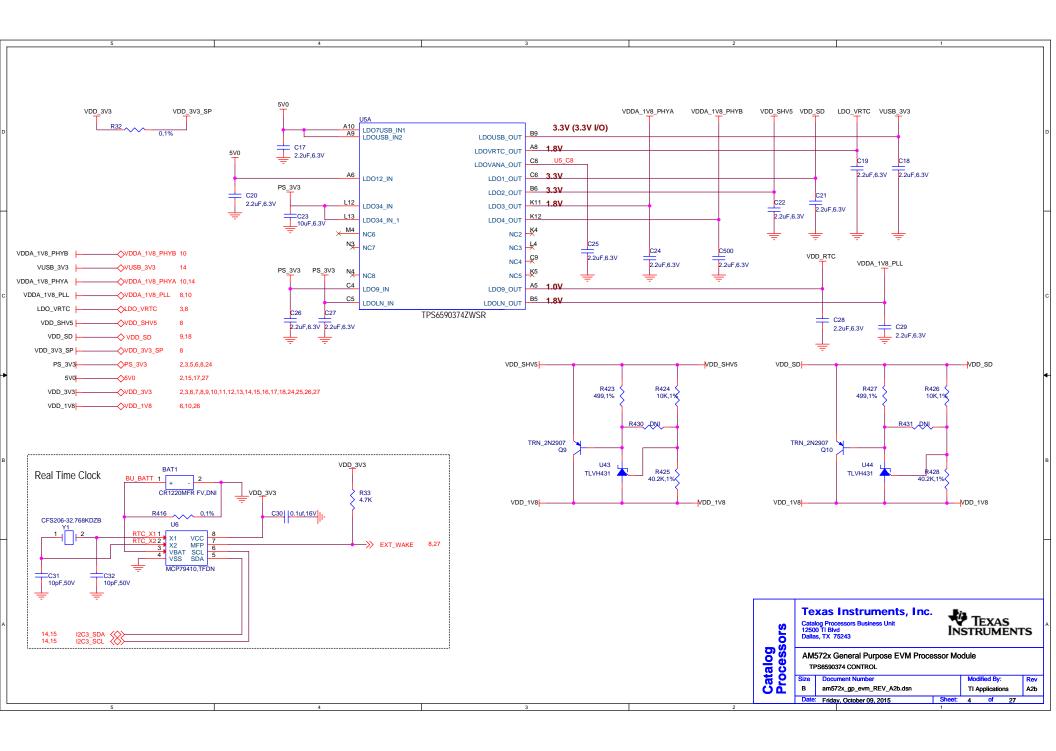
Texas Instruments, Inc.
Catalog Processors Business Unit 12500 TI Blvd Dallas, TX 75243

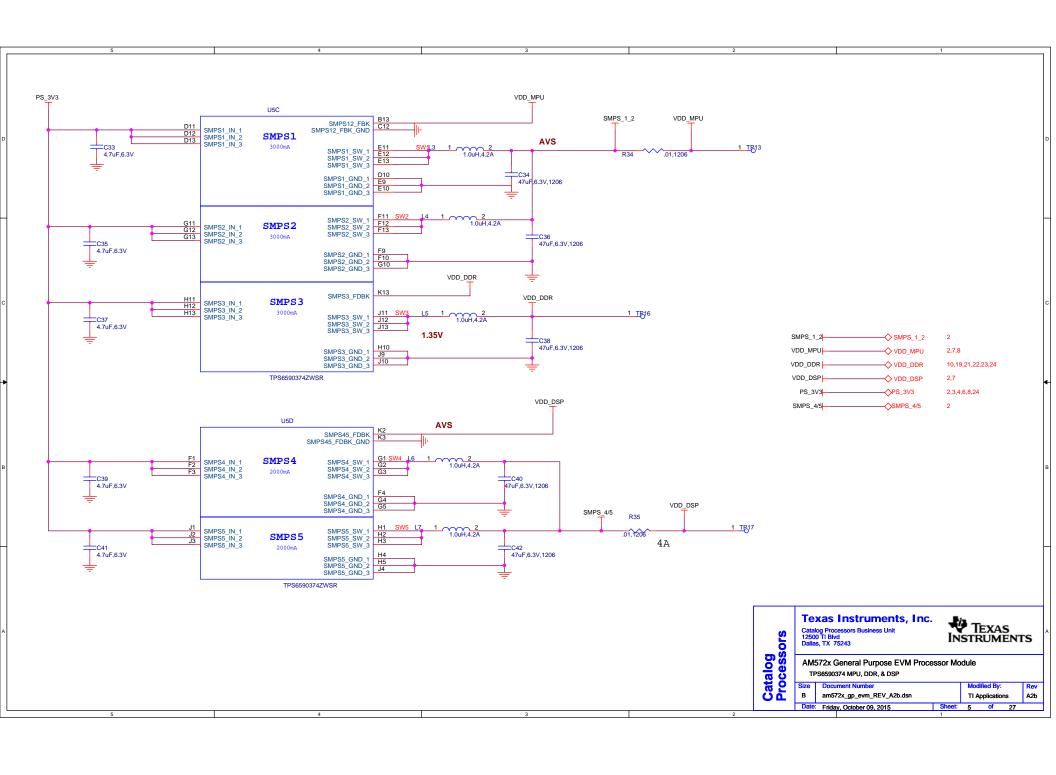
AM572x General Purpose EVM Processor Module Title Page

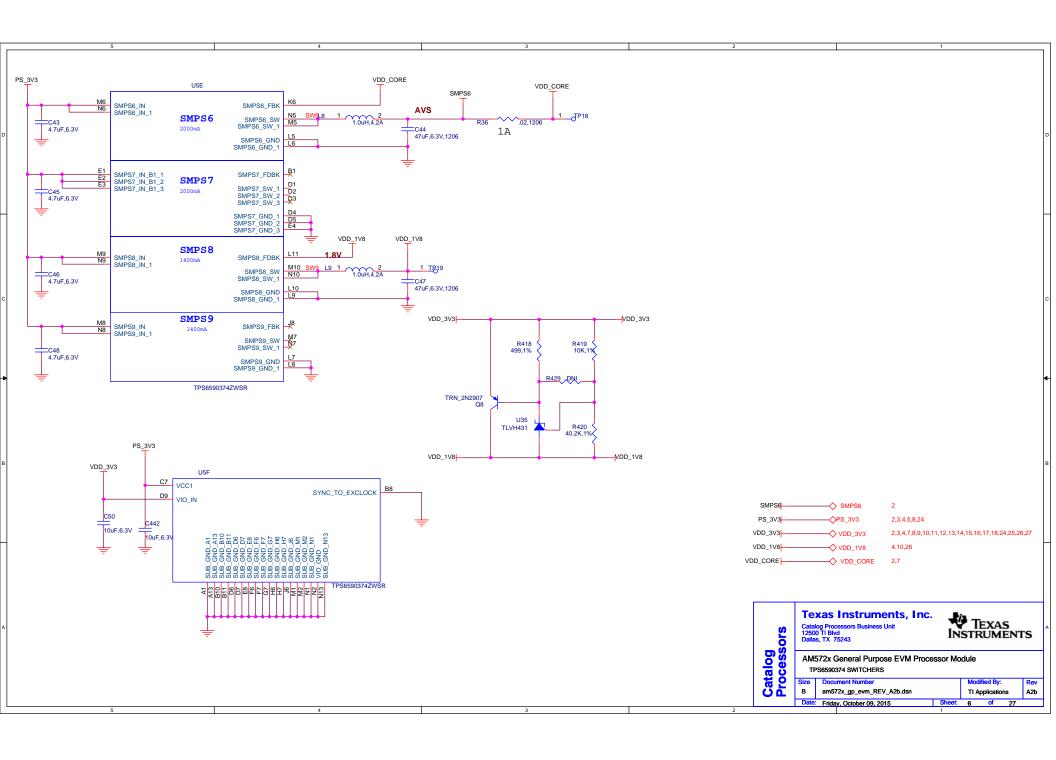
Size | Document Number | Modified By: Rev TI Applications | Rev TI Applications | A2b Date: Friday, October 09, 2015 | Sheet: 1 of 27

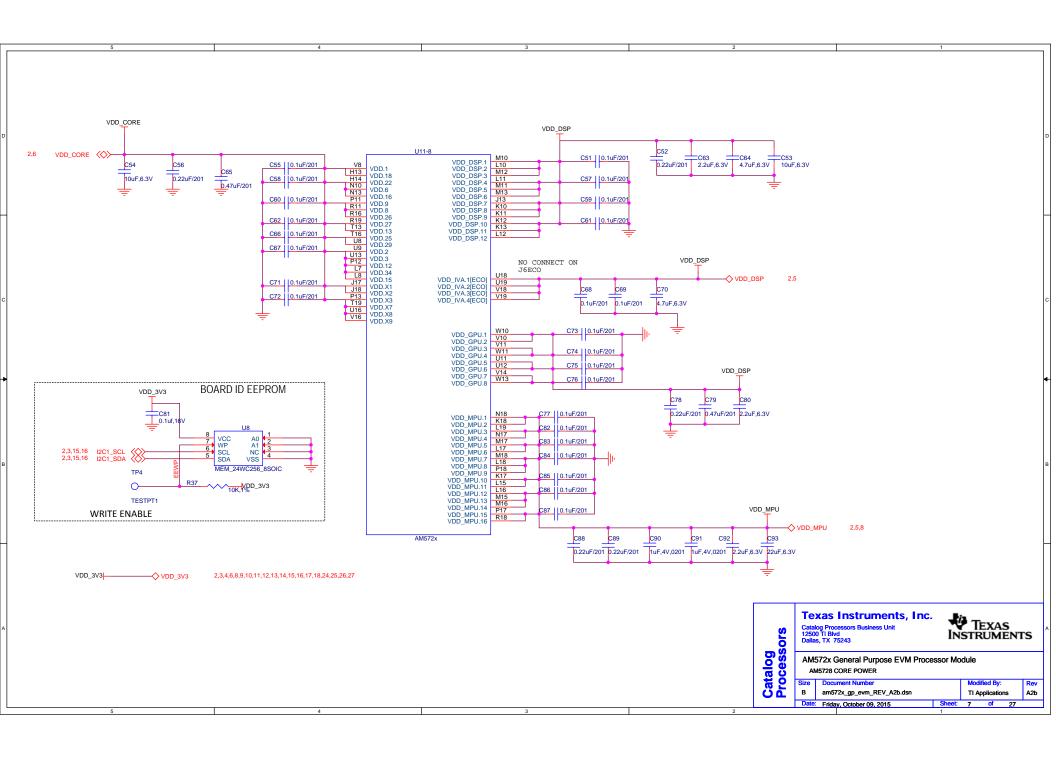


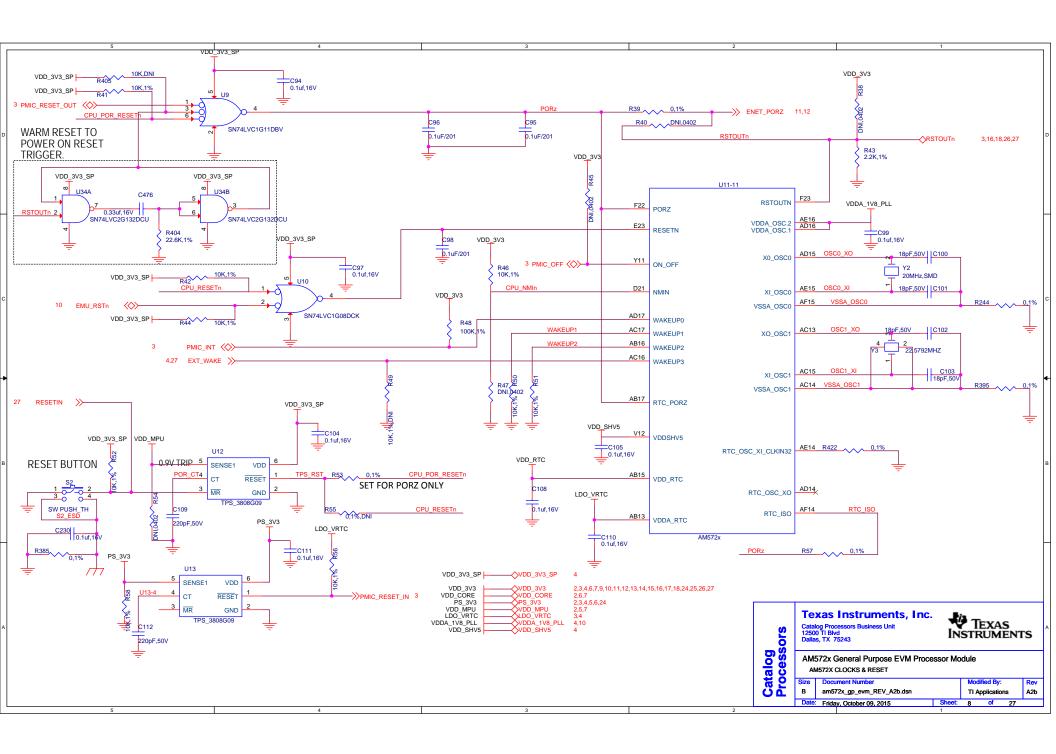


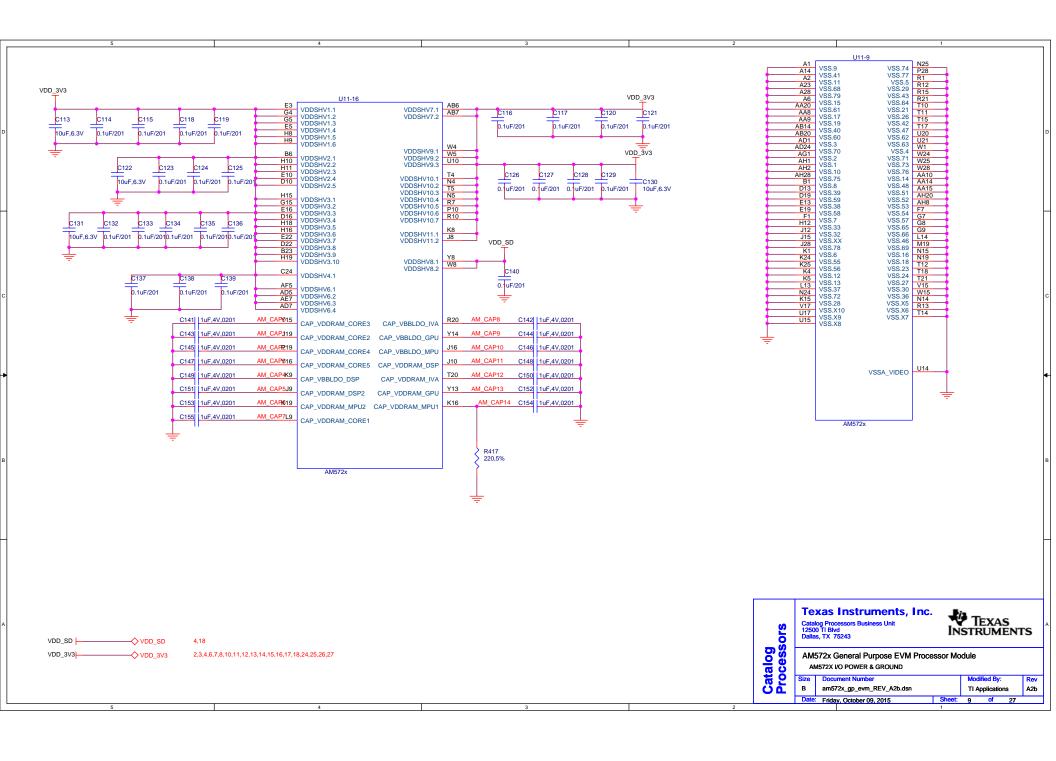


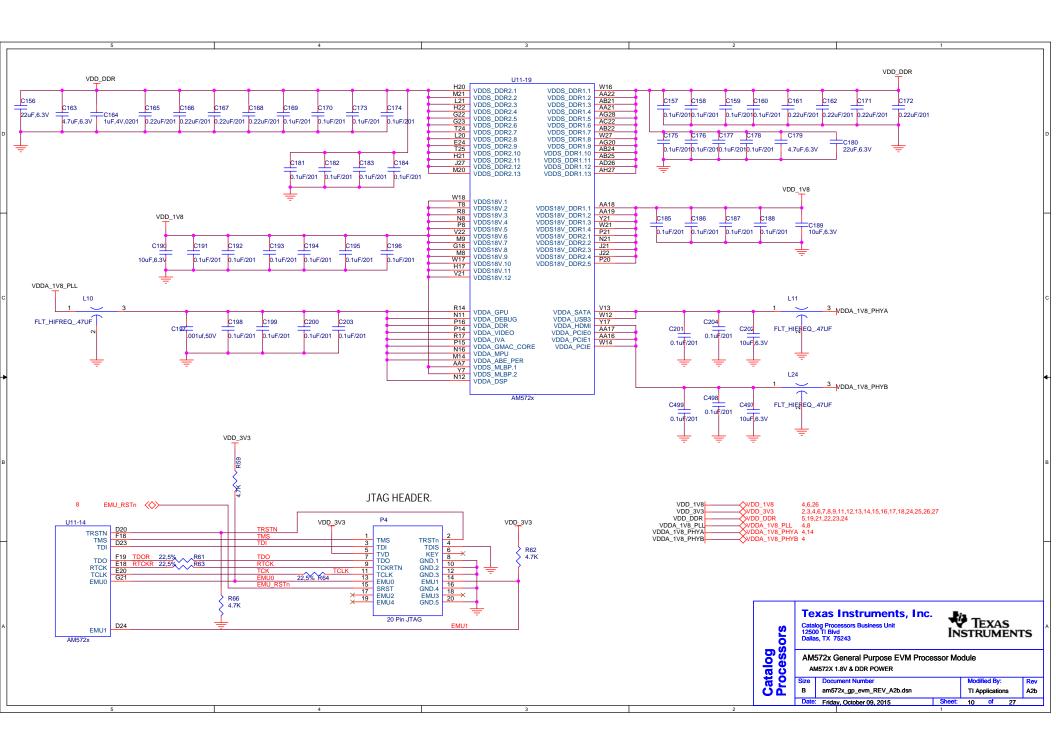


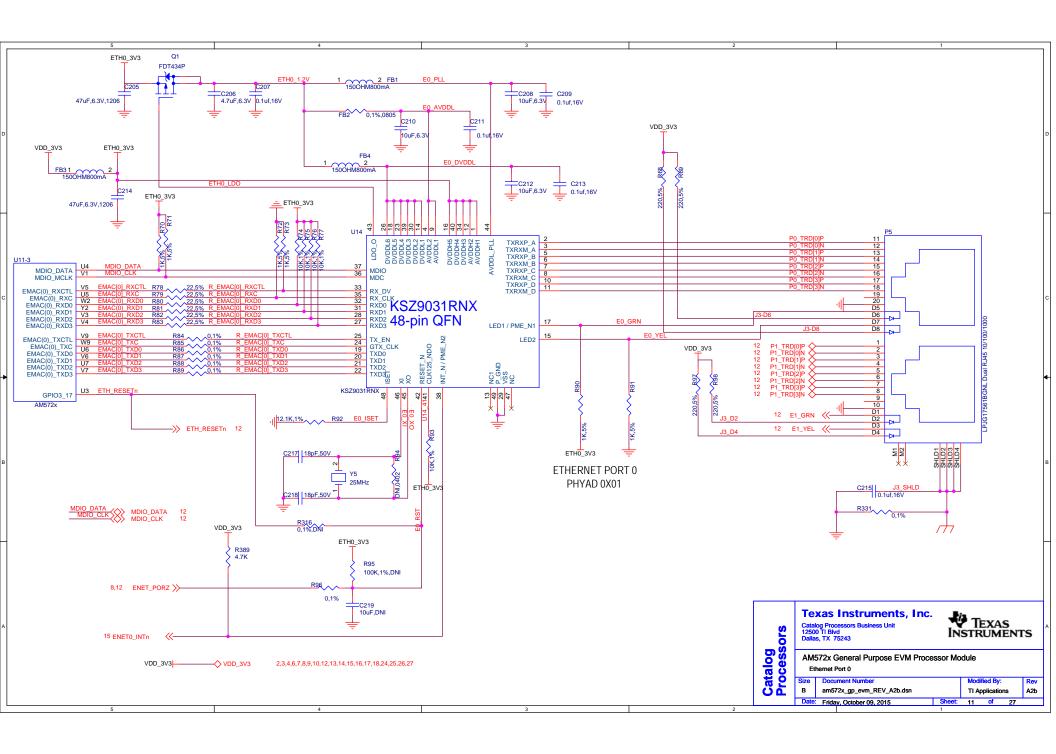


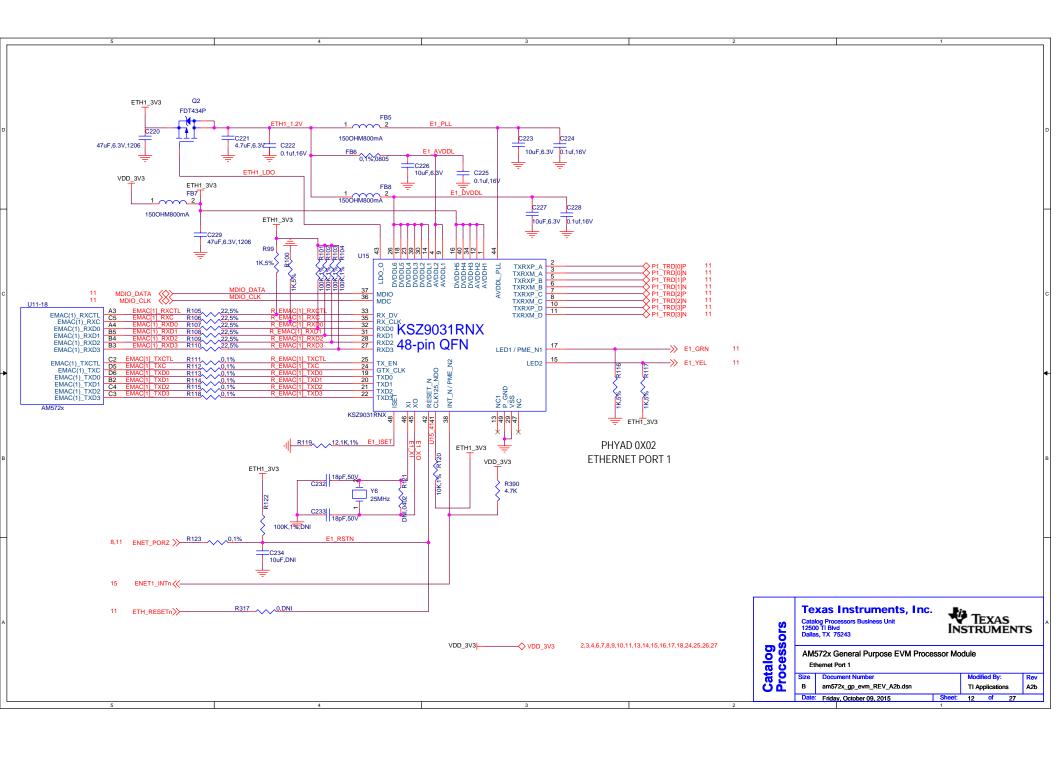


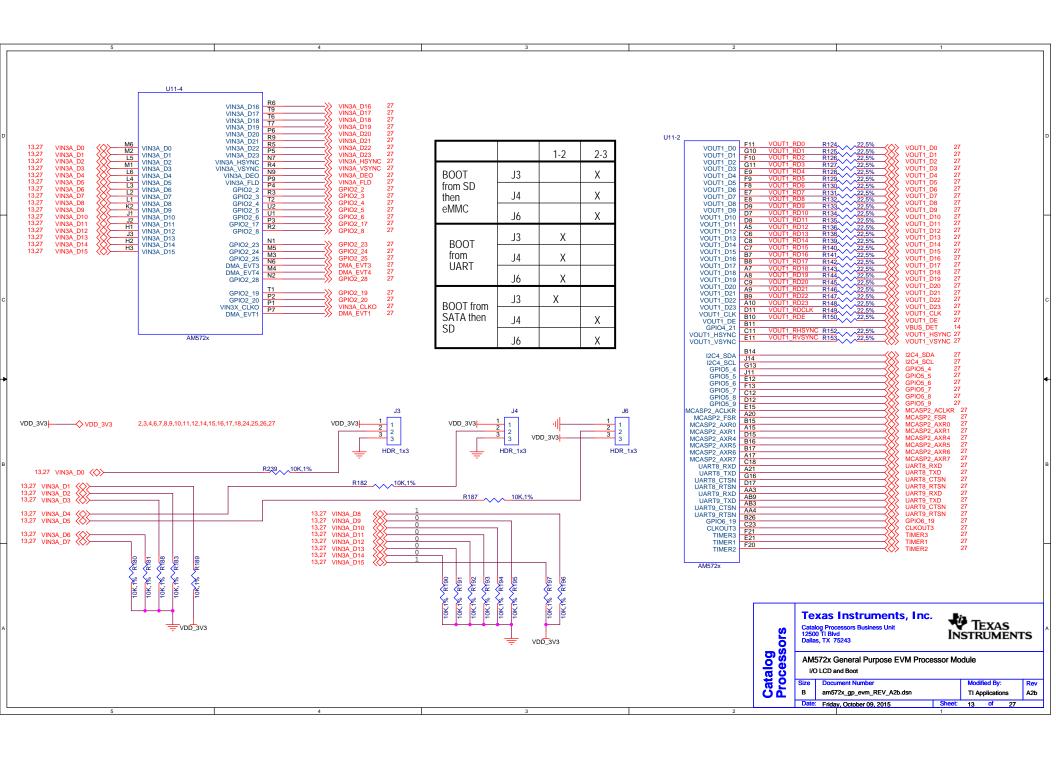


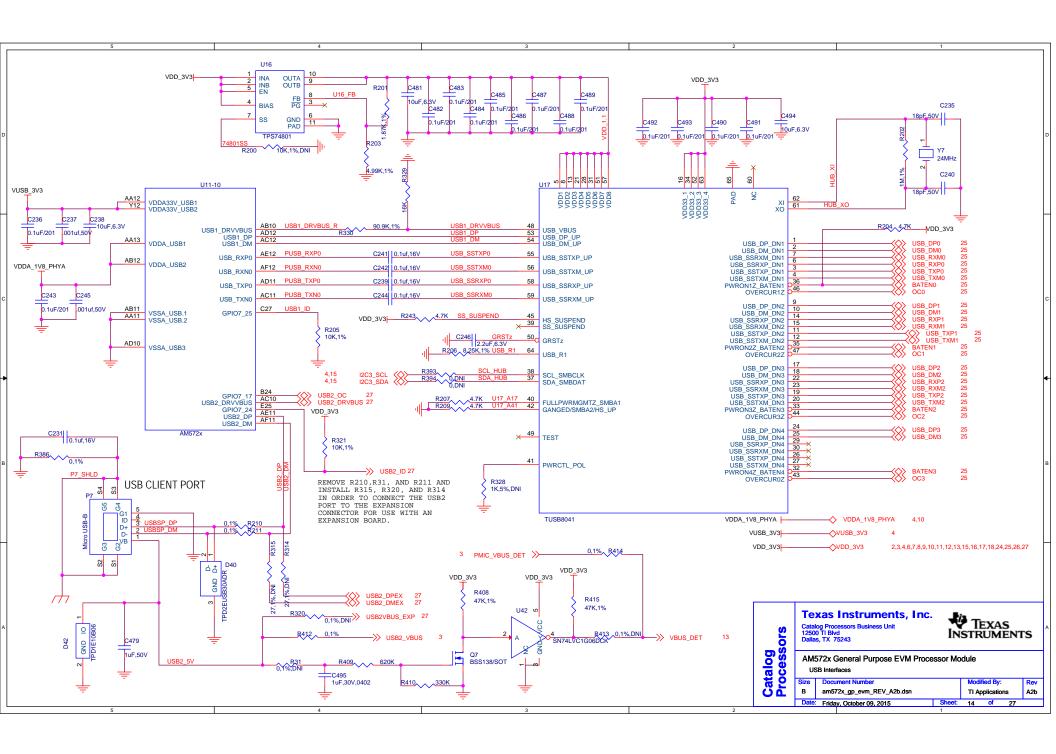


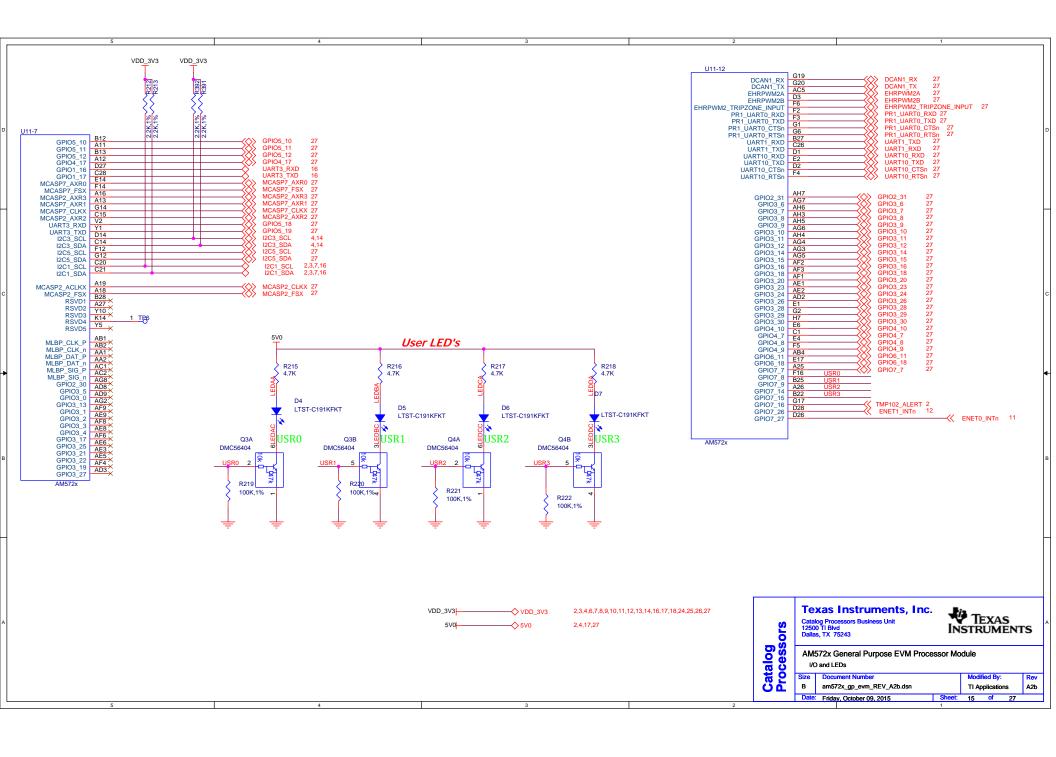


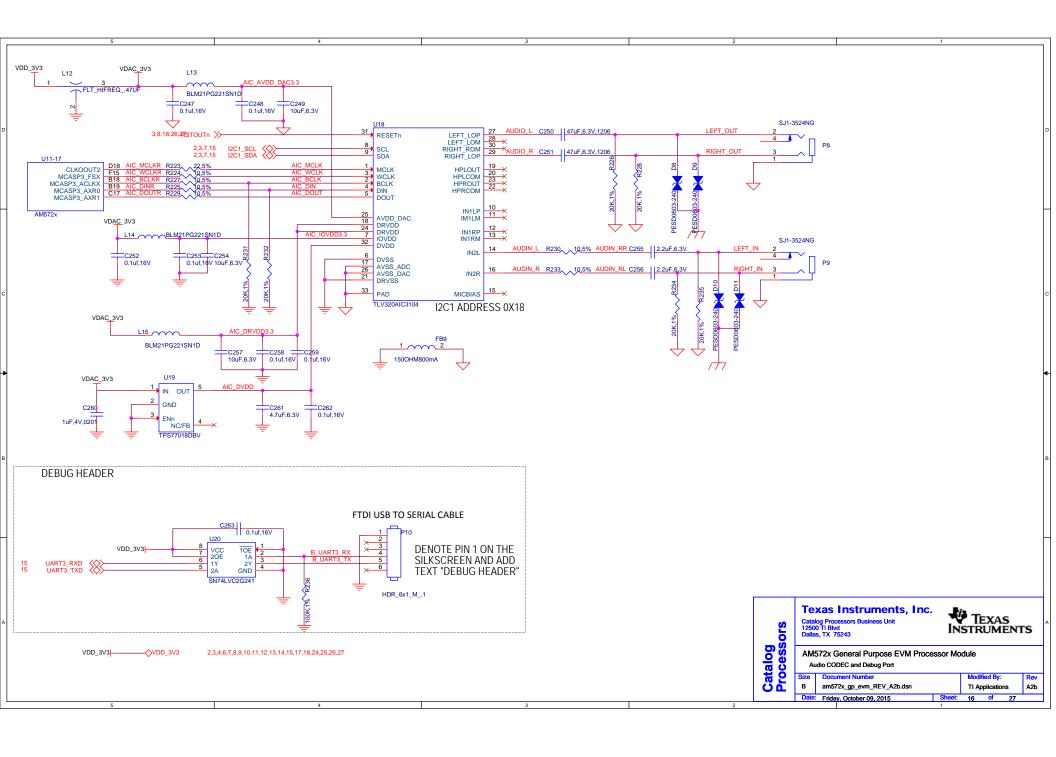


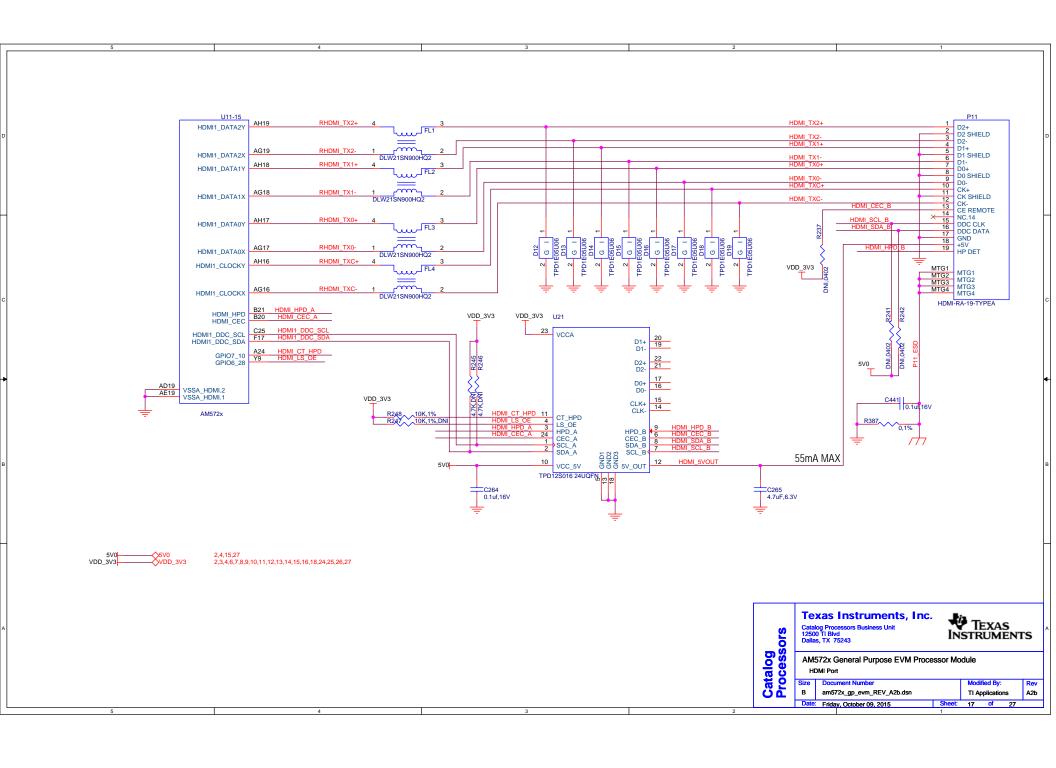


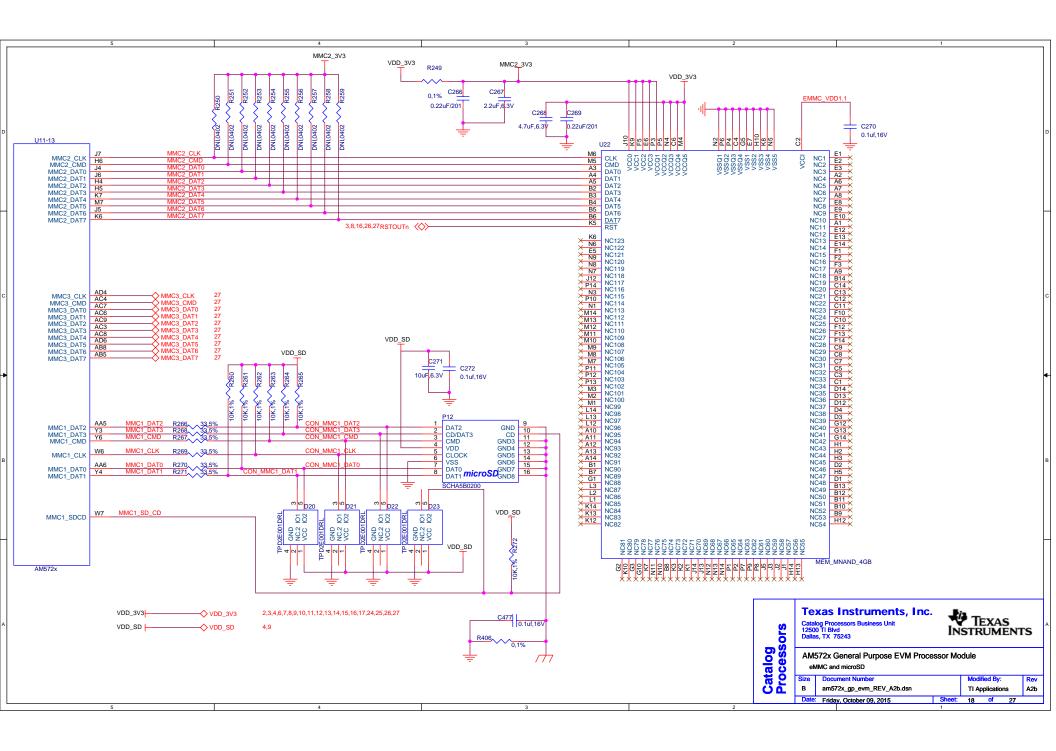


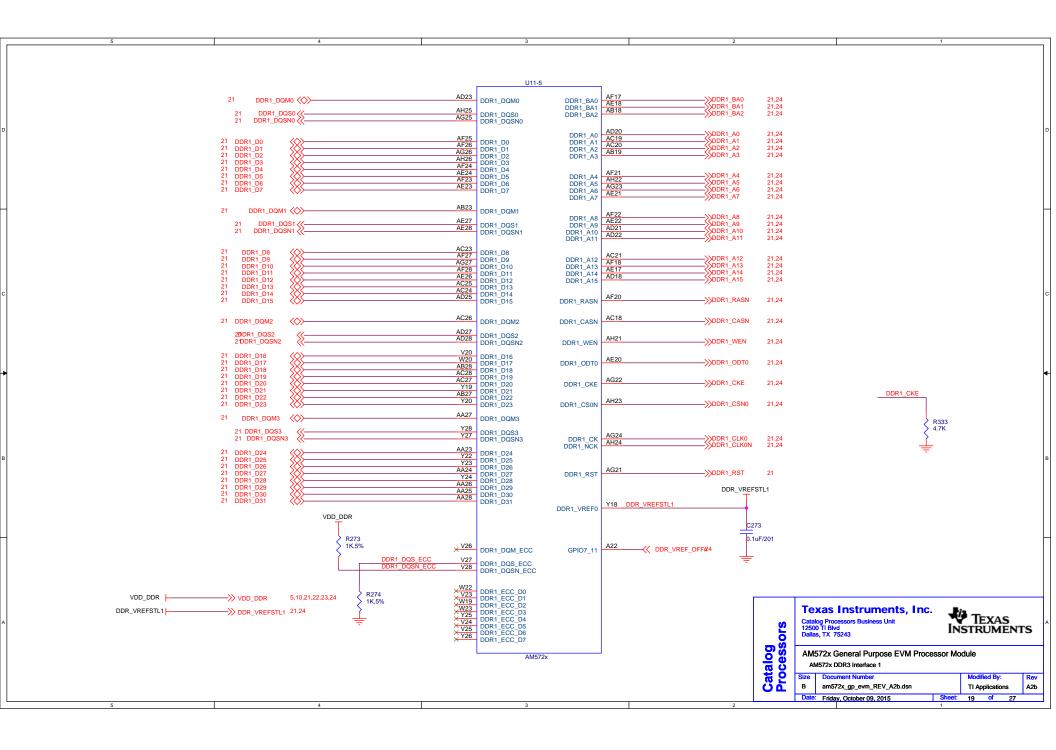


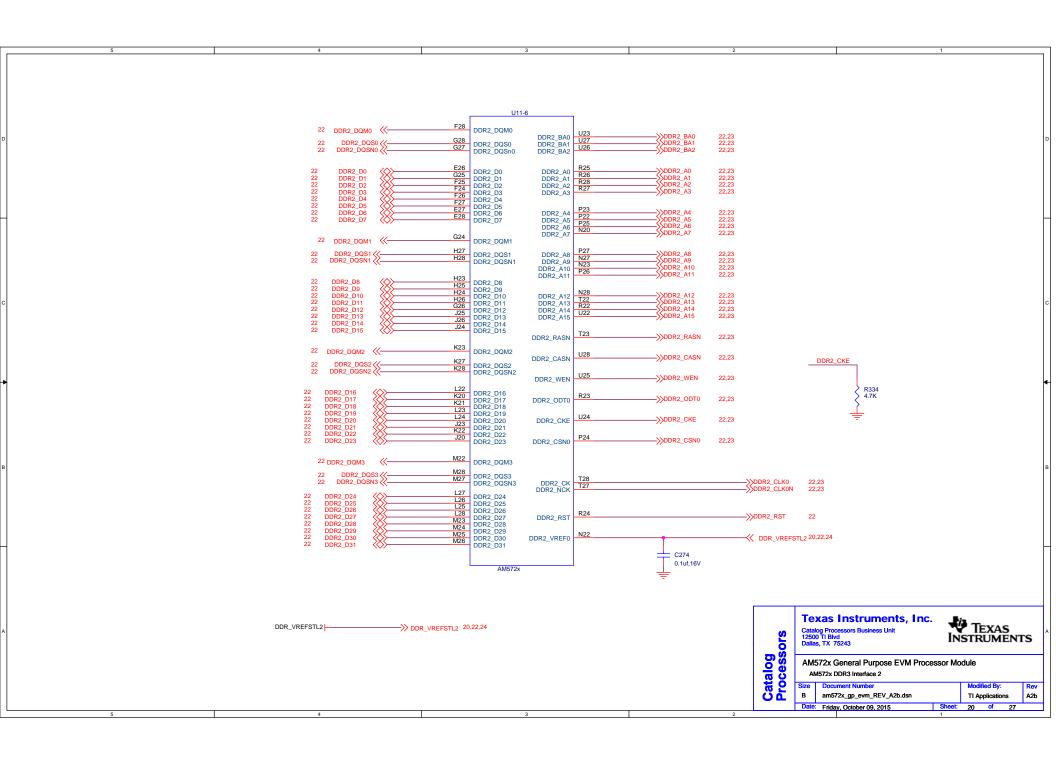


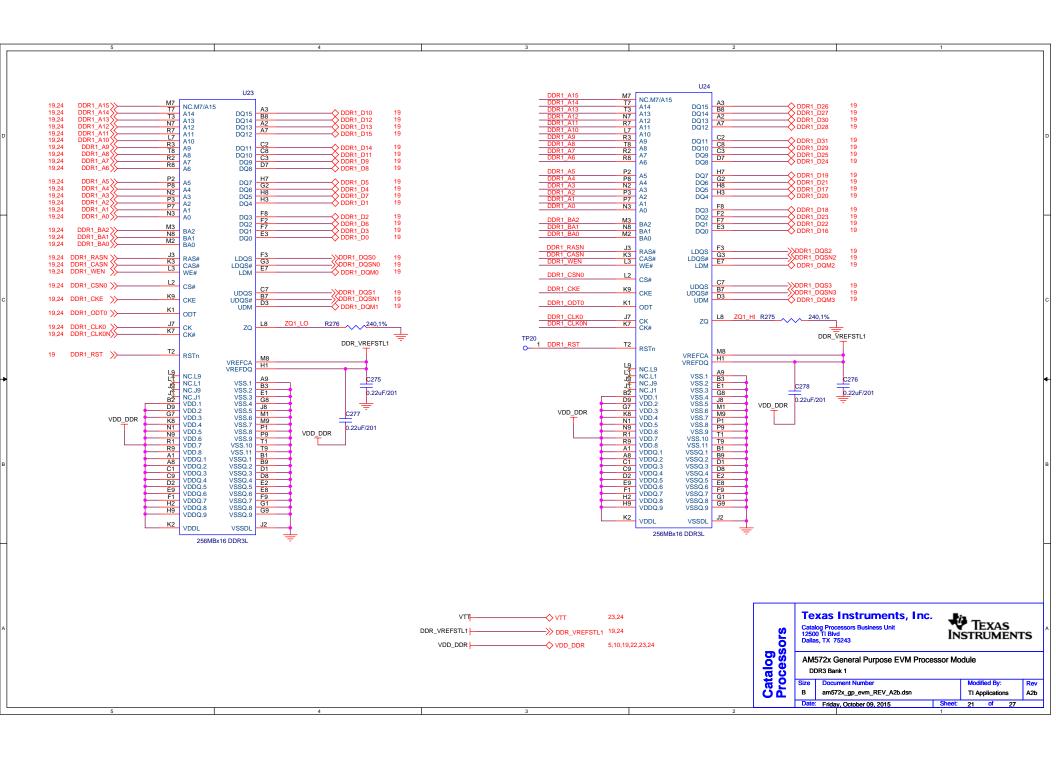


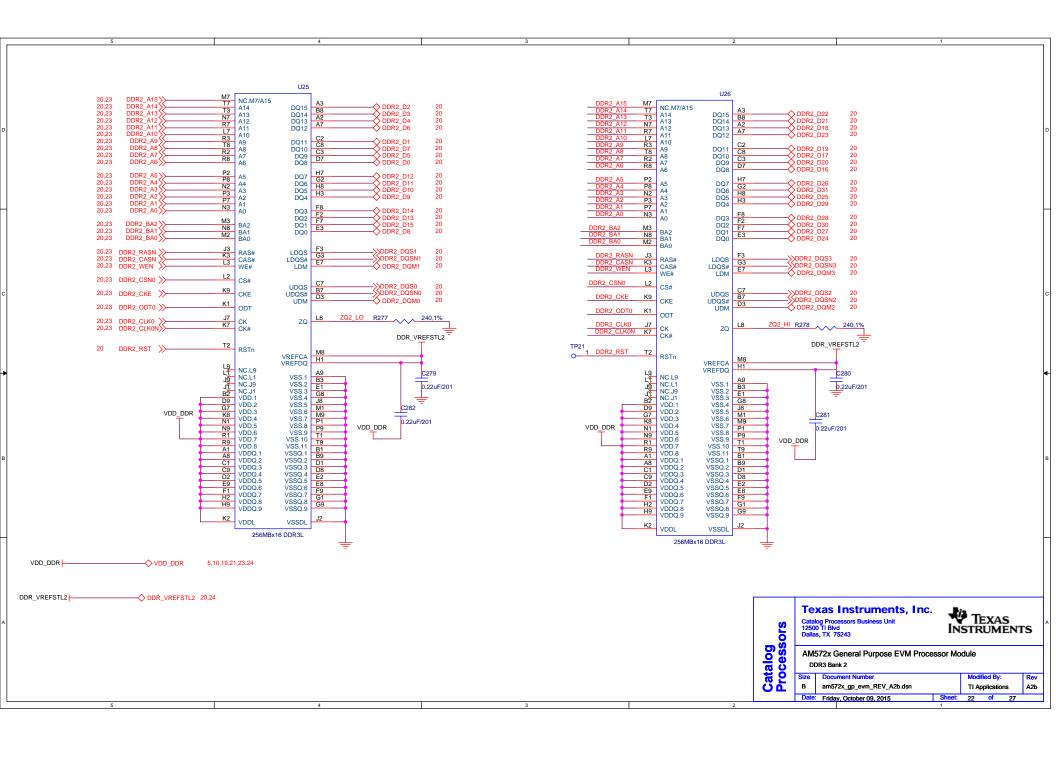


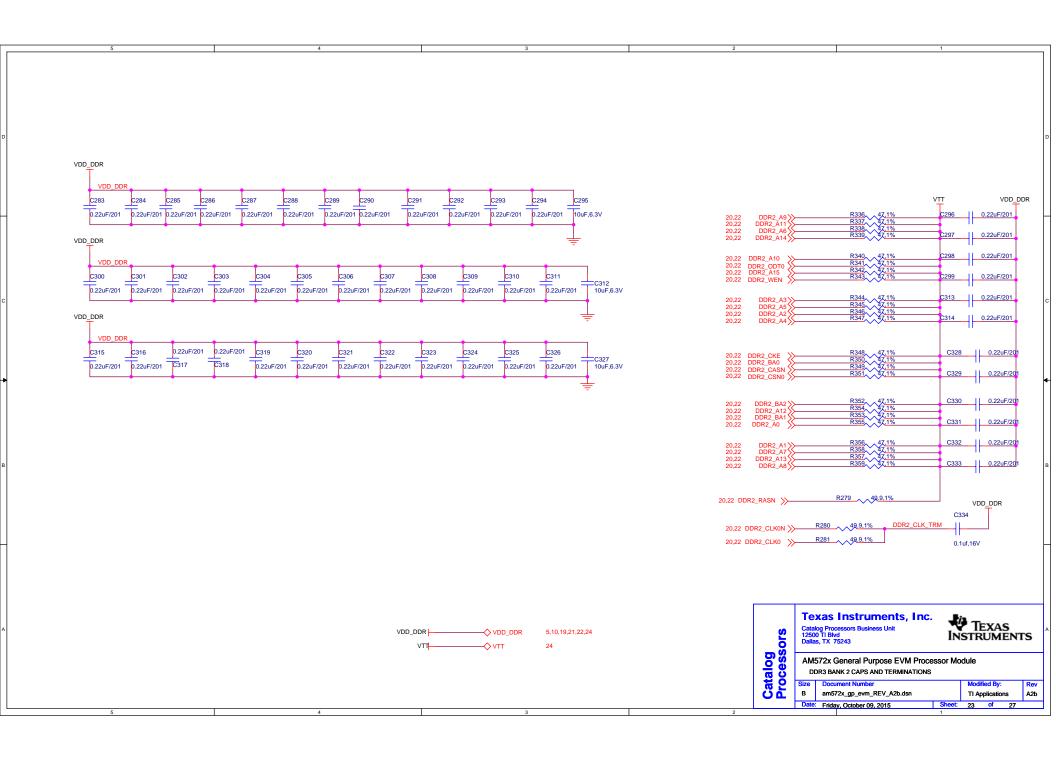


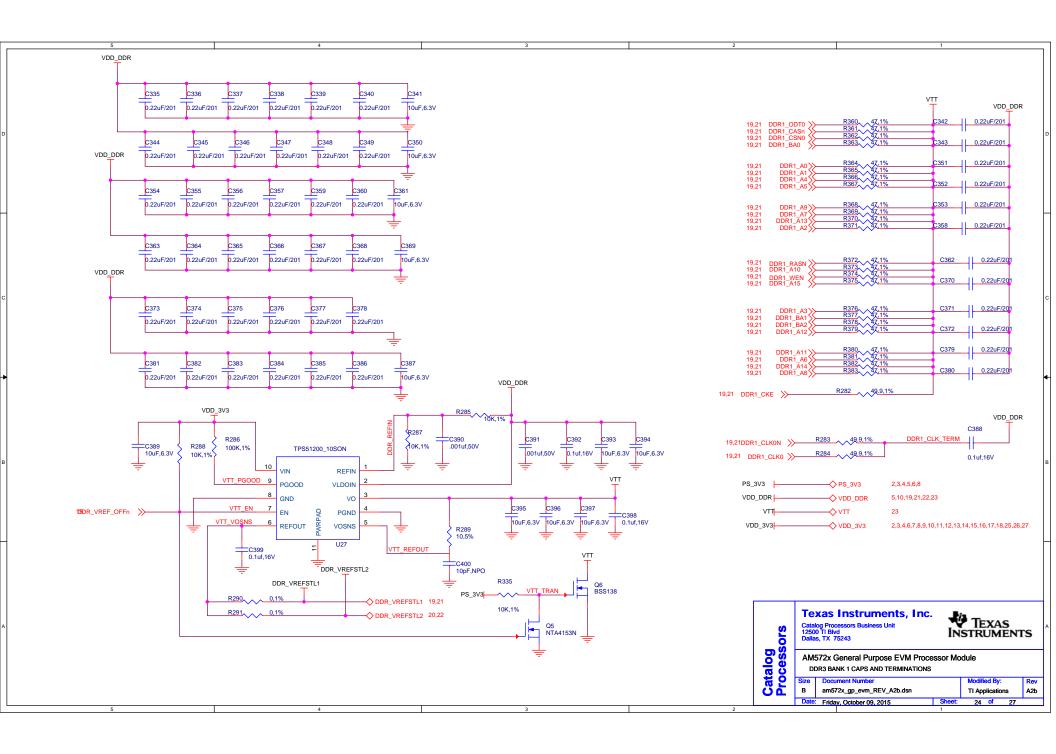


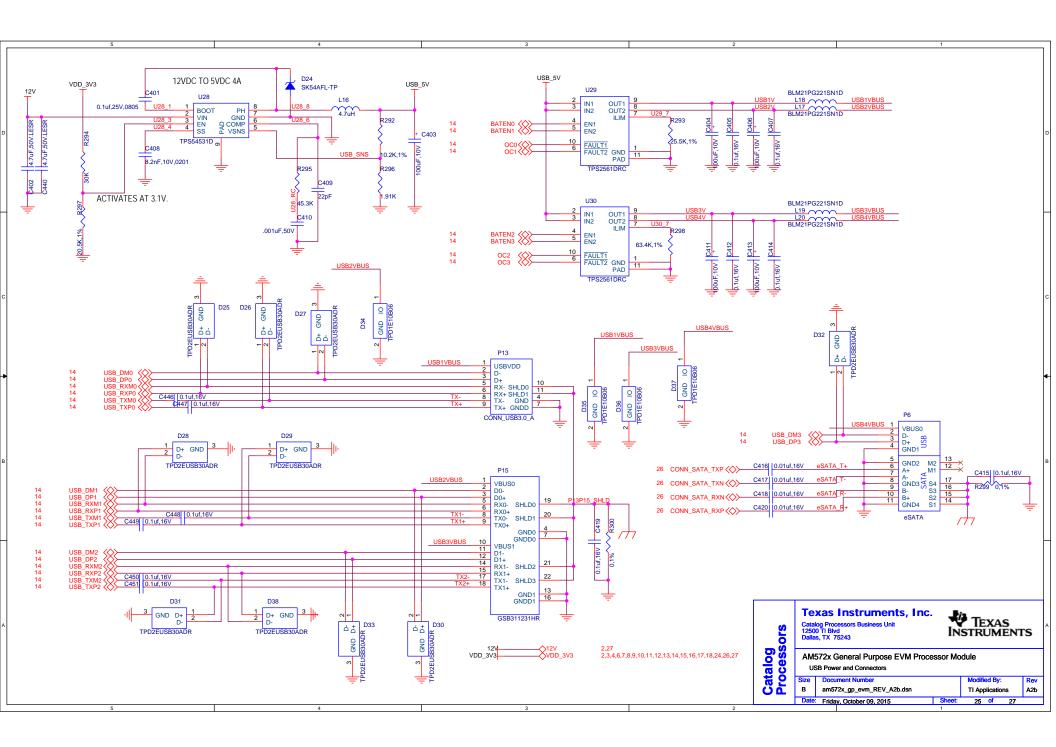












Preliminary Information - Subject to change

