J2
Barrel_Jack_Switch

PWR_FLAG

GND

D1
4001

Batt

Conn_01x04_Socket
J1

PWR_FLAG GND

D3
4001

Batt

D4
4148

U1
LM7809_TO220
VI   VO
GND

F1
500mA

+9V
PWR_FLAG

PowerPos1
ThePowerOut

R1
47k

D2
24V

C1
2200uF

C2
100nF

C3
100nF

D5
10V

GND

H1
MountingHole_Pad

GND

H3
MountingHole_Pad

GND

H2
MountingHole_Pad

GND

H4
MountingHole_Pad

GND

C5
100nF

+9V

GND

VDD

U3E
4066

VSS

GND

C7
100nF

+9V

GND

VDD

U5G
40106

VSS

GND

C4
100nF

+9V

GND

V+

U2C
358

V-

GND

C6
100nF

+9V

GND

V+

U4E
324

V-

GND

C8
100nF

+9V

GND

V+

U6E
324

V-

GND

Sheet: /power/
File: power.kicad_sch

Title:

Size: A4   Date:
KiCad E.D.A. 9.0.6

Rev:
Id: 2/5

IN1

+9V

SW1

R68
100

D16
9V1

D17
9V1

U3D
4066

C19
1uF

TP11
In-1

12

11   10

10
+
9
−
8

324
U6C

2
1
3
1_W
RV3

GND

R61
1k

TP15
Out-1

The inputs for X2 and X3
are reversed to match the
output from the shift register

+9V   C23
100nF

GND

U8
4052

16

VDD

12   X0   X   13
14   X1
15   X2
11   X3

1   Y0
5   Y1
2   Y2
4   Y3

Y   3

6   Inh

10   A
9   B

VSS   VEE
8   7

GND

GND

Q1A
Q2A

GND

U4A
324

3
+
2
−
1

R65
10k

TP19
Select-Out

IN2

+9V

SW2

R69
100

D18
9V1

D19
9V1

U3C
4066

C20
1uF

TP12
In-2

6

8   9

12
+
13
−
14

324
U6D

2
1
3
2_W
RV4

GND

R62
1k

TP16
Out-2

IN3

+9V

SW3

R70
100

D20
9V1

D21
9V1

U3B
4066

C21
1uF

TP13
In-3

5

4   3

3
+
2
−
1

324
U6A

2
1
3
3_W
RV5

GND

R63
1k

TP17
Out-3

IN4

+9V

SW4

R71
100

D22
9V1

D23
9V1

U3A
4066

C22
1uF

TP14
In-4

13

1   2

5
+
6
−
7

324
U6B

2
1
3
4_W
RV6

GND

R64
1k

TP18
Out-4