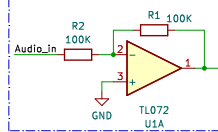
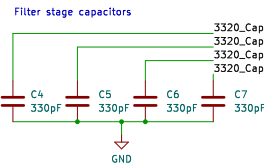
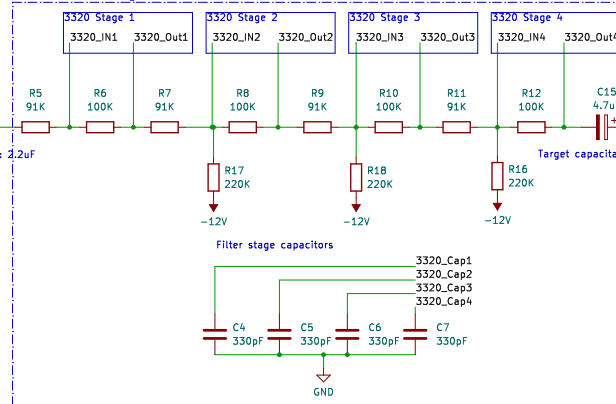


Filter Stages

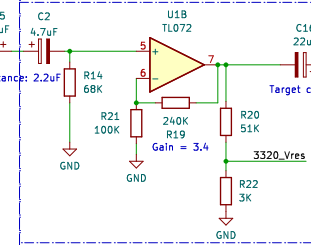
Input inverter



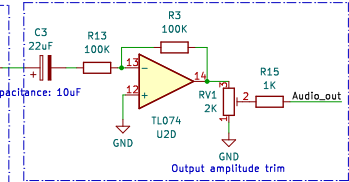
Target Capacitance: 2.2uF



Output Buffer (Sequential Pro One)

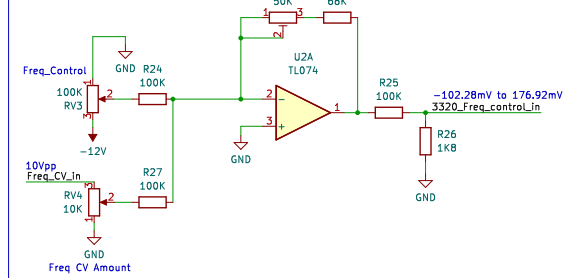


Output inverter

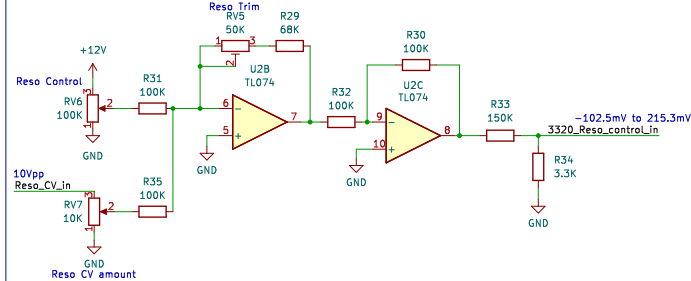


Output amplitude trim

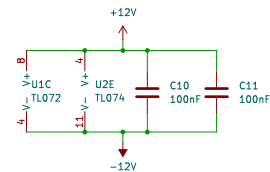
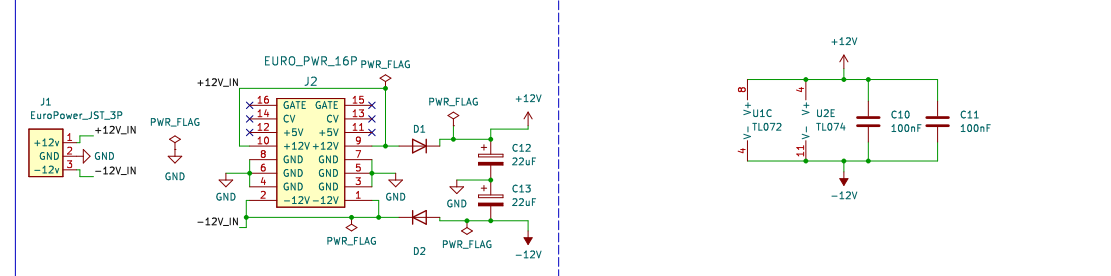
Frequency Control Circuit



Resonance Control Circuit



Power



This version is a prototype

DIYSynthMNL

Sheet: /
File: 3320-VCF.kicad_sch

Title: Eurorack 3320 Low Pass VCF

Size: A3 Date: 2024-01-22
KiCad E.D.A. kicad 7.0.9

Rev: 0.1.0
Id: 1/1