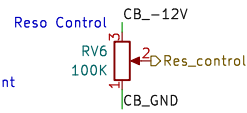
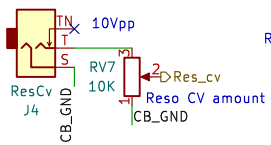
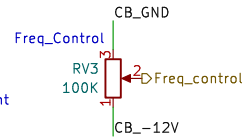
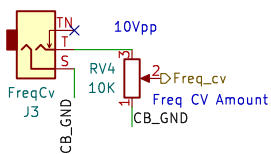
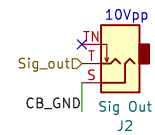
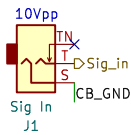


This version is a prototype  
**DIYSynthMNL**

Sheet: /  
File: 3320-VCF.kicad\_sch

**Title: Eurorack 3320 Low Pass VCF**

Size: A4	Date: 2024-02-16	Rev: 0.1.5
KiCad E.D.A. eeschema 7.0.9		Id: 1/3



This version is a prototype  
**DIYSynthMNL**

Sheet: /3320\_Control\_Board/  
File: 3320\_Control\_Board.kicad\_sch

**Title: Eurorack 3320 Low Pass VCF**

Size: A4 Date: 2024-02-16

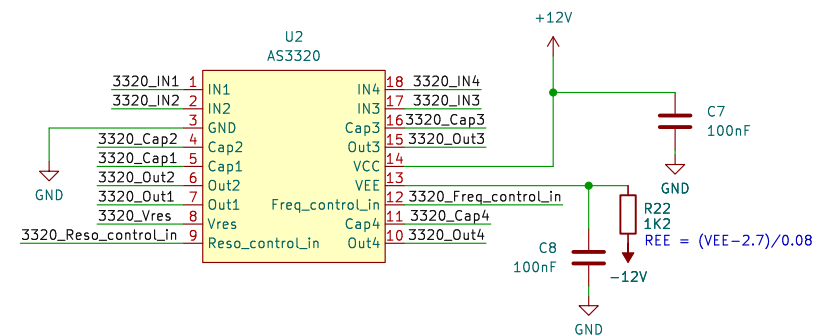
KiCad E.D.A. eeschema 7.0.9



**Rev: 0.1.5**

Id: 2/3

[illegible]

The diagram shows a frequency-to-voltage converter circuit. It features a TL074 op-amp (U3A) configured as an integrator. The non-inverting input (pin 3) is connected to ground. The inverting input (pin 2) is connected to a network of two resistors, R20 (100K) and R24 (100K), which are connected to the 'Freq\_controlD' and 'Freq\_cvD' signals, respectively. The output of the op-amp (pin 1) is connected to a feedback network consisting of a resistor R19 (68K) and a variable resistor RV2 (50K) in parallel. The output signal is then passed through a resistor R21 (100K) and a capacitor R23 (1K) to the '3320\_Freq\_control\_in' input. The output voltage range is specified as -102.28mV to 176.92mV.



 H1  
MountingHole\_Pad  
 GND

Id: 3/3