AC coupling caps can be polarized electrolytics or film caps. Make sure the polarity is right! Filter Stages 3340 Output DC bias: typ = $6.5V \otimes VCC = 15V Vin = 0V$ 3320 Stage 1 3320 Stage 2 3320_IN3 3320_Out3 3320_IN4 3320_Out4 Input buffer Output Buffer (Sequential Pro One) TL072 10Vpp 100K Audio_in R8 100K R10 100K R11 91K R12 100K C15 2.2uF R5 91K R9 91K Test output levels! 100K 91K Gain = 3.2 Output amplitude trim R15 RV1 1K Audio_out R14 68K R21 R16 220K R17 R18 2.2uF 220K 220K 220K R19 R20 The chip can surprisingly handle 10Vpp audio signals! 100K 51K -12V -127 -120 3320_Vres Filter stage capacitors GND 3320 Cap1 R22 3K3 3320_Cap3 3320_Cap4 \uparrow GND C4 270pF C5 C6 270pF C7 270pF GND Frequency Control Circuit Freq CV gain Trim RV2 50K R23 68K +12V U3 AS3320 IN4 18 3320_IN4 IN3 17 3320_IN3 3320_IN1 1 IN1 3320_IN2 2 IN2 GND R24 Freq_Control TL074 Cap3 Out3 15 3320_Out3 100K R25 100K 100nF -102.28mV to 176.92mV 100K 3320_Freq_control_in GND R26 1KB R28 1K2 REE = (VEE-2.7)/0.08 10Vpp Freq_CV_in RV4 10K GND R27 100K GND 100nF -127 GND GND Freq CV Amount Resonance Control Circuit Reso CV gain Trim RV5 R29 50K 68K R30 100K +12V \Box U2B Reso Control U2C TL074 R32 100K 100K RV6 100K -102.5mV to 215.3mV 3320_Reso_control_in GND R34 3.3K GND GND 10Vpp Reso_CV_in R35 100K GND 10K GND Reso CV amount Power Op amp power and decoupling EURO_PWR_16P PWR_FLAG J2 +12V_IN_ +12V +12V PWR_FLAG EuroPower_JST_3P PWR_FLAG +12v_IN +12v_IN 2 GND 3 GND -12v -12v_IN + C12 22uF ÷u1c [‡] U2E C10 100nF GND _TL072 _ TL074 GND + C13 22uF TL074 This version is a prototype DIYSynthMNL -12V_IN_ Sheet: / File: 3320-VCF.kicad_sch D2 PWR_FLAG PWR_FLAG -12V Title: Eurorack 3320 Low Pass VCF Size: A3 Date: 2024-01-22 KiCad E.D.A. kicad 7.0.9 Rev: 0.1.1