

Linear FM Input Bias Network

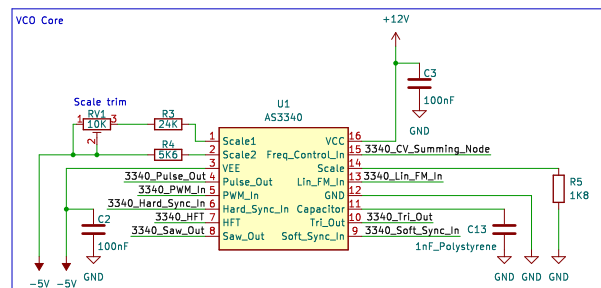
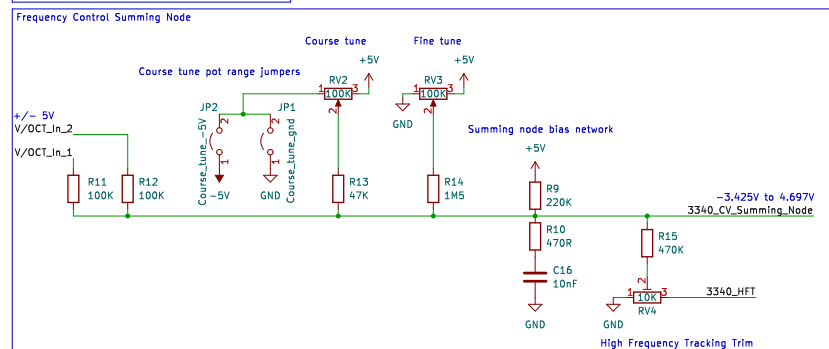
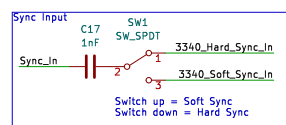
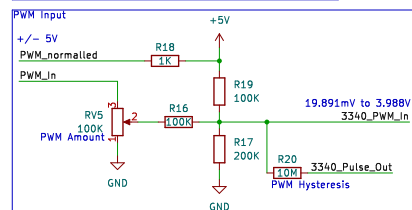
Input: $\pm 5V$ Lin FM In

Components:

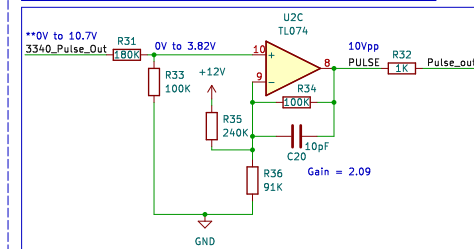
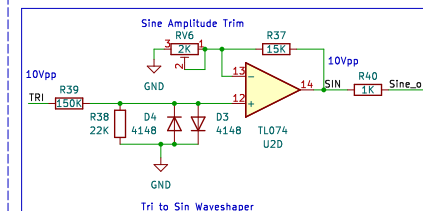
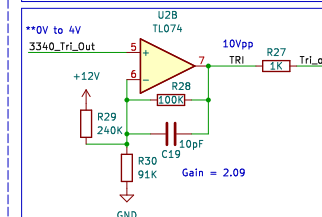
- C15: 100nF
- R8: 1M
- R6: 1M
- R7: 470R
- C14: 10nF

Output: 10.362V to 13.638V

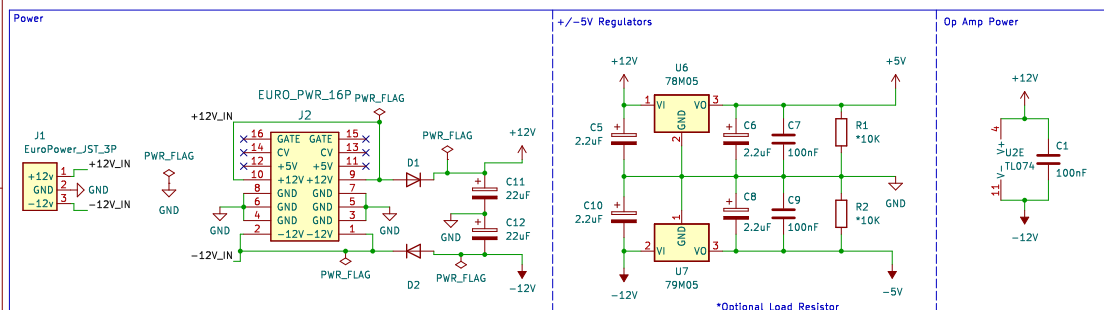
3340 Lin FM In



The circuit diagram shows a non-inverting amplifier configuration using a TL074 op-amp (U2A). The input signal, labeled "0V to 4V", is connected to the non-inverting input (pin 3) through a resistor R21 (100K). The inverting input (pin 2) is connected to ground through a resistor R23 (91K) and to the output through a feedback resistor R24 (100K). A capacitor C18 (0.0pF) is connected between the inverting input and ground. The output (pin 1) is connected to a load resistor R26 (1K) and is labeled "10Vpp SAW". The gain of the amplifier is indicated as "Gain = 2.09".



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**Electric Druid 3340 chip page on calculating the outputs:
Saw = VCC * (2/3)
Tri = VCC * (1/3)
Pulse = VCC - 1.3V
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Id: 1/1