

CORE

IMX1				IMX6UL_CORE			
CSI_HSYNC	A1	1	60	A60	CSI_VSYNC		
CSI_MCLK	A2	2	59	A59	CSI_DATA3		
CSI_DATA2	A3	3	58	A58	CSI_DATA7		
CSI_DATA6	A4	4	57	A57	CSI_DATA1		
CSI_PIXCLK	A5	5	56	A56	CSI_DATA0		
CSI_DATA5	A6	6	55	A55	CSI_DATA4		
LCD_DATA0	A7	7	54	A54	R75 100R	USDHC1_CLK	
LCD_DATA1	A8	8	53	A53	R74 100R	USDHC1_CMD	
LCD_DATA2	A9	9	52	A52	R73 100R	USDHC1_DATA2	
LCD_DATA3	A10	10	51	A51	R72 100R	USDHC1_DATA3	
LCD_DATA4	A11	11	50	A50	R71 100R	USDHC1_DATA1	
LCD_DATA5	A12	12	49	A49	R70 100R	USDHC1_DATA0	
LCD_DATA6	A13	13	48	A48	SNVS_TAMPER9	CT_RST	
LCD_DATA7	A14	14	47	A47	GPIO_5	SD1_VSELECT	
LCD_DATA8	A15	15	46	A46	LCD_DE		
LCD_DATA9	A16	16	45	A45	LCD_PCLK		
LCD_DATA10	A17	17	44	A44	LCD_HSYNC		
LCD_DATA11	A18	18	43	A43	LCD_VSYNC		
LCD_DATA12	A19	19	42	A42			
LCD_DATA13	A20	20	41	A41	PMIC_ON_REQ	VDD_COIN_3V	
LCD_DATA14	A21	21	40	A40	RESET		
LCD_DATA15	A22	22	39	A39	SNVS_TAMPER6	ENET2_INT_TREE#	JTAG_MOD_SP_TXD_INT_GBC_LED
LCD_DATA16	A23	23	38	A38	ENET2_RXD0		
LCD_DATA17	A24	24	37	A37	ENET2_RXD1		
LCD_DATA18	A25	25	36	A36	ENET2_TXD0		
LCD_DATA19	A26	26	35	A35	ENET2_TXD1		
LCD_DATA20	A27	27	34	A34	ENET2_RXER		
LCD_DATA21	A28	28	33	A33	ENET2_CRS_DV		
LCD_DATA22	A29	29	32	A32	ENET2_TXEN		
LCD_DATA23	A30	30	31	A31	ENET2_TX_CLK		

DCDC_5V	B1	1	60	B60			
SNVS_TAMPER4	B2	2	59	B59			
ON_OFF	B3	3	58	B58	USB_OTG2_VBUS		
SNVS_TAMPER1	B4	4	57	B57	USB_OTG1_VBUS		
SNVS_TAMPER0	B5	5	56	B56	USB_OTG2_DP		
BOOT_MODE0	B6	6	55	B55	USB_OTG2_DN		
BOOT_MODE1	B7	7	54	B54	USB_OTG1_DP		
SNVS_TAMPER8	B8	8	53	B53	USB_OTG1_DN		
SNVS_TAMPER7	B9	9	52	B52	nUSB_OTG_CHD		
SNVS_TAMPER5	B10	10	51	B51	GPIO_0	USB_OTG1_ID	
SNVS_TAMPER2	B11	11	50	B50	UART1_TXD		
GPIO_2	B12	12	49	B49	GPIO_1	GBC_KEY	AP_INT
GPIO_4	B13	13	48	B48	UART1_CTS	KEY0	
GPIO_9	B14	14	47	B47	GPIO_3	LED0	
GPIO_8	B15	15	46	B46	UART1_RXD		
JTAG_TDI	B16	16	45	B45	GPIO_7	R22 100R	ENET_MDC
JTAG_TDO	B17	17	44	B44	GPIO_6	R38 100R	ENET_MDIO
JTAG_TCK	B18	18	43	B43	UART1_RTS	USDHC1_CD_B	
JTAG_TMS	B19	19	42	B42	UART2_TXD	ECSPI3_SS0	
JTAG_nTRST	B20	20	41	B41	UART2_RXD	ECSPI3_SCLK	
JTAG_MOD_SP_TXD_INT_GBC_LED	B21	21	40	B40	UART2_RTS	CAN2_RX	ECSPI3_MISO
ENET1_TXD0	B22	22	39	B39	UART2_CTS	CAN2_TX	ECSPI3_MOSI
ENET1_RXD1	B23	23	38	B38	UART3_TXD		
ENET1_CRS_DV	B24	24	37	B37	UART3_RXD		
ENET1_RXD0	B25	25	36	B36	UART3_CTS	CAN1_TX	
ENET1_TXEN	B26	26	35	B35	UART3_RTS	CAN1_RX	
ENET1_TXD1	B27	27	34	B34	UART4_RXD	I2C1_SDA	
ENET1_TX_CLK	B28	28	33	B33	UART4_TXD	I2C1_SCL	
ENET1_RXER	B29	29	32	B32	UART5_RXD	I2C2_SDA	
	B30	30	31	B31	UART5_TXD	I2C2_SCL	

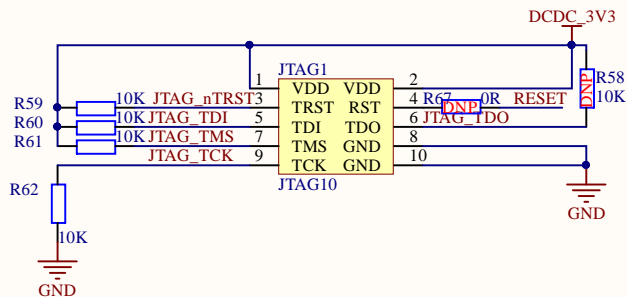
SDIO WIFI

Header 6X2			
DCDC_3V3	1	2	
USDHC1_DATA0	3	4	USDHC1_DATA1
USDHC1_DATA2	5	6	USDHC1_DATA3
USDHC1_CMD	7	8	USDHC1_CLK
WIFI_INT	9	10	WIFI_REG_ON
USDHC1_CD_B	11	12	

I2C

DCDC_3V3	R40	4.7K	I2C2_SCL
	R41	4.7K	I2C2_SDA
	R42	4.7K	I2C1_SCL
	R57	4.7K	I2C1_SDA

JTAG



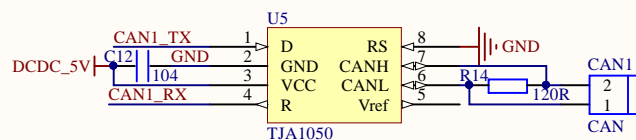
BOOT

SW								BOOT DEVICE
D1	D2	D3	D4	D5	D6	D7	D8	
OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF	USB
ON	OFF	OFF	OFF	OFF	OFF	ON	OFF	MicroSD
ON	OFF	ON	OFF	OFF	ON	ON	OFF	EMMC
ON	OFF	OFF	OFF	ON	OFF	OFF	ON	NAND

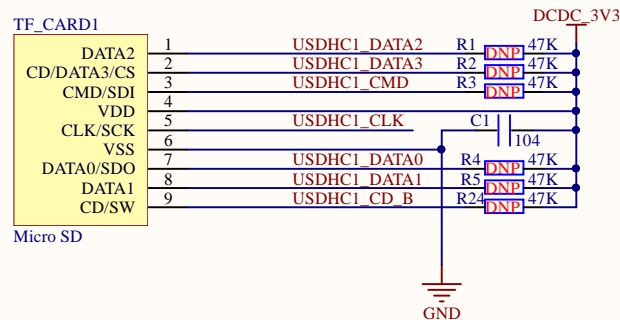
DCDC_3V3	R43	1	OFF	ON	16	BOOT_MODE1
	R44	2			15	BOOT_MODE0
	R45	3			14	LCD_DATA11
	R46	4			13	LCD_DATA3
	R47	5			12	LCD_DATA4
	R48	6			11	LCD_DATA5
	R49	7			10	LCD_DATA6
	R50	8			9	BOOT_DATA7

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Author: lycreturn@ALIENTEK	Size: A3	
Date: 2020-03-19	File: IMX6ULL_MINI_CORE.SchDoc	
Revision: *	Version: V1.0	

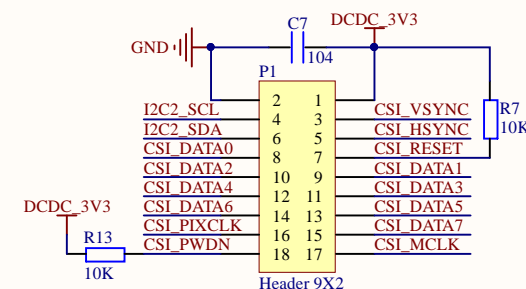
CAN



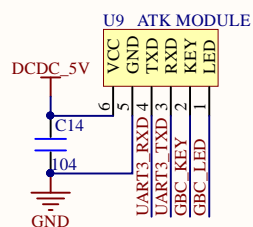
SD CARD



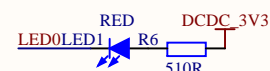
CAMERA



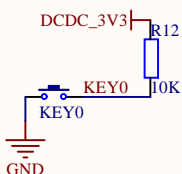
ATK MODULE



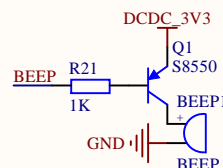
LED



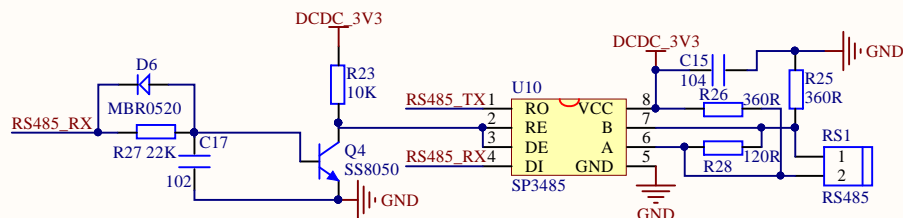
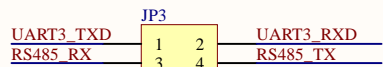
KEY



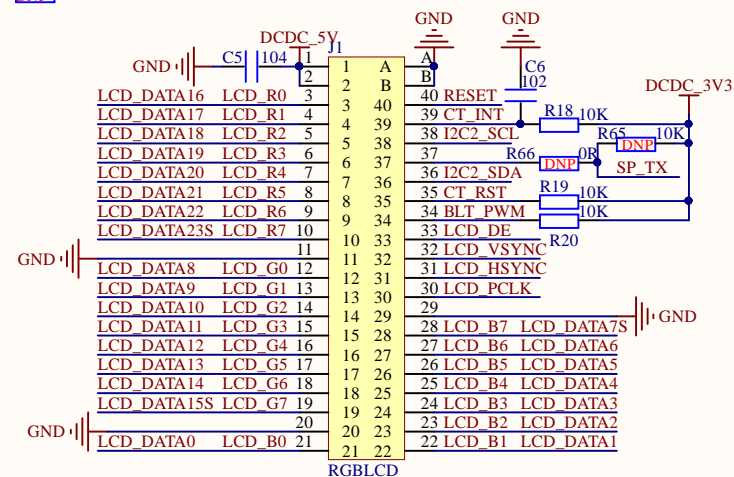
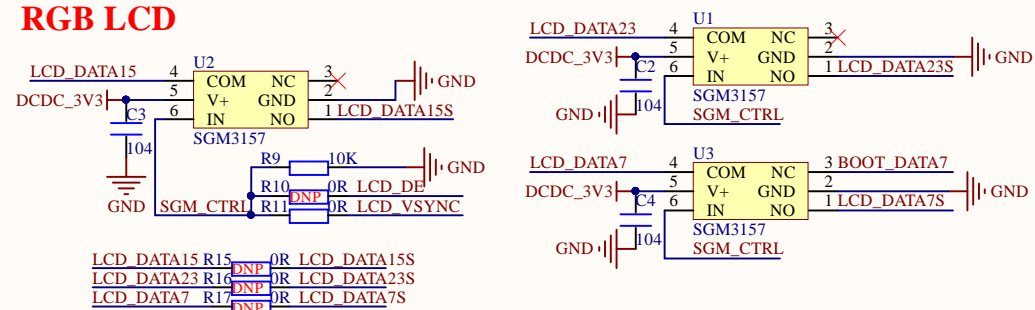
BEEP



RS485

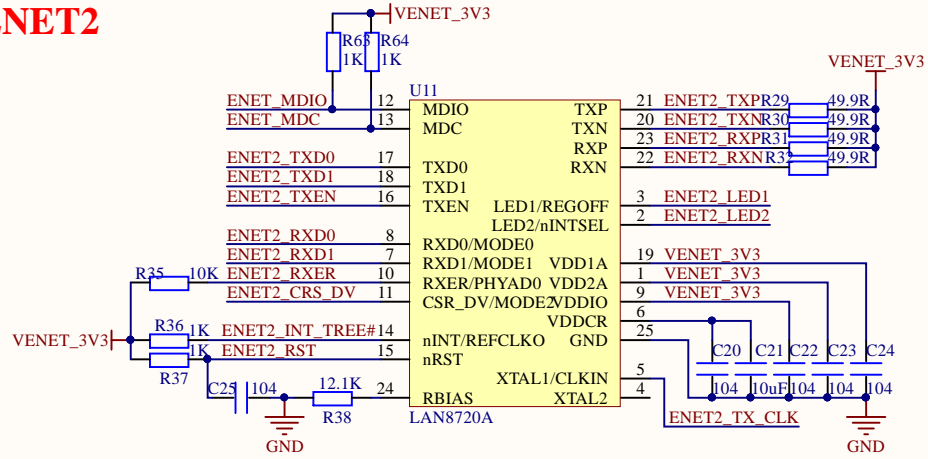


RGB LCD

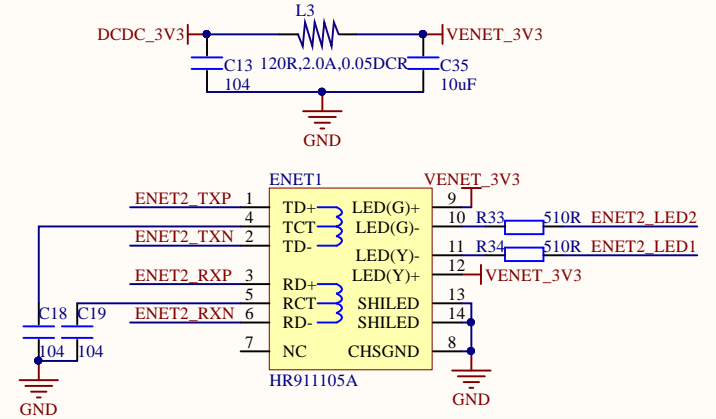


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Revision: *	Version: V1.0	

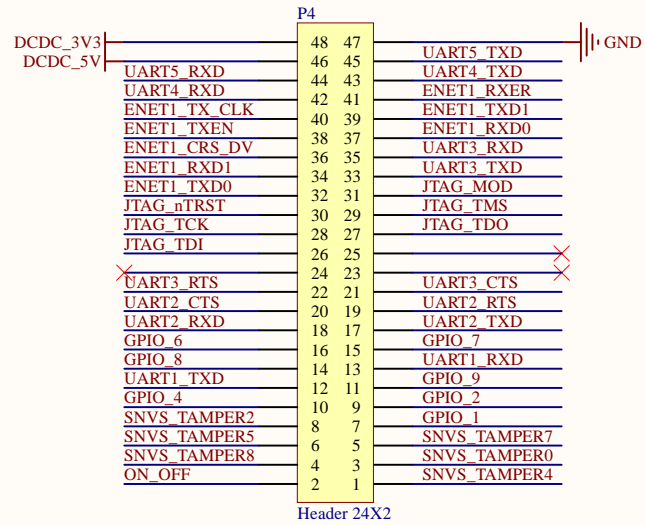
ENET2



PHY ADDR:0X1



PIN



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