

## **Notice for TAIYO YUDEN products**

Please read this notice before using the TAIYO YUDEN products.

## /!\ REMINDERS

## Product Information in this Catalog

Product information in this catalog is as of October 2019. All of the contents specified herein and production status of the products listed in this catalog are subject to change without notice due to technical improvement of our products, etc. Therefore, please check for the latest information carefully before practical application or use of our products.

Please note that TAIYO YUDEN shall not be in any way responsible for any damages and defects in products or equipment incorporating our products, which are caused under the conditions other than those specified in this catalog or individual product specification sheets.

## Approval of Product Specifications

Please contact TAIYO YUDEN for further details of product specifications as the individual product specification sheets are available. When using our products, please be sure to approve our product specifications or make a written agreement on the product specification with TAIYO YUDEN in advance.

## Pre-Evaluation in the Actual Equipment and Conditions

Please conduct validation and verification of our products in actual conditions of mounting and operating environment before using our products.

## Limited Application

## 1. Equipment Intended for Use

The products listed in this catalog are intended for generalpurpose and standard use in general electronic equipment (e.g., AV equipment, OA equipment, home electric appliances, office equipment, information and communication equipment including, without limitation, mobile phone, and PC) and other equipment specified in this catalog or the individual product specification sheets.

TAIYO YUDEN has the line-up of the products intended for use in automotive electronic equipment, telecommunications infrastructure and industrial equipment, or medical devices classified as GHTF Classes A to C (Japan Classes I to III). Therefore, when using our products for these equipment, please check available applications specified in this catalog or the individual product specification sheets and use the corresponding products.

## 2. Equipment Requiring Inquiry

Please be sure to contact TAIYO YUDEN for further information before using the products listed in this catalog for the following equipment (excluding intended equipment as specified in this catalog or the individual product specification sheets) which may cause loss of human life, bodily injury, serious property damage and/or serious public impact due to a failure or defect of the products and/or malfunction attributed thereto.

- (1) Transportation equipment (automotive powertrain control system, train control system, and ship control system, etc.)
- (2) Traffic signal equipment
- (3) Disaster prevention equipment, crime prevention equipment
- (4) Medical devices classified as GHTF Class C (Japan Class III)
- (5) Highly public information network equipment, dataprocessing equipment (telephone exchange, and base station, etc.)
- (6) Any other equipment requiring high levels of quality and/or reliability equal to the equipment listed above

## 3. Equipment Prohibited for Use

Please do not incorporate our products into the following equipment requiring extremely high levels of safety and/or reliability.

- (1) Aerospace equipment (artificial satellite, rocket, etc.)
- (2) Aviation equipment \*1
- (3) Medical devices classified as GHTF Class D (Japan Class IV), implantable medical devices \*2

- (4) Power generation control equipment (nuclear power, hydroelectric power, thermal power plant control system, etc.)
- (5) Undersea equipment (submarine repeating equipment, underwater work equipment, etc.)
- (6) Military equipment
- (7) Any other equipment requiring extremely high levels of safety and/or reliability equal to the equipment listed above

## \*Notes:

- 1. There is a possibility that our products can be used only for aviation equipment that does not directly affect the safe operation of aircraft (e.g., in-flight entertainment, cabin light, electric seat, cooking equipment) if such use meets requirements specified separately by TAIYO YUDEN. Please be sure to contact TAIYO YUDEN for further information before using our products for such aviation equipment.
- Implantable medical devices contain not only internal unit which is implanted in a body, but also external unit which is connected to the internal unit.

## 4. Limitation of Liability

Please note that unless you obtain prior written consent of TAIYO YUDEN, TAIYO YUDEN shall not be in any way responsible for any damages incurred by you or third parties arising from use of the products listed in this catalog for any equipment that is not intended for use by TAIYO YUDEN, or any equipment requiring inquiry to TAIYO YUDEN or prohibited for use by TAIYO YUDEN as described above.

## Safety Design

When using our products for high safety and/or reliability-required equipment or circuits, please fully perform safety and/or reliability evaluation. In addition, please install (i) systems equipped with a protection circuit and a protection device and/or (ii) systems equipped with a redundant circuit or other system to prevent an unsafe status in the event of a single fault for a failsafe design to ensure safety.

## Intellectual Property Rights

Information contained in this catalog is intended to convey examples of typical performances and/or applications of our products and is not intended to make any warranty with respect to the intellectual property rights or any other related rights of TAIYO YUDEN or any third parties nor grant any license under such rights.

## Limited Warranty

Please note that the scope of warranty for our products is limited to the delivered our products themselves and TAIYO YUDEN shall not be in any way responsible for any damages resulting from a failure or defect in our products. Notwithstanding the foregoing, if there is a written agreement (e.g., supply and purchase agreement, quality assurance agreement) signed by TAIYO YUDEN and your company, TAIYO YUDEN will warrant our products in accordance with such agreement

## **■ TAIYO YUDEN's Official Sales Channel**

The contents of this catalog are applicable to our products which are purchased from our sales offices or authorized distributors (hereinafter "TAIYO YUDEN's official sales channel"). Please note that the contents of this catalog are not applicable to our products purchased from any seller other than TAIYO YUDEN's official sales channel.

## Caution for Export

Some of our products listed in this catalog may require specific procedures for export according to "U.S. Export Administration Regulations", "Foreign Exchange and Foreign Trade Control Law" of Japan, and other applicable regulations. Should you have any questions on this matter, please contact our sales staff.

# MULTILAYER CERAMIC CAPACITORS

## **MULTILAYER CERAMIC CAPACITORS**



## PARTS NUMBER

J	М	Κ	3	1	6	Δ	В	J	1	0	6	М	L	_	Т	Δ
1	2	3		4		<b>⑤</b>	(	3)		7		8	9	10	11)	12

△=Blank space

(1)F	Rated	vol	tage

Code	Rated voltage[VDC]
Р	2.5
Α	4
J	6.3
L	10
E	16
Т	25
G	35
U	50
Н	100
Q	250
S	630
Х	2000

Code	End termination
K	Plated
S	Cu Internal Electrodes (For High Frequency)

## (4)Dimension (L × W)

3End termination

4 Dimension (L × W)							
Туре	Dimensions (L×W)[mm]	EIA (inch)					
021	0.25 × 0.125	008004					
042	0.4 × 0.2	01005					
063	0.6 × 0.3	0201					
105	1.0 × 0.5	0402					
105	0.52 × 1.0 ※	0204					
107	1.6 × 0.8	0603					
107	0.8 × 1.6 💥	0306					
212	2.0 × 1.25	0805					
212	1.25 × 2.0 💥	0508					
316	3.2 × 1.6	1206					
325	3.2 × 2.5	1210					
432	4.5 × 3.2	1812					
	/						

Note :  $\mbox{\ensuremath{\mbox{$\times$}}} LW$  reverse type ( $\mbox{\ensuremath{\mbox{$W$}}} K)$  only

## ②Series name

Code	Series name
М	Multilayer ceramic capacitor
V	Multilayer ceramic capacitor for high frequency
W	LW reverse type multilayer capacitor

Code	Туре	L[mm]	W[mm]	T[mm]
Δ	ALL	Standard	Standard	Standard
	063	$0.6 \pm 0.05$	0.3±0.05	0.3±0.05
	105	1.0±0.10	0.5±0.10	0.5±0.10
	107	1.6+0.15/-0.05	0.8+0.15/-0.05	0.8+0.15/-0.05
				0.45±0.05
Α	212	2.0+0.15/-0.05	1.25+0.15/-0.05	0.85±0.10
				1.25+0.15/-0.05
	316	3.2±0.20	1.6±0.20	0.85±0.10
	310	3.2±0.20	1.6±0.20	1.6±0.20
	325	$3.2 \pm 0.30$	2.5±0.30	2.5±0.30
	063	0.6±0.09	0.3±0.09	0.3±0.09
	105	1.0+0.15/-0.05	0.5+0.15/-0.05	0.5+0.15/-0.05
	407	1.6+0.20/-0	0.8+0.20/-0	$0.45 \pm 0.05$
В	107	1.0 + 0.20/ - 0	0.8 + 0.20/ - 0	0.8+0.20/-0
ь				0.45±0.05
	212	2.0+0.20/-0	1.25+0.20/-0	0.85±0.10
				1.25+0.20/-0
	316	3.2±0.30	1.6±0.30	1.6±0.30
С	105	1.0+0.20/-0	0.5+0.20/-0	0.5+0.20/-0
	063	0.6 + 0.25/- 0	0.3 + 0.25/- 0	0.3 + 0.25/ - 0
E	105	1.0+0.30/-0	0.5+0.30/-0	0.5+0.30/-0

Note: cf. STANDARD EXTERNAL DIMENSIONS

△= Blank space

## 6 Temperature characteristics code

■ High dielectric type (Excluding Super low distortion multilayer ceramic capacitor)

Code	Applicable standard		Temperature range[°C]	Ref. Temp.[°C]	Capacitance change	Capacitance tolerance	Tolerance code			
	JIS	В	-25~+ 85	20	±10%	±10%	K			
BJ	JIS	Ь	-25°° + 65	20	± 10%	±20%	М			
БО	EIA	X5R	-55 <b>~</b> + 85	25	±15%	±10%	K			
	EIA	ASK	_55~+ 85		土13%	±20%	М			
В7	EIA	X7R	-55~+125	25	±15%	±10%	K			
Б/	LIA A	λ/Κ	33.5 1 123	2.5	±1370	±20%	М			
C6	EIA X6S	EIA X6S	X6S	-55~+105	25	±22%	±10%	K		
				702	702	702	702	702	702	-55.4 + 105
C7	EIA	X7S	-55~+125	25	±22%	±10%	K			
C/	EIA	A X/S	-55 <b>~</b> +125	25	±22%	±20%	М			
LD(※)			55   05	0.5	1.450/	±10%	K			
	EIA X5R	EIA X5R −55~+ 85 25	25	±15%	±20%	М				

Note: X.LD Low distortion high value multilayer ceramic capacitor

∆= Blank space

<sup>▶</sup> This catalog contains the typical specification only due to the limitation of space. When you consider the purchase of our products, please check our product specification sheets. For details of each product (characteristics graph, reliability information, precautions for use, and so on), see our website (http://www.ty-top.com/).

## for General Electronic Equipment

■Temperature compensating type

Code	Applicable standard		Temperature range[°C]	Ref. Temp.[°C]	Capacitance change	Capacitance tolerance	Tolerance code
			_			±0.05pF	Α
						±0.1pF	В
CG	EIA	C0G	-55 <b>~</b> +125	25	$0\pm30$ ppm/°C	±0.25pF	С
						±0.5pF	D
						±5%	J
	JIS	UJ		20		±0.25pF	С
UJ	JIS	UJ	-55 <b>~</b> +125	20	$-750 \pm 120 \text{ppm/}^{\circ}\text{C}$	±0.5pF	D
	EIA	U2J		25		±5%	J
UK	JIS	UK	<b>−55~+125</b>	20	-750+250nnm/°C	±0.255E	С
UK	EIA U2K	-55~+125	25	- 750±250ppm/°C	±0.25pF		

## 6 Series code

· Super low distortion multilayer ceramic capacitor

	·
Code	Series code
SD	Standard

• Medium-High Voltage Multilayer Ceramic Capacitor

Code	Series code
SD	Standard

Nominal capacitance

Code (example)	Nominal capacitance
0R5	0.5pF
010	1pF
100	10pF
101	100pF
102	1,000pF
103	10,000pF
104	0.1 <i>μ</i> F
105	1.0 <i>μ</i> F
106	10 μ F
107	100 μ F

Note: R=Decimal point

## **8**Capacitance tolerance

Code	Capacitance tolerance
Α	±0.05pF
В	±0.1pF
С	±0.25pF
D	±0.5pF
F	±1pF
G	±2%
J	±5%
K	±10%
М	±20%
Z	+80/-20%

## Thickness

Code	Thickness[mm]						
K	0.125						
Н	0.13						
E	0.18						
С	0.2						
D	0.2						
Р	0.2						
Т	0.3						
K	0.45(107type or more)						
V	0.5						
W	0.5						
Α	0.8						
D	0.85(212type or more)						
F	1.15						
G	1.25						
L	1.6						
N	1.9						
Υ	2.0 max						
М	2.5						

## 10Special code

Code	Special code
_	Standard

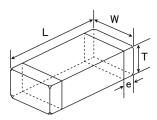
## 11)Packaging

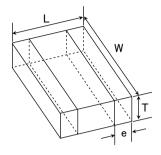
Packaging
$\phi$ 178mm Taping (2mm pitch)
$\phi$ 178mm Taping (4mm pitch)
\$\phi\$178mm Taping (4mm pitch, 1000 pcs/reel)
325 type (Thickness code M)
\$\phi\$178mm Taping (2mm pitch)105type only
(Thickness code E,H)
$\phi$ 178mm Taping(1mm pitch)021/042type only

## 12Internal code

UZINCENIAI COUE	
Code	Internal code
Δ	Standard

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LW reverse type

T/ FIA \		D	imension [mm]					
Type( EIA )	L	W	T	*1	е			
□MK021 (008004)	0.25±0.013	0.125±0.013	0.125±0.013	K	0.0675±0.0275			
□VS021 (008004)	0.25±0.013	0.125±0.013	0.125±0.013	K	0.0675±0.0275			
□MK042(01005)	0.4±0.02	0.2±0.02	0.2±0.02	С	0.1±0.03			
□MK042(01003)	0.4±0.02	0.2±0.02	0.2±0.02	D	0.1±0.03			
□VS042 (01005)	0.4±0.02	0.2±0.02	0.2±0.02	С	0.1±0.03			
DM(000(0001)	0.01.000	0.01.000	0.0.1.0.00	Р	0.15   0.05			
□MK063(0201)	0.6±0.03	0.3±0.03	0.3±0.03	Т	0.15±0.05			
			0.13±0.02	Н				
			0.18±0.02	Е				
☐MK105(0402)	1.0±0.05	$0.5 \pm 0.05$	$0.2 \pm 0.02$	С	0.25±0.10			
			0.3±0.03	Р				
			0.5±0.05	٧				
□VK105(0402)	1.0±0.05	0.5±0.05	0.5±0.05	W	0.25±0.10			
□WK105(0204)※	$0.52 \pm 0.05$	1.0±0.05	$0.3 \pm 0.05$	Р	0.18±0.08			
□MK107(0603)	1.6±0.10	0.8±0.10	0.45±0.05	K	0.35±0.25			
LIMK107(0003)	1.0±0.10	0.8±0.10	0.8±0.10	Α	0.35±0.25			
□WK107(0306)※	0.8±0.10	1.6±0.10	0.5±0.05		0.25±0.15			
			0.45±0.05	K				
☐MK212(0805)	2.0±0.10	1.25±0.10	0.85±0.10	D	$0.5 \pm 0.25$			
			1.25±0.10	G				
□WK212(0508)※	1.25±0.15	2.0±0.15	0.85±0.10	D	0.3±0.2			
			$0.85 \pm 0.10$	D				
□MK316(1206)	3.2±0.15	1.6±0.15	1.15±0.10	F	0.5 + 0.35 / -0.25			
			1.6±0.20	L				
			0.85±0.10	D				
			1.15±0.10	F	0.6±0.3			
☐MK325(1210)	$3.2 \pm 0.30$	2.5±0.20	1.9±0.20	N				
			1.9+0.1/-0.2	Υ				
			2.5±0.20	М				
□MK432(1812)	4.5 ± 0.40	2.2.4.0.20	2.0+0/-0.30	Υ	0.6±0.4			
LINK432 (1812)	4.5±0.40	3.2±0.30	2.5±0.20	М	0.9±0.6			

Note: ※. LW reverse type, \*1.Thickness code

## STANDARD QUANTITY

т	EIA (inch)	Dime	nsion	Standard qu	uantity[pcs]	
Туре	EIA (Inch)	[mm]	Code	Paper tape	Embossed tape	
021	008004	0.125	K	_	50000	
042	01005	0.0	С		40000	
042	01005	0.125  0.2  0.3  0.13  0.18  0.2  0.3  0.5  0.45  0.85  1.25  0.85  1.15  1.6  0.85  1.15  1.9  2.0 max	D	_	40000	
063	0201	0.2	P	15000		
003	0201	0.3	Т	15000	_	
		0.13	Н	_	20000	
		0.18	Е	_	15000	
	0402	0.2	С	20000	_	
105	0402	0.3	Р	15000	_	
		0.5	V			
		0.5	W	10000	_	
	0204 ※	0.30	P			
	0603	0.45	K	4000	_	
107	0003	0.8	Α	4000	_	
	0306 ※	0.50	V	_	- 4000 -	
		0.45	K	4000		
212	0805	0.85	D	4000	_	
212	Ī	1.25	G	-	3000	
	0508 ※	0.85	D	4000	_	
		0.85	D	4000	_	
316	1206	1.15	F	_	3000	
	ſ	1.6	L,	-	2000	
		0.85	D			
		1.15			2000	
325	1210	1.9	N	] _	2000	
		2.0 max	Υ			
		2.5	М	_	1000	
432	1812	2.0 max	Υ	_	1000	
432	1812	2.5	М	_	500	

Note : ※.LW Reverse type(□WK)

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## Medium-High Voltage Multilayer Ceramic Capacitors

## 105TYPE

[Temperature Characteristic B7 : X7R( $-55\sim+125^{\circ}$ C)] 0.5mm thickness(V)

Part number 1	Part number 2	Rated voltage		rature	Capacitance	Capacitance tolerance	tan δ	HTLT	Thickness*3 [mm]	Soldering R:Reflow
T di c Hamboi T	T di c Hamboi 2	[V]	charact	eristics	[F]	[%]	[%]	Rated voltage x %	THIORIESS [HIII]	W:Wave
HMK105 B7221 ŪV-F				X7R	220 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7331∏V-F				X7R	330 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7471∏V-F				X7R	470 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7681∏V-F				X7R	680 p	±10, ±20	2.5	200	0.5±0.05	R
HMK105 B7102□V-F		100		X7R	1000 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7152□V-F				X7R	1500 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7222□V-F				X7R	2200 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7332□V-F				X7R	3300 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R
HMK105 B7472∏V-F				X7R	4700 p	±10, ±20	2.5	200	$0.5 \pm 0.05$	R

## [Temperature Characteristic CG : CG/C0G( $-55\sim+125^{\circ}$ C)] 0.5mm thickness(V)

Dont words on 1	Dont words on 0	Rated voltage	Tempe	rature	Capacitance	0	Q (-+ 1MII-)	HTLT	*3 5 3	Soldering
Part number 1	Part number 2	[V]	charact	eristics	[F]	Capacitance tolerance	(at 1MHz) min	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
HMK105 CG080DV-F			CG	COG	8 p	±0.5pF	560	200	$0.5 \pm 0.05$	R
HMK105 CG090DV-F			CG	COG	9 p	±0.5pF	580	200	$0.5 \pm 0.05$	R
HMK105 CG100DV-F			CG	COG	10 p	±0.5pF	600	200	$0.5 \pm 0.05$	R
HMK105 CG120JV-F			CG	COG	12 p	±5%	640	200	$0.5 \pm 0.05$	R
HMK105 CG150JV-F			CG	COG	15 p	±5%	700	200	0.5±0.05	R
HMK105 CG180JV-F			CG	COG	18 p	±5%	760	200	$0.5 \pm 0.05$	R
HMK105 CG220JV-F			CG	COG	22 p	±5%	840	200	$0.5 \pm 0.05$	R
HMK105 CG240JV-F		100	CG	COG	24 p	±5%	880	200	$0.5 \pm 0.05$	R
HMK105 CG270JV-F		100	CG	COG	27 p	±5%	940	200	$0.5 \pm 0.05$	R
HMK105 CG330JV-F			CG	COG	33 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG390JV-F			CG	COG	39 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG470JV-F			CG	COG	47 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG560JV-F			CG	COG	56 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG680JV-F			CG	COG	68 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG820JV-F		]	CG	COG	82 p	±5%	1000	200	$0.5 \pm 0.05$	R
HMK105 CG101JV-F			CG	COG	100 p	±5%	1000	200	0.5±0.05	R

## ●107TYPE

[Temperature Characteristic BJ : B( $-25 \sim +85 ^{\circ} C$ )/X5R( $-55 \sim +85 ^{\circ} C$ )] 0.8mm thickness(A)

Part number 1	Part number 2	Rated voltage		erature		Capacitance tolerance	tan δ	HTLT	Thickness*3 [mm]	Soldering R:Reflow
		[V]	charact	teristics	[F]	[%]	[%]	Rated voltage x %	THOMISON ENING	W:Wave
HMK107 BJ102□A-T			В	X5R*1	1000 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ152∏A-T			В	X5R*1	1500 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ222□A-T			В	X5R*1	2200 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ332∏A-T			В	X5R*1	3300 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ472∏A-T			В	X5R*1	4700 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ682∏A-T			В	X5R*1	6800 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ103∏A-T		100	В	X5R*1	0.01 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ153∏A-T			В	X5R*1	0.015 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ223∏A-T			В	X5R*1	0.022 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ333∏A-T			В	X5R*1	0.033 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ473∏A-T			В	X5R*1	0.047 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ104[]A-T			В	X5R*1	0.1 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 BJ224∏A-TE			В	X5R*1	0.22 μ	±10, ±20	3.5	150	0.8±0.10	R

## [Temperature Characteristic C7 : X7S( $-55\sim+125^{\circ}$ C)] 0.8mm thickness(A)

L Tomporacaro onarac	7.0110010 07 . 7670	00 1120	O / A O.	Ollinii Cii	101(11000 (7 17					
Part number 1	Part number 2	Rated voltage [V]		erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK107 C7224□A-TE		100		X7S	0.22 μ	$\pm 10, \pm 20$	3.5	150	$0.8 \pm 0.10$	. R

## [Temperature Characteristic B7 : $X7R(-55 \sim +125 ^{\circ}C)$ ] 0.8mm thickness(A)

Tremperature Oriarac	Ceristic D7 . X/II(	00 1120	0.011111 til	ICKI ICSS (A)					
Part number 1	Part number 2	Rated voltage	Temperature	Capacitance Capacitance tolerance		$ an\delta$	HTLT	Thickness*3 [mm]	Soldering R:Reflow
Fart number 1	Part number 2	[V]	characteristics	[F]	[%]	[%]	Rated voltage x %	mickness [mm]	W:Wave
HMK107 B7102[]A-T			X7R	1000 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7152[]A-T			X7R	1500 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7222∏A-T			X7R	2200 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7332∏A-T			X7R	3300 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7472∏A-T			X7R	4700 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7682∏A-T		100	X7R	6800 p	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7103∏A-T		100	X7R	0.01 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7153∏A-T			X7R	0.015 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7223∏A-T			X7R	0.022 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7333∏A-T			X7R	0.033 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7473[]A-T			X7R	0.047 μ	±10, ±20	3.5	200	0.8±0.10	R
HMK107 B7104□A-T			X7R	0.1 μ	±10, ±20	3.5	200	0.8±0.10	R

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## MULTILAYER CERAMIC CAPACITORS

## [Temperature Characteristic SD : Standard $(-55 \sim +125^{\circ}C)$ ] 0.8mm thickness (A)

Part number 1	Part number 2	Rated voltage	Temperature	Capacitance	Capacitance tolerance	$ an\delta$	HTLT	Thickness*3 [mm]	Soldering R:Reflow
Fart number 1	Part Humber 2	[V]	characteristics	[F]	[%]	[%]	Rated voltage x %	Thickness [mm]	W:Wave
HMK107 SD101KA-T				100 p	±10	0.1	200	0.8±0.10	R
HMK107 SD121KA-T				120 p	±10	0.1	200	0.8±0.10	R
HMK107 SD151KA-T				150 p	±10	0.1	200	0.8±0.10	R
HMK107 SD181KA-T				180 p	±10	0.1	200	0.8±0.10	R
HMK107 SD221KA-T				220 p	±10	0.1	200	0.8±0.10	R
HMK107 SD271KA-T				270 p	±10	0.1	200	0.8±0.10	R
HMK107 SD331KA-T		100	Standard Type	330 p	±10	0.1	200	0.8±0.10	R
HMK107 SD391KA-T				390 p	±10	0.1	200	0.8±0.10	R
HMK107 SD471KA-T				470 p	±10	0.1	200	0.8±0.10	R
HMK107 SD561KA-T				560 p	±10	0.1	200	0.8±0.10	R
HMK107 SD681KA-T				680 p	±10	0.1	200	0.8±0.10	R
HMK107 SD821KA-T				820 p	±10	0.1	200	0.8±0.10	R
HMK107 SD102KA-T				1000 p	±10	0.1	200	0.8±0.10	R

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[Temperature Characteristic BJ :  $B(-25 \sim +85^{\circ}C)/X5R(-55 \sim +85^{\circ}C)$ ] 1.25mm thickness(G)

Part number 1	Part number 2	Rated voltage	Tempe	erature	Capacitance	Capacitance tolerance	$ an\delta$	HTLT	Thickness*3 [mm]	Soldering R:Reflow
i ai c ilumber i	Tart Humber 2	[V]	charact	eristics	[F]	[%]	[%]	Rated voltage x %	Thickness [mm]	W:Wave
HMK212 BJ103∏G-T			В	X5R*1	0.01 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ153∏G-T			В	X5R*1	0.015 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ223∏G-T			В	X5R*1	0.022 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ333∏G-T			В	X5R*1	0.033 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ473∏G-T		100	В	X5R*1	0.047 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ683∏G-T		100	В	X5R*1	0.068 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ104∏G-T			В	X5R*1	0.1 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ224∏G-T			В	X5R*1	0.22 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 BJ474∏G-TE		]	В	X5R*1	0.47 μ	±10, ±20	3.5	150	1.25±0.10	R
HMK212BBJ105∏G-TE		]	В	X5R*1	1 μ	±10, ±20	3.5	150	1.25+0.20/-0	R
QMK212 BJ472 G-T			В	X5R*1	4700 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 BJ682[]G-T			В	X5R*1	6800 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 BJ103[]G-T		250	В	X5R*1	0.01 μ	±10, ±20	2.5	150	1.25±0.10	R
QMK212 BJ153[]G-T		] [	В	X5R*1	0.015 μ	±10, ±20	2.5	150	1.25±0.10	R
QMK212 BJ223[]G-T			В	X5R*1	0.022 μ	±10, ±20	2.5	150	1.25±0.10	R

## [Temperature Characteristic BJ : $B(-25 \sim +85^{\circ}C)/X5R(-55 \sim +85^{\circ}C)$ ] 0.85mm thickness(D)

Part number 1	Part number 2	Rated voltage [V]		erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
QMK212 BJ102[]D-T			В	X5R*1	1000 p	±10, ±20	2.5	150	0.85±0.10	R
QMK212 BJ152[]D-T		250	В	X5R*1	1500 p	±10, ±20	2.5	150	0.85±0.10	R
QMK212 BJ222∏D-T		230	В	X5R*1	2200 p	±10, ±20	2.5	150	0.85±0.10	R
QMK212 BJ332∏D-T			В	X5R*1	3300 р	±10, ±20	2.5	150	0.85±0.10	R

## [Temperature Characteristic C7 : X7S ( $-55 \sim +125 ^{\circ}$ C)] 1.25mm thickness(G)

Part number 1	Part number 2	Rated voltage [V]	Temper characte		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK212 C7474∏G-TE		100		X7S	0.47 μ	±10, ±20	3.5	150	1.25±0.10	R
HMK212BC7105[]G-TE		100		X7S	1 μ	±10, ±20	3.5	150	1.25+0.20/-0	R

## [Temperature Characteristic B7 : $X7R(-55\sim+125^{\circ}C)$ ] 1.25mm thickness(G)

Part number 1	Part number 2	Rated voltage	Temperature	Capacitance	Capacitance tolerance	tan δ	HTLT	*3 - 3	Soldering R:Reflow
Part number I	Part number 2	[V]	characteristics	[F]	[%]	[%]	Rated voltage x %	Thickness*3 [mm]	W:Wave
HMK212 B7103[]G-T			X7R	0.01 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7153[]G-T			X7R	0.015 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7223[]G-T			X7R	0.022 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7333∏G-T		100	X7R	0.033 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7473[]G-T		100	X7R	0.047 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7683∏G-T			X7R	0.068 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7104[]G-T			X7R	0.1 μ	±10, ±20	3.5	200	1.25±0.10	R
HMK212 B7224∏G-T			X7R	0.22 μ	±10, ±20	3.5	200	1.25±0.10	R
QMK212 B7472 G-T			X7R	4700 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7682 G-T			X7R	6800 p	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7103[]G-T		250	X7R	0.01 μ	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7153[]G-T			X7R	0.015 μ	±10, ±20	2.5	150	1.25±0.10	R
QMK212 B7223[]G-T			X7R	0.022 μ	±10, ±20	2.5	150	1.25±0.10	R

## [Temperature Characteristic B7 : $X7R(-55 \sim +125 ^{\circ}C)$ ] 0.85mm thickness(D)

Part number 1	Part number 2	Rated voltage [V]	Temper characte		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
QMK212 B7102[]D-T				X7R	1000 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 B7152□D-T		250		X7R	1500 p	±10, ±20	2.5	150	$0.85 \pm 0.10$	R
QMK212 B7222 D-T		230		X7R	2200 p	±10, ±20	2.5	150	0.85±0.10	R
QMK212 B7332[]D-T				X7R	3300 р	±10, ±20	2.5	150	0.85±0.10	R

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## [Temperature Characteristic SD : Standard $(-55 \sim +125 ^{\circ}\text{C})$ ] 0.85mm thickness(D)

		Rated voltage	Temperature	Capacitance	Capacitance tolerance	tan δ	HTLT	42	Soldering
Part number 1	Part number 2	[V]	characteristics	[F]	[%]	Fo/ 1	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
HMK212 SD222KD-T		100		2200 p	±10	0.1	200	$0.85 \pm 0.10$	R
HMK212 SD472KD-T		100		4700 p	±10	0.1	200	$0.85 \pm 0.10$	R
QMK212 SD101KD-T				100 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD121KD-T				120 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD151KD-T				150 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD181KD-T				180 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD221KD-T			Standard Type	220 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD331KD-T		250	Standard Type	330 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD391KD-T		230		390 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD471KD-T				470 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD561KD-T				560 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD681KD-T				680 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD821KD-T				820 p	±10	0.1	150	$0.85 \pm 0.10$	R
QMK212 SD102KD-T				1000 p	±10	0.1	150	$0.85 \pm 0.10$	R

## [Temperature Characteristic SD : Standard $(-55 \sim +125 ^{\circ}\text{C})$ ] 1.25mm thickness (G)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave	
HMK212 SD392KG-T		100	Standard Type	3900 р	±10	0.1	200	1.25±0.10	R	

## 316TYPE

## [Temperature Characteristic BJ : $B(-25 \sim +85^{\circ}C)/X5R(-55 \sim +85^{\circ}C)$ ] 1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage		rature	Capacitance	Capacitance tolerance	tan δ	HTLT	Thickness*3 [mm]	Soldering R:Reflow
		[V]	charact	eristics	[F]	[%]	[%]	Rated voltage x %		W:Wave
HMK316 BJ473[]L-T			В	X5R*1	0.047 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ683[]L-T			В	X5R*1	0.068 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ104□L-T			В	X5R*1	0.1 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ154[]L-T			В	X5R*1	0.15 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ224[]L-T		100	В	X5R*1	0.22 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ334[]L-T			В	X5R*1	0.33 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ474[]L-T			В	X5R*1	0.47 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 BJ105[]L-T			В	X5R*1	1 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316ABJ225□L-TE			В	X5R*1	2.2 μ	±10, ±20	3.5	150	1.6±0.20	R
QMK316 BJ333[]L-T			В	X5R*1	0.033 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 BJ473[L-T		250	В	X5R*1	0.047 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 BJ683□L-T		230	В	X5R*1	0.068 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 BJ104[]L-T			В	X5R*1	0.1 μ	±10, ±20	2.5	150	1.6±0.20	R
SMK316 BJ153□L-T		630	В	X5R*1	0.015 μ	±10, ±20	2.5	120	1.6±0.20	R
SMK316 BJ223□L-T		030	В	X5R*1	0.022 μ	±10, ±20	2.5	120	1.6±0.20	R

## $\begin{tabular}{l} \textbf{[} Temperature Characteristic BJ: B(-25 \sim +85 ^{\circ}C)/X5R(-55 \sim +85 ^{\circ}C) \end{tabular} \begin{tabular}{l} 1.15mm thickness(F) \end{tabular} \label{tabular}$

Part number 1	Part number 2	Rated voltage	Tempe	erature	Capacitance	Capacitance tolerance	$ an\delta$	HTLT	Thickness*3 [mm]	Soldering R:Reflow
Fart number 1	Fart number 2	[V]	charact	teristics	[F]	[%]	[%]	Rated voltage x %	Inickness [mm]	W:Wave
SMK316 BJ102∏F-T			В	X5R*1	1000 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ152∏F-T			В	X5R*1	1500 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ222∏F-T			В	X5R*1	2200 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ332∏F-T		630	В	X5R*1	3300 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ472∏F-T			В	X5R*1	4700 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ682∏F-T			В	X5R*1	6800 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 BJ103∏F-T			В	X5R*1	0.01 μ	±10, ±20	2.5	120	1.15±0.10	R

## [Temperature Characteristic C7 : X7S( $-55 \sim +125 ^{\circ}$ C)] 1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage [V]	Tempe charact		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK316AC7225□L-TE		100		X7S	2.2 μ	±10, ±20	3.5	150	1.6±0.20	R

## [Temperature Characteristic B7 : X7R( $-55 \sim +125 ^{\circ}$ C)] 1.6mm thickness(L)

Part number 1	Part number 2	Rated voltage	Temperature	Capacitance	Capacitance tolerance	$ an\delta$	HTLT	Thickness*3 [mm]	Soldering R:Reflow
T di t Humber 1	T art Humber 2	[V]	characteristics	[F]	[%]	[%]	Rated voltage x %	mickness [mm]	W:Wave
HMK316 B7473[]L-T			X7R	0.047 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7683[]L-T			X7R	0.068 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7104[]L-T			X7R	0.1 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7154[]L-T		100	X7R	0.15 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7224[]L-T		100	X7R	0.22 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7334[]L-T			X7R	0.33 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7474∏L-T			X7R	0.47 μ	±10, ±20	3.5	200	1.6±0.20	R
HMK316 B7105∏L-T			X7R	1 μ	±10, ±20	3.5	200	1.6±0.20	R
QMK316 B7333∏L-T			X7R	0.033 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 B7473□L-T		250	X7R	0.047 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 B7683□L-T		230	X7R	0.068 μ	±10, ±20	2.5	150	1.6±0.20	R
QMK316 B7104 L-T			X7R	0.1 μ	±10, ±20	2.5	150	1.6±0.20	R
SMK316 B7153[]L-T			X7R	0.015 μ	±10, ±20	2.5	120	1.6±0.20	R
SMK316 B7223[]L-T		630	X7R	0.022 μ	±10, ±20	2.5	120	1.6±0.20	R
SMK316AB7333[]L-T		030	X7R	0.033 μ	±10, ±20	2.5	120	1.6±0.20	R
SMK316AB7473[]L-T			X7R	0.047 μ	±10, ±20	2.5	120	1.6±0.20	R

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## [Temperature Characteristic B7 : $X7R(-55 \sim +125^{\circ}C)$ ] 1.15mm thickness(F)

D : 1 1	D	Rated voltage	Tempe	rature	Capacitance	Capacitance tolerance	tan δ	HTLT	*3 = 3	Soldering
Part number 1	Part number 2	[V]	charact	eristics	[F]	[%]	[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
SMK316 B7102[F-T				X7R	1000 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 B7152∏F-T				X7R	1500 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 B7222[F-T				X7R	2200 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 B7332∏F-T		630		X7R	3300 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 B7472[F-T				X7R	4700 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 B7682[F-T				X7R	6800 p	±10, ±20	2.5	120	1.15±0.10	R
SMK316 B7103[F-T				X7R	0.01 μ	±10, ±20	2.5	120	1.15±0.10	R

## [Temperature Characteristic SD : Standard $(-55 \sim +125 ^{\circ}\text{C})$ ] 1.6mm thickness (L)

Part	number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK316 SE	D223KL-T		100	Standard Type	0.022 μ	±10	0.1	200	1.6±0.20	R
QMK316 SE	D103KL-T		250	Standard Type	0.01 μ	±10	0.1	150	1.6±0.20	R

## ■325TYPE

## [Temperature Characteristic BJ : $B(-25 \sim +85^{\circ}C)/X5R(-55 \sim +85^{\circ}C)$ ] 2.5mm thickness(M)

Part number 1	Part number 2	Rated voltage [V]		erature teristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK325 BJ225∏M-P		100	В	X5R*1	2.2 μ	±10, ±20	3.5	200	2.5±0.20	R
HMK325 BJ475∏M-PE		100	В	X5R*1	4.7 μ	±10, ±20	3.5	150	2.5±0.20	R

## [Temperature Characteristic BJ : $B(-25 \sim +85^{\circ}C)/X5R(-55 \sim +85^{\circ}C)$ ] 1.9mm thickness(N)

B		Rated voltage	Tempe	erature	Capacitance	Capacitance tolerance	tan δ	HTLT	*3 = 3	Soldering
Part number 1	Part number 2	[V]	charact	teristics	[F]	[%]	[%]	Rated voltage x %	Thickness*3 [mm]	R:Reflow W:Wave
HMK325 BJ154□N-T			В	X5R*1	0.15 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ224□N-T			В	X5R*1	0.22 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ334∏N-T			В	X5R*1	0.33 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ474□N-T		100	В	X5R*1	0.47 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ684∏N-T			В	X5R*1	0.68 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ105∏N-T			В	X5R*1	1 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 BJ475□N-TE			В	X5R*1	4.7 μ	±10, ±20	3.5	150	1.9±0.20	R
QMK325 BJ473[N-T			В	X5R*1	0.047 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 BJ104[]N-T		250	В	X5R*1	0.1 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 BJ154[]N-T		250	В	X5R*1	0.15 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 BJ224[]N-T			В	X5R*1	0.22 μ	±10, ±20	2.5	150	1.9±0.20	R
SMK325 BJ223∏N-T			В	X5R*1	0.022 μ	±10, ±20	2.5	120	1.9±0.20	R
SMK325 BJ333∏N-T		630	В	X5R*1	0.033 μ	±10, ±20	2.5	120	1.9±0.20	R
SMK325 BJ473[]N-T			В	X5R*1	0.047 μ	±10, ±20	2.5	120	1.9±0.20	R

## $\begin{tabular}{l} \textbf{[} Temperature Characteristic BJ : B($-25$$$\sim$+85$$°C)/X5R($-55$$\sim$+85$$°C) \begin{tabular}{l} 1.15mm thickness(F) \end{tabular} \label{table_eq:eq:energy_property} \end{tabular}$

Part number 1	Part number 2	Rated voltage [V]	Tempe charact	erature eristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave	
HMK325 BJ104∏F-T	•	100	В	X5R*1	0.1 μ	±10, ±20	3.5	200	1.15±0.10	R	

## [Temperature Characteristic B7 : $X7R(-55 \sim +125^{\circ}C)$ ] 2.5mm thickness(M)

Part number 1	Part number 2	Rated voltage [V]	Temperatu characterist		Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK325 B7225[]M-P		100	X	7R 2.2 μ	±10, ±20	3.5	200	2.5±0.20	R

## [Temperature Characteristic B7 : X7R( $-55\sim+125^{\circ}$ C)] 1.9mm thickness(N)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK325 B7154∏N-T			X7R	0.15 μ	±10, ±20	3.5	200	1.9±0.20	R R
HMK325 B7224[]N-T			X7R	0.22 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7334[]N-T		100	X7R	0.33 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7474[]N-T		100	X7R	0.47 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7684[]N-T		]	X7R	0.68 μ	±10, ±20	3.5	200	1.9±0.20	R
HMK325 B7105[]N-T			X7R	1 μ	±10, ±20	3.5	200	1.9±0.20	R
QMK325 B7473[]N-T			X7R	0.047 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 B7104[]N-T		250	X7R	0.1 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 B7154[]N-T		230	X7R	0.15 μ	±10, ±20	2.5	150	1.9±0.20	R
QMK325 B7224[N-T			X7R	0.22 μ	±10, ±20	2.5	150	1.9±0.20	R
SMK325 B7223∏N-T			X7R	0.022 μ	±10, ±20	2.5	120	1.9±0.20	R
SMK325 B7333∏N-T		630	X7R	0.033 μ	±10, ±20	2.5	120	1.9±0.20	R
SMK325 B7473∏N-T			X7R	0.047 μ	±10, ±20	2.5	120	1.9±0.20	R

## 【Temperature Characteristic C7: X7S(−55~+125°C)】 2.5mm thickness(M)

L Tomporacaro onarac	700110010 07 . 7070 (	00 1120	O / 1 L.	OIIIIII CIII	101(11000 (111)					
Part number 1	Part number 2	Rated voltage [V]	Tempe charact		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK325 C7475∏M-PE		100		X7S	4.7 μ	±10, ±20	3.5	150	2.5±0.20	R

## [Temperature Characteristic C7 : X7S( $-55\sim+125^{\circ}$ C)] 1.9mm thickness(N)

Part number 1	Part number 2	Rated voltage [V]	Temperatu characterist		Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK325 C7475∏N-TE		100	Х	7S 4.7 μ	±10, ±20	3.5	150	1.9±0.20	R

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## MULTILAYER CERAMIC CAPACITORS

[Temperature Characteristic B7 :  $X7R(-55\sim+125^{\circ}C)$ ] 1.15mm thickness(F)

Part number 1	Part number 2	Rated voltage [V]		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
HMK325 B7104∏F-T		100	X7R	0.1 μ	±10, ±20	3.5	200	1.15±0.10	R

## 432TYPE

[Temperature Characteristic BJ :  $B(-25\sim+85^{\circ}C)/X5R(-55\sim+85^{\circ}C)$ ] 2.5mm thickness(M)

	Part number 1	Part number 2	Rated voltage	Tempe	erature	Capacitance	Capacitance tolerance	$ an\delta$	HTLT	Thickness*3 [mm]	R:Reflow
	Part number 1	Part number 2	[V]	charact	eristics	[F]	[%]	[%]	Rated voltage x %	Thickness [mm]	W:Wave
Н	IMK432 BJ474[]M-T			В	X5R*1	0.47 μ	±10, ±20	3.5	200	2.5±0.20	R
Н	IMK432 BJ105[]M−T		100	В	X5R*1	1 μ	±10, ±20	3.5	200	2.5±0.20	R
Н	IMK432 BJ155[]M−T		100	В	X5R*1	1.5 μ	±10, ±20	3.5	200	2.5±0.20	R
Н	IMK432 BJ225[]M−T			В	X5R*1	2.2 μ	±10, ±20	3.5	200	2.5±0.20	R
G	MK432 BJ104∏M-T			В	X5R*1	0.1 μ	±10, ±20	2.5	150	2.5±0.20	R
G	MK432 BJ224∏M-T		250	В	X5R*1	0.22 μ	±10, ±20	2.5	150	2.5±0.20	R
G	MK432 BJ334∏M-T		230	В	X5R*1	0.33 μ	±10, ±20	2.5	150	2.5±0.20	R
G	MK432 BJ474∏M-T			В	X5R*1	0.47 μ	±10, ±20	2.5	150	2.5±0.20	R
S	MK432 BJ473∏M-T			В	X5R*1	0.047 μ	±10, ±20	2.5	120	2.5±0.20	R
S	MK432 BJ683∏M-T		630	В	X5R*1	0.068 μ	±10, ±20	2.5	120	2.5±0.20	R
S	MK432 BJ104[M-T			В	X5R*1	0.1 μ	±10, ±20	2.5	120	2.5±0.20	R

[Temperature Characteristic B7 : X7R( $-55\sim+125^{\circ}$ C)] 2.5mm thickness(M)

Part number 1	Part number 2	Rated voltage [V]	Temperature characteristics	Capacitance [F]	Capacitance tolerance	tan δ [%]	HTLT	Thickness*3 [mm]	Soldering R:Reflow
		[4]	Criaracteristics	0.1	[70]	[70]	Rated voltage x %		W:Wave
HMK432 B7474[]M-T			X7R	0.47 μ	±10, ±20	3.5	200	$2.5 \pm 0.20$	R
HMK432 B7105[]M-T		100	X7R	1 μ	±10, ±20	3.5	200	$2.5 \pm 0.20$	R
HMK432 B7155∏M-T		100	X7R	1.5 μ	±10, ±20	3.5	200	$2.5 \pm 0.20$	R
HMK432 B7225∏M-T			X7R	2.2 μ	±10, ±20	3.5	200	$2.5 \pm 0.20$	R
QMK432 B7104[]M-T			X7R	0.1 μ	±10, ±20	2.5	150	$2.5 \pm 0.20$	R
QMK432 B7224□M-T		250	X7R	0.22 μ	±10, ±20	2.5	150	$2.5 \pm 0.20$	R
QMK432 B7334∏M-T		230	X7R	0.33 μ	±10, ±20	2.5	150	$2.5 \pm 0.20$	R
QMK432 B7474□M-T			X7R	0.47 μ	±10, ±20	2.5	150	$2.5 \pm 0.20$	R
SMK432 B7473 M-T			X7R	0.047 μ	±10, ±20	2.5	120	$2.5 \pm 0.20$	R
SMK432 B7683[]M-T		630	X7R	0.068 μ	±10, ±20	2.5	120	2.5±0.20	R
SMK432 B7104[]M-T			X7R	0.1 μ	±10, ±20	2.5	120	2.5±0.20	R

[Temperature Characteristic B7 :  $X7R(-55 \sim +125^{\circ}C)$ ] 2.0mm thickness(Y)

•										
Part number 1	Part number 2	Rated voltage [V]	Temperat characteri		Capacitance [F]	Capacitance tolerance [%]	tan δ [%]	HTLT Rated voltage x %	Thickness*3 [mm]	Soldering R:Reflow W:Wave
XMK432 B7472KY-TE		2000		X7R	4700 p	±10	2.5	110	2.0+0/-0.30	R

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## Multilayer Ceramic Capacitors

## ■PACKAGING

## 1 Minimum Quantity

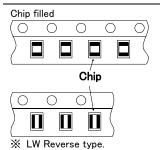
Taped package	TILL		0, 1, 1	F 3	
Type(EIA)	Thick mm	code	Paper tape	uantity [pcs] Embossed tape	
□MK021(008004)	0.125	K	- парет саре	50000	
□VS021(008004)	0.123	IX		30000	
☐MK042(01005)	0.2	C, D	_	40000	
□VS042(01005)	0.2	С	_	40000	
☐MK063(0201)	0.3	P,T	15000	_	
□WK105(0204) ※	0.3	Р	10000	_	
	0.13	Н	_	20000	
DM(105(0400)	0.18	E	_	15000	
☐MK105(0402) ☐MF105(0402)	0.2	С	20000	_	
MF 105(0402)	0.3	Р	15000	_	
	0.5	V	10000	_	
□VK105(0402)	0.5	W	10000	_	
□MK107(0603)	0.45	K	4000	_	
□WK107(0306) ※	0.5	V	_	4000	
□MF107(0603)	0.8	Α	4000	_	
□VS107(0603)	0.7	С	4000	_	
□MJ107(0603)	0.8	Α	3000	3000	
□MK212(0805)	0.45	K	4000		
□WK212(0508) ※	0.85	D	4000	_	
□MF212(0805)	1.25	G	_	3000	
□VS212(0805)	0.85	D	4000	_	
	0.85	D	4000	_	
□MJ212(0805)	1.25	G	_	2000	
	0.85	D	4000	_	
□MK316(1206)	1.15	F	_	3000	
□MF316(1206)	1.6	L	_	2000	
	1.15	F	_	3000	
□MJ316(1206)	1.6	L	_	2000	
	0.85	D			
	1.15	F	1		
☐MK325(1210)	1.9	N	1 -	2000	
□MF325(1210)	2.0max.	Y	1		
	2.5	M	_	1000	
[] 1 1005(1015)	1.9	N	_	2000	
□MJ325(1210)	2.5	М	_	500(T), 1000(P)	
□MK432(1812)	2.5	М	_	500	

Note: 

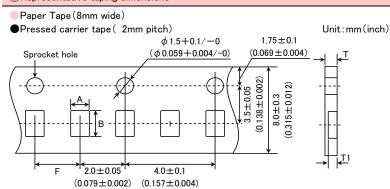
K LW Reverse type.

## \*\*No bottom tape for pressed carrier tape Card board carrier tape Top tape Base tape Sprocket hole Chip cavity Base tape Chip cavity

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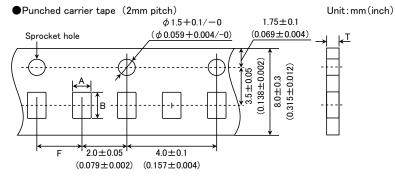
## 3 Representative taping dimensions



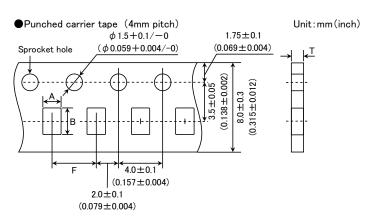
Type(EIA)	Chip Cavity		Insertion Pitch	Tape Thickness	
Type(EIA)	Α	В	F	Т	T1
□MK063(0201)	0.37	0.67		0.45max.	0.42max.
□WK105(0204) ※			2.0±0.05	0.45max.	0.42max.
□MK105(0402) (*1 C)	0.65	1.15	2.0±0.05	0.4max.	0.3max.
□MK105(0402) (*1 P)			0.45max.	0.42max.	

Note \*1 Thickness, C:0.2mm ,P:0.3mm. \* LW Reverse type.

Unit:mm



Type(EIA)	Chip	Cavity	Insertion Pitch	Tape Thickness
Type(EIA)	Α	В	F	Т
☐MK105 (0402)				
☐MF105 (0402)	0.65	1.15	$2.0 \pm 0.05$	0.8max.
□VK105 (0402)				
	•			Unit:mm

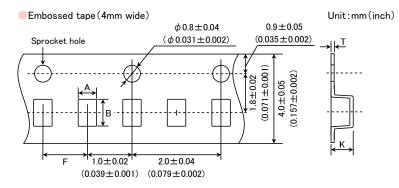


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Type(EIA)	Chip (	Cavity	Insertion Pitch	Tape Thickness
Type(EIA)	Α	В	F	Т
☐MK107(0603)				
□WK107(0306) ※	1.0	1.8		1.1max.
☐MF107(0603)			40+01	
☐MK212(0805)	1.65	0.4	4.0±0.1	
□WK212(0508) ※	1.65	2.4		1.1max.
☐MK316(1206)	2.0	3.6		

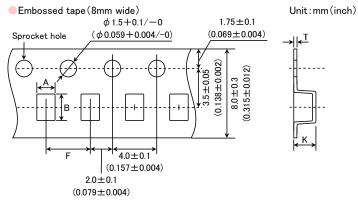
Note: Taping size might be different depending on the size of the product. X LW Reverse type.

Unit:mm



Type(EIA)	Chip Cavity		Insertion Pitch	Tape Thickness	
Type(EIA)	Α	В	F	K	Т
☐MK021(008004)	0.135	0.27	1.0±0.02	0.5max.	0.25max.
□VS021(008004)					
☐MK042(01005)	0.23	0.43			
□VS042(01005)					

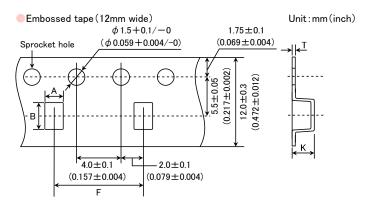
Unit:mm



Type(EIA)	Chip (	Cavity	Insertion Pitch	Tape Thickness	
Type(EIA)	Α	В	F	K	Т
☐MK105(0402)	0.6	1.1	2.0±0.1	0.6max	0.2±0.1
□WK107(0306) ※	1.0	1.8		1.3max.	0.25±0.1
☐MK212(0805) ☐MF212(0805)	1.65	2.4			
☐MK316(1206) ☐MF316(1206)	2.0	3.6	4.0±0.1	3.4max.	0.6max.
☐MK325(1210) ☐MF325(1210)	2.8	3.6			

Note: ※ LW Reverse type. Unit:mm

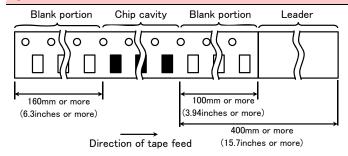
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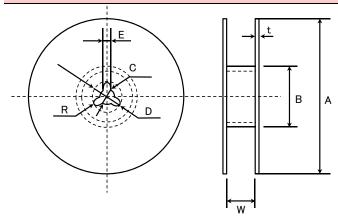
Type(EIA)	Chip Cavity		Insertion Pitch	Tape Thickness	
Type(EIA)	Α	В	F	K	Т
☐MK325(1210)	3.1	4.0	8.0±0.1	4.0max.	0.6max.
☐MK432(1812)	3.7	4.9	8.0±0.1	4.0max.	0.6max.

Unit:mm

## 4 Trailer and Leader



## ⑤Reel size



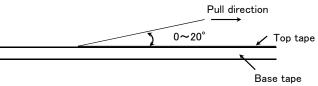
Α	В	С	D	E	R
$\phi$ 178 ± 2.0	<i>ф</i> 50min.	$\phi$ 13.0 $\pm$ 0.2	$\phi$ 21.0 ± 0.8	2.0±0.5	1.0

	T	W
4mm wide tape	1.5max.	5±1.0
8mm wide tape	2.5max.	10±1.5
12mm wide tape	2.5max.	14±1.5

Unit:mm

## **6**Top Tape Strength

The top tape requires a peel-off force of 0.1 to 0.7N in the direction of the arrow as illustrated below.



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## Medium-High Voltage Multilayer Ceramic Capacitor

## ■RELIABILITY DATA

1. Operating Tempe	rature Range				
	Temperature Compensating(Class1)	CG : -55 to +125°C			
		X7R, X7S : -55 to +125°C			
Specified Value	High Permittivity (Class2)	X5R : -55 to +85°C			
	(e.ass_/	B : -25 to +85°C			
		SD : −55 to +125°C			
2. Storage Tempera	ture Range				
	Temperature Compensating(Class1)	CG : -55 to +125°C			
		X7R, X7S : −55 to +125°C			
Specified Value	High Permittivity (Class2)	X5R : −55 to +85°C			
	, (	B : -25 to +85°C			
		SD : −55 to +125°C			
3. Rated Voltage					
Specified Value	Temperature Compensating(Class1)	100VDC(HMK)			
Specified value	High Permittivity (Class2)	100VDC(HMK), 250VDC(QMK), 630VDC(SMK), 2000VDC(XMK)			
4 Withstanding Volt	age (Between terminals)				
Specified Value	No breakdown or damage				
		×2.5(HMK), Rated voltage ×2(QMK), Rated voltage ×1.2(SMK)(XMK)			
Test Methods and	Duration : 1 to 5sec.				
Remarks	Charge/discharge current : 50mA max.				
5. Insulation Resista		-			
Specified Value	Temperature Compensating(Class1)	10000 MΩ min.			
·	High Permittivity (Class2)	100M $\Omega$ • $\mu$ F or 10G $\Omega$ , whichever is smaller.			
Test Methods and	Applied voltage : Rated voltage (HMK, QMK), 500V (SMK,XMK)				
Remarks	Duration : 60±5sec.  Charge/discharge current : 50mA max.				
	Onarge/ discharge current . John max.				
6. Capacitance(To	lerance)				
		0.2pF≦C≦5pF : ±0.25pF			
Specified Value	Temperature Compensating(Class1)	0.2pF≦C≦10pF : ±0.5pF			
opcomed value		C>10pF : ±5% or ±10%			
	High Permittivity (Class2)	±10%, ±20%			
	Temperature Compensating(Class1)	Measuring frequency : 1MHz±10%  Measuring voltage : 0.5∼5Vrms			
Test Methods and	Temperature Compensating (Class I)	Measuring voltage : 0.5∼5Vrms Bias application : None			
Remarks		Measuring frequency : 1kHz±10%			
	High Permittivity (Class2)	Measuring voltage : 1±0.2Vrms			
		Bias application : None			
7. Q or Dissipation	Factor				
		C<30pF: Q≧400+20C			
Specified Value	Temperature Compensating(Class1)	C≧30pF : Q≧1000 (C:Nominal capacitance)			
	High Permittivity (Class2)	3.5%max(HMK),2.5%max(QMK, SMK)			
	T	Measuring frequency : 1MHz±10%			
Took Made and	Temperature Compensating(Class1)	Measuring voltage : 0.5∼5Vrms Bias application : None			
Test Methods and					
Remarks		I Measuring frequency : 1kHz ± 10%			
Remarks	High Permittivity (Class2)	Measuring frequency : 1kHz±10%  Measuring voltage : 1±0.2Vrms			

<sup>►</sup> This catalog contains the typical specification only due to the limitation of space. When you consider the purchase of our products, please check our specification. For details of each product (characteristics graph, reliability information, precautions for use, and so on), see our Web site (http://www.ty-top.com/).

8. Temperature Ch	Temperature Compensating (Class1)	CG :0±30ppm/°C(-55 to +125°C)
Specified Value	High Permittivity (Class2)	B : $\pm 10\%(-25 \text{ to } +85^{\circ}\text{C})$ X5R : $\pm 15\%(-55 \text{ to } +85^{\circ}\text{C})$ X7R : $\pm 15\%(-55 \text{ to } +125^{\circ}\text{C})$ X7S : $\pm 22\%(-55 \text{ to } +125^{\circ}\text{C})$ SD : $-(-55 \text{ to } +125^{\circ}\text{C})$
	Class 1 Capacitance at 20°C and 85°C shall be mediately following equation. $\frac{(C_{85}-C_{20})}{C_{20}\times\Delta T} \times 10^6 (\text{ppm/°C})$	easured in thermal equilibrium, and the temperature characteristic shall be calculated from the $\Delta T {=} 65$

Test Methods and Remarks

Capacitance at each step shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation.

0 1				
Step	В	X5R, X7R, X6S, X7S		
1	Minimum operating temperature			
2	20°C	25°C		
3	Maximum operating temperature			

$$\frac{(C-C_2)}{C_2} \times 100(\%)$$

C : Capacitance value in Step 1 or Step 3

Capacitance measurement shall be conducted with the board bent.

C2: Capacitance value in Step 2

9. Deflection	
Specified Value	Temperature Compensating(Class1)  Appearance : No abnormality Capacitance change : Within ±5% or ±0.5 pF, whichever is larger.
opeomed value	High Permittivity (Class2)  Appearance : No abnormality Capacitance change : Within±10%
Test Methods and Remarks	Warp : 1mm Duration : 10sec. Test board : Glass epoxy-resin substrate Thickness : 1.6mm  Board  Warp  Warp  (Unit: mm)

10. Adhesive Strength of Terminal Electrodes

Specified Value

Temperature Compensating(Class1)
High Permittivity (Class2)

No terminal separation or its indication.

Applied force : 5N

Test Methods and	Applied force : 5N				
Remarks	Duration : 30±5sec.				
11. Solderability					
C:::::	Temperature Compensating(	Class1)	A.L 05% C.L		
Specified Value	High Permittivity (Class2)		At least 95% of terminal electrode is covered by new solder		
		Eutecti	c solder	Lead-free solder	7
Test Methods and	Solder type	H60A d	or H63A	Sn-3.0Ag-0.5Cu	
Remarks	Solder temperature	230	±5°C	245±3°C	
	Duration		4±1 sec.		

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12. Resistance to S	oldering				
0 15 11/1	Temperature Compensati	Appearance Capacitance change Q Insulation resistance Withstanding voltage	: Within : Initial : Initial		
Specified Value	High Permittivity (Class2)		Appearance Capacitance change Dissipation facto Insulation resistance Withstanding voltage	: No abnormality : Within±15%(HMK), ±10%(QMK, SMK,XMK) : Initial value : Initial value (between terminals) : No abnormality	
		Temperatu	re Compensating(Class1)		
	Preconditioning	None			
	Solder temperature	270±5°C			
	Duration	3±0.5sec.			
	Preheating conditions	80 to 100°C, 2 to 5 min. 150 to 200°C, 2 to 5min.			
Test Methods and	Recovery	24±2hrs under the standard condition Note3			
Remarks		1	2 '11' '1 (01 0)		1
	D 1911		Permittivity (Class2)		
	Preconditioning		(at 150°C for 1hr) Note1		
	Solder temperature	270±5°C			
	Duration	3±0.5sec.			
	Preheating conditions	80 to 100°C, 2 to 5			
		150 to 200°C, 2 to			
	Recovery	24±2hrs under the	e standard condition Note	3	

13. Temperature Cy	cle (Thermal Shock)					
Specified Value	Temperature Compen	sating(Class1)	Appearance Capacitance change Q Insulation resistance Withstanding voltage	: Initial value : Initial value	±0.25pF, whichev	•
Specified value	High Permittivity(Cla	ass2)	Appearance Capacitance change Dissipation facto Insulation resistance Withstanding voltage	: No abnormality : Within±15%(HMK), ±10%(QMK, SMK,XMK) : Initial value : Initial value (between terminals) : No abnormality		
			Class 1		Class 2	
	Preconditioning		None	Thermal treat	ment (at 150°C fo	or 1 hr) Note 1
		Step	Temperati	ure(°C)	Time (min.)	
T . M .! ! !		1	Minimum operatir	ng temperature	30±3	
Test Methods and	1 cycle	2	Normal tem	perature	2 to 3	
Remarks		3	Maximum operatin	g temperature	30±3	
		4	Normal tem	perature	2 to 3	
	Number of cycles		5	times		-
	Recovery	6 to 24 hrs (Sta	ndard condition) Note 3	24±2 hrs	Standard conditi	on) Note 3

14. Humidity (Stea	dy state)			
Specified Value	Temperature Compensating(Class1)		Appearance Capacitance chang Q Insulation resistance	: $C < 10pF$ : $Q \ge 200 + 10C$ $10 \le C < 30pF$ : $Q \ge 275 + 2.5C$ $C \ge 30pF$ : $Q \ge 350$ (C: Nominal capacitance)
	High Permittivity (Class2)		Appearance Capacitance chang Dissipation factor Insulation resistance	: 7%max(HMK), 5%max(QMK, SMK, XMK).
		Class	1	Class 2
	Preconditioning	None		Thermal treatment( at 150°C for 1 hr) Note 1
Test Methods and	Temperature	40±2°	С	40±2°C
Remarks	Humidity	90 to 95%	6RH	90 to 95%RH
	Duration	500+24/-	-0 hrs	500+24/-0 hrs
	Recovery	6 to 24 hrs (Standard	condition) Note 3	24±2 hrs (Standard condition) Note 3

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15. Humidity Loadin	g						
Specified Value	Temperature Comper	nsating(Class1)	Appearance Capacitance change Q Insulation resistance	: No abnormality : Within $\pm 7.5\%$ or $\pm 0.75$ pF, whichever is larger (HMK). : $C < 30$ pF: $Q \ge 100 + 10$ C/3 $C \ge 30$ pF: $Q \ge 200$ (C: Nominal capacitance) : $500 \text{ M}\Omega$ min.			
	High Permittivity (Class2)		Appearance Capacitance change Dissipation factor Insulation resistance	: No abnormality : Within $\pm$ 15% : 7%max (HMK), 5%max (QMK, SMK, XMK). : 10M $\Omega$ $\mu$ F or 500M $\Omega$ , whichever is smaller.			
	According to JIS 5101-1.						
		Cla	ass 1	Class 2			
	Preconditioning	N	lone	Voltage treatment (Rated voltage are applied for 1 hour at 40°C) Note 2			
	Temperature	40±2°C		40±2°C			
Test Methods and	Humidity	90 to	95%RH	90 to 95%RH			
Remarks	Duration	500+2	4/-0 hrs	500+24/-0 hrs			
	Applied voltage	Rated	l voltage	Rated voltage			
	Charge/discharge current	50m	A max.	50mA max.			
	Recovery	6 to 24 hrs (Stand	ard condition)Note 3	24±2 hrs(Standard condition) Note 3			

16. High Temperature Loading							
Specified Value	Temperature Compensating(Class1)		Appearance Capacitance cha Q Insulation resista	: $C < 30pF: Q \ge 100 + 10C/3$ $C \ge 30pF: Q \ge 200$ (C:Nominal capacitance)			
	High Permittivity(Class2)		Appearance Capacitance cha Dissipation facto Insulation resista	tor : 7%max(HMK), 5%max(QMK, SMK, XMK).			
	According to JIS 5101-1.						
		Class 1		Class 2			
	Preconditioning	None		Voltage treatment Note 2			
	Temperature	Maximum operating temperature		Maximum operating temperature			
Test Methods and	Duration	1000+48/-	·0 hrs	1000+48/-0  hrs			
Remarks	Applied voltage	Rated voltage ×	2(HMK)	Rated voltage × 2(HMK), Rated voltage × 1.5 (QMK), Rated voltage × 1.2 (SMK,XMK)			
	Charge/discharge current	50mA ma	ıx.	50mA max.			
	Recovery	6 to 24hr (Standard co	ondition) Note 3	24±2 hrs (Standard condition) Note 3			

Note1 Thermal treatment : Initial value shall be measured after test sample is heat-treated at  $150 \pm 0/-10^{\circ}$ C for an hour and kept at room temperature for  $24 \pm 2$ hours.

Note2 Voltage treatment: Initial value shall be measured after test sample is voltage—treated for an hour at both the temperature and voltage specified in the test conditions, and kept at room temperature for 24±2hours.

Note3 Standard condition : Temperature: 5 to  $35^{\circ}$ C, Relative humidity: 45 to 85 % RH, Air pressure: 86 to 106kPa

When there are questions concerning measurement results, in order to provide correlation data, the test shall be conducted under the following condition.

Temperature:  $20\pm2^{\circ}$ C, Relative humidity: 60 to 70 % RH, Air pressure: 86 to 106kPa Unless otherwise specified, all the tests are conducted under the "standard condition".

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## Precautions on the use of Multilayer Ceramic Capacitors

## PRECAUTIONS

## 1. Circuit Design

- ◆ Verification of operating environment, electrical rating and performance
  - 1. A malfunction of equipment in fields such as medical, aerospace, nuclear control, etc. may cause serious harm to human life or have severe social ramifications.

Therefore, any capacitors to be used in such equipment may require higher safety and reliability, and shall be clearly differentiated from them used in general purpose applications.

## Precautions

- ◆Operating Voltage (Verification of Rated voltage)
  - 1. The operating voltage for capacitors must always be their rated voltage or less.
    - If an AC voltage is loaded on a DC voltage, the sum of the two peak voltages shall be the rated voltage or less.
    - For a circuit where an AC or a pulse voltage may be used, the sum of their peak voltages shall also be the rated voltage or less.
  - 2. Even if an applied voltage is the rated voltage or less reliability of capacitors may be deteriorated in case that either a high frequency AC voltage or a pulse voltage having rapid rise time is used in a circuit.

## 2. PCB Design

Precautions

- ◆Pattern configurations (Design of Land-patterns)
- 1. When capacitors are mounted on PCBs, the amount of solder used (size of fillet) can directly affect the capacitor performance. Therefore, the following items must be carefully considered in the design of land patterns:
  - (1) Excessive solder applied can cause mechanical stresses which lead to chip breaking or cracking. Therefore, please consider appropriate land-patterns for proper amount of solder.
  - (2) When more than one component are jointly soldered onto the same land, each component's soldering point shall be separated by solder-resist.
- ◆Pattern configurations (Capacitor layout on PCBs)

After capacitors are mounted on boards, they can be subjected to mechanical stresses in subsequent manufacturing processes (PCB cutting, board inspection, mounting of additional parts, assembly into the chassis, wave soldering of the boards, etc.). For this reason, land pattern configurations and positions of capacitors shall be carefully considered to minimize stresses.

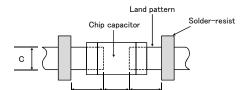
◆Pattern configurations (Design of Land-patterns)

The following diagrams and tables show some examples of recommended land patterns to prevent excessive solder amounts.

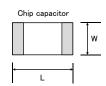
- (1) Recommended land dimensions for typical chip capacitors
- Multilayer Ceramic Capacitors : Recommended land dimensions (unit: mm)

Wave-soldering

Ту	Type 107 212		316	325	
Size	┙	1.6	2.0	3.2	3.2
	W	0.8	1.25	1.6	2.5
A	4	0.8 to 1.0	1.0 to 1.4	1.8 to 2.5	1.8 to 2.5
В		0.5 to 0.8	0.8 to 1.5	0.8 to 1.7	0.8 to 1.7
С		0.6 to 0.8	0.9 to 1.2	1.2 to 1.6	1.8 to 2.5
С		0.6 to 0.8	0.9 to 1.2	1.2 to 1.6	1.8 to 2.5



Land patterns for PCBs



## Technical considerations

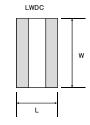
## Reflow-soldering

110	Tellow Soldering									
Ту	ре	021	042	063	105	107	212	316	325	432
Size	L	0.25	0.4	0.6	1.0	1.6	2.0	3.2	3.2	4.5
Size	W	0.125	0.2	0.3	0.5	0.8	1.25	1.6	2.5	3.2
A	4	0.095~0.135	0.15~0.25	0.20~0.30	0.45~0.55	0.6~0.8	0.8~1.2	1.8~2.5	1.8~2.5	2.5~3.5
E	3	0.085~0.125	0.10~0.20	0.20~0.30	0.40~0.50	0.6~0.8	0.8~1.2	1.0~1.5	1.0~1.5	1.5~1.8
(	)	0.110~0.150	0.15~0.30	0.25~0.40	0.45~0.55	0.6~0.8	0.9~1.6	1.2~2.0	1.8~3.2	2.3~3.5

 $Note: Recommended \ land \ size \ might be \ different \ according \ to \ the \ allowance \ of \ the \ size \ of \ the \ product.$ 

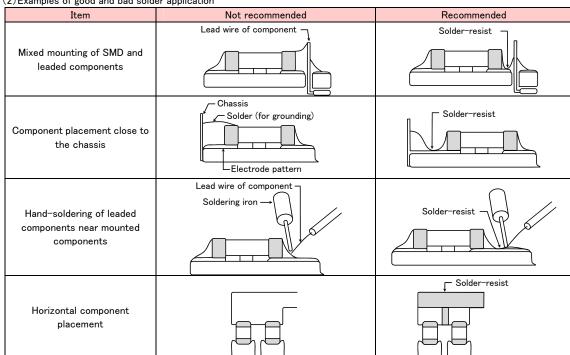
## ●LWDC: Recommended land dimensions for reflow-soldering (unit: mm)

,,						
Туре		105	107	212		
C:==	┙	0.52	0.8	1.25		
Size	W	1.0	1.6	2.0		
-	4	0.18~0.22	0.25~0.3	0.5~0.7		
Е	3	0.2~0.25	0.3~0.4	0.4~0.5		
С		0.9~1.1	1.5~1.7	1.9~2.1		



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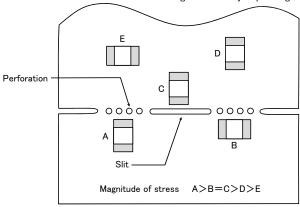
(2) Examples of good and bad solder application



- ◆Pattern configurations (Capacitor layout on PCBs)
  - 1-1. The following is examples of good and bad capacitor layouts; capacitors shall be located to minimize any possible mechanical stresses from board warp or deflection.

Ītems	Not recommended	Recommended
Deflection of board		Place the product at a right angle to the direction of the anticipated mechanical stress.

1-2. The amount of mechanical stresses given will vary depending on capacitor layout. Please refer to diagram below.



1-3. When PCB is split, the amount of mechanical stress on the capacitors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: push-back, slit, V-grooving, and perforation. Thus, please consider the PCB, split methods as well as chip location.

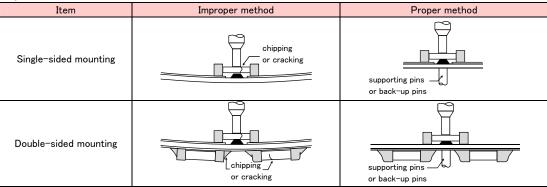
## 3. Mounting

- ◆Adjustment of mounting machine
  - 1. When capacitors are mounted on PCB, excessive impact load shall not be imposed on them.
  - 2. Maintenance and inspection of mounting machines shall be conducted periodically.
- ◆Selection of Adhesives Precautions
  - 1. When chips are attached on PCBs with adhesives prior to soldering, it may cause capacitor characteristics degradation unless the following factors are appropriately checked: size of land patterns, type of adhesive, amount applied, hardening temperature and hardening period. Therefore, please contact us for further information.

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## ◆Adjustment of mounting machine

- 1. When the bottom dead center of a pick-up nozzle is too low, excessive force is imposed on capacitors and causes damages. To avoid this, the following points shall be considerable.
  - (1) The bottom dead center of the pick-up nozzle shall be adjusted to the surface level of PCB without the board deflection.
  - (2) The pressure of nozzle shall be adjusted between 1 and 3 N static loads.
  - (3) To reduce the amount of deflection of the board caused by impact of the pick-up nozzle, supporting pins or back-up pins shall be used on the other side of the PCB. The following diagrams show some typical examples of good and bad pick-up nozzle placement:



## Technical considerations

2. As the alignment pin is worn out, adjustment of the nozzle height can cause chipping or cracking of capacitors because of mechanical impact on the capacitors.

To avoid this, the monitoring of the width between the alignment pins in the stopped position, maintenance, check and replacement of the pin shall be conducted periodically.

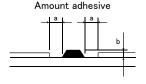
## ◆Selection of Adhesives

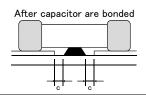
Some adhesives may cause IR deterioration. The different shrinkage percentage of between the adhesive and the capacitors may result in stresses on the capacitors and lead to cracking. Moreover, too little or too much adhesive applied to the board may adversely affect components. Therefore, the following precautions shall be noted in the application of adhesives.

- (1) Required adhesive characteristics
  - a. The adhesive shall be strong enough to hold parts on the board during the mounting & solder process.
  - b. The adhesive shall have sufficient strength at high temperatures.
  - c. The adhesive shall have good coating and thickness consistency.
  - d. The adhesive shall be used during its prescribed shelf life.
  - e. The adhesive shall harden rapidly.
  - f. The adhesive shall have corrosion resistance.
  - g. The adhesive shall have excellent insulation characteristics.
  - h. The adhesive shall have no emission of toxic gasses and no effect on the human body.
- (2) The recommended amount of adhesives is as follows;

[Recommended condition]

a 0.3mm min b 100 to 120 μ m c Adhesives shall not contact land	Figure	212/316 case sizes as examples
	а	0.3mm min
c Adhesives shall not contact land	b	100 to 120 $\mu$ m
	С	Adhesives shall not contact land





## 4. Soldering

Precautions

Technical

considerations

## ◆Selection of Flux

Since flux may have a significant effect on the performance of capacitors, it is necessary to verify the following conditions prior to use;

- (1) Flux used shall be less than or equal to 0.1 wt%( in CI equivalent) of halogenated content. Flux having a strong acidity content shall not be applied.
- (2) When shall capacitors are soldered on boards, the amount of flux applied shall be controlled at the optimum level.
- (3) When water-soluble flux is used, special care shall be taken to properly clean the boards.

## **♦**Soldering

Temperature, time, amount of solder, etc. shall be set in accordance with their recommended conditions.

Sn-Zn solder paste can adversely affect MLCC reliability.

For details of each product (characteristics graph, reliability information, precautions for use, and so on), see our Web site (http://www.ty-top.com/) .

Please contact us prior to usage of Sn-Zn solder.

## ◆Selection of Flux

- 1-1. When too much halogenated substance (Chlorine, etc.) content is used to activate flux, or highly acidic flux is used, it may lead to corrosion of terminal electrodes or degradation of insulation resistance on the surfaces of the capacitors.
- 1-2. Flux is used to increase solderability in wave soldering. However if too much flux is applied, a large amount of flux gas may be emitted and may adversely affect the solderability. To minimize the amount of flux applied, it is recommended to use a flux-bubbling system.
- 1-3. Since the residue of water-soluble flux is easily dissolved in moisture in the air, the residues on the surfaces of capacitors in high humidity conditions may cause a degradation of insulation resistance and reliability of the capacitors. Therefore, the cleaning methods and the capability of the machines used shall also be considered carefully when water-soluble flux is used.

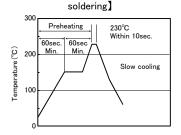
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## **♦**Soldering

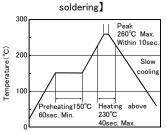
- · Ceramic chip capacitors are susceptible to thermal shock when exposed to rapid or concentrated heating or rapid cooling.
- · Therefore, the soldering must be conducted with great care so as to prevent malfunction of the components due to excessive thermal shock
- Preheating: Capacitors shall be preheated sufficiently, and the temperature difference between the capacitors and solder shall be within 130°C.
- · Cooling: The temperature difference between the capacitors and cleaning process shall not be greater than 100°C.

## [Reflow soldering]

[Recommended conditions for eutectic

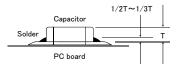


## [Recommended condition for Pb-free



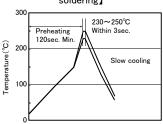
## Caution

- 1The ideal condition is to have solder mass(fillet) controlled to 1/2 to 1/3 of the thickness of a capacitor.
- ②Because excessive dwell times can adversely affect solderability, soldering duration shall be kept as close to recommended times as possible. soldering for 2 times.

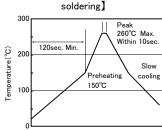


## [Wave soldering]

[Recommended conditions for eutectic soldering]



## [Recommended condition for Pb-free

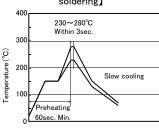


## Caution

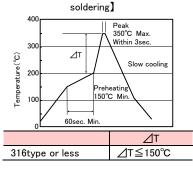
①Wave soldering must not be applied to capacitors designated as for reflow soldering only. soldering for 1 times.

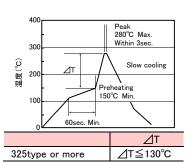
## [Hand soldering]

【Recommended conditions for eutectic soldering】



## [Recommended condition for Pb-free





## Caution

- ①Use a 50W soldering iron with a maximum tip diameter of 1.0 mm.
- 2The soldering iron shall not directly touch capacitors. soldering for 1 times.

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## 5. Cleaning Cleaning conditions 1. When PCBs are cleaned after capacitors mounting, please select the appropriate cleaning solution in accordance with the intended use Precautions of the cleaning. (e.g. to remove soldering flux or other materials from the production process.) 2. Cleaning condition shall be determined after it is verified by using actual cleaning machine that the cleaning process does not affect capacitor's characteristics. 1. The use of inappropriate cleaning solutions can cause foreign substances such as flux residue to adhere to capacitors or deteriorate their outer coating, resulting in a degradation of the capacitor's electrical properties (especially insulation resistance). 2. Inappropriate cleaning conditions (insufficient or excessive cleaning) may adversely affect the performance of the capacitors. In the case of ultrasonic cleaning, too much power output can cause excessive vibration of PCBs which may lead to the cracking of Technical considerations capacitors or the soldered portion, or decrease the terminal electrodes' strength. Therefore, the following conditions shall be carefully checked: 40 kHz or less Ultrasonic output: 20 W/Q or les Ultrasonic frequency: Ultrasonic washing period: 5 min. or less

## 6. Resin coating and mold

## Precautions

1. With some type of resins, decomposition gas or chemical reaction vapor may remain inside the resin during the hardening period or while left under normal storage conditions resulting in the deterioration of the capacitor's performance.

2. When a resin's hardening temperature is higher than capacitor's operating temperature, the stresses generated by the excessive heat may lead to damage or destruction of capacitors.

The use of such resins, molding materials etc. is not recommended.

## 7. Handling

## ◆Splitting of PCB

## Precautions

1. When PCBs are split after components mounting, care shall be taken so as not to give any stresses of deflection or twisting to the board.

2. Board separation shall not be done manually, but by using the appropriate devices.

## ◆Mechanical considerations

Be careful not to subject capacitors to excessive mechanical shocks.

- (1) If ceramic capacitors are dropped onto a floor or a hard surface, they shall not be used.
- (2) Please be careful that the mounted components do not come in contact with or bump against other boards or components.

## 8. Storage conditions

1. To maintain the solderability of terminal electrodes and to keep packaging materials in good condition, care must be taken to control temperature and humidity in the storage area. Humidity should especially be kept as low as possible.

## Recommended conditions

Ambient temperature : Below 30°C

Humidity: Below 70% RH

## Precautions

The ambient temperature must be kept below 40°C. Even under ideal storage conditions, solderability of capacitor is deteriorated as time passes, so capacitors shall be used within 6 months from the time of delivery.

- ·Ceramic chip capacitors shall be kept where no chlorine or sulfur exists in the air.
- 2. The capacitance values of high dielectric constant capacitors will gradually decrease with the passage of time, so care shall be taken to design circuits. Even if capacitance value decreases as time passes, it will get back to the initial value by a heat treatment at 150°C for

## Technical considerations

If capacitors are stored in a high temperature and humidity environment, it might rapidly cause poor solderability due to terminal oxidation and quality loss of taping/packaging materials. For this reason, capacitors shall be used within 6 months from the time of delivery. If exceeding the above period, please check solderability before using the capacitors.

\*\*RCR-2335B(Safety Application Guide for fixed ceramic capacitors for use in electronic equipment) is published by JEITA. Please check the guide regarding precautions for deflection test, soldering by spot heat, and so on.

This catalog contains the typical specification only due to the limitation of space. When you consider the purchase of our products, please check our specification. For details of each product (characteristics graph, reliability information, precautions for use, and so on), see our Web site (http://www.ty-top.com/) .