DISHA PATEL

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EDUCATION

LDRP Institute of Technology & Research   **2013-2016**
   Bachelor's in technology   *INDIA*

NIRMA UNIVERSITY   **2009-2013**
   Diploma in Electronics & Communication   *INDIA*

EXPERIANTIAL LEARNING

TATA TELE SERVICE   *May 2015 – Aug 2015*

HITACHI PVT. LTD.   *June 2014 – July 2014*

ADANI POWER LTD.   *Dec 2012 – Jan 2013*

ADANI GAS LTD.   *May 2013 – July 2013*

PROJECTS

- ASTABLE MULTIVIBRATOR
- NOTCH FILTERS TO RECOVER A CORRUPTED SPEECH SIGNAL
- SEVEN SEGMENT DISPLAY – 2-BIT BINARY ADDER
- DC POWER SUPPLY (FIXED)
- REGULATED POWER SUPPLY
- WAVE GENERATOR FOR FIBRE OPTIC LINK
- INCOMING CELL PHONE CALL DETECTOR
- RESEARCH ON 3D INTERNET
- AMPLITUDE MODULATION
- FABRICATION OF MONOPOLE ANTENNA
- BPSK MODULATOR USING OPAMP
- FIR FILTER ON FPGA KIT
- PASSWORD BASED DOOR LOCKING USING AT89C51 MICROCONTROLLER
- LINE FOLLOWER USING ATMEGA16
- TOUCH SCREEN DIRECTED ROBOT

COMPUTER SKILL

- Microsoft XP applications (Word, Excel, Office, PowerPoint, Outlook, Access)
- C and C++ languages, MATLAB, MULTISIM, EAGLE, Micro wind, Quartus, MySQL, HTML

- NS2 Simulator Programming
- Acrobat Reader 6.0 – Intermediate
- 8086 and 8051 Assembly level programming language (Keil u-vision)
- EMBEDDED Programming
- ATMEGA microcontroller programming – Using the binary format
- **OS Knowledge:** WINDOWS, UBUNTU, and MACINTOSH

REFERENCES

- **Prof. Prashant Oza**
  LDRP Institute
  Email: prashant_ecldrp.ac.in

- **Prof. Jayesh Patel**
  Nirma University
  Email: jayesh.patel@nirmauni.ac.in