

A Reconfigurable FTL Architecture for NAND Flash Based Applications

Park, C., Cheon, W., Kang, J., Roh, K., Cho, W., and Kim, J. 2008

2023.07.25

Presentation by Kim Boseung, 주용지에

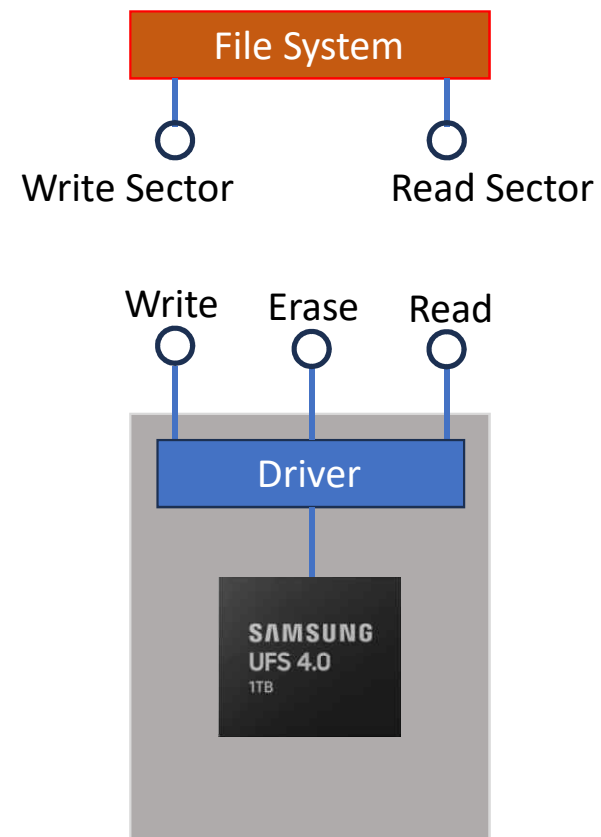
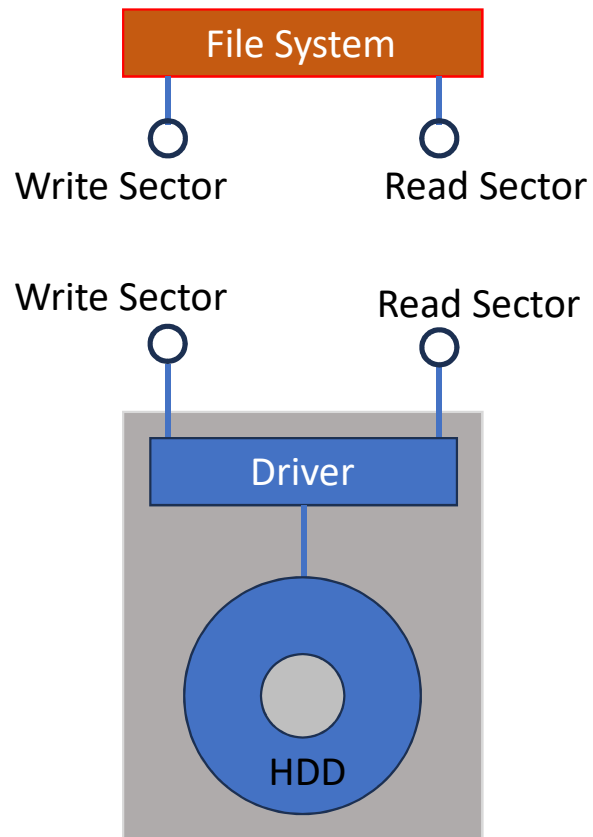
kbskbs1102@dankook.ac.kr

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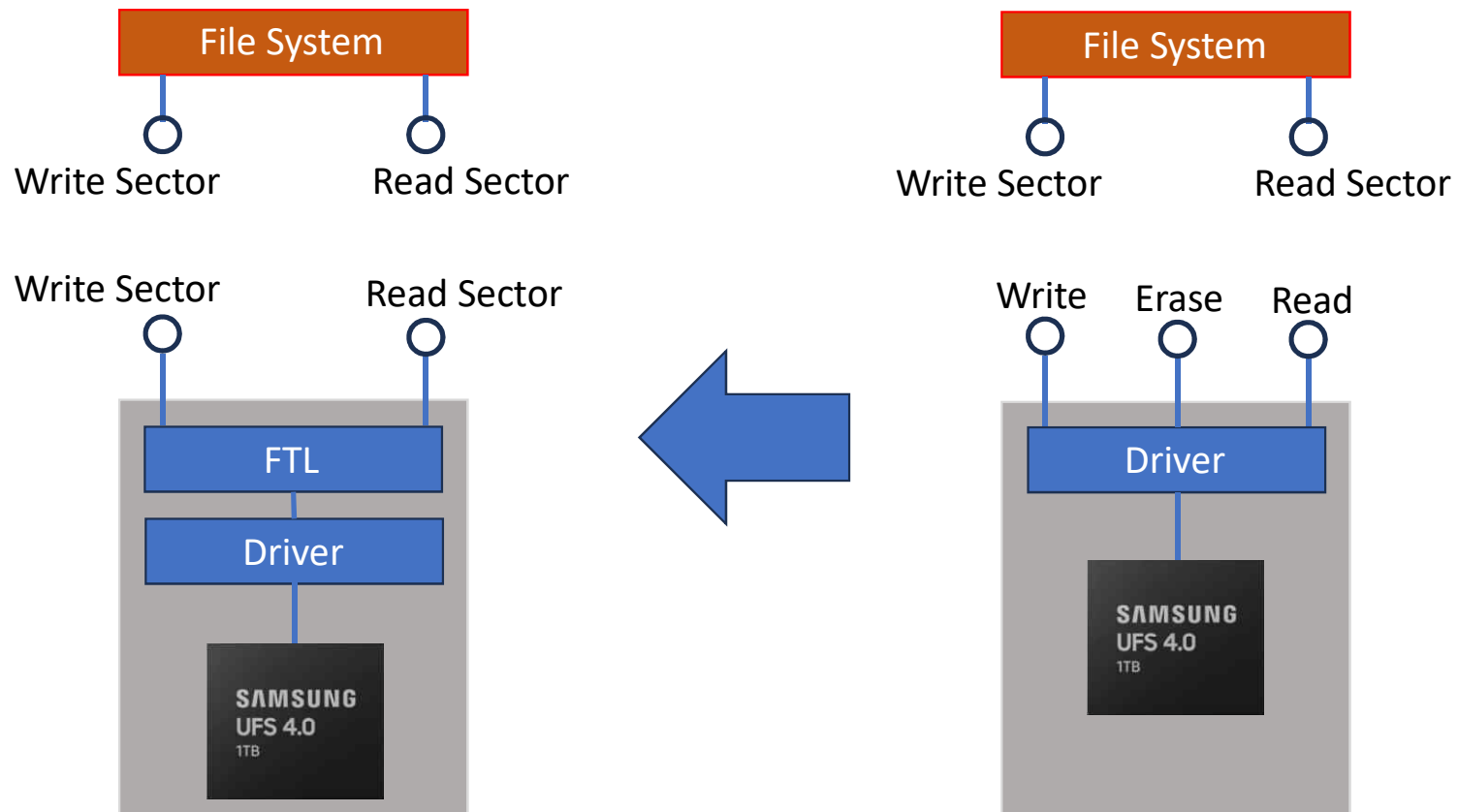
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2. NAND Flash Structure
3. FTL Concepts
4. Mapping Scheme
5. Flexible Group Mapping
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7. Experimental Results
8. Conclusions

1. Introduction

1. Introduction



1. Introduction



2. NAND Flash Structure

2. NAND Flash Structure

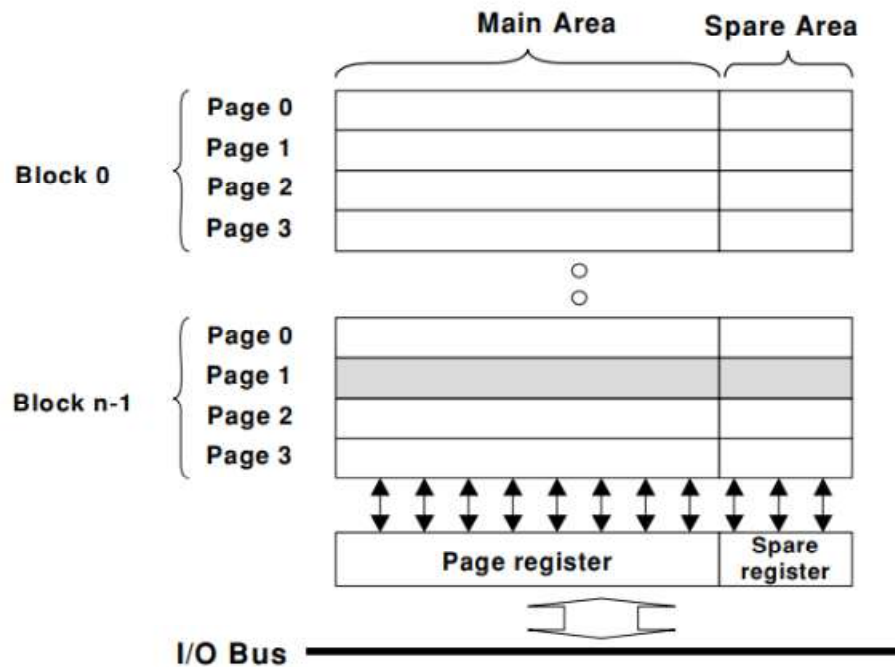


Fig. 3. NAND flash structure.

The **read/write unit** in NAND flash is a “**page**”, and **erase unit** is a “**block**”.

But it adopts the **Erase Before-Write approach**.

Main Area: The main area is used to store **user data**.

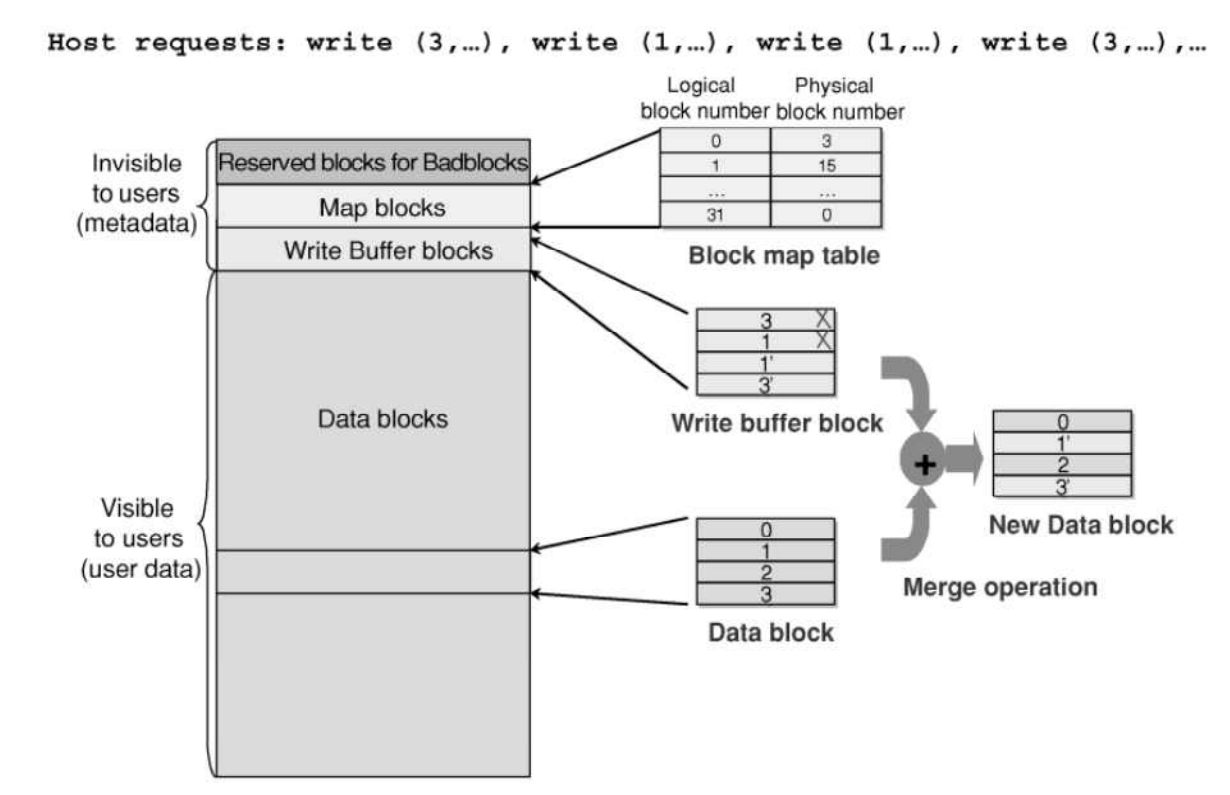
Spare Area: The spare area is used to store auxiliary **information** and **metadata**:

Bad-block Identification;
Error-Correction Code(ECC).

3. FTL Concepts

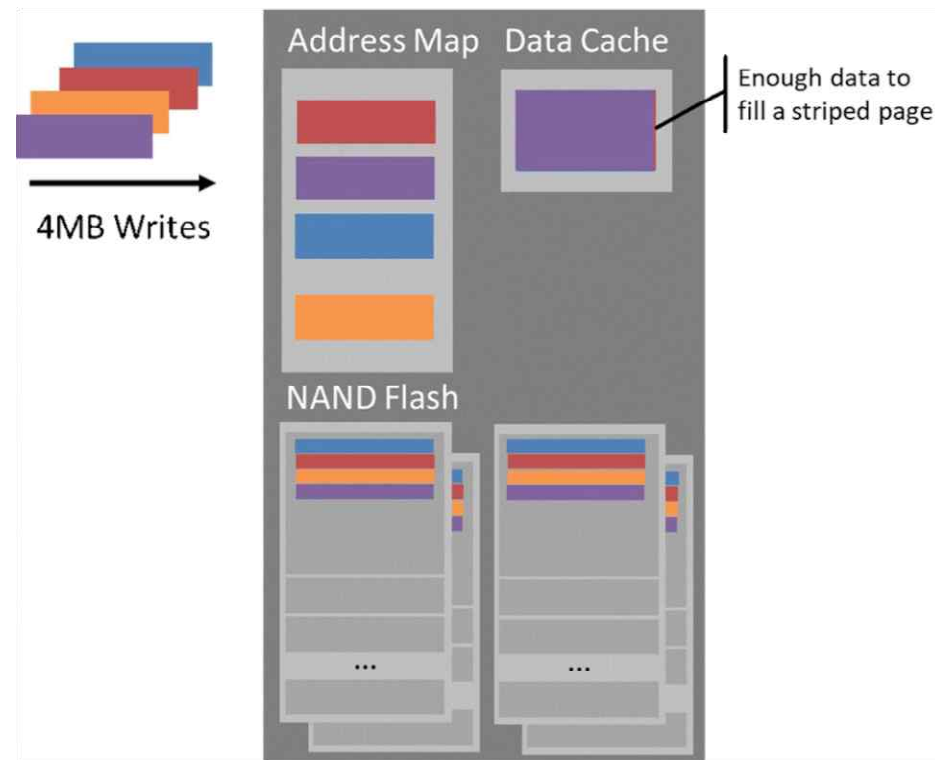
3. FTL Concepts

■ Logical view of the FTL of NAND flash memory.



3. FTL Concepts

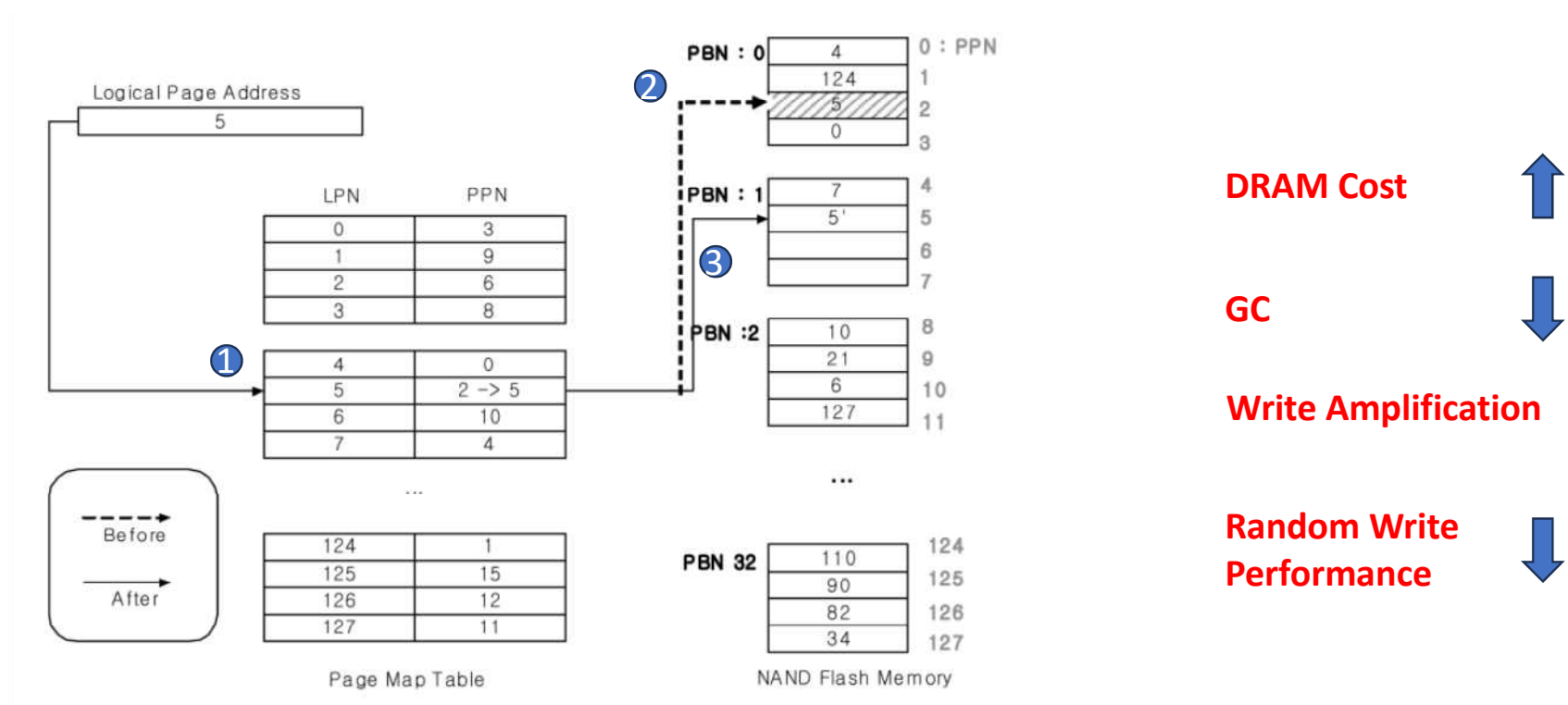
- Logical view of the FTL of NAND flash memory.



4. Mapping Scheme

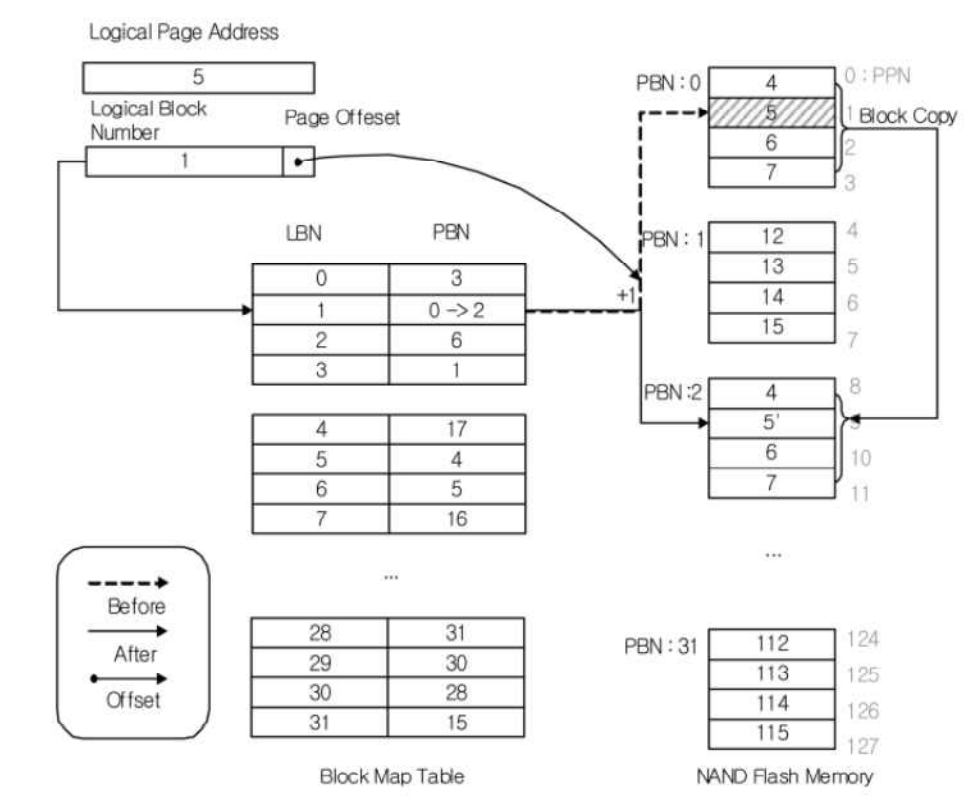
4. Mapping Scheme

■ Page-mapping scheme



4. Mapping Scheme

■ Block-mapping scheme

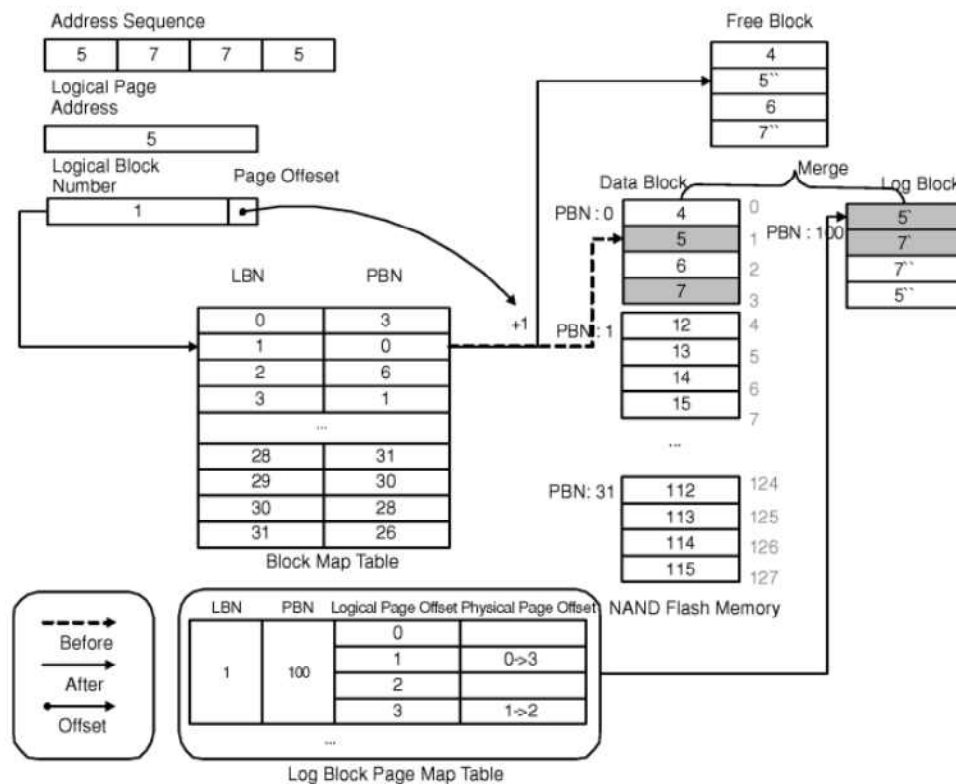


Block internal waster

Block-level copying

4. Mapping Scheme

■ Hybrid-mapping scheme



Latency

(When Log Blocks are full or merging)

5. Flexible Group Mapping

5. Flexible Group Mapping

- Basic idea

Parameter N: Block level spatial locality parameter

**Indicate the associativity among neighboring blocks
number of data blocks in a data block group**

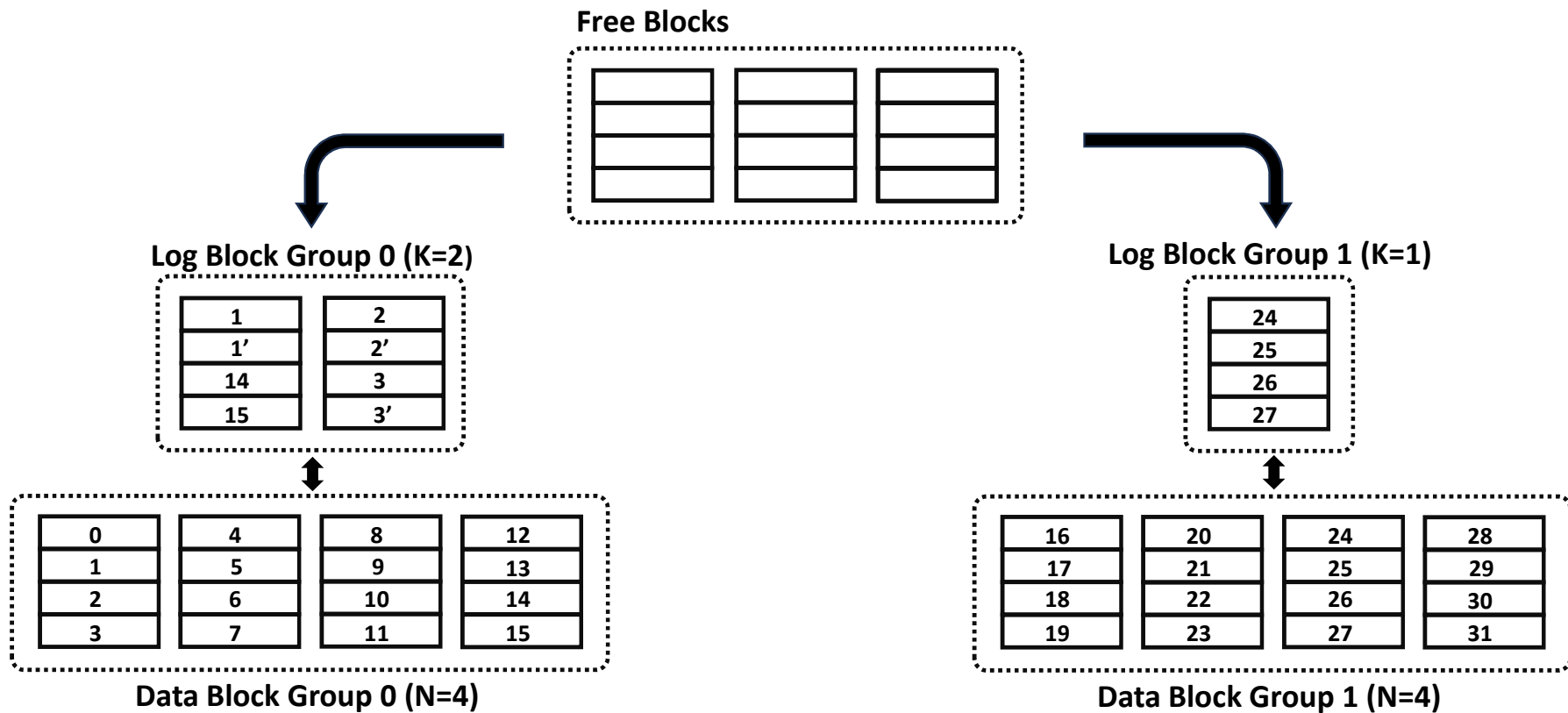
Parameter K: Delayed merge parameter

**Explain the type of temporal locality within a block
maximum number of log blocks**

5. Flexible Group Mapping

Flexible Group Mapping Scheme

Write request : 1, 1, 14, 15, 2, 2, 3, 3, 24, 25, 26, 27



5. Flexible Group Mapping

Write operation (N = 4, K = 2)

- 1) WRITE: LPN = 3, Num of Pages = 2
- 2) WRITE: LPN = 11, Num of Pages = 4
- 3) WRITE: LPN = 17, Num of Pages = 4

LBN	PBN
0	100
1	101
2	102
3	103
4	104
5	105
6	206
7	303

Data Block Mapping Table

DGN	PBN
0	300
	400
1	500
2	

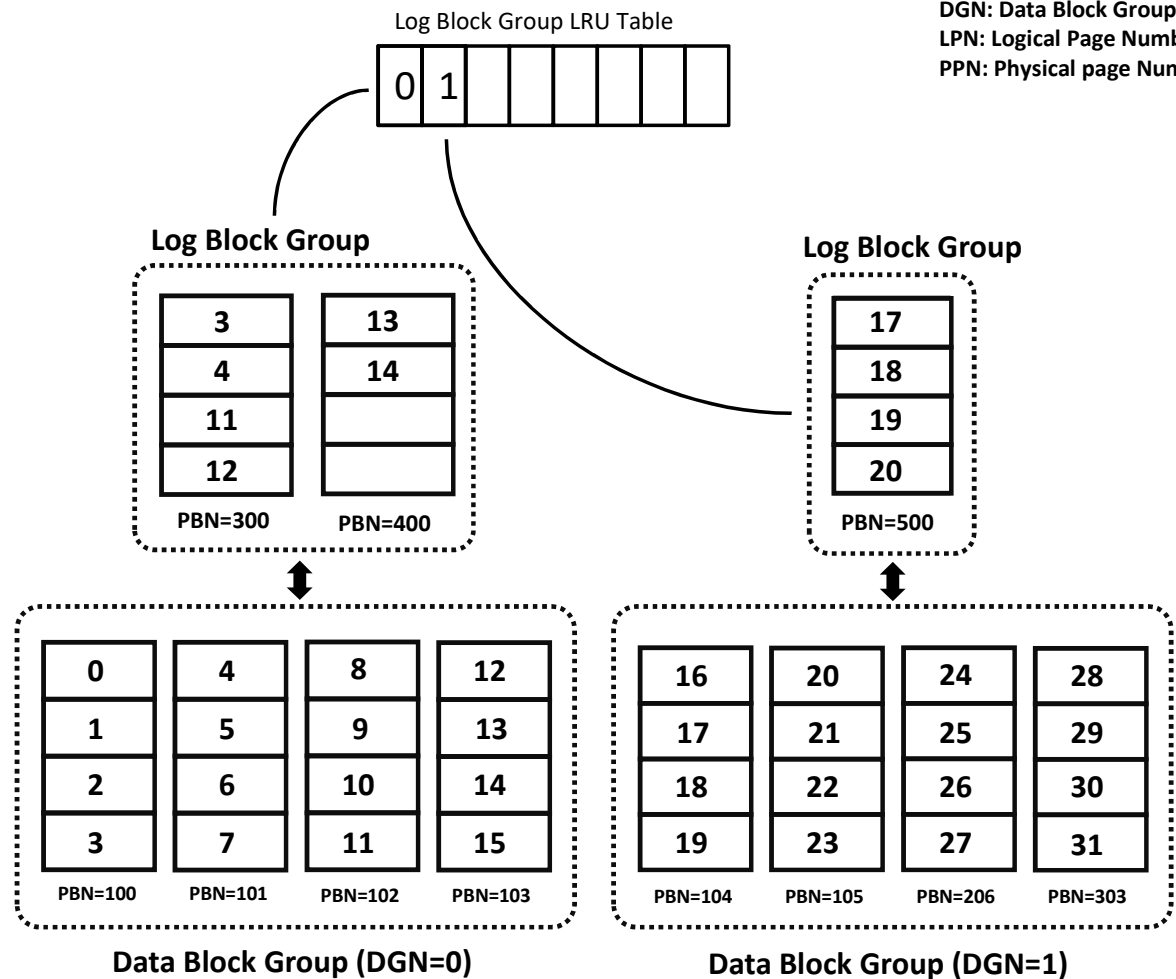
Log Block Mapping Table

DGN	LPN	PPN
0	3	1200
	4	1201
	11	1202
	12	1203
	13	1600
	14	1601
1	17	2000
	18	2001
	19	2002
	20	2003

Log Page Mapping Table

*DGN = LPN div (N × the number of pages per block)

LBN: Logical Block Number
PBN: Physical Block Number
DGN: Data Block Group Number
LPN: Logical Page Number
PPN: Physical page Number



5. Flexible Group Mapping

■ Simple Merge operation

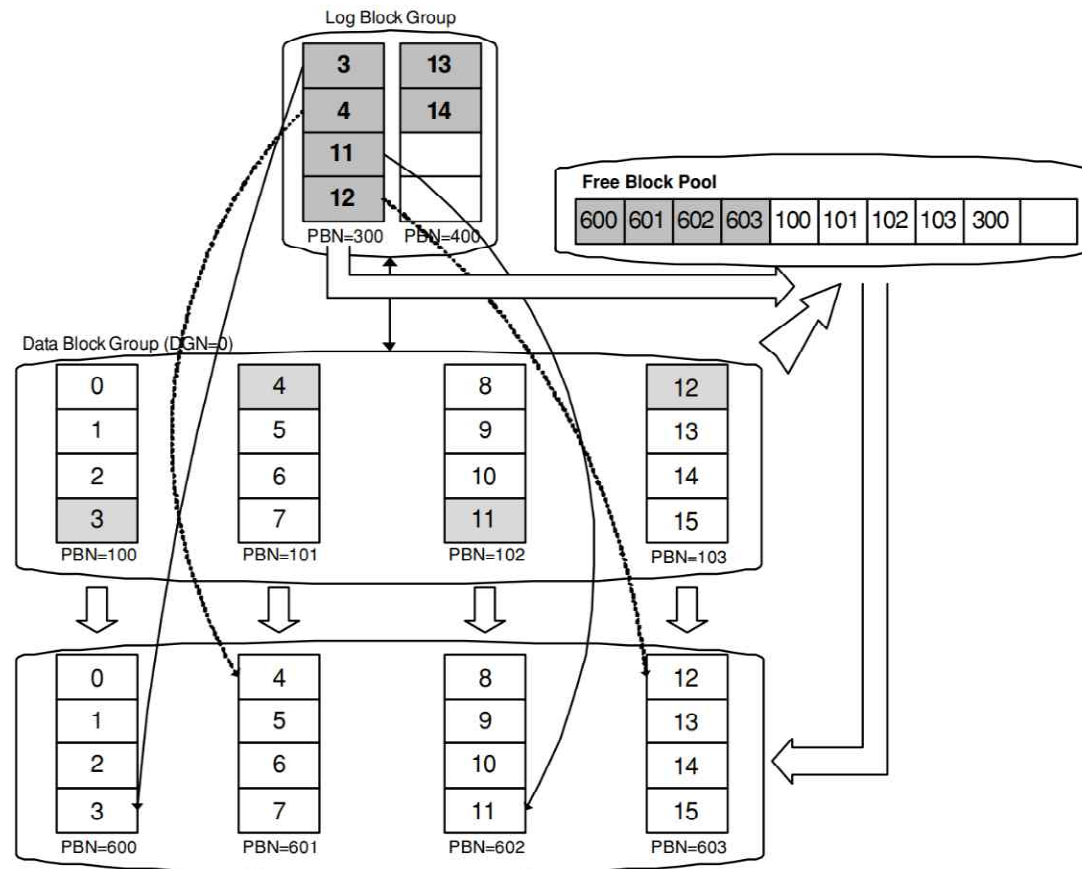
LBN: Logical Block Number
 PBN: Physical Block Number
 DGN: Data Block Group Number
 LPN: Logical Page Number
 PPN: Physical page Number

LBN	PBN
0	600
1	601
2	602
3	603
4	104
5	105
6	206
7	303

Data Block Mapping Table

DGN	PBN
0	400
1	500
2	

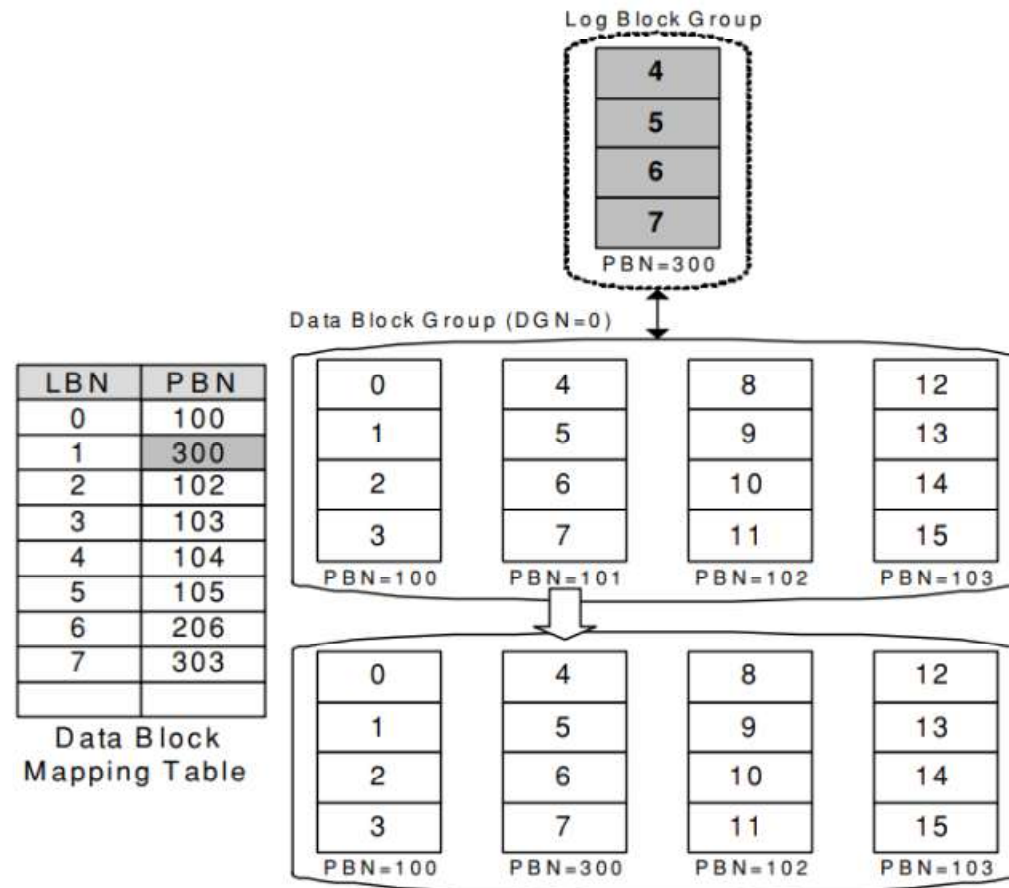
Log Block Mapping Table



5. Flexible Group Mapping

■ Swap Merge operation

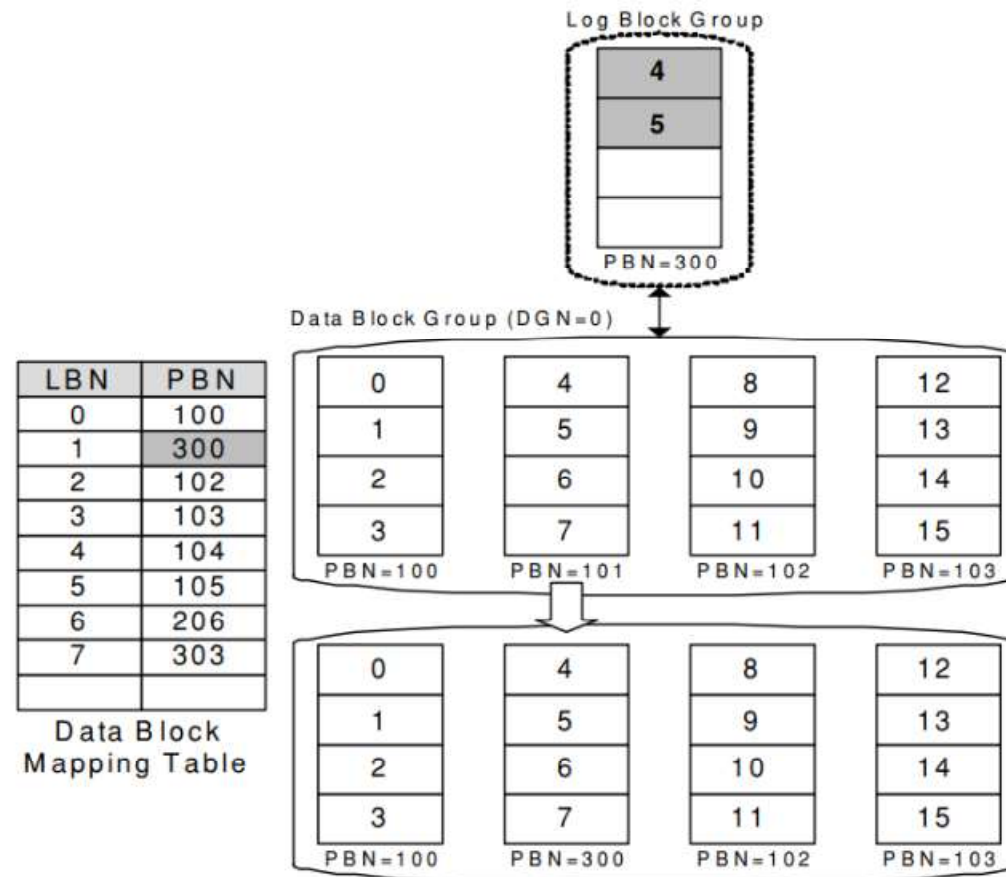
LBN: Logical Block Number
PBN: Physical Block Number
DGN: Data Block Group Number
LPN: Logical Page Number
PPN: Physical page Number



5. Flexible Group Mapping

■ Copy Merge operation

LBN: Logical Block Number
PBN: Physical Block Number
DGN: Data Block Group Number
LPN: Logical Page Number
PPN: Physical page Number



6. Analysis

6. Analysis

- **Workload Analysis**

$$R = \langle R_0, R_1, \dots, R_{M-1} \rangle \quad \begin{array}{l} D \\ D \end{array} \quad \begin{array}{l} \text{--- } l\text{+}b \text{ write request} \\ \text{--- } l\text{+}b \text{ write request} \end{array}$$

$$W_j = j\text{th request window of size } |W|$$

contains $R_{j*|W|}, \dots, R_{(j+1)*|W|-1}$ for $j = 0, 1, \dots, N_w - 1$

[illegible]

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13</																																																																																							

$$DN_{req} = \text{request density} = C_{req}/|M|$$

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6. Analysis

■ Workload Analysis

LBN0	Page 0	W_0	W_1	W_2	W_3	W_1
	Page 1	R_1				
	Page 2	R_2				
	Page 3	R_3				R_{16}, R_{17}
LBN1	Page 4		R_4, R_6			
	Page 5					
	Page 6		R_5, R_7			
	Page 7					
LBN2	Page 8			R_8		
	Page 9			R_9		
	Page 10				R_{14}	R_{18}
	Page 11					
LBN3	Page 12				R_{15}	
	Page 13			R_{10}	R_{12}	R_{19}
	Page 14					
	Page 15			R_{11}	R_{13}	

Example Request Distribution Table

⋮
⋮
⋮

$$|W| = 4$$

$$C_{2,3} = 1 \Rightarrow RD_{0,0} = 1$$

$$C_{2,3} = 1 \Rightarrow RD_{2,3} = 0.25$$

6. Analysis

■ Workload Analysis

N: Associativity parameter,

Determines how many LBNs are to be assigned to one log block group

N is high -> a large number of LBNs, N is low -> a small number of LBNS

$$\vec{x}_j = \begin{bmatrix} x_{j,1} \\ x_{j,2} \\ \vdots \\ x_{j,N} \end{bmatrix} \quad \text{for } j = 1, 2, \dots, M$$

$$, \quad \begin{bmatrix} 1 \\ \vdots \\ 1 \end{bmatrix} \quad \text{for } i \in \text{all LBNs}$$

6. Analysis

■ Workload Analysis

		<i>W</i> ₃	<i>W</i> ₂	<i>W</i> ₁	<i>W</i> ₃	<i>W</i> ₂
LBN0	Page 0	<i>R</i> ₀				
	Page 1	<i>R</i> ₁				
	Page 2	<i>R</i> ₂				
	Page 3	<i>R</i> ₃				<i>R</i> ₁₆ <i>R</i> ₁₇
LBN1	Page 4		<i>R</i> ₄ , <i>R</i> ₆			
	Page 5					
	Page 6		<i>R</i> ₅ , <i>R</i> ₇			
	Page 7					
LBN2	Page 8			<i>R</i> ₈		
	Page 9			<i>R</i> ₉		
	Page 10				<i>R</i> ₁₄	<i>R</i> ₁₈
	Page 11					
LBN3	Page 12				<i>R</i> ₁₅	
	Page 13			<i>R</i> ₁₀	<i>R</i> ₁₂	<i>R</i> ₁₉
	Page 14					
	Page 15			<i>R</i> ₁₁	<i>R</i> ₁₃	

Request Distribution Table

$$N_j = \left\lceil \min \left(\frac{1}{RD_{i,j}} \right) \right\rceil$$

	<i>W</i> ₀	<i>W</i> ₁	<i>W</i> ₂	<i>W</i> ₀	<i>W</i> ₂
LBN0	1.00	0.00	0.00	0.00	0.50
LBN1	0.00	1.00	0.00	0.00	0.00
LBN2	0.00	0.00	0.50	0.25	0.25
LBN3	0.00	0.00	0.50	0.75	0.25
<i>N_i</i>	1	1	2	4	4

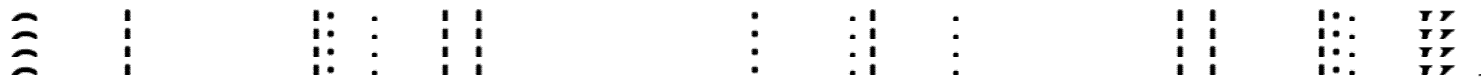
Estimating *N_i*:

Estimating *N_i*:

6. Analysis

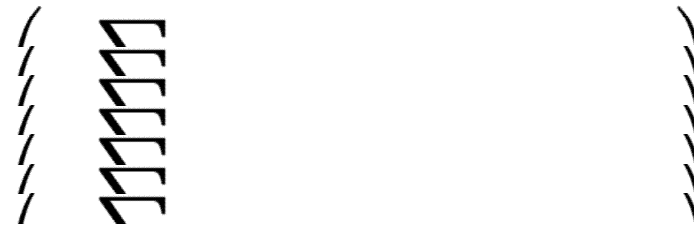
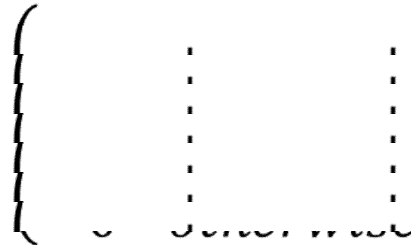
■ Workload Analysis

K: Delayed merge parameter



$$K_{i,0} = 0$$

$$K_{i,j} = K_{i,j-1} + d_{i,j} \text{ for } j > 0$$



- ↳ $DACE_{i,j}$ = set of pages in LRN_i written during W_j .
- ↳ $DACE_{i,j}$ = set of pages in LRN_i written during W_j .

6. Analysis

- **Workload Analysis**

		W_0	W_1	W_1	W_0	W_4
LBN0	Page 0	R_0				
	Page 1	R_1				
	Page 2	R_2				
	Page 3	R_3				$R_{16} R_{17}$
LBN1	Page 4		R_4, R_6			
	Page 5					
	Page 6		R_5, R_7			
	Page 7					
LBN2	Page 8			R_8		
	Page 9			R_9		
	Page 10				R_{14}	R_{18}
	Page 11					
LBN3	Page 12				R_{15}	
	Page 13			R_{10}	R_{12}	R_{19}
	Page 14					
	Page 15			R_{11}	R_{13}	

Request Distribution Table

[illegible]

	W_0	W_0	W_0	W_0	W_0	K
LBN0	0	0	0	0	1	1
LBN1	0	1	1	1	1	1
LBN2	0	0	0	0	1	1
LBN3	0	0	0	1	2	2

Estimating K_i

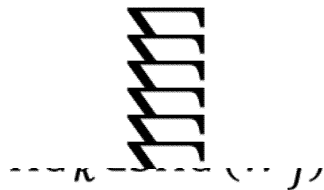
6. Analysis

■ Performance Analysis

$$A_K \quad \text{active data block accessed by } R_K$$
[illegible][illegible]
$$L(AG_k) = \text{number of log blocks associated with } AG_k$$
 $LB = \text{maximum number of log blocks}$

6. Analysis

■ Performance Analysis



↓ $L(AG_k) \leq K$

$$|\mathcal{S}_{\text{AG}}(\mathbf{u})| = \frac{1}{2} \|\mathbf{u}\|_2^2 = \frac{1}{2} \|\mathbf{u}\|_1^2 \quad \text{for } (\mathbf{u} = \mathbf{0} \text{ or } \mathbf{u} = \mathbf{1})$$

 λ is associated with the update frequency for the input pattern

Year	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100
1995	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100

6. Analysis

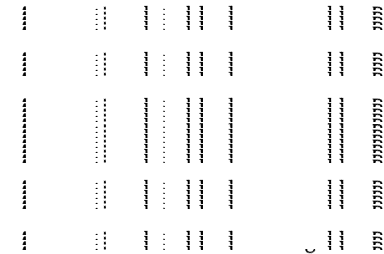
■ Performance Analysis

$$N^{\varepsilon} = \begin{pmatrix} C_1 & A_1 & f_1 & 1 & 1 & 1 \\ C_2 & A_2 & f_2 & 1 & 1 & 1 \\ C_3 & A_3 & f_3 & 1 & 1 & 1 \end{pmatrix}$$

 α is associated with the associativity of the input pattern

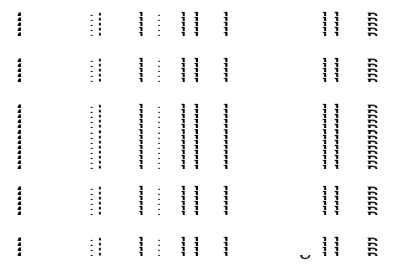
When associativity is strong $\rightarrow \varepsilon$ go to 1
When associativity is strong $\rightarrow \varepsilon$ go to 1

When associativity is strong $\rightarrow \varepsilon$ go to 1
 When associativity is strong $\rightarrow \varepsilon$ go to 1



6. Analysis

- Performance Analysis



When the size of W_j is reasonably large: $|SA(W_j)| = C$

$$\sum_{AG_k \in SAG(W_j)} \dots \quad |SAG(W_j)| = \frac{|SA(W_j)|}{N^\epsilon}, \quad |SA(W_j)| = C$$

$$\sum_{AG_k \in SAG(W_j)} \dots \rightarrow \dots \frac{K^\partial}{N^\epsilon} \dots$$

6. Analysis

■ Memory Requirement Analysis

1. W : the number of valid pages in the log

n_K = the number of valid pages in the log

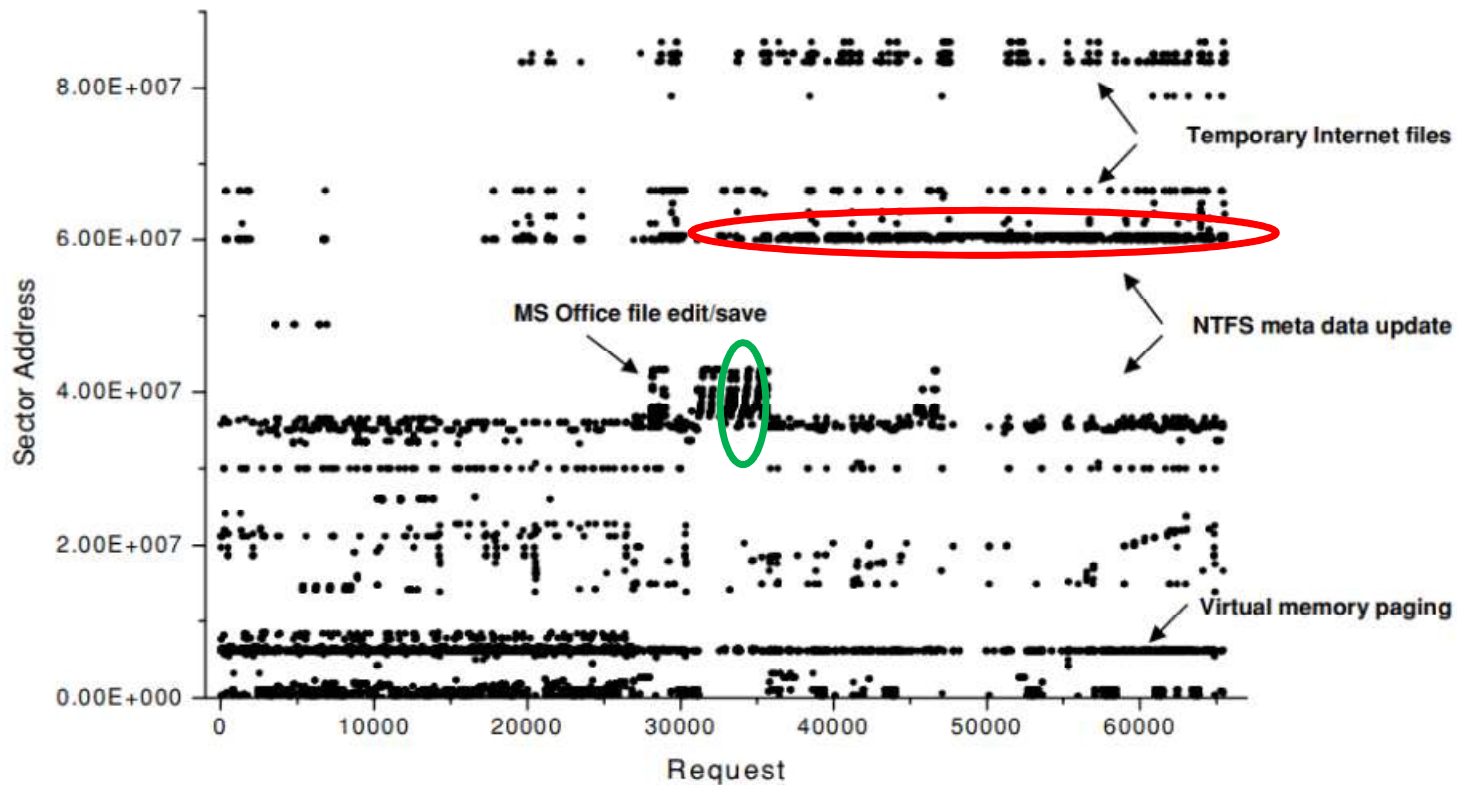
n_L = the number of logs

2. W : the number of valid pages in the log

7. Experimental Results

7. Experimental Results

Trace distribution



○ : temporal locality

○ : spatial locality

α is close to 1

β is close to 1

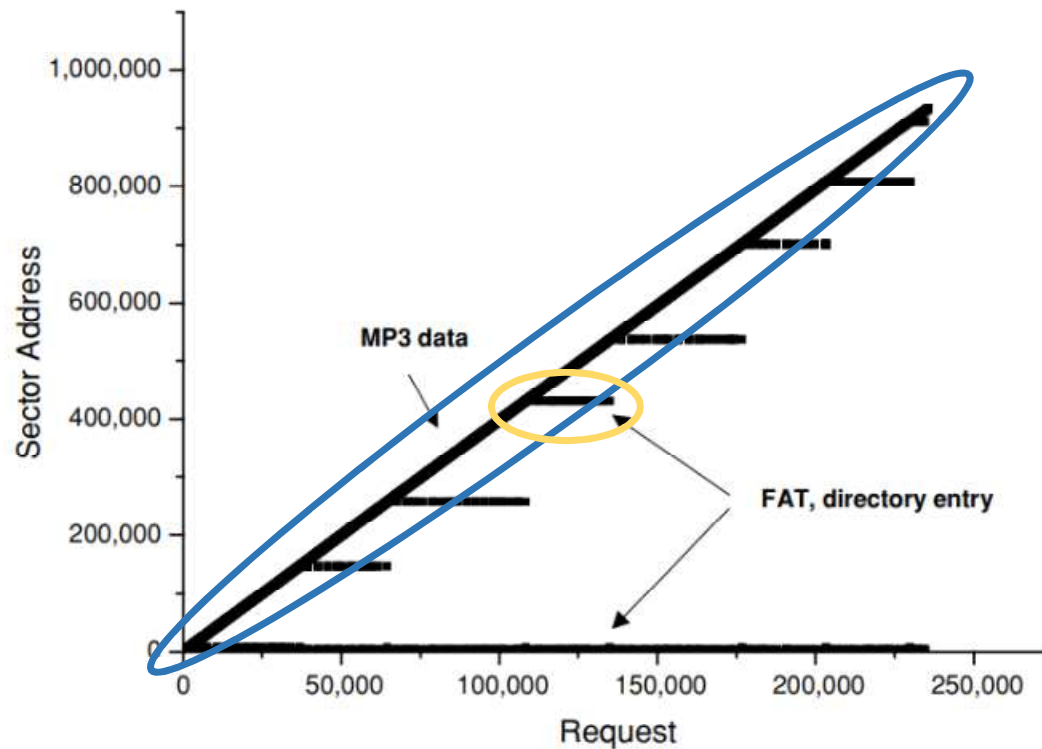
ϵ is not large

$$C \times \frac{K^\alpha}{N^\epsilon} > LB$$

Internet/MS Office use case

7. Experimental Results

Trace distribution



MP3 download use case

○ : Sequential access

○ : Random access

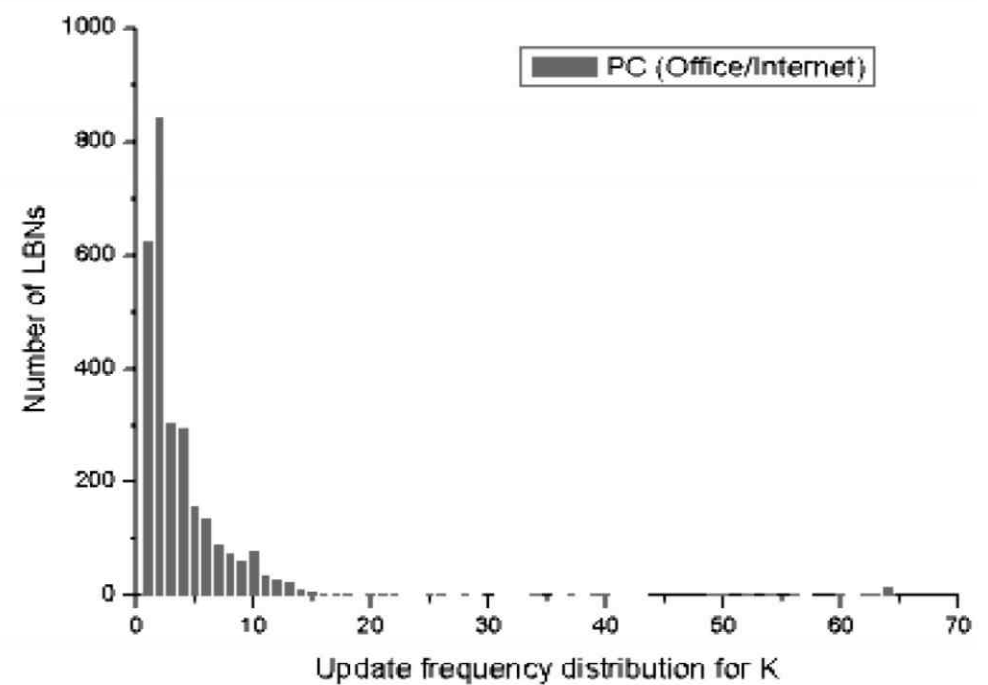
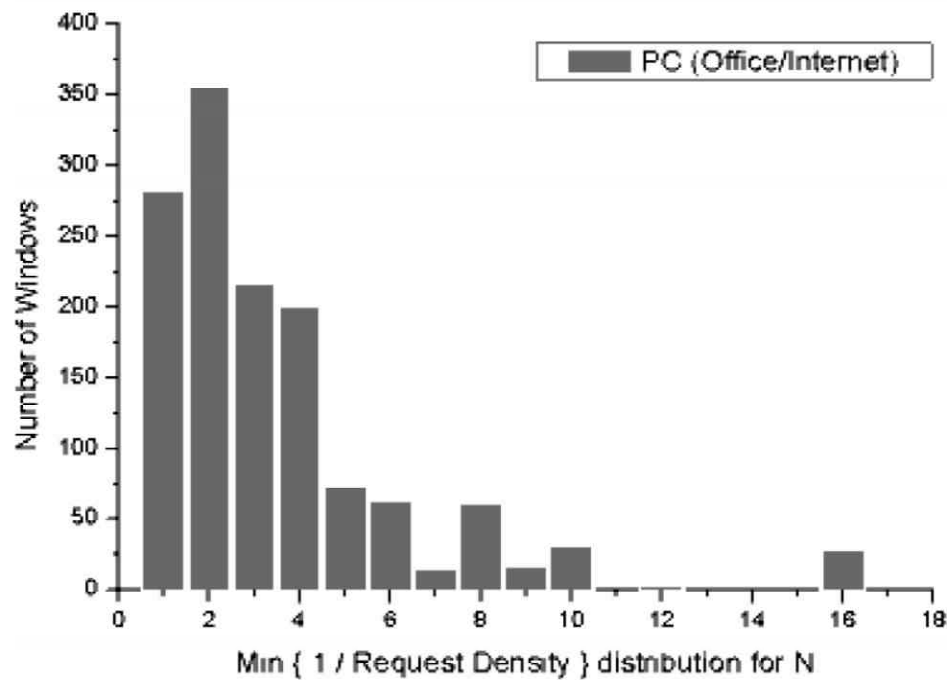
∂ is close to 0

ε is close to 0

$$C \times \frac{K^\partial}{N^\varepsilon} > LB$$

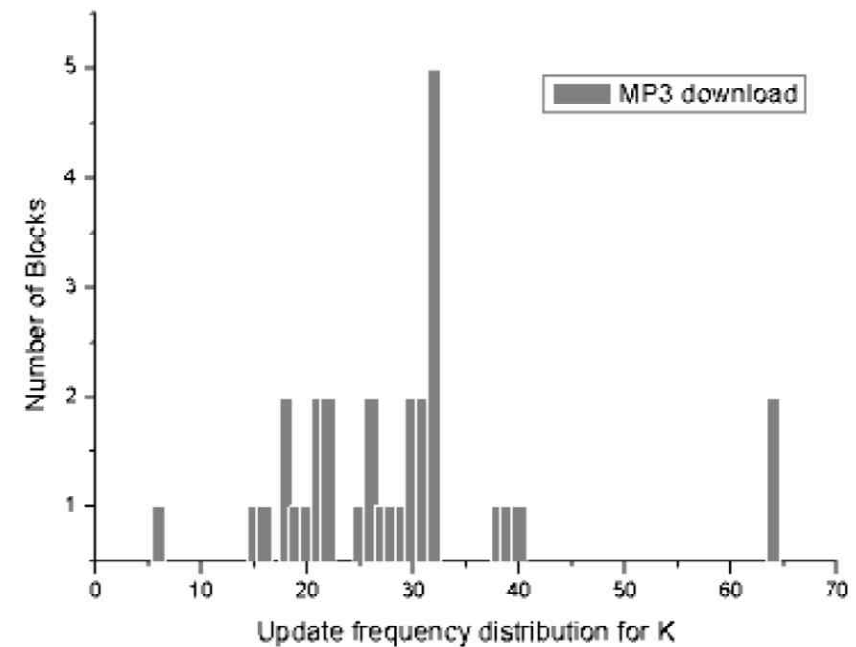
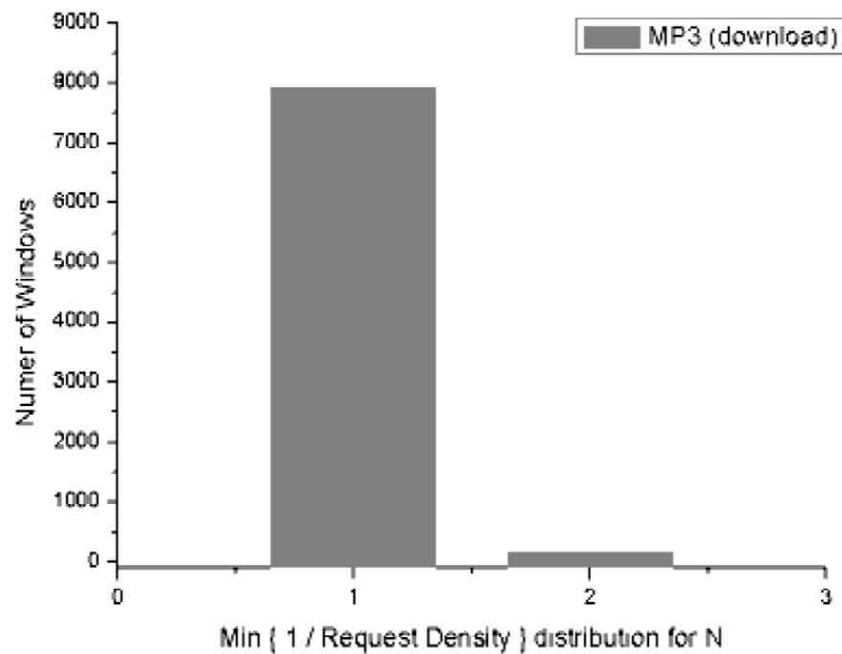
7. Experimental Results

- Distribution of N's and K's for PC applications.



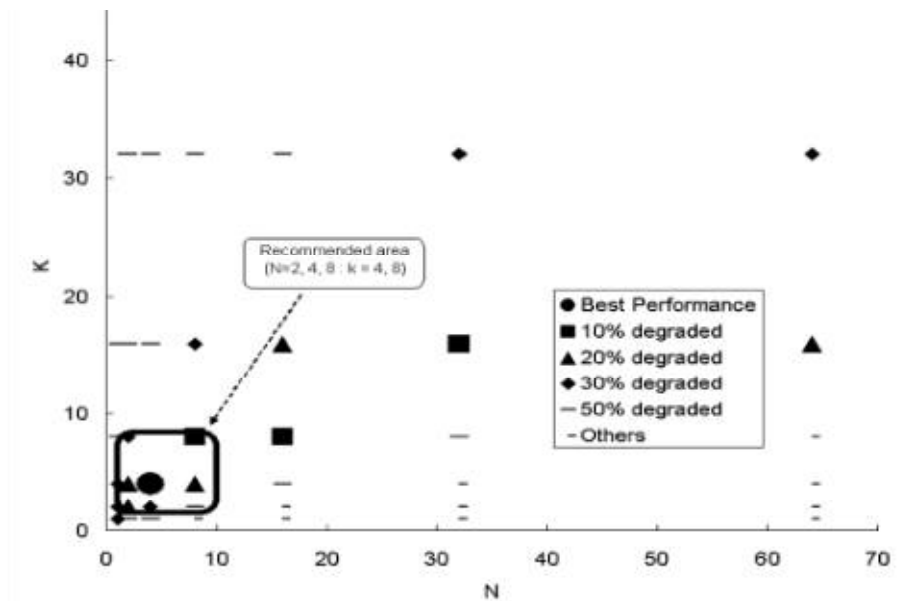
7. Experimental Results

■ Distribution of N and K values for the MP3 application

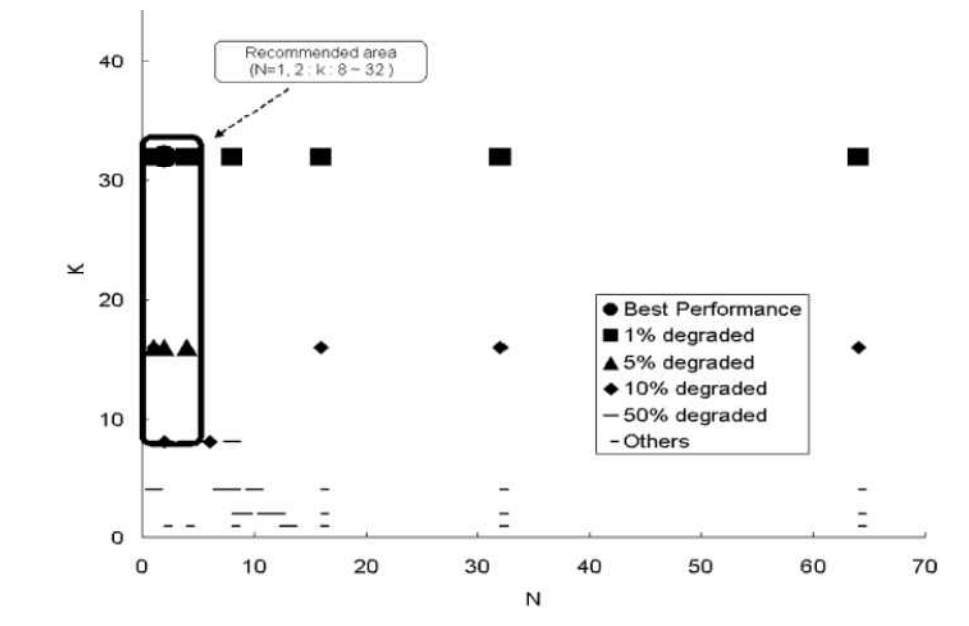


7. Experimental Results

■ Performance variation with the change of N and K



PC application



MP3 download

8. Conclusions

8. Conclusions

■ Conclusions

Introduced a reconfigurable FTL architecture to efficiently handle diverse NAND flash applications

The associativity between data blocks was parameterized using:

- **N**: the number of data blocks in a data block group
- **K**: the maximum number of log blocks in a log block group

The proposed analysis method
can efficiently find the optimal **N** and **K**

A Reconfigurable FTL Architecture for NAND Flash Based Applications

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Thank you!
Q & A ?

2023.07.25

Presentation by Kim Boseung, 주용지에

kbskbs1102@dankook.ac.kr