

# MSWasm: Soundly Enforcing Memory-Safe Execution of Unsafe Code\*

## (Technical Report)

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\*To better explain and clarify notions, this paper uses colours. For a better experience, please print or view this paper in colours. Specifically, in this paper we use a **blue, sans-serif** font for **source** elements, an **orange, bold** font for **target** elements, and a **black, italic** font for elements common to either languages (to avoid repeating similar definitions twice). Thus, **P** is a source program, **P** is a target program, and *C* is generic notation for either program.

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# 1 Memory Safety Monitor

Address  $a \in \mathbb{N}$   
 Color tag  $c \in \mathbb{C}$   
 Shade tag  $s \in \mathbb{S}$   
 Shading fun.  $\phi : \{1..n-1\} \rightarrow \mathbb{S}$   
 Event  $\alpha = \epsilon \mid \mathbf{read}(a^c) \mid \mathbf{write}(a^c) \mid \mathbf{alloc}(n, a^c, \phi) \mid \mathbf{sfree}(a^c)$   
 Events  $\bar{\alpha} = \epsilon \mid \alpha \cdot \bar{\alpha}$   
 Abstract Segment  $T \in (\mathbb{N} \rightarrow \{A(c, s), F(c, s)\}, \times Y)$   
 History  $Y = \bar{\alpha}$

$$\begin{array}{c}
 \text{(MS-Empty)} \\
 \hline
 \vdash T \xrightarrow{\epsilon} T \\
 \text{(MS-Read)} \quad \text{(MS-Write)} \\
 \frac{T(a) = A(c, s)}{\vdash T \xrightarrow{\mathbf{read}(a^{(c,s)})} T} \quad \frac{T(a) = A(c, s)}{\vdash T \xrightarrow{\mathbf{write}(a^{(c,s)})} T} \\
 \text{(MS-Alloc)} \\
 \frac{\mathbf{fresh}(c) \quad \forall j \in \{0..n-1\}. T(a+j) = F(\_, \_) \quad T'.Y = T.Y \cdot \mathbf{alloc}(n, a^c, \phi)}{T' = T[a+i \mapsto A(c, \phi(i)) \mid i \in \{0..n-1\}] \quad T'.Y = T.Y \cdot \mathbf{alloc}(n, a^c, \phi)} \\
 \hline
 \vdash T \xrightarrow{\mathbf{alloc}(n, a^c, \phi)} T' \\
 \text{(MS-Free)} \\
 \frac{T.Y = \alpha_1^* \cdot \mathbf{alloc}(n, a^c, \phi) \cdot \alpha_2^* \quad \mathbf{sfree}(a^c) \notin \alpha_2^*}{T' = T[i \mapsto F(c, s_i) \mid i \mapsto A(c, s_i) \in T] \quad T'.Y = T.Y \cdot \mathbf{sfree}(a^c)} \\
 \hline
 \vdash T \xrightarrow{\mathbf{sfree}(a^c)} T'
 \end{array}$$

We map alloc/frees to single events since we believe this is the closest to a programmer's mental model, while reads and writes are no longer segments.

MS-Wasm memory safety

$$T_0 = ([i \mapsto F(c_0, s_0) \mid i \in 0..n-1], []) \quad \text{for size } n$$

$$\mathbf{MS}(\bar{\alpha}) \stackrel{\text{def}}{=} \mathbf{MS}(\bar{\alpha}, T_0)$$

$$\mathbf{MS}(\bar{\alpha}, T) \stackrel{\text{def}}{=} \exists T'. \vdash T \xrightarrow{\bar{\alpha}} T'$$

$$\begin{array}{c}
 \text{(Empty)} \quad \text{(Cons)} \\
 \frac{}{\bar{\alpha} \vdash T \xrightarrow{\epsilon} T} \quad \frac{\vdash T_1 \xrightarrow{\alpha} T'_1 \quad \vdash T'_1 \xrightarrow{\bar{\alpha}} T_2}{\vdash T_1 \xrightarrow{\alpha \cdot \bar{\alpha}} T_2}
 \end{array}$$

Memory safety monitor for multiple events  $\vdash T \xrightarrow{\alpha} T'$ .

**Lemma 1** (Split Abstract MS Monitor). Let  $\vdash T \xrightarrow{\overline{\alpha}_1 \cdot \overline{\alpha}_2} T''$ . If  $\vdash T \xrightarrow{\overline{\alpha}_1} T'$  then  $\vdash T' \xrightarrow{\overline{\alpha}_2} T''$ .

*Proof.* Immediate from (Cons) □

**Lemma 2** (Split Abstract Segment). If

1.  $\vdash T \xrightarrow{\overline{\alpha}} T''$  and
2.  $\exists T', T' \subseteq T''$

then  $\exists \overline{\alpha}_1, \overline{\alpha}_2$  such that

- $\overline{\alpha} = \overline{\alpha}_1 \cdot \overline{\alpha}_2$  and
- $\vdash T \xrightarrow{\overline{\alpha}_1} T'$  and
- $\vdash T' \xrightarrow{\overline{\alpha}_2} T''$

*Proof.* Immediate from (Cons) □

## 2 Source Language

A C-like language.

### 2.1 Syntax

#### Additions

Modules  $P, M ::= I, D, F, n_{hs}$   
 Imports  $I ::= \emptyset \mid I; (w \ g(x : w))$   
 Struct. Defs.  $D ::= \emptyset \mid s \mapsto S$   
 Structs  $S ::= f : \tau \mid f : \tau; S$   
 Types  $w ::= \tau \mid \text{struct } s \mid \text{array } \tau$   
 Word Types  $\tau ::= \text{int} \mid \text{ptr } w$   
 Functions  $F ::= \emptyset \mid \tau \ g(x : \tau) \mapsto ((y : \tau)^*, e); F$   
 Function Types  $\sigma ::= \tau \rightarrow \tau$   
 Expressions  $e ::= n \mid n^{(n, n, w, n_{id})} \mid x \mid e; e$   
 $\mid \text{malloc}(\tau, e) \mid \text{malloc}(w) \mid e \oplus e$   
 $\mid e[e] := e \mid \&e \rightarrow f \mid *e \mid e[e]$   
 $\mid x := e \mid *e := e \mid x := \text{call } g(e)$   
 $\mid \text{if } e \text{ then } e \text{ else } e \mid \text{free}(e)$   
 Values  $v ::= n \mid n^{(n, n, w, n_{id})}$   
 Slots  $s ::= (n, n, w, n_{id})$   
 Allocator States  $A ::= (s^*, s^*, Y)$   
 Allocator Histories  $Y ::= \bar{\alpha}$   
 Heaps  $H ::= v^*$   
 Eval. Contexts  $E ::= [\cdot] \mid E; e \mid E \oplus e \mid v \oplus E$   
 $\mid \&E \rightarrow f \mid *E \mid x := E \mid *E := e \mid *v := E$   
 $\mid E[e] \mid a^{(b, \ell, w, n_{id})}[E] \mid E[e_2] := e_3$   
 $\mid a^{(b, \ell, w, n_{id})}[E] := e \mid a^{(b, \ell, w, n_{id})}[n] := E$   
 $\mid \text{malloc}(\tau, E) \mid x := \text{call } g(E)$   
 $\mid \text{if } E \text{ then } e \text{ else } e \mid \text{free}(E)$   
 Environments  $\Gamma ::= \emptyset \mid \Gamma; (x : \tau)$   
 Struct Envs.  $\Sigma ::= \emptyset \mid \Sigma; (n^\tau)$   
 Stack Frames  $\theta ::= \emptyset \mid x \mapsto v; \theta$   
 Stacks  $K ::= \emptyset \mid \theta_f; K$   
 Op. Sem. Prog. States  $\Omega ::= P, K, H, A \triangleright (e, E^*)$   
 Substitutions  $\gamma ::=$

Traces  $\bar{\alpha} ::= \emptyset \mid \alpha \cdot \bar{\alpha}$   
 Actions  $\alpha ::= \epsilon \mid \text{alloc}(a^{(n,n,w,n_{id})}) \mid \text{read}(n^{(n,n,w,n_{id})}, v) \mid \text{write}(n^{(n,n,w,n_{id})}, v)$   
 $\mid \text{fread}(n, v) \mid \text{fwrite}(n, v) \mid \text{free}(n^{(n,n,w,n_{id})}) \mid \text{ffree } n$

## 2.2 Typing

$\Gamma_f = (\tau_r, \{x : \tau_a, y_1 : \tau_1, y_2 : \tau_2, \dots\})$  where  $\tau_r f(x : \tau_a) \mapsto ((y_1 : \tau_1)^*, e_f) \in P.F$   
 $\Delta(f) = \Gamma_f$

Typing judgments are as follows:

Typing expressions	$P, \Gamma \vdash e : \tau$
Typing evaluation contexts	$P, \Gamma \vdash E : w \Rightarrow w'$
Typing functions	$P \vdash w_r f(x : w_a) \mapsto e_f : w_a \rightarrow w_r$
Typing programs	$\vdash P : \text{wt}$
Typing configurations	$\vdash P, K, H, A \triangleright (e_f, E^*)$

$\frac{\begin{array}{c} \text{(T-Config)} \\ P, \Gamma_f \vdash e : w \quad P, \Delta \vdash E^* : w \Rightarrow w' \\ P, \Delta \vdash K \end{array}}{\vdash P, K, H, A \triangleright (e_f, E^*)}$	
$\frac{\forall \tau_r f(x : \tau_a) \mapsto ((y : \tau)^*, e_f) \in P.F, P \vdash \tau_r f(x : \tau_a) \mapsto ((y : \tau)^*, e_f) : \tau_a \rightarrow \tau_r}{\vdash P : \text{wt}} \quad \text{(T-Prog)}$	

### Typing Programs and Configurations

$\frac{}{P, \Gamma_f \vdash \emptyset} \quad \text{(T-Stack-Frame-Nil)}$	$\frac{\begin{array}{c} \text{(T-Stack-Frame)} \\ P, \Gamma_f \vdash x : w \quad P, \Gamma_f \vdash v : w \\ P, \Gamma_f \vdash \theta_f \end{array}}{P, \Gamma_f \vdash (x \mapsto v); \theta_f}$	$\frac{}{P, \Delta \vdash \emptyset} \quad \text{(T-Stack-Nil)}$
$\frac{\begin{array}{c} \text{(T-Stack)} \\ P, \Delta(f) \vdash \theta_f \\ P, \Delta \vdash K \end{array}}{P, \Delta \vdash \theta_f; K}$	$\frac{\begin{array}{c} \text{(T-Ctx-Stack-Nil)} \\ P, \Gamma_f \vdash E_f : w' \Rightarrow w \\ P \vdash E_f; \emptyset : w' \Rightarrow w \end{array}}{P \vdash E_f; \emptyset : w' \Rightarrow w}$	$\frac{\begin{array}{c} \text{(T-Ctx-Stack)} \\ P, \Delta(f) \vdash E_f : w_1 \Rightarrow w_2 \\ P, \Delta \vdash E^* : w_2 \Rightarrow w_3 \end{array}}{P, \Delta \vdash E_f; E^* : w_1 \Rightarrow w_3}$
Typing Stack Frames, Stack and a list of Eval Contexts $P, \Delta \vdash E_f; E^* : w' \Rightarrow w$		

$\frac{\begin{array}{c} \text{(T-Fun)} \\ P, (x : w_a, (y : w)^*) \vdash e_f : w_r \end{array}}{P \vdash w_r f(x : w_a) \mapsto ((y : w)^*, e_f) : w_a \rightarrow w_r}$
--

### Typing Functions

(T-Constant-int)	(T-Raw-Ptr)	(T-Authentic-Ptr)
$\frac{}{P, \Gamma \vdash n : \text{int}}$	$\frac{}{P, \Gamma \vdash n : \text{ptr } w}$	$\frac{w \neq \text{array } \tau}{P, \Gamma \vdash n^{(n_b, 1, w, n_{id})} : \text{ptr } w}$
	(T-Authentic-Arr-Ptr)	(T-Var)
	$\frac{n_\ell > 1}{P, \Gamma \vdash n^{(n_b, n_\ell, \tau, n_{id})} : \text{ptr array } \tau}$	$\frac{\Gamma(x) = w}{P, \Gamma \vdash x : w}$
(T-BinOp)	(T-Ptr-Arith)	
$\frac{P, \Gamma \vdash e_j : \text{int} \quad j \in \{1, 2\}}{P, \Gamma \vdash e_1 \oplus e_2 : \text{int}}$	$\frac{P, \Gamma \vdash e_1 : \text{ptr array } \tau \quad P, \Gamma \vdash e_2 : \text{int}}{P, \Gamma \vdash e_1 \oplus e_2 : \text{ptr array } \tau}$	
(T-Malloc-Array)	(T-Malloc)	
$\frac{P, \Gamma \vdash e : \text{int}}{P, \Gamma \vdash \text{malloc}(\tau, e) : \text{ptr array } \tau}$	$\frac{}{P, \Gamma \vdash \text{malloc}(w) : \text{ptr } w}$	
(T-Var-Assign)	(T-Assign)	
$\frac{P, \Gamma \vdash x : \tau \quad P, \Gamma \vdash e : \tau}{P, \Gamma \vdash x := e : \text{int}}$	$\frac{P, \Gamma \vdash e_1 : \text{ptr } \tau \quad P, \Gamma \vdash e_2 : \tau}{P, \Gamma \vdash *e_1 := e_2 : \text{int}}$	
(T-Array-Assign)	(T-Seq)	(T-Deref)
$\frac{P, \Gamma \vdash e_1 : \text{ptr array } \tau \quad P, \Gamma \vdash e_3 : \tau \quad P, \Gamma \vdash e_2 : \text{int}}{P, \Gamma \vdash e_1[e_2] := e_3 : \text{int}}$	$\frac{P, \Gamma \vdash e_1 : w_1 \quad P, \Gamma \vdash e_2 : w_2}{P, \Gamma \vdash e_1; e_2 : w_2}$	$\frac{P, \Gamma \vdash e : \text{ptr } \tau}{P, \Gamma \vdash *e : \tau}$
(T-Arr-Deref)	(T-Struct-field)	
$\frac{P, \Gamma \vdash e_1 : \text{ptr array } \tau \quad P, \Gamma \vdash e_2 : \text{int}}{P, \Gamma \vdash e_1[e_2] : \tau}$	$\frac{P, \Gamma \vdash e : \text{ptr struct } s \quad P.D(s) = \dots; \tau_f f; \dots}{P, \Gamma \vdash \&e \rightarrow f : \text{ptr } \tau_f}$	
(T-If)	(T-Call)	
$\frac{P, \Gamma \vdash e_0 : \text{int} \quad P, \Gamma \vdash e_1 : w \quad P, \Gamma \vdash e_2 : w}{P, \Gamma \vdash \text{if } e_0 \text{ then } e_1 \text{ else } e_2 : w}$	$\frac{w_r \ g(x : w_1) \mapsto ((y_j : w_j)^*, e_g) \in P.F \quad P, \Gamma \vdash e_1 : w_1 \quad P, \Gamma \vdash y : w_r}{P, \Gamma \vdash y := \text{call } g(e_1) : w_r}$	
	(T-Free)	
	$\frac{P, \Gamma \vdash e : \text{ptr } w}{P, \Gamma \vdash \text{free}(e) : \text{int}}$	

Source Typing expressions

(T-Ctx-BinOp1)	(T-Ctx-BinOp2)
$\frac{P, \Gamma \vdash E : w \Rightarrow \text{int} \quad P, \Gamma \vdash e_2 : \text{int}}{P, \Gamma \vdash E \oplus e_2 : w \Rightarrow \text{int}}$	$\frac{P, \Gamma \vdash E : w \Rightarrow \text{int} \quad P, \Gamma \vdash v_1 : \text{int}}{P, \Gamma \vdash v_1 \oplus E : w \Rightarrow \text{int}}$
(T-Ctx-Malloc)	(T-Ctx-Var-Assign)
$\frac{P, \Gamma \vdash E : w' \Rightarrow \text{int}}{P, \Gamma \vdash \text{malloc}(\tau, E) : w' \Rightarrow \text{array } \tau}$	$\frac{P, \Gamma \vdash E : w' \Rightarrow w \quad P, \Gamma \vdash x : w}{P, \Gamma \vdash x := E : w' \Rightarrow w}$

$\frac{\text{(T-Ctx-Assign1)} \quad \begin{array}{l} P, \Gamma \vdash E : w' \Rightarrow \text{ptr } w \\ P, \Gamma \vdash e_2 : w \end{array}}{P, \Gamma \vdash *E := e_2 : w' \Rightarrow \text{int}}$	$\frac{\text{(T-Ctx-Assign2)} \quad \begin{array}{l} P, \Gamma \vdash E : w' \Rightarrow w \\ P, \Gamma \vdash v : \text{ptr } w \end{array}}{P, \Gamma \vdash *v := E : w' \Rightarrow \text{int}}$
$\frac{\text{(T-Ctx-Arr-Assign1)} \quad \begin{array}{l} P, \Gamma \vdash E : w' \Rightarrow \text{ptr array } \tau \\ P, \Gamma \vdash e_2 : \text{int} \\ P, \Gamma \vdash e_3 : \tau \end{array}}{P, \Gamma \vdash E[e_2] := e_3 : w' \Rightarrow \text{int}}$	$\frac{\text{(T-Ctx-Arr-Assign2)} \quad \begin{array}{l} P, \Gamma \vdash E : w \Rightarrow \text{int} \\ P, \Gamma \vdash a^{(b, \ell, w', n_{\text{id}})} : \text{ptr array } \tau \\ P, \Gamma \vdash e : \tau \end{array}}{P, \Gamma \vdash a^{(b, \ell, \tau, n_{\text{id}})}[E] := e : w \Rightarrow \text{int}}$
$\frac{\text{(T-Ctx-Arr-Assign3)} \quad \begin{array}{l} P, \Gamma \vdash E : w \Rightarrow \tau \\ P, \Gamma \vdash a^{(b, \ell, \tau, n_{\text{id}})} : \text{ptr array } \tau \\ P, \Gamma \vdash n : \text{int} \end{array}}{P, \Gamma \vdash a^{(b, \ell, \tau, n_{\text{id}})}[n] := E : w \Rightarrow \text{int}}$	$\frac{\text{(T-Ctx-Seq)} \quad \begin{array}{l} P, \Gamma \vdash E : w' \Rightarrow w_1 \\ P, \Gamma \vdash e : w \end{array}}{P, \Gamma \vdash E; e : w' \Rightarrow w}$
$\frac{\text{(T-Ctx-Deref)} \quad \begin{array}{l} P, \Gamma \vdash E : w' \Rightarrow \text{ptr } w \end{array}}{P, \Gamma \vdash *E : w' \Rightarrow w}$	$\frac{\text{(T-Ctx-Arr-Deref1)} \quad \begin{array}{l} P, \Gamma \vdash E : w \Rightarrow \text{ptr array } \tau \\ P, \Gamma \vdash e : \text{int} \end{array}}{P, \Gamma \vdash E[e] : w \Rightarrow \tau}$
$\frac{\text{(T-Ctx-Arr-Deref2)} \quad \begin{array}{l} P, \Gamma \vdash a^{(b, \ell, \tau, n_{\text{id}})} : \text{ptr array } \tau \\ P, \Gamma \vdash E : w \Rightarrow \text{int} \end{array}}{P, \Gamma \vdash a^{(b, \ell, \tau, n_{\text{id}})}[E] : w \Rightarrow \tau}$	$\frac{\text{(T-Ctx-Struct-field)} \quad \begin{array}{l} P, \Gamma \vdash E : w \Rightarrow \text{ptr struct } s \\ P.D(s) = \dots; \tau_f f; \dots \end{array}}{P, \Gamma \vdash \&E \rightarrow f : w \Rightarrow \text{ptr } \tau_f}$
$\frac{\text{(T-Ctx-Branch)} \quad \begin{array}{l} P, \Gamma \vdash E : w' \Rightarrow \text{int} \\ P, \Gamma \vdash e_1 : w \\ P, \Gamma \vdash e_2 : w \end{array}}{P, \Gamma \vdash \text{if } E \text{ then } e_1 \text{ else } e_2 : w' \Rightarrow w}$	
$\frac{\text{(T-Ctx-Call)} \quad \begin{array}{l} w_r \ g(x : w_1) \mapsto (y_j : w_j)^*, e_g \in P.F \\ P, \Gamma \vdash E : w \Rightarrow w_1 \\ P, \Gamma \vdash y : w_r \end{array}}{P, \Gamma \vdash y := \text{call } g(E) : w \Rightarrow w_r}$	
<div style="border: 1px solid black; padding: 5px; display: inline-block;">Source Typing Evaluation Contexts <math>P, \Gamma \vdash E : w \Rightarrow w'</math></div>	

## 2.3 Source Semantics

The semantics follow these judgements:

$M \vdash P, K, H, A \triangleright e \xrightarrow{\alpha} P, K', H', A' \triangleright e'$	primitive, labelled reductions
$\Omega \xRightarrow{\alpha} \Omega'$	single top-level small-step reduction
$\Omega \xRightarrow{\bar{\alpha}^*} \Omega'$	reflexive-transitive closure of $\Rightarrow$
$P, K, H, A \triangleright e \Downarrow_{\bar{\alpha}} P, K', H', A' \triangleright v$	big step trace semantics



Stacks work as follows: when a call is done, stuff is pushed from the current pointer, erasing the next entry. When a return is done, the current pointer is shifted back by one. A stack contains a list of mappings from vars to values.

$$\begin{aligned}
K &::= \emptyset \mid \theta; K \\
\theta &::= \emptyset \mid x \mapsto v; \theta \\
\Omega &::= P, K, H, A \triangleright (e, E^*)
\end{aligned}$$

Some helpers. The initial state has an infinite stack.

$$\begin{array}{c}
\text{(Initial)} \\
\frac{(w \text{ main}(x : w)) \mapsto (y : w)^*, e \in P.F}{\Omega_0(P) = P, \theta_0, H_0, A \triangleright (e, \emptyset)}
\end{array}
\quad
\begin{array}{c}
\text{(Stack-top-update)} \\
\frac{K = \theta; K^* \quad K' = \theta[x / n]; K^*}{K + x \mapsto n = K'}
\end{array}$$

$$\begin{array}{c}
\text{(S-Var)} \\
\frac{x \mapsto n' \in \theta}{M \vdash P, \theta; K, H, A \triangleright x \xrightarrow{\epsilon} P, \theta; K, H, A \triangleright n'} \\
\text{(S-BinOp)} \\
\frac{n = n_1 \oplus n_2}{M \vdash P, K, H, A \triangleright n_1 \oplus n_2 \xrightarrow{\epsilon} P, K, H, A \triangleright n} \\
\text{(S-Ptr-Arith)} \\
\frac{M \vdash P, K, H, A \triangleright a^{(b, \ell, w, n_{id})} \oplus n \xrightarrow{\epsilon} P, K, H, A \triangleright (a + n)^{(b, \ell, w, n_{id})}}{M \vdash P, K, H, A \triangleright x := v \xrightarrow{\epsilon} P, K', H, A \triangleright v} \\
\text{(S-Var-Assign)} \\
\frac{K' = K + x \mapsto v}{M \vdash P, K, H, A \triangleright x := v \xrightarrow{\epsilon} P, K', H, A \triangleright v} \\
\text{(S-Assign)} \\
\frac{H' = H[a \mapsto v]}{M \vdash P, K, H, A \triangleright *a^{(b, \ell, w, n_{id})} := v \xrightarrow{\text{write}(a^{(b, \ell, w, n_{id})}, v)} P, K, H', A \triangleright 0} \\
\text{(S-Assign-Forge)} \\
\frac{H' = H[a \mapsto v]}{M \vdash P, K, H, A \triangleright *a := v \xrightarrow{\text{fwrite}(a, v)} P, K, H', A \triangleright 0} \\
\text{(S-Arr-Assign)} \\
\frac{H' = H[a + n \mapsto v]}{M \vdash P, K, H, A \triangleright a^{(b, \ell, w, n_{id})}[n] := v \xrightarrow{\text{write}((a+n)^{(b, \ell, w, n_{id})}, v)} P, K, H', A \triangleright 0} \\
\text{(S-Sequence)} \\
\frac{M \vdash P, K, H, A \triangleright v; e \xrightarrow{\epsilon} P, K, H', A \triangleright e}{M \vdash P, K, H, A \triangleright \text{if } n \text{ then } e_1 \text{ else } e_2 \xrightarrow{\epsilon} P, K, H, A \triangleright e_i} \\
\text{(S-Ife)} \\
i = \begin{cases} 1 & \text{if } n = 1 \\ 2 & \text{if } n = 0 \end{cases} \\
\text{(S-Dereference)} \\
\frac{H[a] = v}{M \vdash P, K, H, A \triangleright *a^{(b, \ell, w, n_{id})} \xrightarrow{\text{read}(a^{(b, \ell, w, n_{id})}, v)} P, K, H, A \triangleright v} \\
\text{(S-Dereference-Forge)} \\
\frac{H[a] = v}{M \vdash P, K, H, A \triangleright *a \xrightarrow{\text{fread}(a, v)} P, K, H, A \triangleright v} \\
\text{(S-Arr-Dereference)} \\
\frac{H[a + n] = v}{M \vdash P, K, H, A \triangleright a^{(b, \ell, w, n_{id})}[n] \xrightarrow{\text{read}((a+n)^{(b, \ell, w, n_{id})}, v)} P, K, H, A \triangleright v} \\
\text{(S-Struct-Field)} \\
\frac{D(s) = \{f_1 : \tau_1, \dots, f_k : \tau_k \dots f_n : \tau_n\} \quad o_1 = k - 1}{M \vdash P, K, H, A \triangleright \&a^{(b, 1, \text{struct } s, n_{id})} \rightarrow f_k \xrightarrow{\epsilon} P, K, H, A \triangleright (a + o_1)^{(b+o_1, 1, \tau_k, n_{id})}}
\end{array}$$

$$\begin{array}{c}
\text{(Struct-Field-Forge)} \\
\frac{D(s) = \{f_1 : \tau_1 \dots f_k : \tau_k \dots\}}{M \vdash P, K, H, A \triangleright \&a \rightarrow f_k \xrightarrow{\epsilon} P, K, H, A \triangleright a + k} \\
\text{(S-Malloc-Single)} \\
\frac{w \neq \text{array} \quad \text{alloc}(a^{(a,1,w,n_{id})})}{\langle H, A \rangle \xrightarrow{\text{alloc}(a^{(a,1,w,n_{id})})} \langle H', A' \rangle} \\
\hline
M \vdash P, K, H, A \triangleright \text{malloc}(w) \xrightarrow{\text{alloc}(a^{(a,1,w,n_{id})})} P, K, H', A' \triangleright a^{(a,1,w,n_{id})} \\
\text{(S-Malloc-Array)} \\
\frac{\langle H, A \rangle \xrightarrow{\text{alloc}(a^{(a,\ell,\tau,n_{id})})} \langle H', A' \rangle}{M \vdash P, K, H, A \triangleright \text{malloc}(\tau, \ell) \xrightarrow{\text{alloc}(a^{(a,\ell,\tau,n_{id})})} P, K, H', A' \triangleright a^{(a,\ell,\tau,n_{id})}} \\
\text{(S-Free)} \\
\frac{\langle H, A \rangle \xrightarrow{\text{free}(a^{(b,\ell,w,n_{id})})} \langle H', A' \rangle}{M \vdash P, K, H, A \triangleright \text{free}(a^{(b,\ell,w,n_{id})}) \xrightarrow{\text{free}(a^{(b,\ell,w,n_{id})})} P, K, H', A' \triangleright 0} \\
\text{(S-Free-Nop)} \\
\frac{\langle H, A \rangle \xrightarrow{\text{free}(a^{(b,\ell,w,n_{id})})} \langle H, A \rangle}{M \vdash P, K, H, A \triangleright \text{free}(a^{(b,\ell,w,n_{id})}) \xrightarrow{\text{free}(a^{(b,\ell,w,n_{id})})} P, K, H, A \triangleright 0} \\
\text{(S-Free-Forge)} \\
\frac{\langle H, A \rangle \xrightarrow{\text{ffree } a} \langle H', A' \rangle}{M \vdash P, K, H, A \triangleright \text{free}(a) \xrightarrow{\text{ffree } a} P, K, H', A' \triangleright 0} \\
\hline
\text{Redex step: } M \vdash P, K, H, A \triangleright e \xrightarrow{\alpha} P, K', H', A' \triangleright e' \\
\hline
\text{(S-Step)} \\
\frac{M \vdash P, K, H, A \triangleright e \xrightarrow{\alpha} P, K', H', A' \triangleright e'}{P, K, H, A \triangleright (E[e], E^*) \xRightarrow{\alpha} P, K', H', A' \triangleright (E[e'], E^*)} \\
\text{(S-Call)} \\
\frac{\begin{array}{l} (w \ g(x' : w')) \mapsto ((y_j : w_j)^*, e_b) \in P.F \\ K' = K; \{x' \mapsto n', y_0 \mapsto 0, y_1 \mapsto 0, \dots\} \end{array}}{P, K, H, A \triangleright (E_1 [x := \text{call } g(n')], E^*) \xRightarrow{\epsilon} P, K', H, A \triangleright (e_b, E_1 [x := [\cdot]] : E^*)} \\
\text{(S-Return)} \\
\frac{K' = K; \{y \mapsto \dots\}}{P, K', H, A \triangleright (v, E_1 : E^*) \xRightarrow{\epsilon} P, K, H, A \triangleright (E_1 [v], E^*)} \\
\hline
\text{Source Top-level step: } P, K, H, A \triangleright (e, E^*) \xRightarrow{\alpha} P, K', H', A' \triangleright (e', E'^*)
\end{array}$$

$$\begin{array}{c}
\text{(S-Base)} \\
\hline
\Omega \xRightarrow{\epsilon^*} \Omega \\
\text{(S-Steps)} \\
\Omega \xRightarrow{\alpha} \Omega' \quad \Omega' \xRightarrow{\bar{\alpha}^*} \Omega'' \\
\hline
\Omega \xRightarrow{\alpha \cdot \bar{\alpha}^*} \Omega''
\end{array}$$

---

Source Trace Semantics

---

$$\begin{array}{c}
\text{(Src-Alloc)} \\
\begin{array}{l}
s_F^* = s_1^* \cdot (a, \ell', w', n_{id}') \cdot s_2^* \quad 0 < \ell \leq \ell' \quad a + \ell < |H| \\
H' = H[a + j \mapsto 0 \mid j \in 0..\ell - 1] \quad Y' = Y \cdot \text{alloc}(a^{(a, \ell, w, n_{id})}) \\
s_F'^* = s_1^* \cdot (a + n, \ell' - \ell, w', n_{id}') \cdot s_2^* \quad \text{fresh}(n_{id}) \quad s_A'^* = (a, \ell, w, n_{id}) \cdot s_U^*
\end{array} \\
\hline
\langle H, (s_A^*, s_F^*, Y) \rangle \xrightarrow{\text{alloc}(a^{(a, \ell, w, n_{id})})} \langle H', (s_A'^*, s_F'^*, Y') \rangle \\
\text{(Src-Free)} \\
\begin{array}{l}
s_A^* = s_1^* \cdot (a, \ell, w, n_{id}) \cdot s_2^* \quad s_F'^* = (a, \ell, w, n_{id}) \cdot s_F^* \\
s_A'^* = s_1^* \cdot s_2^* \quad Y' = Y \cdot \text{free}(a^{(a, \ell, w, n_{id})})
\end{array} \\
\hline
\langle H, (s_A^*, s_F^*, Y) \rangle \xrightarrow{\text{free}(a^{(a, \ell, w, n_{id})})} \langle H, (s_A'^*, s_F'^*, Y') \rangle \\
\text{(Src-Free-Nop)} \\
(\nexists (a, \_, \_, n_{id}) \in s_A^*) \vee (\text{free}(a^{(b, \ell, w, n_{id})}) \in Y) \\
\hline
\langle H, (s_A^*, s_F^*, Y) \rangle \xrightarrow{\text{free}(a^{(b, \ell, w, n_{id})})} \langle H, (s_A^*, s_F^*, Y) \rangle \\
\text{(Src-Free-Forged)} \\
s_A^* = s_1^* \cdot (a, \_, \_, \_) \cdot s_2^* \quad s_F^* = (a, \_, \_, \_) \cdot s_F^* \quad s_A'^* = s_1^* \cdot s_2^* \\
\hline
\langle H, (s_A^*, s_F^*, Y) \rangle \xrightarrow{\text{ffree } a} \langle H, (s_A^*, s_F^*, Y) \rangle \\
\text{(Src-Free-Forged-Nop)} \\
(a, \_, \_, \_) \notin s_A^* \\
\hline
\langle H, (s_A^*, s_F^*, Y) \rangle \xrightarrow{\text{ffree } a} \langle H, (s_A'^*, s_F'^*, Y') \rangle
\end{array}$$

---

Source allocator semantics

---

## 2.4 Source Examples

**Example 1.** The below snippet of code allocates a pointer to array of 10 integers and then assigns 42 to array index 2.

```

1  x := malloc(ptr(array int)); /* Allocate a pointer to array */
2  y := malloc(int, 10);       /* Allocate an array of size 10 int */
3  *x := y;                    /* Contents of pointer point to the above array */
4  **x ⊕ 2 := 42;              /* y[2] := 42 */
5  **x ⊕ 2                      /* Output y[2] */

```

The type of `x` is `ptr(array int)`; type of `y` is `array int` and the type of `*(x ⊕ 2)` is `int`.

## 2.5 Source Memory Safety

**Definition 1** (Source Color Assignment). The source color assignment,  $\delta$ , is a map from source address to the abstract memory location and the tuple of color ( $\mathbb{N}$ ) and shades ( $\mathbb{N}$ ). Without loss of generality, we use shade 0 for array allocations.

$$\delta : \text{Address} \times \text{Tid} \rightarrow \mathbb{N} \times \{\mathbb{N}, \mathbb{N}\}$$

**Definition 2** (Valid  $\delta$ ). A  $\delta : \text{Address} \times \text{Tid} \rightarrow \mathbb{N} \times \{\mathbb{N}, \mathbb{N}\}$  is a valid coloring scheme, denoted  $\vdash \delta$ , if the mapped colors are unique.

**Definition 3** (Trace Agreement ( $\alpha =_\delta \alpha$ )).

$$\begin{array}{c}
 \text{(Tr-Concatenate)} \\
 \frac{\alpha =_\delta \bar{\alpha}_1 \quad \bar{\alpha} =_\delta \bar{\alpha}_2}{\alpha \cdot \bar{\alpha} =_\delta \bar{\alpha}_1 \cdot \bar{\alpha}_2} \\
 \\
 \text{(Tr-Read-Authentic)} \\
 \frac{n = \text{srcsz}(w) \quad \delta(b, n_{\text{id}}) = b^{(c,s)} \quad o = a - b \quad a = b + (o * n)}{\text{read}(a^{(b, \ell, w, n_{\text{id}})}, v) =_\delta \text{read}(a^{(c,s)}) \cdot \text{read}((a+1)^{(c,s)}) \cdot \dots \cdot \text{read}((a+n-1)^{(c,s)})} \\
 \\
 \text{(Tr-Write-Authentic)} \\
 \frac{n = \text{srcsz}(w) \quad \delta(b, n_{\text{id}}) = b^{(c,s)} \quad o = a - b \quad a = b + (o * n)}{\text{write}(a^{(b, \ell, w, n_{\text{id}})}, v) =_\delta \text{write}(a^{(c,s)}) \cdot \text{write}((a+1)^{(c,s)}) \cdot \dots \cdot \text{write}((a+n-1)^{(c,s)})} \\
 \\
 \text{(Tr-SAlloc)} \\
 \frac{n = \text{srcsz}(w) * n \quad \delta(a, n_{\text{id}}) = a^{(c,0)} \quad \phi_0 = \lambda \_ . 0}{\text{salloc}(a, n, \tau, n_{\text{id}}) =_\delta \text{alloc}(n, a^c, \phi_0)} \\
 \\
 \text{(Tr-SAlloc-Struct)} \\
 \frac{\begin{array}{l} D(s) = \{f_0 : \tau_0, \dots, f_{n-1} : \tau_{n-1}\} \\ \forall i \in \{0..n-1\}. \phi_i = [\Sigma_{k=0}^{i-1} \text{srcsz}(\tau_k) + j \mapsto i \mid j \in \{0..\text{srcsz}(\tau_i) - 1\}] \\ \phi = \bigcup_i \phi_i \quad \forall i \in \{0..n-1\}. \delta(a+i, n_{\text{id}}) = (a + \Sigma_{k=0}^{i-1} \text{srcsz}(\tau_k))^{(c, \phi(\Sigma_{k=0}^{i-1} \text{srcsz}(\tau_k)))} \end{array}}{\text{salloc}(a, 1, \text{struct } s, n_{\text{id}}) =_\delta \text{alloc}(\text{srcsz}(\text{struct } s), a^c, \phi)} \\
 \\
 \text{(Tr-SFree)} \\
 \frac{\delta(b, n_{\text{id}}) = a^{(c, -)}}{\text{free}(a^{(b, \ell, w, n_{\text{id}})}) =_\delta \text{sfree}(a^c)}
 \end{array}$$

**Lemma 3** (Empty Source Trace). If  $\alpha =_\delta \bar{\alpha}$  and  $\bar{\alpha} = \epsilon$ , then  $\alpha = \epsilon$ .

*Proof.* Case analysis on  $\alpha =_\delta \bar{\alpha}$ . □

**Lemma 4** (Splitting Source Trace Equivalence). If  $\alpha \cdot \bar{\alpha} =_\delta \bar{\alpha}$  then  $\exists \bar{\alpha}', \bar{\alpha}'',$

1.  $\bar{\alpha} = \bar{\alpha}' \cdot \bar{\alpha}''$  and
2.  $\alpha =_{\delta} \bar{\alpha}'$  and
3.  $\bar{\alpha} =_{\delta} \bar{\alpha}''$

*Proof.* Inverting rule (Tr-Concatenate) gives us the required proof.  $\square$

**Lemma 5** (Monotonicity of Address Map-1). If  $\alpha =_{\delta} \bar{\alpha}$  and  $\delta' \supseteq \delta$ , then  $\alpha =_{\delta'} \bar{\alpha}$ .

*Proof.* Case analysis on  $\alpha =_{\delta} \bar{\alpha}$ . Note that the coloring scheme of  $\delta$  is preserved in  $\delta'$ , hence the proof follows.  $\square$

**Definition 4** (Segment Agreement  $T =_{\delta} A$ ).

$$\begin{array}{c}
\text{(Allocator-Shadow-Memory-Agreement)} \\
\forall (\mathbf{b}, \mathbf{n}_{\text{id}}, a^{(c,s)}) \in \delta \\
\forall X \in \{A, F\} \\
\exists \tau, \ell. (\mathbf{b}, \ell, \tau, \mathbf{n}_{\text{id}}) \in \mathbf{s}_X^* \iff \forall i \in \{0.. \ell \times \text{srcsz}(\tau) - 1\}. T(a + i) = X(c, s) \\
\vee \\
\exists \mathbf{s}. (\mathbf{b}, \mathbf{1}, \text{struct } \mathbf{s}, \mathbf{n}_{\text{id}}) \in \mathbf{s}_X^* \wedge \mathbf{D}(\mathbf{s}) = \{\mathbf{f}_1 : \tau_1, \mathbf{f}_2 : \tau_2 \dots \mathbf{f}_k : \tau_k\} \iff \\
\forall i \in \{0..k - 1\}. a_i^{(c,s_i)} = \delta(\mathbf{b} + i, \mathbf{n}_{\text{id}}) \wedge \\
a_i = a_0 + \sum_{h=0}^{i-1} \text{srcsz}(\tau_h) \wedge \\
\forall j \in 0..\text{srcsz}(\tau_i) - 1. T(a_i + j) = X(c, s_i) \\
\mathbf{Y} =_{\delta} T.Y \\
\hline
T =_{\delta} \mathbf{s}_A^*, \mathbf{s}_F^*, \mathbf{Y}
\end{array}$$

**Definition 5** (Top-level Memory Safety for C-like).  $\text{MS}(\bar{\alpha}) \stackrel{\text{def}}{=} \exists \bar{\alpha}, \delta,$

1.  $\bar{\alpha} =_{\delta} \bar{\alpha}$  and
2.  $\text{MS}(\bar{\alpha})$

### 3 Target Language: MSWasm

We first describe the basic Wasm language semantics and then incrementally add MSWasm features.

### 3.1 Wasm Syntax

$$\begin{aligned}
& \text{*Indexes } \mathbf{n} \in \mathbb{N} \\
& \text{*Value Types } \tau ::= \mathbf{u32} \mid \mathbf{s32} \mid \dots \\
& \text{*Instr. Types } \rho ::= \tau^* \rightarrow \tau^* \\
& \text{+Import Types } \beta ::= \rho^* \\
& \text{+Module Types } \mu ::= \beta \rightarrow \beta \\
& \text{*Typing Ctx. } \Gamma ::= \{\mathbf{funcs} \rho^*, \mathbf{locals} \tau^*, \mathbf{return} \tau^*\} \\
& \text{*Instructions } \mathbf{i} ::= \mathbf{nop} \mid \mathbf{trap} \mid \tau.\mathbf{const} \mathbf{c} \mid \tau.\otimes \mid \mathbf{get} \mathbf{n} \mid \mathbf{set} \mathbf{n} \\
& \quad \mid \tau.\mathbf{load} \mathbf{n} \mid \tau.\mathbf{store} \mathbf{n} \mid \mathbf{dup} \mid \mathbf{drop} \mid \mathbf{branch} \mathbf{i}^* \mid \mathbf{i}^* \\
& \quad \mid \mathbf{call} \mathbf{n} \mid \mathbf{return} \mid \mathbf{alloc} \mathbf{n} \mid \mathbf{free} \mathbf{n} \\
& \text{*Functions } \mathbf{f} ::= \{\mathbf{var} \tau^*, \mathbf{body} \mathbf{i}^*\} \\
& \text{*Imports } \mathbf{Imp} ::= \{\mathbf{funcs} \mathbf{n}^*\} \\
& \text{*Functions } \Phi^* ::= (\mathbf{f} : \rho)^* \\
& \text{*Module Dfs. } \mathbf{M} ::= \{\mathbf{heaps} \mathbf{u32}, \mathbf{funcs} \Phi^*, \mathbf{import} \mathbf{Imp}\}
\end{aligned}$$

## Modules Syntax.

$$\begin{aligned}
& \text{*Module Id. } \mathbf{m} \\
& \text{*Funs. and Memories Id. } \pi ::= \mathbf{n} \\
& \quad \text{*Bytes } \mathbf{b} \in \{0, \dots, 2^8 - 1\} \\
& \quad \text{*Heaps } \mathbf{H} ::= \mathbf{b}^* \\
& \quad \text{*Values } \mathbf{v} ::= \mathbf{c} \mid \dots \\
& \quad + \text{Local Maps } \theta \in \mathbb{N} \rightarrow \mathbf{Value} \\
& \quad \text{*Local Frames } \mathbf{F} ::= (\theta, \mathbf{i}^*, \mathbf{v}^*) \\
& \quad \text{*Stack Frames } \mathbf{S} ::= \mathbf{F}_\pi \\
& \quad \quad \text{Slots } \mathbf{s} ::= (\mathbf{n}, \mathbf{n}) \\
& \quad \text{Allocator States } \mathbf{A} ::= (\mathbf{s}^*, \mathbf{s}^*) \\
& \quad \text{Module Stores } \Sigma, \mathbf{I} \in \langle \mathbf{H}, \mathbf{A} \rangle \\
& \quad + \text{Local Configurations } \mathbf{L} ::= \langle \Sigma, \mathbf{F} \rangle \\
& \quad \text{*Configurations } \Omega ::= \langle \Sigma, \mathbf{S}^* \rangle \\
& \quad \text{Local Events } \gamma ::= \mathbf{read}_\tau(\mathbf{a}) \mid \mathbf{write}_\tau(\mathbf{a}) \mid \mathbf{alloc}(\mathbf{a}, \mathbf{n}) \mid \mathbf{free}(\mathbf{a})
\end{aligned}$$

$$\begin{aligned} & | \text{call}(\pi, \mathbf{v}^*, \mathbf{i}^*) | \text{return}(\mathbf{v}^*) | \text{trap} | \epsilon \\ \text{Events } \alpha ::= & \text{read}_{\tau}(\mathbf{a}) | \text{write}_{\tau}(\mathbf{a}) | \text{alloc}(\mathbf{a}, \mathbf{n}) | \text{free}(\mathbf{a}) \\ & | \text{trap} | \epsilon \end{aligned}$$


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Runtime instances.
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### 3.2 Wasm Static Semantics

$\frac{}{\Gamma \vdash \text{nop} : [] \rightarrow []}$ <p>(Nop)</p>	$\frac{}{\Gamma \vdash \text{trap} : \tau_1^* \rightarrow \tau_2^*}$ <p>(Trap)</p>
$\frac{\tau \neq \text{handle}}{\Gamma \vdash \tau.\text{const } c : [] \rightarrow [\tau]}$ <p>(Const)</p>	$\frac{\tau \neq \text{handle}}{\Gamma \vdash \tau.\otimes : [\tau, \tau] \rightarrow [\tau]}$ <p>(Bop)</p>
$\frac{\Gamma.\text{local}[n] = \tau}{\Gamma \vdash \text{get } n : [] \rightarrow [\tau]}$ <p>(Get)</p>	$\frac{\Gamma.\text{local}[n] = \tau}{\Gamma \vdash \text{set } n : [\tau] \rightarrow []}$ <p>(Set)</p>
$\frac{n < \Gamma.\text{heaps}}{\Gamma \vdash \tau.\text{load } n : [\text{i32}] \rightarrow [\tau]}$ <p>(Load)</p>	$\frac{n < \Gamma.\text{heaps}}{\Gamma \vdash \tau.\text{write } n : [\text{i32}, \tau] \rightarrow []}$ <p>(Store)</p>
$\frac{n < \Gamma.\text{heaps}}{\Gamma \vdash \text{alloc } n : [\text{i32}] \rightarrow [\text{i32}]}$ <p>(Alloc)</p>	$\frac{n < \Gamma.\text{heaps}}{\Gamma \vdash \text{free } n : [\text{i32}] \rightarrow []}$ <p>(Free)</p>
$\frac{\Gamma.\text{funcs}[n] = \rho}{\Gamma \vdash \text{call } n : \rho}$ <p>(Call)</p>	$\frac{\Gamma.\text{return} = \tau_1^*}{\Gamma \vdash \text{return} : \tau_1^* \tau^* \rightarrow \tau_2^*}$ <p>(Return)</p>
$\frac{j \in \{1, 2\} \quad \Gamma \vdash i_j^* : [] \rightarrow []}{\Gamma \vdash \text{branch } i_1^* i_2^* : [\text{i32}] \rightarrow []}$ <p>(If)</p>	$\frac{}{\Gamma \vdash \epsilon : \tau^* \rightarrow \tau^*}$ <p>(Instr-Empty)</p>
$\frac{\Gamma \vdash i_1^* : \tau_1^* \rightarrow \tau_2^* \quad \Gamma \vdash i_2^* : \tau_2^* \rightarrow \tau_3^*}{\Gamma \vdash i_1^* i_2^* : \tau_1^* \rightarrow \tau_3^*}$ <p>(Instr-Seq)</p>	$\frac{\Gamma \vdash i^* : \tau_1^* \rightarrow \tau_2^*}{\Gamma \vdash i^* : \tau_1^* \tau \rightarrow \tau_2^* \tau}$ <p>(Instr-Weaken)</p>
<div style="border: 1px solid black; padding: 5px; display: inline-block;"> Wasm Instructions: <math>\Gamma \vdash i : \rho</math> and <math>\Gamma \vdash i^* : \rho</math>. </div>	
$\frac{\Gamma\{\text{local} = \tau^a \tau^v \text{return} = \tau^*\} \vdash i^* : [] \rightarrow \tau^*}{\Gamma \vdash \{\text{var } \tau^v, \text{body } i^*\} : \tau^a \rightarrow \tau^*}$ <p>(Fun)</p>	
$\frac{\Gamma = \{\text{funcs} = \rho_I^* \rho^n\} \quad  \Phi^*  = n \quad \forall f_j \in \Phi^*. \Gamma \vdash f_j : \rho_j \quad \text{Imp} = \{\text{funcs } m^*\} \quad \rho_I^* = [\Gamma.\text{funcs}[x] \mid x \in m^*]}{\vdash \{\text{heaps } u32, \text{funcs } \Phi^*, \text{import Imp}\}}$ <p>(Module)</p>	
<div style="border: 1px solid black; padding: 5px; display: inline-block;"> Functions <math>\Gamma \vdash f : \rho</math>, module definitions <math>\vdash M</math>. </div>	

## Instance Types.

Instance Ctx.  $\Delta \in \pi \rightarrow \Gamma$

Other types and contexts.

$$\begin{array}{c}
 \text{(Stack-Empty)} \quad \frac{}{\vdash \epsilon : []} \quad \text{(Handle)} \quad \frac{}{\vdash \langle \mathbf{n}_{\text{base}}, \mathbf{n}_{\text{offset}}, \mathbf{n}_{\text{bound}}, \mathbf{b}_{\text{valid}} \rangle : \text{handle}} \\
 \text{(Stack-Cons)} \quad \frac{\vdash \mathbf{v} : \tau \quad \vdash \mathbf{v}^* : \tau^*}{\vdash \mathbf{vv}^* : \tau\tau^*} \quad \text{(Local-Map)} \quad \frac{\forall \tau_{\mathbf{i}} \in \tau^*. \theta(\mathbf{i}) = \tau_{\mathbf{i}}}{\vdash \theta : \tau^*}
 \end{array}$$

Values stacks  $\vdash \mathbf{v}^* : \tau^*$ , and local maps  $\vdash \theta : \tau^*$ .

$$\begin{array}{c}
 \text{(Local-Frame)} \quad \frac{\mathbf{F} = \langle \theta, \mathbf{i}^*, \mathbf{v}^* \rangle \quad \vdash \theta : \Gamma.\text{locals} \quad \vdash \mathbf{v}^* : \tau_{\mathbf{v}}^* \quad \Gamma \vdash \mathbf{i}^* : \rho \quad \rho = \tau_{\mathbf{v}}^* \rightarrow \tau_{\mathbf{2}}^*}{\Gamma \vdash \mathbf{F} : \tau_{\mathbf{v}}^* \rightarrow \tau_{\mathbf{2}}^*} \\
 \text{(Stack-Frame)} \quad \frac{\Delta(\mathbf{n}) \vdash \mathbf{F} : \rho}{\Delta \vdash \mathbf{F}_{\mathbf{n}} : \rho} \quad \text{(FStack-Empty)} \quad \frac{}{\Gamma \vdash \epsilon : [] \rightarrow []} \\
 \text{(FStack-Cons)} \quad \frac{\Delta \vdash \mathbf{F}_{\mathbf{n}} : \tau_1^* \rightarrow \tau_2^* \quad \Delta \vdash \mathbf{S}^* : \tau_2^* \rightarrow \tau_3^*}{\Delta \vdash \mathbf{F}_{\mathbf{n}}\mathbf{S}^* : \tau_1^* \rightarrow \tau_3^*}
 \end{array}$$

Frame stacks frame stacks  $\Delta \vdash \mathbf{S}^* : \rho$ , frames  $\Delta \vdash \mathbf{S} : \rho$ , and local frames  $\Delta \vdash \mathbf{S}^* : \rho$ .

$$\begin{array}{c}
 \text{(MInst)} \quad \Sigma = \langle \mathbf{H}, \mathbf{A} \rangle \\
 \text{What about typing functions?} \quad \frac{}{\Delta \vdash \Sigma} \quad \text{Instance typing: } \vdash \Sigma : \beta.
 \end{array}$$

$$\begin{array}{c}
 \text{(Stack)} \quad \frac{\Delta \vdash \mathbf{S}^* : \rho}{\Delta \vdash \mathbf{S}^*} \quad \text{(Config)} \quad \frac{\vdash \Sigma \quad \Delta \vdash \mathbf{S}^*}{\Delta \vdash \langle \Sigma, \mathbf{S}^* \rangle}
 \end{array}$$

Typing for stores  $\vdash \Sigma$ , Stacks and configurations  $\Delta \vdash \Omega$

### 3.3 Wasm Dynamic Semantics

- Semantics for a basic block-based allocator  $(\mathbf{H}, \mathbf{A}) \xrightarrow{\alpha} (\mathbf{H}, \mathbf{A})$ .
- Intra-function semantics  $\Phi^* \vdash \langle \Sigma, \mathbf{F} \rangle \xrightarrow{\gamma} \langle \Sigma, \mathbf{F} \rangle$ .
- Top-level semantics  $\Phi^* \vdash \Omega \xrightarrow{\alpha} \Omega$ .
- Trace semantics  $\Phi^* \vdash \Omega \xRightarrow{\bar{\alpha}} \Omega$ .

$$\begin{array}{c}
\text{(Nop)} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{nop} : \mathbf{i}^*, \mathbf{v}^* \rangle \twoheadrightarrow \langle \Sigma, \theta, \mathbf{i}^*, \mathbf{v}^* \rangle \\
\text{(Const)} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{const } \mathbf{c} : \mathbf{i}^*, \mathbf{v}^* \rangle \twoheadrightarrow \langle \Sigma, \theta, \mathbf{i}^*, \mathbf{c} : \mathbf{v}^* \rangle \\
\text{(Bop)} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \tau.\otimes : \mathbf{i}^*, \mathbf{v1} : \mathbf{v2} : \mathbf{v}^* \rangle \twoheadrightarrow \langle \Sigma, \theta, \mathbf{i}^*, \llbracket \mathbf{v1} \oplus \mathbf{v2} \rrbracket : \mathbf{v}^* \rangle \\
\text{(Get)} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{get } \mathbf{x} : \mathbf{i}^*, \mathbf{v}^* \rangle \twoheadrightarrow \langle \Sigma, \theta, \mathbf{i}^*, \theta(\mathbf{x}) : \mathbf{v}^* \rangle \\
\text{(Set)} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{set } \mathbf{x} : \mathbf{i}^*, \mathbf{v} : \mathbf{v}^* \rangle \twoheadrightarrow \langle \Sigma, \theta[\mathbf{x} \mapsto \mathbf{v}], \mathbf{i}^*, \mathbf{v}^* \rangle \\
\text{(Load)} \\
\hline
\begin{array}{c}
\mathbf{H} = \Sigma.\mathbf{H} \quad \mathbf{a} + |\tau| < |\mathbf{H}| \\
\mathbf{H}[\mathbf{a} : \mathbf{a} + |\tau|] = \mathbf{b}^{|\tau|} \quad \mathbf{v} = \tau.\text{unpack}(\mathbf{b}^{|\tau|})
\end{array} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{loadn} : \mathbf{i}^*, \mathbf{a} : \mathbf{v}^* \rangle \xrightarrow{\text{read}_\tau(\mathbf{a})} \langle \Sigma, \theta, \mathbf{i}^*, \mathbf{v} : \mathbf{v}^* \rangle \\
\text{(Store)} \\
\hline
\begin{array}{c}
\mathbf{H} = \Sigma.\mathbf{H} \quad \mathbf{a} + |\tau| < |\mathbf{H}| \quad \mathbf{b}^{|\tau|} = \tau.\text{pack}(\mathbf{v}) \\
\mathbf{H}' = \mathbf{H}[\mathbf{a} + \mathbf{j} \mapsto \mathbf{b}_j \mid \mathbf{j} \in 0..|\tau| - 1] \quad \Sigma' = \Sigma_{\psi.\mathbf{H}}[(\pi.\mathbf{m}, \mathbf{n}) \mapsto \mathbf{H}']
\end{array} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{store } \mathbf{n} : \mathbf{i}^*, \mathbf{v} : \mathbf{a} : \mathbf{v}^* \rangle \xrightarrow{\text{write}_\tau(\mathbf{a})} \langle \Sigma', \theta, \mathbf{i}^*, \mathbf{v}^* \rangle
\end{array}$$

Wasm Intra-function semantics:  $\Phi^* \vdash \langle \Sigma, \mathbf{F} \rangle \xrightarrow{\gamma} \langle \Sigma, \mathbf{F} \rangle$ .

$$\begin{array}{c}
\text{(If-True)} \\
\mathbf{c} = \mathbf{0} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{branch } \mathbf{i}_1^* \mathbf{i}_2^* : \mathbf{i}^*, \mathbf{c} : \mathbf{v}^* \rangle \twoheadrightarrow \langle \Sigma, \theta, \mathbf{i}_1^*, \mathbf{v}^* \rangle \\
\text{(If-False)} \\
\mathbf{c} \neq \mathbf{0} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{branch } \mathbf{i}_1^* \mathbf{i}_2^* : \mathbf{i}^*, \mathbf{c} : \mathbf{v}^* \rangle \twoheadrightarrow \langle \Sigma, \theta, \mathbf{i}_2^*, \mathbf{v}^* \rangle \\
\text{(Trap)} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{trap} : \mathbf{i}^*, \mathbf{v}^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle
\end{array}$$

$$\begin{array}{c}
\text{(Load-Trap)} \\
\frac{\mathbf{H} = \Sigma.\mathbf{H} \quad \mathbf{a} + |\tau| \geq |\mathbf{H}|}{\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{loadn} : \mathbf{i}^*, \mathbf{a} : \mathbf{v}^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle} \\
\text{(Store-Trap)} \\
\frac{\mathbf{H} = \Sigma.\mathbf{H} \quad \mathbf{a} + |\tau| \geq |\mathbf{H}|}{\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{store } \mathbf{n} : \mathbf{i}^*, \mathbf{v} : \mathbf{a} : \mathbf{v}^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle}
\end{array}$$

Wasm Intra-function semantics (cont).

$$\begin{array}{c}
\text{(Call)} \\
\frac{\Sigma_\phi(\mathbf{n}) = \{\text{var } \tau^j, \text{body } \mathbf{i}_1^*\} : \rho \quad \rho = \tau^k \rightarrow \tau^* \quad \mathbf{v}_1^* = \mathbf{v}^k \mathbf{0}^j \quad \pi_1 = \mathbf{n}}{\Phi^* \vdash \langle \Sigma, \theta, \text{call } \mathbf{n} : \mathbf{i}^*, \mathbf{v}^k \mathbf{v}^* \rangle \xrightarrow{\text{call}(\pi_1, \mathbf{i}_1^*, \mathbf{v}_1^*)} \langle \Sigma, \theta, \mathbf{i}^*, \mathbf{v}^* \rangle} \\
\text{(Return-Explicit)} \\
\frac{\Sigma_\phi(\pi) = \{\_ \} : \tau^* \rightarrow \tau^k}{\Phi^* \vdash \langle \Sigma, \theta, \text{return} : \mathbf{i}^*, \mathbf{v}^k \mathbf{v}^* \rangle \xrightarrow{\text{return}(\mathbf{v}^k)} \langle \Sigma, \theta, \mathbf{i}^*, \mathbf{v}^* \rangle} \\
\text{(Return-Implicit)} \\
\frac{\Sigma_\phi(\pi) = \{\_ \} : \tau^* \rightarrow \tau^k}{\Phi^* \vdash \langle \Sigma, \theta, \epsilon, \mathbf{v}^k \mathbf{v}^* \rangle \xrightarrow{\text{return}(\mathbf{v}^k)} \langle \Sigma, \theta, \epsilon, \mathbf{v}^* \rangle}
\end{array}$$

Wasm Function call and return (intra-function semantics).

$$\begin{array}{c}
\text{(Ctx)} \\
\frac{\Phi^* \vdash \langle \Sigma, \mathbf{F} \rangle \xrightarrow{\gamma} \langle \Sigma', \mathbf{F}' \rangle \quad \gamma \notin \{\text{trap}, \text{call}(\_, \_, \_), \text{return}(\_)\}}{\Phi^* \vdash \langle \Sigma, \mathbf{F} : \mathbf{S}^* \rangle \xrightarrow{\gamma} \langle \Sigma', \mathbf{F}' : \mathbf{S}^* \rangle} \\
\text{(Ctx-Call)} \\
\frac{\Phi^* \vdash \langle \Sigma, \mathbf{F} \rangle \xrightarrow{\text{call}(\pi_2, \mathbf{i}^*, \mathbf{v}^*)} \langle \Sigma', \mathbf{F}' \rangle \quad \theta = [\mathbf{j} \mapsto \mathbf{v}_j \mid \mathbf{v}_j \in \mathbf{v}^*] \quad \mathbf{F}'' = \langle \theta, \mathbf{i}^*, \epsilon \rangle}{\Phi^* \vdash \langle \Sigma, \mathbf{F} : \mathbf{S}^* \rangle \rightarrow \langle \Sigma', \mathbf{F}'' : \mathbf{F}' : \mathbf{S}^* \rangle} \\
\text{(Ctx-Return)} \\
\frac{\Phi^* \vdash \langle \Sigma, \mathbf{F} \rangle \xrightarrow{\text{return}(\mathbf{v}^k)} \langle \Sigma', \mathbf{F}' \rangle \quad \mathbf{F}''_{\mathbf{b}} = \langle \theta, \mathbf{i}^*, \mathbf{v}^* \rangle \quad \mathbf{F}''_{\mathbf{a}} = \langle \theta, \mathbf{i}^*, \mathbf{v}^k \mathbf{v}^* \rangle}{\Phi^* \vdash \langle \Sigma, \mathbf{F}' : \mathbf{F}''_{\mathbf{b}} : \mathbf{S}^* \rangle \rightarrow \langle \Sigma', \mathbf{F}''_{\mathbf{a}} : \mathbf{S}^* \rangle} \\
\text{(Exit)} \\
\frac{\Phi^* \vdash \langle \Sigma, \mathbf{F} \rangle \xrightarrow{\text{return}(\epsilon)} \langle \Sigma', \mathbf{F}' \rangle}{\Phi^* \vdash \langle \Sigma, \mathbf{F}_{\mathbf{n}} : \square \rangle \rightarrow \langle \Sigma', \square \rangle} \\
\text{(Trap)} \\
\frac{\Phi^* \vdash \langle \Sigma, \mathbf{F} \rangle \xrightarrow{\text{trap}} \langle \Sigma, \mathbf{F}' \rangle}{\Phi^* \vdash \langle \Sigma, \mathbf{F}_{\mathbf{n}} : \mathbf{S}^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \square \rangle}
\end{array}$$

Wasm Inter-function Semantics:  $\Phi^* \vdash \Omega \xrightarrow{\alpha} \Omega$

$$\begin{array}{c}
\text{(Base)} \\
\hline
\Phi^* \vdash \langle \Sigma, S^* \rangle \xRightarrow{\epsilon} \langle \Sigma, S^* \rangle \\
\\
\text{(Step)} \\
\frac{\Phi^* \vdash \langle \Sigma, S^* \rangle \xrightarrow{\alpha} \langle \Sigma', S'^* \rangle \quad \Phi^* \vdash \langle \Sigma', S'^* \rangle \xRightarrow{\bar{\alpha}} \langle \Sigma'', S''^* \rangle}{\Phi^* \vdash \langle \Sigma, S^* \rangle \xRightarrow{\alpha \bar{\alpha}} \langle \Sigma'', S''^* \rangle}
\end{array}$$
  

Wasm Trace Semantics.  $\Phi^* \vdash \Omega \xRightarrow{\bar{\alpha}} \Omega$

### 3.4 MSWasm Extensions

We now add MSWasm specific extensions to the Wasm language described above.

Value Types  $\tau ::= \dots \mid \text{handle}$   
 Values  $\mathbf{v} ::= \dots \mid \mathbf{h}$   
 Handles  $\mathbf{h} ::= \langle \text{base } \mathbf{n}, \text{offset } \mathbf{n}, \text{bound } \mathbf{n}, \text{valid } \mathbf{b}, \text{id } \mathbf{n}_{\text{id}} \rangle$   
 Instructions  $\mathbf{i} ::= \dots \mid \tau.\text{segload} \mid \tau.\text{segstore} \mid \text{slice}$   
 $\mid \text{segalloc} \mid \text{segfree} \mid \text{handle.add}$   
 Heap cell tag  $\mathbf{t} ::= \mathbf{D} \mid \mathbf{H}$   
 Tagged Heaps  $\mathbf{T} ::= (\mathbf{b}, \mathbf{t})^*$   
 Module Defs.  $\mathbf{M} ::= \{\text{heaps } \mathbf{u32}^*, \text{segments } \mathbf{u32}^*, \text{funcs } \mathbf{f}^*, \text{export } \mathbf{E}\}$   
 Module Instances/Stores  $\mathbf{I}, \Sigma ::= (\text{heap } \mathbf{H}, \text{segment } \mathbf{T}, \text{alloc } \mathbf{A})$   
 Local Events  $\gamma ::= \dots \mid \text{salloc}(\mathbf{h}) \mid \text{sfree}(\mathbf{h}) \mid \text{read}_\tau(\mathbf{h}) \mid \text{write}_\tau(\mathbf{h})$   
 Events  $\alpha ::= \dots \mid \text{salloc}(\mathbf{h}) \mid \text{sfree}(\mathbf{h}) \mid \text{read}_\tau(\mathbf{h}) \mid \text{write}_\tau(\mathbf{h})$

Slots  $\mathbf{s} ::= (\mathbf{n}, \mathbf{n}, \mathbf{n}_{\text{id}})$   
 Allocator States  $\mathbf{A} ::= \mathbf{s}^*, \mathbf{s}^*, \mathbf{Y}$   
 Allocator Histories  $\mathbf{Y} ::= \bar{\alpha}$

$$\begin{array}{c}
 \text{(Load-Handle)} \\
 \hline
 \Gamma \vdash \tau.\text{segload} : [\text{handle}] \rightarrow [\tau] \\
 \text{(Store-Handle)} \\
 \hline
 \Gamma \vdash \tau.\text{segstore} : [\text{handle}, \tau] \rightarrow [] \\
 \text{(Slice)} \\
 \hline
 \Gamma \vdash \text{slice} : [\text{handle}, \mathbf{i32}, \mathbf{i32}] \rightarrow [\text{handle}] \\
 \text{(Alloc-Handle)} \\
 \hline
 \Gamma \vdash \text{segalloc} : [\mathbf{i32}] \rightarrow [\text{handle}] \\
 \text{(Add)} \\
 \hline
 \Gamma \vdash \text{handle.add} : [\text{handle}, \mathbf{i32}] \rightarrow [\text{handle}] \\
 \text{(Free)} \\
 \hline
 \Gamma \vdash \text{segfree} : [\text{handle}] \rightarrow []
 \end{array}$$


---

Typing for MSWasm Extensions

---

$$\mathbf{MS}(\mathbf{v}, \tau) = \mathbf{o} \geq \mathbf{0} \wedge \mathbf{o} + |\tau| < \mathbf{n}_2 \quad \text{where } \mathbf{v} = \langle \mathbf{n}_1, \mathbf{o}, \mathbf{n}_2, \_ \rangle$$

$$\begin{array}{c}
\text{(H-Load)} \\
\tau \neq \text{handle} \\
\frac{v_1 = \langle n_1, o, n_2, 1, n_{id} \rangle \quad (H, T, A, \_) = \Sigma \quad MS(v_1, \tau) \quad n_{id} \in A.\text{allocated} \quad a = n_1 + o \quad (b^*, t^*) = [T[a+j] \mid j \in \{0..|\tau|-1\}] \quad v_2 = \tau.\text{unpack}(b^*)}{\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{segload} : i^*, v_1 : v^* \rangle \xrightarrow{\text{read}_\tau(v_1)} \langle \Sigma, \theta, i^*, v_2 : v^* \rangle} \\
\text{(H-Load-Handle)} \\
\tau = \text{handle} \\
\frac{v_1 = \langle n_1, o, n_2, 1, n_{id} \rangle \quad (H, T, A, \_) = \Sigma \quad MS(v_1, \tau) \quad n_{id} \in A.\text{allocated} \quad a = (n_1 + o) \% |\text{handle}| \quad (b^*, t^*) = [T[a+j] \mid j \in \{0..|\tau|-1\}] \quad c = \bigwedge_{t \in t^*} (t = \square) \quad v_2 = \text{handle.unpack}(b^*) = \langle n'_1, a', n'_2, c', n_{id} \rangle \quad v'_2 = \langle n'_1, a', n'_2, c \wedge c', n_{id} \rangle}{\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{segload} : i^*, v_1 : v^* \rangle \xrightarrow{\text{read}_\tau(v_1)} \langle \Sigma, \theta, i^*, v'_2 : v^* \rangle} \\
\text{(H-Store)} \\
\tau \neq \text{handle} \quad v_1 = \langle n_1, o, n_2, 1, n_{id} \rangle \\
\frac{(H, T, A, \_) = \Sigma \quad MS(v_1, \tau) \quad n_{id} \in A.\text{allocated} \quad b^* = \tau.\text{pack}(v_2) \quad t = \square \quad a = (n_1 + o) \quad T' = T[a+j \mapsto (b_j, t) \mid j \in \{0..|\tau|-1\}] \quad \Sigma' = (H, T', A)}{\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{segstore} : i^*, v_2 : v_1 : v^* \rangle \xrightarrow{\text{write}_\tau(v_1)} \langle \Sigma', \theta, i^*, v^* \rangle} \\
\text{(H-Store-Handle)} \\
\tau = \text{handle} \quad v_1 = \langle n_1, o, n_2, 1, n_{id} \rangle \\
\frac{(H, T, A, f^*) = \Sigma \quad MS(v_1, \tau) \quad n_{id} \in A.\text{allocated} \quad b^* = \tau.\text{pack}(v_2) \quad t = \square \quad a = (n_1 + o) \% |\text{handle}| \quad T' = T[a+j \mapsto (b_j, t) \mid j \in \{0..|\tau|-1\}] \quad \Sigma' = (H, T', A, f^*)}{\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{segstore} : i^*, v_2 : v_1 : v^* \rangle \xrightarrow{\text{write}_\tau(v_1)} \langle \Sigma', \theta, i^*, v^* \rangle} \\
\text{(H-Load-Trap)} \\
\frac{v_1 = \langle n_1, o, n_2, c, n_{id} \rangle \quad (H, T, A, \_) = \Sigma \quad c = \text{false} \vee \neg MS(v_1, \tau \vee n_{id} \notin A.\text{allocated})}{\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{segload} : i^*, v_1 : v^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle} \\
\text{(H-Store-Trap)} \\
\frac{v_1 = \langle n_1, o, n_2, c \rangle \quad (H, T, A, \_) = \Sigma \quad c = \text{false} \vee \neg MS(v_1, \tau) \vee n_{id} \notin A.\text{allocated}}{\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{segstore} : i^*, v_2 : v_1 : v^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle} \\
\text{(H-Alloc)} \\
\frac{(H, T, A) = \Sigma \quad \langle T, A \rangle \xrightarrow{\text{salloc}(a, n, n_{id})} \langle T', A' \rangle \quad v = \langle a, 0, n, 1, n_{id} \rangle \quad \Sigma' = (H, T', A')}{\Phi^* \vdash \langle \Sigma, \theta, \text{segalloc} : i^*, n : v^* \rangle \xrightarrow{\text{salloc}(v)} \langle \Sigma', \theta, i^*, v : v^* \rangle}
\end{array}$$

$$\begin{array}{c}
\text{(H-Free)} \\
\hline
\langle \mathbf{H}, \mathbf{T}, \mathbf{A} \rangle = \Sigma \quad \mathbf{v} = \langle \mathbf{a}, \mathbf{0}, \mathbf{n}', \mathbf{1}, \mathbf{n}_{\text{id}} \rangle \quad \langle \mathbf{T}, \mathbf{A} \rangle \xrightarrow{\text{sfree}(\mathbf{a}, \mathbf{n}_{\text{id}})} \langle \mathbf{T}', \mathbf{A}' \rangle \quad \Sigma' = \langle \mathbf{H}, \mathbf{T}', \mathbf{A}' \rangle \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{segfree} : \mathbf{i}^*, \mathbf{v} : \mathbf{v}^* \rangle \xrightarrow{\text{sfree}(\mathbf{v})} \langle \Sigma', \theta, \mathbf{i}^*, \mathbf{v}^* \rangle \\
\text{(H-Free-trap)} \\
\hline
\mathbf{v} = \langle \mathbf{a}, \mathbf{n}, \mathbf{n}', \mathbf{0}, \mathbf{n}_{\text{id}} \rangle \quad \vee \quad \left( \mathbf{v} = \langle \mathbf{a}, \mathbf{0}, \mathbf{n}', \mathbf{1}, \mathbf{n}_{\text{id}} \rangle \wedge \langle \mathbf{T}, \mathbf{A} \rangle \xrightarrow{\text{sfree}(\mathbf{a}, \mathbf{n}_{\text{id}})} \langle \mathbf{T}, \mathbf{A} \rangle \right) \quad \vee \\
\left( \mathbf{v} = \langle \mathbf{a}, \mathbf{n}, \mathbf{n}', \mathbf{1}, \mathbf{n}_{\text{id}} \rangle \wedge \mathbf{n} \neq \mathbf{0} \right) \vee \mathbf{n}_{\text{id}} \notin \mathbf{A}.\text{allocated} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{segfree} : \mathbf{i}^*, \mathbf{v} : \mathbf{v}^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle \\
\text{(Slice)} \\
\hline
\mathbf{v} = \langle \mathbf{n}_1, \mathbf{o}, \mathbf{n}_2, \mathbf{b}, \mathbf{n}_{\text{id}} \rangle \quad \mathbf{o}_1 \geq \mathbf{0} \quad \mathbf{o}_2 \geq \mathbf{0} \\
\mathbf{o}_1 < \mathbf{n}_2 \quad \mathbf{v}' = \langle \mathbf{n}_1 + \mathbf{o}_1, \mathbf{o}, \mathbf{n}_2 - \mathbf{o}_2, \mathbf{b}, \mathbf{n}_{\text{id}} \rangle \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{slice} : \mathbf{i}^*, \mathbf{o}_2 : \mathbf{o}_1 : \mathbf{v} : \mathbf{v}^* \rangle \longrightarrow \langle \Sigma, \theta, \mathbf{i}^*, \mathbf{v}' : \mathbf{v}^* \rangle \\
\text{(Slice-Trap)} \\
\hline
\mathbf{v} = \langle \mathbf{n}_1, \mathbf{o}, \mathbf{n}_2, \mathbf{b}, \mathbf{n}_{\text{id}} \rangle \quad \mathbf{o}_1 < \mathbf{0} \vee \mathbf{o}_1 \geq \mathbf{n}_2 \vee \mathbf{o}_2 < \mathbf{0} \vee \mathbf{n}_{\text{id}} \notin \mathbf{A}.\text{allocated} \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{slice} : \mathbf{i}^*, \mathbf{v} : \mathbf{o}_1 : \mathbf{o}_2 : \mathbf{v}^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle \\
\text{(Handle-Add)} \\
\hline
\mathbf{v} = \langle \mathbf{n}_1, \mathbf{o}, \mathbf{n}_2, \mathbf{b}, \mathbf{n}_{\text{id}} \rangle \quad \mathbf{v}' = \langle \mathbf{n}_1, \mathbf{o} + \mathbf{n}, \mathbf{n}_2, \mathbf{b}, \mathbf{n}_{\text{id}} \rangle \\
\hline
\Phi^* \vdash \langle \Sigma, \theta, \text{handle.add} : \mathbf{i}^*, \mathbf{n} : \mathbf{v} : \mathbf{v}^* \rangle \longrightarrow \langle \Sigma, \theta, \mathbf{i}^*, \mathbf{v}' : \mathbf{v}^* \rangle \\
\hline
\text{MSWasm Semantics.}
\end{array}$$

$$\begin{array}{c}
\text{(Seg-Alloc)} \\
\hline
\mathbf{s}_f^* = \mathbf{s}_1^* \cdot (\mathbf{a}, \mathbf{n}', \mathbf{n}'_{\text{id}}) \cdot \mathbf{s}_2^* \quad \mathbf{0} < \mathbf{n} \leq \mathbf{n}' \quad \mathbf{a} + \mathbf{n} < |\mathbf{T}| \\
\text{aligned}(\mathbf{a}) \quad \mathbf{T}' = \mathbf{T}[\mathbf{a} + \mathbf{j} \mapsto (\mathbf{0}, \mathbf{D}) \mid \mathbf{j} \in \mathbf{0}.. \mathbf{n} - 1] \quad \mathbf{Y}' = \mathbf{Y} \cdot \text{salloc}(\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}}) \\
\mathbf{s}_f'^* = \mathbf{s}_1^* \cdot (\mathbf{a} + \mathbf{n}, \mathbf{n}' - \mathbf{n}, \mathbf{n}'_{\text{id}}) \cdot \mathbf{s}_2^* \quad \text{fresh}(\mathbf{n}_{\text{id}}) \quad \mathbf{s}_A'^* = (\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}}) \cdot \mathbf{s}_A^* \\
\hline
\langle \mathbf{T}, (\mathbf{s}_A^*, \mathbf{s}_f^*, \mathbf{Y}) \rangle \xrightarrow{\text{salloc}(\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}})} \langle \mathbf{T}', (\mathbf{s}_A'^*, \mathbf{s}_f'^*, \mathbf{Y}') \rangle \\
\text{(Seg-Free)} \\
\hline
\mathbf{s}_A^* = \mathbf{s}_1^* \cdot (\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}}) \cdot \mathbf{s}_2^* \quad \mathbf{s}_f'^* = (\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}}) \cdot \mathbf{s}_f^* \\
\mathbf{s}_A'^* = \mathbf{s}_1^* \cdot \mathbf{s}_2^* \quad \mathbf{Y}' = \mathbf{Y} \cdot \text{sfree}(\mathbf{a}, \mathbf{n}_{\text{id}}) \\
\hline
\langle \mathbf{T}, (\mathbf{s}_A^*, \mathbf{s}_f^*, \mathbf{Y}) \rangle \xrightarrow{\text{sfree}(\mathbf{a}, \mathbf{n}_{\text{id}})} \langle \mathbf{T}, (\mathbf{s}_A'^*, \mathbf{s}_f'^*, \mathbf{Y}') \rangle \\
\text{(Seg-Free-nop)} \\
\hline
(\nexists (\mathbf{a}, \_, \_) \in \mathbf{s}_A^*) \vee (\text{sfree}(\mathbf{a}, \mathbf{n}_{\text{id}}) \in \mathbf{Y}) \\
\hline
\langle \mathbf{T}, (\mathbf{s}_A^*, \mathbf{s}_f^*, \mathbf{Y}) \rangle \xrightarrow{\text{sfree}(\mathbf{a}, \mathbf{n}_{\text{id}})} \langle \mathbf{T}, (\mathbf{s}_A^*, \mathbf{s}_f^*, \mathbf{Y}) \rangle
\end{array}$$

$$\text{MSWasm Allocator Semantics: } (\mathbf{T}, \mathbf{A}) \overset{\alpha}{\rightsquigarrow} (\mathbf{T}, \mathbf{A}).$$



### 3.5 MSWasm Memory Safety

In this section, we develop the machinery necessary to define the memory safety for MSWasm. Similar to source, the memory safety of MSWasm is defined in terms of the abstract MS monitor.

**Definition 6** (Target Color Assignment). The target color assignment,  $\delta$ , is a map from source address to the tuple of color ( $\mathbb{C}$ ) and shades ( $\mathbb{S}$ ).

$$\delta : \text{Address} \times \text{Tid} \rightarrow \mathbb{N} \times \mathbb{C} \times \mathbb{S}$$

**Definition 7** (Trace Agreement ( $\alpha =_\delta \alpha$ )).

$$\begin{array}{c}
\text{(Tr-Empty)} \\
\hline
\epsilon =_\delta \epsilon
\end{array}
\quad
\begin{array}{c}
\text{(Tr-Concatenate)} \\
\hline
\frac{\alpha =_\delta \alpha \quad \bar{\alpha} =_\delta \bar{\alpha}}{\alpha \cdot \bar{\alpha} =_\delta \alpha \cdot \bar{\alpha}}
\end{array}$$

$$\begin{array}{c}
\text{(Tr-Read)} \\
\hline
\frac{n = \text{sz}(\tau) \quad \delta(\mathbf{b}, \mathbf{n}_{\text{id}}) = b^{(c,s)} \quad a = b + \mathbf{o}}{\text{read}_\tau(\langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle) =_\delta \text{read}(a^{(c,s)}) \cdot \text{read}((a+1)^{(c,s)}) \dots \text{read}((a+n-1)^{(c,s)})}
\end{array}$$

$$\begin{array}{c}
\text{(Tr-Write)} \\
\hline
\frac{n = \text{sz}(\tau) \quad \delta(\mathbf{b}, \mathbf{n}_{\text{id}}) = b^{(c,s)} \quad a = b + \mathbf{o}}{\text{write}_\tau(\langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle) =_\delta \text{write}(a^{(c,s)}) \cdot \text{write}((a+1)^{(c,s)}) \dots \text{write}((a+n-1)^{(c,s)})}
\end{array}$$

$$\begin{array}{c}
\text{(Tr-SAlloc)} \\
\hline
\frac{\mathbf{h} = \langle \mathbf{a}, \_, \mathbf{n}, \_, \mathbf{n}_{\text{id}} \rangle \quad n = \mathbf{n} \quad \forall i \in \{0 \dots n-1\}, \delta(\mathbf{a} + i, \mathbf{n}_{\text{id}}) = a^{(c, \phi(i))}}{\text{salloc}(\mathbf{h}) =_\delta \text{alloc}(n, a^c, \phi)}
\end{array}$$

$$\begin{array}{c}
\text{(Tr-Sfree)} \\
\hline
\frac{\mathbf{h} = \langle \mathbf{b}, \_, \mathbf{n}, \_, \mathbf{n}_{\text{id}} \rangle \quad \delta(\mathbf{b}, \mathbf{n}_{\text{id}}) = a^{(c, \_)}}{\text{sfree}(\mathbf{h}, \mathbf{n}_{\text{id}}) =_\delta \text{sfree}(a^c)}
\end{array}
\quad
\begin{array}{c}
\text{(Tr-Trap)} \\
\hline
\text{trap} =_\delta \epsilon
\end{array}$$

**Definition 8** (Safe Memory Allocator). A memory allocator is *safe* if the set of allocated addresses are disjoint from the free addresses and all individual slots are non-overlapping.

$$\begin{array}{c}
\text{(Safe Allocator)} \\
\hline
\frac{\mathbf{sA}^* \cap \mathbf{sF}^* = \emptyset}{(\mathbf{sA}^*, \mathbf{sF}^*, \_) \star}
\end{array}$$

**Definition 9** (Segment Agreement).

$$\begin{array}{c}
\text{(Segment Agreement)} \\
\hline
\frac{\begin{array}{l} \forall (\mathbf{b}, \ell, \mathbf{n}_{\text{id}}) \in \mathbf{sA}^*. \text{ let } a^{(c, \_)} = \delta(\mathbf{b}, \mathbf{n}_{\text{id}}) \text{ in } \forall i \in 0.. \ell - 1. T(a+i) = A(c, \_) \\ \forall (\mathbf{b}, \ell, \mathbf{n}_{\text{id}}) \in \mathbf{sF}^*. \text{ let } a^{(c, \_)} = \delta(\mathbf{b}, \mathbf{n}_{\text{id}}) \text{ in } \forall i \in 0.. \ell - 1. T(a+i) = F(c, \_) \\ \mathbf{Y} =_\delta T.Y \end{array}}{T =_\delta \mathbf{sA}^*, \mathbf{sF}^*, \mathbf{Y}}
\end{array}$$

**Lemma 6** (Monotonicity of Address Map). Let  $T =_{\delta} \mathbf{A}$ . If  $\delta' \supseteq \delta$  then  $T =_{\delta'} \mathbf{A}$ .

*Proof.* Trivial.  $\square$

**Definition 10** (Top-level Memory Safety for **C**-like).  $\text{MS}(\bar{\alpha}) \stackrel{\text{def}}{=} \exists \bar{\alpha}, \delta,$

1.  $\bar{\alpha} =_{\delta} \bar{\alpha}$  and
2.  $\text{MS}(\bar{\alpha})$

### 3.6 MSWasm Language Properties

In this section, we prove a few important properties of well-typed MSWasm programs. Specifically, we prove the following:

1. Type preservation.
2. Well-typedness implies robust memory safety (Def 16).

We first define what it means for a configuration to be valid and then prove that validity and well-typedness of configurations is preserved during the step.

$$\begin{array}{c}
 \text{(const)} \\
 \hline
 \mathbf{A} \vdash \text{isValid } c \\
 \text{(cor-handle)} \\
 (\mathbf{n}_b, \mathbf{n}_l, \mathbf{n}_{id}) \in \mathbf{A} \\
 \hline
 \mathbf{A} \vdash \text{isValid } \langle \text{base } \mathbf{n}_b, \text{offset } \mathbf{n}_o, \text{bound } \mathbf{n}_l, \text{valid } 1, \mathbf{n}_{id} \rangle \\
 \text{(incor-handle)} \\
 \hline
 \mathbf{A} \vdash \text{isValid } \langle \text{base } \mathbf{n}_b, \text{offset } \mathbf{n}_o, \text{bound } \mathbf{n}_l, \text{valid } 0, \mathbf{n}_{id} \rangle \\
 \text{(cons-stack-frame)} \\
 \hline
 \mathbf{A} \vdash \text{isValid } v^* \quad \mathbf{A} \vdash \text{isValid } S^* \\
 \hline
 \mathbf{A} \vdash \text{isValid } (\_, \_, v^*)_n : S^* \\
 \text{(cons-data-stack)} \\
 \hline
 \mathbf{A} \vdash \text{isValid } v' \quad \mathbf{A} \vdash \text{isValid } (\_, \_, v^*)_n : S^* \\
 \hline
 \mathbf{A} \vdash \text{isValid } (\_, \_, v' : v^*)_n : S^* \\
 \text{(empty-data-stack)} \\
 \hline
 \mathbf{A} \vdash \text{isValid } (\_, \_, \emptyset)_n : S^* \\
 \text{(segment)} \\
 \hline
 \mathbf{A} \vdash \text{isValid } H \\
 \mathbf{t}^1 = \square \wedge \mathbf{t}^2 = \square \wedge \dots \wedge \mathbf{t}^{|\text{handle}|} \implies \mathbf{h} = \text{handle.unpack}(\mathbf{b}^1 \dots \mathbf{b}^{|\text{handle}|}) \implies \mathbf{A} \vdash \text{isValid } \mathbf{h} \\
 \hline
 \mathbf{A} \vdash \text{isValid } (\mathbf{b}^1, \mathbf{t}^1) \dots (\mathbf{b}^{|\text{handle}|}, \mathbf{t}^{|\text{handle}|}) : H \\
 \text{(conf)} \\
 \hline
 \Sigma = \langle H, T, A \rangle \quad \mathbf{A} \vdash \text{isValid } T \quad \mathbf{A} \vdash \text{isValid } S^* \\
 \hline
 \vdash \text{isValid } \langle \Sigma, S^* \rangle
 \end{array}$$


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Valid Stack and Segment Configurations

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### 3.6.1 Type Preservation

**Lemma 7** (Type and Validity Preservation for Configurations-Trace Semantics). If

1.  $\Delta \vdash \Omega$
2.  $\vdash \text{isValid } \Omega$
3.  $\Phi^* \vdash \Omega \xRightarrow{\bar{\alpha}} \Omega'$

then

1.  $\Delta \vdash \Omega'$
2.  $\vdash \text{isValid } \Omega'$

*Proof.* Proof is by induction on  $\Phi^* \vdash \Omega \xRightarrow{\bar{\alpha}} \Omega'$ . For the case (Step), we have:

$$\Phi^* \vdash \Omega \xrightarrow{\alpha} \Omega' \quad (1)$$

$$\Phi^* \vdash \Omega' \xRightarrow{\bar{\alpha}} \Omega'' \quad (2)$$

Applying Lemma 8 (Type and Validity Preservation for Configurations-Inter) on (1) we have

$$\Delta \vdash \Omega' \quad (3)$$

$$\vdash \text{isValid } \Omega' \quad (4)$$

This gives us necessary premises to apply I.H to (2), and we have the required proof.  $\square$

**Lemma 8** (Type and Validity Preservation for Configurations-Inter). If

1.  $\Delta \vdash \Omega$
2.  $\vdash \text{isValid } \Omega$  and
3.  $\Phi^* \vdash \Omega \xrightarrow{\alpha} \Omega'$

then

1.  $\Delta \vdash \Omega'$
2.  $\vdash \text{isValid } \Omega'$

*Proof.* Proof is by induction on  $\Phi^* \vdash \Omega \xrightarrow{\alpha} \Omega'$ . For the case (Ctx), invoke Lemma 9 (Type and Validity Preservation for Configurations-Intra). The other cases (Ctx-Call), (Ctx-Return), (Exit) and (Trap) are relatively straightforward.  $\square$

**Lemma 9** (Type and Validity Preservation for Configurations-Intra). If

1.  $\Delta \vdash \Omega$
2.  $\vdash \text{isValid } \Omega$  and
3.  $\Phi^* \vdash \Omega \xrightarrow{\alpha} \gg_n \Omega'$

then

1.  $\Delta \vdash \Omega'$
2.  $\vdash \text{isValid } \Omega'$

*Proof.* Proof is by induction on  $\Phi^* \vdash \Omega \xrightarrow{\alpha} \gg_n \Omega'$ . The proof for type preservation is straightforward. Below we only describe the interesting cases for proving the validity of the configuration.

**Case (H-Alloc):** Let  $\Sigma = \langle \mathbf{H}, \mathbf{T}, \mathbf{A} \rangle$  and  $\Sigma' = \langle \mathbf{H}, \mathbf{T}', \mathbf{A}' \rangle$ .

Given  $\Phi^* \vdash \langle \Sigma, (\theta, \text{segalloc} : \mathbf{i}^*, \mathbf{n}' : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \rangle \xrightarrow{\text{alloc}(\mathbf{v})} \gg_n \langle \Sigma', (\theta, \mathbf{i}^*, \mathbf{v}' : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \rangle$ .

We have to prove

$$\mathbf{A}' \vdash \text{isValid } (\theta, \mathbf{i}^*, \mathbf{v}' : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^*$$

where  $\mathbf{v} = \langle \mathbf{a}, \mathbf{0}, \mathbf{a} + \mathbf{n}'', \mathbf{1}, \mathbf{n}_{\text{id}} \rangle$  and  $\mathbf{n}'' = \mathbf{n}' * \text{sz}(\tau)$ .

We are given  $\mathbf{A} \vdash \text{isValid } (\theta, \text{segalloc} ; \mathbf{i}^*, \mathbf{n}' : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^*$ . Inverting (cons-data-stack), we have  $\mathbf{A} \vdash \text{isValid } (\theta, \text{segalloc} ; \mathbf{i}^*, \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^*$ . This implies

$$\mathbf{A} \vdash \text{isValid } (\theta, \mathbf{i}^*, \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \quad (5)$$

Since  $(\mathbf{a}, \mathbf{a} + \mathbf{n}'') \in \lfloor \mathbf{A}' \rfloor_1$ , from (cor-handle), we have

$$\mathbf{A}' \vdash \text{isValid } \mathbf{v}' \quad (6)$$

Combining (5) and (6) yield the required premises for (cons-data-stack).

We still have to prove  $\mathbf{A}' \vdash \text{isValid } \mathbf{T}'$ . From (Seg-Alloc), we have that  $\mathbf{T}' = \mathbf{T}[\mathbf{a} + \mathbf{j} \mapsto (\mathbf{0}, \bigcirc) \mid \mathbf{j} \in \mathbf{0}..\mathbf{n}'' - 1]$ . That is, no new handle has been allocated in the segment. Since  $\lfloor \mathbf{A} \rfloor_1 \subseteq \lfloor \mathbf{A}' \rfloor_1$ ,  $\mathbf{A} \vdash \text{isValid } \mathbf{T}$  implies  $\mathbf{A}' \vdash \text{isValid } \mathbf{T}'$ .

**Case (H-Load) and (H-Load-Handle):** Let  $\Sigma = \langle \mathbf{H}, \mathbf{T}, \mathbf{A} \rangle$  and  $\Sigma' = \langle \mathbf{H}, \mathbf{T}', \mathbf{A} \rangle$ .

Given  $\Phi^* \vdash \langle \Sigma, (\theta, \tau.\text{segload} : \mathbf{i}^*, \mathbf{v}_1 : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \rangle \xrightarrow{\text{read}_\tau(\mathbf{v}_1, \mathbf{v}_2)} \gg_n \langle \Sigma', (\theta, \mathbf{i}^*, \mathbf{v}_2 : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \rangle$ .

We have to prove  $\mathbf{A} \vdash \text{isValid } (\theta, \mathbf{i}^*, \mathbf{v}_2 : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^*$  where

$\mathbf{v}_2 = \tau.\text{unpack}([\mathbf{T}[\mathbf{a} + \mathbf{j}] \mid \mathbf{j} \in \{\mathbf{0}..\text{sz}(\tau) - 1\}])$ . We are given  $\mathbf{A} \vdash \text{isValid } (\theta, \tau.\text{segload} ; \mathbf{i}^*, \mathbf{v}_1 : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^*$ . Inverting (cons-data-stack), we have  $\mathbf{A} \vdash \text{isValid } (\theta, \tau.\text{segload} ; \mathbf{i}^*, \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^*$ . This implies

$$\mathbf{A} \vdash \text{isValid } (\theta, \mathbf{i}^*, \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \quad (7)$$

Let  $\tau = \text{handle}$ . Since we have  $\mathbf{A} \vdash \text{isValid } \mathbf{T}$ , then  $\mathbf{A} \vdash \text{isValid } \mathbf{v}_2$ . From (7) and (cons-data-stack), we thus have  $\mathbf{A} \vdash \text{isValid } (\theta, \mathbf{i}^*, \mathbf{v}_2 : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^*$ . Since  $\mathbf{T}' = \mathbf{T}$ , we also have  $\mathbf{A} \vdash \text{isValid } \mathbf{T}'$ .

Case **(H-Load-Trap)**: Given  $\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{segload} : i^*, v_1 : v^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle$ .  
 Since stack is emptied, from rule **(empty-data-stack)**, we have that the final stack is valid. Since  $\Sigma$  is not changed, we have  $\vdash \text{isValid } \Omega'$  where  $\Omega' = \langle \Sigma, \theta, \epsilon, \epsilon \rangle$ .

Case **(H-Store)** and **(H-Store-Handle)**: Almost similar to **(H-Load)** and **(H-Load-Handle)** case except that if a handle is being written, i.e.,  $\tau = \text{handle}$ , we have that the handle is valid (because initial stack is valid).

Case **(H-Store-Trap)**: Similar to the above **(H-Load-Trap)** case.

Case **(H-Free)** Given the reduction, we have to prove

$$A' \vdash \text{isValid } (\theta, i^*, v' : v^*)_n : S_0^*$$

The only change is in  $A$ , but the validity of handles still follows from Rule **(cor-handle)**.

Case **(H-Free-trap)**: Trivial, since this is a pop.

□

### 3.6.2 Memory Safety Properties

We develop the machinery necessary to define the attacker model and what it means for a MSWasm module to be robustly memory safe.

**Definition 11** (Module instantiation). Let  $M = \{\text{heaps } h_s, \text{segments } t_s, \text{funcs } f^*, \text{import } \text{Imp}\}$ . Define  $\text{instantiate}(M) \stackrel{\text{def}}{=} \langle H_0, T_0, A_0 \rangle$  such that  $H_0$  and  $T_0$  are heap and segment memory of size  $h_s$  and  $t_s$ , respectively, and are initialized to  $0$  whereas allocator  $A_0$  is safe memory allocator with no used slots, that is,  $(\epsilon, (0, t_s))$ .

**Definition 12** (Initial Config).  $\Omega_0(M, n_{\text{entry}}) = \langle \text{instantiate}(M), (\theta_{\text{entry}}, i^*, \epsilon)_{n_{\text{entry}}} \rangle$  where

$$\begin{aligned} n_{\text{entry}} &= \{\text{var } \tau^v, \text{body } i^*\} \\ \theta_{\text{entry}} &= \{j \mapsto 0 \mid j \in \{1 \dots v\}\} \end{aligned}$$

**Definition 13** (Attacker).

$$\begin{aligned} M \vdash A : \text{attacker} &\stackrel{\text{def}}{=} \vdash A \text{ and } A = \{0, 0, [\text{main}; f_a^*], I_a\} \\ \text{where } M &= \{\_, \_, f_m^*, I_m\} \text{ and } \text{dom}(I_a) = \text{dom}(f_m^*) \text{ and } \text{dom}(I_m) = \text{dom}(\text{main}; f_a^*) \end{aligned}$$

A composition (or module join) is union of all heaps, segments and functions.

**Definition 14** (Linking).

$$\begin{aligned} M^1 \circ M^2 &\stackrel{\text{def}}{=} \{h^{1*}h^{2*}, s^{1*}s^{2*}, [f_1^1; \dots; f_n^1; f_1^2; \dots; f_m^2], \emptyset\} \\ \text{where } M^1 &\stackrel{\text{def}}{=} \{h^{1*}, s^{1*}, [f_1^1; \dots; f_n^1], [f_1^2; \dots; f_m^2]\} \text{ and } M^2 \stackrel{\text{def}}{=} \{h^{2*}, s^{2*}, [f_1^2; \dots; f_m^2], [f_1^1; \dots; f_n^1]\} \end{aligned}$$

**Definition 15** (Module is memory-safe). Module  $\mathbf{M}$  is memory-safe, denoted  $\vdash \mathbf{MS}(\mathbf{M})$ , if  $\Phi^* \vdash \Omega_0(\mathbf{M}, \mathbf{n}_{\text{main}}) \xRightarrow{\bar{\alpha}} \_$ . then  $\mathbf{MS}(\bar{\alpha})$ .

**Definition 16** (Module is memory-safe robustly).

$$\vdash \mathbf{RMS}(\mathbf{M}) \stackrel{\text{def}}{=} \forall \mathbf{A}, \mathbf{M} \vdash \mathbf{A} : \mathbf{attacker} \implies \vdash \mathbf{MS}(\mathbf{M} \circ \mathbf{A})$$

**Lemma 10** (Memory Safety Preservation-Intra). If

1.  $\Delta \vdash \Omega$  and
2.  $\vdash \text{isValid } \Omega$  and
3.  $\Phi^* \vdash \Omega \xrightarrow{\alpha}_{\mathbf{n}} \Omega'$  and
4.  $T =_{\delta} \Omega.A$

then  $\exists \bar{\alpha}, \delta', T'$  such that

- $\delta' \supseteq \delta$  and
- $T' =_{\delta'} \Omega'.A$  and
- $\alpha =_{\delta'} \bar{\alpha}$  and
- $\vdash T \xrightarrow{\bar{\alpha}} T'$

*Proof.* Proof is by induction on the single evaluation step. We first prove for interesting cases that contribute to traces.

**Case (H-Alloc):** Given:

$\Phi^* \vdash \langle \Sigma, (\theta, \text{segalloc} : \mathbf{i}^*, \mathbf{n}' : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \rangle \xrightarrow{\text{alloc}(\mathbf{v}')}_{\mathbf{n}_f} \langle \Sigma', (\theta, \mathbf{i}^*, \mathbf{v}' : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \rangle$ .  
Let  $\Omega' = \langle \Sigma', \mathbf{S}_0^* \rangle$  and  $\Sigma' = \langle \mathbf{H}', \mathbf{T}', \mathbf{A}' \rangle$ . We have  $\alpha = \text{alloc}(\mathbf{a}, \mathbf{n}', \mathbf{n}_{\text{id}})$ .  
We are also given that  $T =_{\delta} \mathbf{A}$ . We first have to prove

$$\exists \delta', \delta' \supseteq \delta \tag{8}$$

$$\exists \bar{\alpha}, \alpha =_{\delta'} \bar{\alpha} \tag{9}$$

Let  $\delta' = \delta[\mathbf{a}, \mathbf{n}_{\text{id}} \mapsto (a, (c, 0)) \dots \mathbf{a} + n' - 1, \mathbf{n}_{\text{id}} \mapsto (a + n' - 1, (c, 0))]$  where  $c$  is a fresh color and  $\mathbf{n}' = n'$ . Thus from rule (Tr-SAlloc), we have  $\bar{\alpha} = \text{alloc}(n', a^c, \lambda \_ . 0)$ .

We now have to prove that

$$\exists T', T' =_{\delta'} \mathbf{A}' \tag{10}$$

From rule (H-Alloc), we have  $\langle \mathbf{T}, \mathbf{s}_{\mathbf{A}}^*, \mathbf{s}_{\mathbf{f}}^* \rangle \xrightarrow{\text{salloc}(\mathbf{a}, \mathbf{n}', \mathbf{n}_{\text{id}})} \langle \mathbf{T}', \mathbf{s}_{\mathbf{A}}'^*, \mathbf{s}_{\mathbf{f}}'^* \rangle$ .

Thus  $\mathbf{A}' = (\mathbf{s}_\mathbf{A}'^*, \mathbf{s}_\mathbf{F}'^*)$ . From rule (Seg-Alloc), we have

$$\mathbf{s}_\mathbf{f}^* = \mathbf{s}_1^* \cdot (\mathbf{a}, \mathbf{n}'', \mathbf{n}'_{\text{id}}) \cdot \mathbf{s}_2^* \quad (11)$$

$$\mathbf{0} < \mathbf{n}' \leq \mathbf{n}'' \quad (12)$$

$$\mathbf{a} + \mathbf{n}' < |\mathbf{T}| \quad (13)$$

$$\text{aligned}(\mathbf{a}) \quad (14)$$

$$\mathbf{T}' = \mathbf{T}[\mathbf{a} + \mathbf{j} \mapsto (\mathbf{0}, \mathbf{D}) \mid \mathbf{j} \in \mathbf{0}..\mathbf{n}' - 1] \quad (15)$$

$$\mathbf{Y}' = \mathbf{Y} \cdot \text{salloc}(\mathbf{a}, \mathbf{n}', \mathbf{n}_{\text{id}}) \quad (16)$$

$$\mathbf{s}_\mathbf{f}'^* = \mathbf{s}_1^* \cdot (\mathbf{a} + \mathbf{n}', \mathbf{n}'' - \mathbf{n}', \mathbf{n}'_{\text{id}}) \cdot \mathbf{s}_2^* \quad (17)$$

$$\text{fresh}(\mathbf{n}_{\text{id}}) \quad (18)$$

$$\mathbf{s}_\mathbf{A}'^* = (\mathbf{a}, \mathbf{n}', \mathbf{n}_{\text{id}}) \cdot \mathbf{s}_\mathbf{A}^* \quad (19)$$

Let  $T' = (T[a + i \mapsto (c, 0) \mid i \in \{0, \dots, n' - 1\}], T.Y.\text{alloc}(n', a^c, \lambda_.0))$  where  $n' = \mathbf{n}'$ . Note that  $c$  is picked to be fresh in  $\delta'$ , thus it is also fresh in  $T'$  since  $\delta' \supseteq \delta$  and  $T =_\delta \Omega'.\mathbf{A}$ . From Definition 9 (Segment Agreement), we thus have  $T' =_{\delta'} \Omega'.\mathbf{A}$ .

From the action relation, we also get the history relation  $T'.Y =_{\delta'} \mathbf{Y}'$ .

Finally, from rule (MS-Alloc) we have  $\vdash T \xrightarrow{\bar{\alpha}} T'$ .

Case (H-Load) and (H-Load-Handle): Given

$$\Phi^* \vdash \langle \Sigma, (\theta, \tau.\text{segload} : \mathbf{i}^*, \mathbf{h} : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \rangle \xrightarrow{\text{read}_\tau(\mathbf{h})}_{\mathbf{n}_f} \langle \Sigma, (\theta, \mathbf{i}^*, \mathbf{v}' : \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \rangle.$$

Let  $\Sigma' = \langle \mathbf{H}, \mathbf{T}', \mathbf{A}' \rangle$ ,  $\mathbf{h} = \langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle$ . Given  $\alpha = \text{read}_\tau(\mathbf{a})$  and  $T =_\delta \mathbf{A}$ . We first have to prove

$$\exists \delta', \delta' \supseteq \delta$$

$$\exists \bar{\alpha}, \alpha =_{\delta'} \bar{\alpha}$$

From the segment agreement between  $\mathbf{A}$  and  $T$  (rule (Segment Agreement)), we have  $\delta(\mathbf{b}, \mathbf{n}_{\text{id}}) = b^{(c,s)}$ .

Let  $\delta' = \delta$  and  $\bar{\alpha} = \text{read}(a^{(c,s)}) \cdot \text{read}((a+1)^{(c,s)}) \dots \text{read}((a+n-1)^{(c,s)})$  where  $n = \text{sz}(\tau)$  and  $a = b + \mathbf{o}$ .

From rule (Tr-Read), we have  $\text{read}_\tau(\langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle) =_{\delta'} \text{read}(a^{(c,s)}) \cdot \text{read}((a+1)^{(c,s)}) \dots \text{read}((a+n-1)^{(c,s)})$ .

From (H-Load), we have  $\mathbf{A}' = \mathbf{A}$ . Let  $T' = T$ . It follows that  $T' =_{\delta'} \mathbf{A}'$ .

We still have to prove that  $\vdash T \xrightarrow{\bar{\alpha}} T'$ .

From the segment agreement we know that the slots are in the allocated part of  $T'$ , so from  $\alpha =_{\delta'} \bar{\alpha}$  and  $T' =_{\delta'} \mathbf{A}'$ .

However, in order to conclude that  $\vdash T \overset{\bar{\alpha}}{\rightsquigarrow} T'$ , we need to prove  $\vdash T \overset{\text{read}(a+i^{(c,s)})}{\rightsquigarrow} T$  for all  $i \in \{0, \dots, n-1\}$ . That is, we have to prove the premises of rule **(MS-Read)**. Specifically, we have to show that  $T(a) = A(c, s) \cdots T(a+n-1) = A(c, s)$ . We derive that from (1)  $\vdash \text{isValid } \Omega$  (the handle is valid and correspond to an allocator slot), (2)  $T =_{\delta} \Omega.A$  (colors for locations accessed are in agreement with abstract memory), and (3) bounds and tid checks from the rule **(H-Load)** (or rule **(H-Load-Handle)**) (access is in bounds, i.e., within the slot, therefore all locations have the same color). It follows that  $\vdash T \overset{\bar{\alpha}}{\rightsquigarrow} T'$  (from rule **(MS-Read)** and rule **(Cons)**) with  $T' = T$ .

**Case (H-Store) and (H-Store-Handle):** Similar to the above **(H-Load)** case.

**Case (H-Load-Trap):** Given  $\Phi^* \vdash \langle \Sigma, \theta, \tau.\text{segload} : i^*, v_1 : v^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle$ . Let  $\delta' = \delta$  and  $T' = T$ . Since  $\Sigma$  has not changed during the step, we trivially have  $T' =_{\delta'} \Sigma.A$ . Also, from rule **(Tr-Trap)**, we have  $\text{trap} =_{\delta} \epsilon$ . Thus  $\bar{\alpha} = \epsilon$ . From rule **(Empty)**, we have  $\vdash T \overset{\epsilon}{\rightsquigarrow} T$ .

**Case (H-Store-Trap):** Similar to the above **(H-Load-Trap)** case.

**Case (H-Free)** We have this reduction

$$\frac{\begin{array}{c} \text{(H-Free-full)} \\ \langle \mathbf{H}, \mathbf{T}, \mathbf{A} \rangle = \Sigma \quad \mathbf{v} = \langle \mathbf{a}, \mathbf{n}, \mathbf{n}', \mathbf{b}, \mathbf{n}_{\text{id}} \rangle \quad \langle \mathbf{T}, \mathbf{A} \rangle \xrightarrow{\text{sfree}(\mathbf{a})} \langle \mathbf{T}', \mathbf{A}' \rangle \quad \Sigma' = (\mathbf{H}, \mathbf{T}', \mathbf{A}') \end{array}}{\Phi^* \vdash \langle \Sigma, \theta, \text{segfree} : i^*, v : v^* \rangle \xrightarrow{\text{sfree}(\mathbf{v})} \langle \Sigma', \theta, i^*, v^* \rangle}$$

Let  $\alpha = \text{sfree}(a^c)$  for  $\mathbf{a}, \mathbf{n}_{\text{id}} =_{\delta} (a, c)$ .

Let  $\delta' = \delta$ .

Let  $T' = T[a+i \mapsto F(c, s_i) | i \in 0..n-1]$ , given that  $n = |\text{rng}(T) \cap A(c, \_)|$  and  $\forall i \in 0..n-1. T[a+i] = A(c, s)$

From the segment agreement we get that  $\delta(\mathbf{a}, \mathbf{n}_{\text{id}}) = a^{(c, \_)}$ , so from Rule **(Tr-Sfree)** the traces are related.

To prove  $T' =_{\delta'} \mathbf{A}'$  we need to show that the freed slot is tagged  $F$  in  $T'$ , which is the case.

Then, from the action relation, we also get the history relation  $T'.Y =_{\delta'} \Omega'.A.Y$ .

From Rule **(MS-Free)** the monitor steps to  $T'$ .

**Remaining Cases:** Do not emit any traces related to the memory safety monitor. Hence, they step on empty events (see rule **(Empty)**).

□

**Lemma 11** (Memory Safety Preservation-Inter). If



1.  $\Delta \vdash \Omega$  and
2.  $\vdash \text{isValid } \Omega$  and
3.  $\Phi^* \vdash \Omega \xrightarrow{\alpha} \Omega'$  and
4.  $T =_{\delta} \Omega.A$

then  $\exists \bar{\alpha}, \delta', T'$  such that

- $\delta' \supseteq \delta$  and
- $T' =_{\delta'} \Omega'.A$  and
- $\alpha =_{\delta'} \alpha$  and
- $\vdash T \xrightarrow{\bar{\alpha}} T'$

*Proof.* Proof by induction on the step relation:  $\Phi^* \vdash \Omega \xrightarrow{\alpha} \Omega'$ . The only interesting case is rule (Ctx). Lift intra-function semantics to inter-function semantics and invoke Lemma 10 (Memory Safety Preservation-Intra).

For rule (Ctx-Call), rule (Ctx-Return), rule (Exit) and rule (Trap), the events do not affect the memory safety monitor and use the monitor step rule (Empty).  $\square$

**Lemma 12** (Memory Safety for Trace Semantics). If

1.  $\Delta \vdash \Omega$  and
2.  $\vdash \text{isValid } \Omega$  and
3.  $\Phi^* \vdash \Omega \xRightarrow{\bar{\alpha}} \Omega'$  and
4.  $T =_{\delta} \Omega.A$

then  $\exists \bar{\alpha}, \delta', T'$  such that

- $\delta' \supseteq \delta$  and
- $T' =_{\delta'} \Omega'.A$  and
- $\bar{\alpha} =_{\delta'} \bar{\alpha}$  and
- $\vdash T \xrightarrow{\bar{\alpha}} T'$

*Proof.* Proof is by induction on the trace step.

**Base:** Given  $\Phi^* \vdash \Omega \xRightarrow{\epsilon} \Omega$ .  $\vdash T \xrightarrow{\bar{\alpha}} T'$  follows from rule (Empty), with  $T = T'$ .

Case **(Step)**: Given

$$\Phi^* \vdash \langle \Sigma, (\theta, \mathbf{i}^*, \mathbf{v}^*)_{\mathbf{n}} : \mathbf{S}_0^* \rangle \xRightarrow{\alpha \cdot \bar{\alpha}} \langle \Sigma', \mathbf{S}'^* \rangle \quad (20)$$

where  $\Sigma = (\mathbf{H}, \mathbf{T}, \mathbf{A})$  and  $\Sigma' = (\mathbf{H}', \mathbf{T}', \mathbf{A}')$ . Inverting the rule **(Step)**, we have the

$$\Phi^* \vdash \langle \Sigma, (\theta, \mathbf{i}^*, \mathbf{v}^*) : \mathbf{S}_0^* \rangle \xrightarrow{\alpha} \langle \Sigma'', \mathbf{S}''^* \rangle \quad (21)$$

$$\Phi^* \vdash \langle \Sigma'', \mathbf{S}''^* \rangle \xRightarrow{\bar{\alpha}} \langle \Sigma', \mathbf{S}'^* \rangle \quad (22)$$

such that  $\Sigma'' = (\mathbf{H}'', \mathbf{T}'', \mathbf{A}'')$ .

We are given  $T =_{\delta} \mathbf{A}$ . Since we have required premises, applying Lemma 11 (**Memory Safety Preservation-Inter**) on (20) give

$$\delta'' \supseteq \delta \quad (23)$$

$$\alpha =_{\delta''} \bar{\alpha} \quad (24)$$

$$T'' =_{\delta''} \mathbf{A}'' \quad (25)$$

$$\vdash T \xRightarrow{\bar{\alpha}} T'' \quad (26)$$

From Lemma 8 (**Type and Validity Preservation for Configurations-Inter**), we further have

$$\Delta \vdash \langle \Sigma'', \mathbf{S}''^* \rangle \quad (27)$$

$$\vdash \text{isValid } \langle \Sigma'', \mathbf{S}''^* \rangle \quad (28)$$

Applying I.H. on (22) using (27), (28) and (25),

$$\delta' \supseteq \delta \quad (29)$$

$$\bar{\alpha} =_{\delta'} \bar{\alpha}'' \quad (30)$$

$$T' =_{\delta'} \Sigma'. \mathbf{A} \quad (31)$$

$$\vdash T'' \xRightarrow{\bar{\alpha}''} T' \quad (32)$$

From (23) and (29), we have  $\delta' \supseteq \delta$ .

Combining (24) and (30) using rule **(Tr-Concatenate)** we have  $\alpha \cdot \bar{\alpha} = \bar{\alpha} \cdot \bar{\alpha}''$ .

Combining (26) and (32) using the monitor step **(Cons)** we can conclude

$$\vdash T \xRightarrow{\bar{\alpha} \cdot \bar{\alpha}''} T'.$$

Hence proved. □

**Lemma 13** (Instantiated Config is Valid). If  $\Omega_0(\mathbf{M}, \mathbf{n}_f)$  then  $\vdash \text{isValid } \Omega$ .

*Proof.* Follows from the validity of initialized segment (rule (segment)) and empty stack (rule (empty-data-stack)).  $\square$

**Theorem 1** (Robust Memory-Safety). If  $\vdash \mathbf{M}$  then  $\vdash \text{RMS}(\mathbf{M} \circ \mathbf{A})$ .

*Proof Sketch.* Let  $\mathcal{A}$  be some attacker such that  $\text{main} = \{\text{var } \tau^v, \text{body } i^*\}$ . Let the following:

$$\Omega = \Omega_0(\mathcal{A} \circ \mathbf{M}, \mathbf{n}_{\text{main}}) \quad (33)$$

$$\Phi^* \vdash \Omega \xRightarrow{\bar{\alpha}} \Omega' \quad (34)$$

$$\delta = \emptyset \quad (35)$$

We need to show  $\text{MS}(\bar{\alpha})$ . We will invoke Lemma 12 (Memory Safety for Trace Semantics) to get the required proof. However, we need the following:

$$\Delta \vdash \Omega \quad (36)$$

$$\vdash \text{isValid } \Omega \quad (37)$$

$$\Phi^* \vdash \Omega \xRightarrow{\bar{\alpha}} \Omega' \quad (38)$$

$$T =_{\delta} \Omega.A \quad (39)$$

Applying Lemma 13 (Instantiated Config is Valid) to  $\Omega_0(\mathcal{A} \circ \mathbf{M}, \mathbf{n}_{\text{main}})$ , we have (37).

Given  $\vdash \mathcal{A} \circ \mathbf{M}$ . From rule (Module) and rule (Fun), we have

$$\Gamma\{\text{local} = \tau^a \tau^v \text{ return} = \tau^*\} \vdash i^* : [] \rightarrow \tau^* \quad (40)$$

From rule (Stack), we have  $\Delta \vdash (\theta, i^*, \epsilon)_{\mathbf{n}_{\text{main}}}$ . Thus, we have (36).

Since the initial allocator has no used slots, i.e.,  $\Omega.A = (\epsilon, \_)$ , we have (39).

Invoking Lemma 12 (Memory Safety for Trace Semantics) using (36) to (39), we have

$$\delta' \supseteq \delta \quad (41)$$

$$T' =_{\delta'} A' \quad (42)$$

$$\bar{\alpha} =_{\delta'} \bar{\alpha} \quad (43)$$

$$\vdash T \xRightarrow{\bar{\alpha}} T' \quad (44)$$

Since  $T$  is empty, (43) and (44) together imply  $\text{MS}(\bar{\alpha})$ . Hence proved.  $\square$

## 4 Compiler

We define the formal compilation from source C language to MSWasm.

### 4.1 Compilation of Programs

The compilation judgment for types, environments, program, functions and expressions:

$$\begin{array}{c}
\llbracket w \rrbracket = \tau \\
\llbracket P, F_{\text{map}} \rrbracket^{\text{glob}} = \Delta \\
\llbracket P \rrbracket^{\text{prog}} = M \\
\llbracket w_r \text{ f}(x : w_a) \mapsto e \rrbracket^{\text{fun}} = i \\
\llbracket P, \Gamma \vdash e : w \rrbracket^{\text{exp}} = i
\end{array}$$


---

(Integers)	(C-Pointers)	(C-Array)
$\llbracket \text{int} \rrbracket = \mathbf{u32}$	$\llbracket \text{ptr } w \rrbracket = \mathbf{handle}$	$\llbracket \text{array } \tau \rrbracket = \mathbf{handle}$

---

(C-T-All-FuncTypes)

$$\frac{\forall k \in |P.F|, \tau_{rk} \text{ f}_k(x : \tau_{ak}) \mapsto \_ \in P.F \implies \llbracket \tau_{ak} \rrbracket = \tau_{ak} \implies \llbracket \tau_{rk} \rrbracket = \tau_{rk} \implies \rho[k] = \tau_{ak} \rightarrow \tau_{rk}}{\llbracket P.F \rrbracket^{\text{all}} = \rho^*}$$


---

(C-T-Env)

$$\frac{\begin{array}{c} \tau_r \text{ f}(x : \tau_a) \mapsto ((y_j : \tau_j)^*, e_f) \in P.F \\ \llbracket \tau_a \rrbracket = \tau_a \quad \llbracket \tau_r \rrbracket = \tau_r \quad \llbracket \tau_j \rrbracket = \tau_j \\ \llbracket P.F \rrbracket^{\text{all}} = \rho^* \\ \Gamma_f = \{\mathbf{funcs } \rho^*, \mathbf{locals } \tau_a \tau_1 \tau_2 \dots, \mathbf{return } \tau_r\} \end{array}}{\llbracket P, \Gamma_f \rrbracket^{\text{env}} = \Gamma_f}$$


---

(C-T-Global-Env)

$$\frac{\forall i, \llbracket P, \Delta[i] \rrbracket^{\text{env}} = \Delta[i]}{\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta}$$


---

Compilation of types and environments

---

(C-Function)

$$\frac{\begin{array}{c} F_{\text{map}}(f) = \mathbf{n}_f \\ \llbracket \tau_a \rrbracket = \tau_a \quad \llbracket \tau_r \rrbracket = \tau_r \quad \forall i, \llbracket \tau_i \rrbracket = \tau_i \quad \tau^j = \tau_a; \tau_1; \tau_2; \dots \\ \llbracket P, [x \mapsto \tau_a] \vdash e : \tau_r \rrbracket^{\text{exp}} = i^* \end{array}}{\llbracket \tau_r \text{ f}(x : \tau_a) \mapsto ((y_j : \tau_j)^*, e) \rrbracket^{\text{fun}} = \langle \mathbf{var } \tau^j, i^* \rangle}$$


---

(Program)

$$\frac{\begin{array}{c} \forall k, P.F[k] = f_k, \llbracket w_{rk} \text{ f}_k(x : w_{ak}) \mapsto ((y_{jk} : w_{jk})^*, e_k) \rrbracket^{\text{fun}} = f_k \\ |s| = \mathbf{sz}(P.n_{hs}) \\ M = \{\mathbf{heap } 0, \mathbf{segment } s, \mathbf{funcs } f_0 f_1 \dots f_n\} \end{array}}{\llbracket P \rrbracket^{\text{prog}} = M}$$

Compilation of function and program

$$\begin{array}{c}
\text{(C-Constant-int)} \\
\frac{|n| = |\mathbf{n}|}{\llbracket P, \Gamma \vdash n : \text{int} \rrbracket^{\text{exp}} = \text{u32.const } \mathbf{n}} \quad \text{(C-Var)} \quad \frac{}{\llbracket P, \Gamma \vdash x : w \rrbracket^{\text{exp}} = \text{get } x} \\
\\
\text{(C-Ptr-Arith)} \\
\frac{\begin{array}{l} \llbracket P, \Gamma \vdash e_1 : \text{ptr array } \tau \rrbracket^{\text{exp}} = \mathbf{i}_1^* \\ \llbracket P, \Gamma \vdash e_2 : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}_2^* \\ \mathbf{n} = \text{sz}(w) \end{array}}{\llbracket P, \Gamma \vdash e_1 \oplus e_2 : \text{ptr array } \tau \rrbracket^{\text{exp}} = \mathbf{i}_1^*; \mathbf{i}_2^*; \text{u32.const } \mathbf{n}; \text{u32.}\times; \text{handle.add}} \\
\\
\text{(C-BinOp)} \\
\frac{\begin{array}{l} \llbracket P, \Gamma \vdash e_j : w \rrbracket^{\text{exp}} = \mathbf{i}_j^* \\ \mathbf{j} \in \{1, 2\} \\ \llbracket \tau \rrbracket = \tau \end{array}}{\llbracket P, \Gamma \vdash e_1 \oplus e_2 : \tau \rrbracket^{\text{exp}} = \mathbf{i}_1^*; \mathbf{i}_2^*; \tau.\otimes} \\
\\
\text{(C-Malloc-Array)} \\
\frac{\begin{array}{l} \llbracket P, \Gamma \vdash e : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}^* \\ \mathbf{n}' = \text{sz}(\tau) \end{array}}{\llbracket P, \Gamma \vdash \text{malloc}(\tau, e) : \text{ptr array } \tau \rrbracket^{\text{exp}} = \mathbf{i}^*; \text{u32.const } \mathbf{n}'; \text{u32.mult}; \text{segalloc}} \\
\\
\text{(C-Malloc-Single)} \\
\frac{}{\llbracket P, \Gamma \vdash \text{malloc}(w) : \text{ptr } w \rrbracket^{\text{exp}} = \text{u32.const } \mathbf{n}; \text{segalloc}} \\
\\
\text{(C-Free)} \\
\frac{\llbracket P, \Gamma \vdash e : \text{ptr } w \rrbracket^{\text{exp}} = \mathbf{i}^*}{\llbracket P, \Gamma \vdash \text{free}(e) : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}^*; \text{free}; \text{u32.const } 0}
\end{array}$$

Compilation of expressions

$$\begin{array}{c}
\text{(C-Deref)} \\
\frac{\llbracket P, \Gamma \vdash e : \text{ptr } \tau \rrbracket^{\text{exp}} = \mathbf{i}^*}{\llbracket P, \Gamma \vdash *e : \tau \rrbracket^{\text{exp}} = \mathbf{i}^*; \tau.\text{segload}} \\
\\
\text{(C-Struct-field)} \\
\frac{\begin{array}{l} \llbracket P, \Gamma \vdash e : \text{ptr struct } s \rrbracket^{\text{exp}} = \mathbf{i}^* \\ D(s) = \{f_1 : \tau_1, \dots, f_k : \tau_k \dots f_n : \tau_n\} \\ (\mathbf{o}_1, \mathbf{o}_2) = (\sum_{i=1}^{k-1} \text{sz}(\tau_i), (\text{sz}(\text{struct } s) - \text{sz}(\tau_k))) \end{array}}{\llbracket P, \Gamma \vdash \&e \rightarrow f_k : \text{ptr } \tau_k \rrbracket^{\text{exp}} = \mathbf{i}^*; \text{u32.const } \mathbf{o}_1; \text{u32.const } \mathbf{o}_2, \text{slice}} \\
\\
\text{(C-Arr-Deref)} \\
\frac{\begin{array}{l} \llbracket P, \Gamma \vdash e_1 : \text{array } \tau \rrbracket^{\text{exp}} = \mathbf{i}_1^* \\ \llbracket P, \Gamma \vdash e_2 : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}_2^* \\ \mathbf{n} = \text{sz}(\tau) \end{array}}{\llbracket P, \Gamma \vdash e_1[e_2] : \text{ptr array } \tau \rrbracket^{\text{exp}} = \mathbf{i}_1^*; \mathbf{i}_2^*; \text{u32.const } \mathbf{n}; \text{u32.}\times; \text{handle.add}; \tau.\text{segload}}
\end{array}$$

$$\begin{array}{c}
\text{(C-Var-Assign)} \\
\frac{\llbracket \tau \rrbracket = \tau \quad \llbracket P, \Gamma \vdash e : \tau \rrbracket^{\text{exp}} = \mathbf{i}^*}{\llbracket P, \Gamma \vdash x := e : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}^*; \text{set } x; \text{u32.const } 0} \\
\text{(C-Assign)} \\
\frac{\llbracket \tau \rrbracket = \tau \quad \llbracket P, \Gamma \vdash e_1 : \tau \rrbracket^{\text{exp}} = \mathbf{i}_1^* \quad \llbracket P, \Gamma \vdash e_2 : \tau \rrbracket^{\text{exp}} = \mathbf{i}_2^*}{\llbracket P, \Gamma \vdash *e_1 := e_2 : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}_1^*; \mathbf{i}_2^*; \tau.\text{segstore}; \text{u32.const } 0} \\
\text{(C-Array-Assign)} \\
\frac{\llbracket \tau \rrbracket = \tau \quad \llbracket P, \Gamma \vdash e_1 : \text{array } \tau \rrbracket^{\text{exp}} = \mathbf{i}_1^* \quad \llbracket P, \Gamma \vdash e_2 : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}_2^* \quad \llbracket P, \Gamma \vdash e_3 : \tau \rrbracket^{\text{exp}} = \mathbf{i}_3^* \quad \mathbf{n} = \text{sz}(\tau)}{\llbracket P, \Gamma \vdash e_1[e_2] := e_3 : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}_1^*; \mathbf{i}_2^*; \text{u32.const } \mathbf{n}; \text{u32.}\times; \text{handle.add}; \mathbf{i}_3^*; \tau.\text{segstore}; \text{u32.const } 0} \\
\text{(C-Branch)} \\
\frac{\llbracket P, \Gamma \vdash e_0 : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}_0^* \quad \llbracket P, \Gamma \vdash e_1 : w \rrbracket^{\text{exp}} = \mathbf{i}_1^* \quad \llbracket P, \Gamma \vdash e_2 : w \rrbracket^{\text{exp}} = \mathbf{i}_2^*}{\llbracket P, \Gamma \vdash \text{if } e_0 \text{ then } e_1 \text{ else } e_2 : w \rrbracket^{\text{exp}} = \mathbf{i}_0^*; \text{branch } \mathbf{i}_1^* \mathbf{i}_2^*} \\
\text{(C-Seq)} \\
\frac{\llbracket P, \Gamma \vdash e_1 : w_1 \rrbracket^{\text{exp}} = \mathbf{i}_1^* \quad \llbracket P, \Gamma \vdash e_2 : w' \rrbracket^{\text{exp}} = \mathbf{i}_2^*}{\llbracket P, \Gamma \vdash e_1; e_2 : w' \rrbracket^{\text{exp}} = \mathbf{i}_1^*; \text{drop}; \mathbf{i}_2^*} \\
\text{(C-Call)} \\
\frac{\begin{array}{l} F_{\text{map}}(g) = \mathbf{n}_g \\ V_{\text{map}}(x) = \mathbf{n}_x \\ \text{flookup}(P, g) = w_2 \ g(x : w_1) \mapsto ((y_j : w_j)^*, e_g) \\ \llbracket w_2 \ g(x : w_1) \mapsto ((y_j : w_j)^*, e_g) \rrbracket^{\text{fun}} = \langle \text{var } \tau^j, \mathbf{i}^* \rangle \\ \llbracket P, \Gamma \vdash e_1 : w_1 \rrbracket^{\text{exp}} = \mathbf{i}_1^* \end{array}}{\llbracket P, \Gamma \vdash x := \text{call } g(e_1) : w' \rrbracket^{\text{exp}} = \mathbf{i}_1^*; \text{call } \mathbf{n}_g; \text{set } \mathbf{n}_x}
\end{array}$$

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Compilation of expressions

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## 4.2 Evaluation Context Compilation

Evaluation context compilation is the following relation:

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$P, \Gamma \vdash E : w' \Rightarrow w \sim_\delta \langle \mathbf{i}^*, \mathbf{v}^* \rangle$

---

(Ctx-Hole)

$$\frac{}{P, \Gamma \vdash [\ ] : w \Rightarrow w \sim_\delta \langle [\ ], [\ ] \rangle}$$

$$\begin{array}{c}
\text{(Ctx-Op1)} \\
\frac{\begin{array}{c} \llbracket w \rrbracket = \tau \\ \llbracket P, \Gamma \vdash e : w \rrbracket^{\text{exp}} = \mathbf{i}^* \\ P, \Gamma \vdash E : w' \Rightarrow w \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle \end{array}}{P, \Gamma \vdash E \oplus e : w' \Rightarrow w \sim_{\delta} \langle \mathbf{i}^*, \mathbf{i}^*; \tau \cdot \otimes, \mathbf{v}^* \rangle} \\
\text{(Ctx-Op2)} \\
\frac{\begin{array}{c} \llbracket w \rrbracket = \tau \\ P, \Gamma \vdash v' : w \sim_{\delta} \mathbf{v}' \\ P, \Gamma \vdash E : w' \Rightarrow w \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle \end{array}}{P, \Gamma \vdash v' \oplus E : w' \Rightarrow w \sim_{\delta} \langle \mathbf{i}^*, \tau \cdot \otimes, (\mathbf{v}^*, \mathbf{v}') \rangle} \\
\text{(Ctx-Field)} \\
\frac{\begin{array}{c} \llbracket P, \Gamma \vdash E : w' \Rightarrow \text{ptr struct } s \rrbracket^{\text{exp}} = \langle \mathbf{i}^*, \mathbf{v}^* \rangle \\ (\mathbf{o}_1, \mathbf{o}_2) = \text{offset}(s, f) \end{array}}{P, \Gamma \vdash \&E \rightarrow f : w' \Rightarrow \text{ptr } \tau_f \sim_{\delta} \langle \mathbf{i}^*, \mathbf{u32.const } \mathbf{o}_1; \mathbf{u32.const } \mathbf{o}_2; \text{slice}, \mathbf{v}^* \rangle} \\
\text{(Ctx-Malloc)} \\
\frac{\begin{array}{c} P, \Gamma \vdash E : w' \Rightarrow \text{int} \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle \\ \mathbf{n} = \text{sz}(\tau) \end{array}}{P, \Gamma \vdash \text{malloc}(\tau, E) : w' \Rightarrow \text{array } w \sim_{\delta} \langle \mathbf{i}^*, \mathbf{u32.const } \mathbf{n}; \mathbf{u32} \cdot \times; \text{segalloc}, \mathbf{v}^* \rangle} \\
\text{(Ctx-Deref)} \\
\frac{\begin{array}{c} \llbracket w \rrbracket = \tau \\ P, \Gamma \vdash E : w' \Rightarrow \text{ptr } w \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle \end{array}}{P, \Gamma \vdash *E : w' \Rightarrow w \sim_{\delta} \langle \mathbf{i}^*, \tau \cdot \text{segload}, \mathbf{v}^* \rangle} \\
\text{(Ctx-Arr-Deref)} \\
\frac{\begin{array}{c} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash E : w \Rightarrow \text{ptr array } \tau \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle \\ P, \Gamma \vdash \mathbf{n} : \text{int} \sim_{\delta} \mathbf{n} \\ \mathbf{n}' = \text{sz}(\tau) \end{array}}{P, \Gamma \vdash E[\mathbf{n}] : w \Rightarrow \tau \sim_{\delta} \langle \mathbf{i}^*, \mathbf{u32.const } \mathbf{n}; \mathbf{u32.const } \mathbf{n}'; \mathbf{u32} \cdot \times; \text{handle.add}; \tau \cdot \text{segload}, \mathbf{v}^* \rangle} \\
\text{(Ctx-Arr-Deref1)} \\
\frac{\begin{array}{c} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash E : w \Rightarrow \text{int} \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle \\ P, \Gamma \vdash a^{(\mathbf{b}, \ell, \tau, \mathbf{n}_{\text{id}})} : \text{ptr array } \tau \sim_{\delta} \mathbf{a} \\ \mathbf{n}' = \text{sz}(\tau) \end{array}}{P, \Gamma \vdash a^{(\mathbf{b}, \ell, \tau, \mathbf{n}_{\text{id}})}[E] : w \Rightarrow \tau \sim_{\delta} \langle \mathbf{i}^*, \mathbf{u32.const } \mathbf{n}'; \mathbf{u32} \cdot \times; \text{handle.add}; \tau \cdot \text{segload}, \mathbf{v}^*; \mathbf{a} \rangle} \\
\text{(Ctx-Var-Assign)} \\
\frac{\begin{array}{c} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash E : w' \Rightarrow \tau \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle \end{array}}{P, \Gamma \vdash x := E : w' \Rightarrow \tau \sim_{\delta} \langle \mathbf{i}^*, \text{dup}; \text{set } x, \mathbf{v}^* \rangle} \\
\text{(Ctx-Assign)} \\
\frac{\begin{array}{c} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash E : w' \Rightarrow \text{ptr } \tau \sim_{\delta} \langle \mathbf{i}_1^*, \mathbf{v}^* \rangle \\ \llbracket P, \Gamma \vdash e_2 : \tau \rrbracket^{\text{exp}} = \mathbf{i}_2^* \end{array}}{P, \Gamma \vdash *E := e_2 : w' \Rightarrow \tau \sim_{\delta} \langle \mathbf{i}_1^*, \mathbf{i}_2^*, \tau \cdot \text{segstore}; \mathbf{u32.const } 0, \mathbf{v}^* \rangle}
\end{array}$$

$$\begin{array}{c}
\text{(Ctx-Assign2)} \\
\frac{\begin{array}{c} \llbracket w \rrbracket = \tau \\ P, \Gamma \vdash v' : \text{ptr } \tau \sim_{\delta} v' \\ P, \Gamma \vdash E : w' \Rightarrow \tau \sim_{\delta} \langle i_2^*, v^* \rangle \end{array}}{P, \Gamma \vdash *v' := E : w' \Rightarrow \text{int} \sim_{\delta} \langle i_2^*, \tau.\text{segstore}; \text{u32.const } 0, (v^*; v') \rangle} \\
\text{(Ctx-Arr-Assign1)} \\
\frac{\begin{array}{c} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash E : w' \Rightarrow \text{ptr } \tau \sim_{\delta} \langle i_1^*, v^* \rangle \\ \llbracket P, \Gamma \vdash e_2 : \tau \rrbracket^{\text{exp}} = i_2^* \end{array}}{P, \Gamma \vdash *E := e_2 : w' \Rightarrow \tau \sim_{\delta} \langle i_1^*, i_2^*; \tau.\text{segstore}; \text{u32.const } 0, v^* \rangle} \\
\text{(Ctx-Arr-Assign2)} \\
\frac{\begin{array}{c} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash E : w' \Rightarrow \text{ptr } \tau \sim_{\delta} \langle i_1^*, v^* \rangle \\ \llbracket P, \Gamma \vdash e_2 : \tau \rrbracket^{\text{exp}} = i_2^* \end{array}}{P, \Gamma \vdash *E := e_2 : w' \Rightarrow \tau \sim_{\delta} \langle i_1^*, i_2^*; \tau.\text{segstore}; \text{u32.const } 0, v^* \rangle} \\
\text{(Ctx-Arr-Assign3)} \\
\frac{\begin{array}{c} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash E : w' \Rightarrow \text{ptr } \tau \sim_{\delta} \langle i_1^*, v^* \rangle \\ \llbracket P, \Gamma \vdash e_2 : \tau \rrbracket^{\text{exp}} = i_2^* \end{array}}{P, \Gamma \vdash *E := e_2 : w' \Rightarrow \tau \sim_{\delta} \langle i_1^*, i_2^*; \tau.\text{segstore}; \text{u32.const } 0, v^* \rangle} \\
\text{(Ctx-Seq)} \\
\frac{\begin{array}{c} \llbracket P, \Gamma \vdash e : w_2 \rrbracket^{\text{exp}} = i_2^* \\ P, \Gamma \vdash E : w' \Rightarrow w_1 \sim_{\delta} \langle i_1^*, v^* \rangle \end{array}}{P, \Gamma \vdash E; e : w' \Rightarrow w_2 \sim_{\delta} \langle i_1^*, i_2^*, v^* \rangle} \\
\text{(Ctx-Branch)} \\
\frac{\begin{array}{c} P, \Gamma \vdash E : w' \Rightarrow \text{int} \sim_{\delta} \langle i_0^*, v^* \rangle \\ \llbracket P, \Gamma \vdash e_1 : w \rrbracket^{\text{exp}} = i_1^* \\ \llbracket P, \Gamma \vdash e_2 : w \rrbracket^{\text{exp}} = i_2^* \end{array}}{P, \Gamma \vdash \text{if } E \text{ then } e_1 \text{ else } e_2 : w' \Rightarrow w \sim_{\delta} \langle i_0^*; \text{branch } i_1^* \ i_2^*, v^* \rangle} \\
\text{(Ctx-Call)} \\
\frac{\begin{array}{c} F_{\text{map}}(g) = n_g \\ V_{\text{map}}(x) = n_x \\ \text{flookup}(P, g) = w_2 \ g(x : w_1) \mapsto e_g \\ P, \Gamma \vdash E : w'' \Rightarrow w_1 \sim_{\delta} \langle i_1^*, v^* \rangle \\ \llbracket P, (\Gamma, x : w_2) \vdash e_2 : w' \rrbracket^{\text{exp}} = i_2 \end{array}}{P, \Gamma \vdash x := \text{call } g(E) : w'' \Rightarrow w' \sim_{\delta} \langle i_1^*; \text{call } n_g; \text{set } n_x; i_2^*, v^* \rangle} \\
\hline
\boxed{P, \Gamma \vdash E : w' \Rightarrow w \sim_{\delta} \langle i^*, v^* \rangle} \\
\text{(Ctx-Stack)} \\
\frac{\forall j \in |E_f^*|. P, \Gamma_f \vdash E_f : w_{1f} \Rightarrow w_{2f} \sim_{\delta} \langle i_j^*, v_j^* \rangle \implies K[j] \sim_{\delta, V_{\text{map}}} \langle \theta_j, i_j^*, v_j^* \rangle \implies S[j] = \langle \theta_j, i_j^*, v_j^* \rangle}{\langle P, K, E_f^* \rangle \sim_{\delta} S^*} \\
\hline
\boxed{\langle P, K, E_f^* \rangle \sim_{\delta} S^*}
\end{array}$$



### 4.3 Trace Relation

**Definition 17** (Functional relation source and target events  $\alpha =_\delta \alpha$ ).

$$\begin{array}{c}
\text{(Tr-Rel-Read)} \\
\frac{\vdash v : \tau \quad \llbracket \tau \rrbracket = \tau \quad w \neq \text{struct } \_ \quad a^{(b, \ell, w, n_{id})} \sim_\delta \langle b, o, \ell, 1, n_{id} \rangle}{\text{read}(a^{(b, \ell, w, n_{id})}, v) =_\delta \text{read}_\tau(\langle b, o, \ell, 1, n_{id} \rangle_F)} \\
\\
\text{(Tr-Rel-Write)} \\
\frac{\vdash v : \tau \quad \llbracket \tau \rrbracket = \tau \quad w \neq \text{struct } \_ \quad a^{(b, \ell, w, n_{id})} \sim_\delta \langle b, o, \ell, 1, n_{id} \rangle}{\text{write}(a^{(b, \ell, w, n_{id})}, v) =_\delta \text{write}_\tau(\langle b, o, \ell, 1, n_{id} \rangle_F)} \\
\\
\text{(Tr-Rel-Allocate)} \\
\frac{(a^{(b, \ell, w, n_{id})}) \sim_\delta \langle b, o, \ell, 1, n_{id} \rangle}{\text{salloc}(a^{(b, \ell, w, n_{id})}) =_\delta \text{salloc}(\langle b, o, \ell, 1, n_{id} \rangle)} \\
\\
\text{(Tr-Rel-Free)} \qquad \text{(Tr-Rel-Read-Forge)} \\
\frac{(a^{(b, \ell, w, n_{id})}) \sim_\delta \langle b, o, \ell, 1, n_{id} \rangle}{\text{free}(a^{(b, \ell, w, n_{id})}) =_\delta \text{free}(\langle b, o, \ell, 1, n_{id} \rangle)} \qquad \frac{}{\text{fread}(a, v) =_\delta \text{trap}} \\
\\
\text{(Tr-Rel-Write-Forge)} \qquad \text{(Tr-Rel-Free-Forge)} \\
\frac{}{\text{fwrite}(a, v) =_\delta \text{trap}} \qquad \frac{}{\text{ffree } a =_\delta \text{trap}}
\end{array}$$

**Definition 18** (Functional relation source and target traces  $\bar{\alpha} =_\delta \bar{\alpha}$ ).

$$\begin{array}{c}
\text{(Empty)} \qquad \text{(Tr-Concat)} \\
\frac{}{\epsilon =_\delta \epsilon} \qquad \frac{\alpha =_\delta \alpha \quad \bar{\alpha} =_\delta \bar{\alpha}}{\alpha \cdot \bar{\alpha} =_\delta \alpha \cdot \bar{\alpha}}
\end{array}$$

### 4.4 Equivalence Relation for State

**Definition 19** (Address Map  $\delta$ ). Address map,  $\delta : \mathbb{N} \times n_{id} \rightarrow \mathbb{N} \times n_{id}$ , maps a source address to a target address.

**Definition 20** (Heap Equivalence).

$$\begin{array}{c}
\text{(Heap-Equiv)} \\
\frac{\forall b \in \text{sz}(H - 1). \text{ let } H[b] = v : \tau \quad (a, n, \_, n_{id}) \in [A]_1 \quad b \in [a..a + n] \quad \text{let } b, n_{id} = \delta(b, n_{id}) \quad \llbracket \tau \rrbracket = \tau \quad s = \text{sz}(\tau) \quad T[b]..T[b + s - 1] = (b, t)_{0..(b, t)_{s-1}} \quad v \sim_\delta (b, t)_{0..(b, t)_{s-1}}}{H \sim_{\delta, A} T}
\end{array}$$

**Definition 21** (Stack Frame Equivalence). Let  $\mathbf{F}_{\mathbf{n}_f} = (\theta, \mathbf{i}^*, \mathbf{v}^*)_{\mathbf{n}_f}$ . Let  $\mathbf{B}_f$  and  $\mathbf{F}_{\mathbf{n}_f}$  be source and target stack frames such that  $F_{\text{map}}[f] = \mathbf{n}_f$ . Stack frames  $\mathbf{B}_f$  and  $\mathbf{F}_{\mathbf{n}_f}$  are equivalent, denoted as  $\mathbf{B}_f \sim_\delta \mathbf{F}_{\mathbf{n}_f}$ , if for all  $x$ ,  $\mathbf{B}[x] \sim_\delta \theta[x]$ .

**Definition 22** (Stack Equivalence). Stacks  $\mathbf{K}$  and  $\mathbf{S}^*$  are equivalent, denoted as  $\mathbf{K} \sim_\delta \mathbf{S}$ , if all stack frames are equivalent. That is, for all  $i$ ,  $\mathbf{K}[i] \sim_\delta \mathbf{S}[i]$

**Definition 23** (Slot Equivalence).

$$\frac{\begin{array}{c} \text{(Slot-Eq-Prim)} \\ n \times \text{srcsz}(\tau) = \mathbf{n} \quad \forall i \in \{0..n-1\}. \delta(\mathbf{a} + \mathbf{i}, \mathbf{n}_{\text{id}}) = (\mathbf{a} + \mathbf{i} \times \text{srcsz}(\tau), \mathbf{n}_{\text{id}}) \end{array}}{(\mathbf{a}, \mathbf{n}, \tau, \mathbf{n}_{\text{id}}) \sim_\delta (\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}})}$$

$$\frac{\begin{array}{c} \text{(Slot-Eq-Struct)} \\ \text{srcsz}(\mathbf{s}) = \mathbf{n} \quad \mathbf{D}(\mathbf{s}) = \{f_1 : \tau_1 \dots f_n : \tau_n\} \\ \forall i \in 0..n-1. \delta(\mathbf{a} + \mathbf{i}, \mathbf{n}_{\text{id}}) = (\mathbf{a} + \sum_{k=0}^{i-1} \text{srcsz}(\tau_{k+1}), \mathbf{n}_{\text{id}}) \end{array}}{(\mathbf{a}, 1, \text{struct } \mathbf{s}, \mathbf{n}_{\text{id}}) \sim_\delta (\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}})}$$

**Definition 24** (Allocator Equivalence).

$$\frac{\begin{array}{c} \text{(Alloc-Eq)} \\ \forall X \in \{A, F\}, \mathbf{s}_X^* \sim_\delta \mathbf{s}_X^* \quad \mathbf{Y} =_\delta \mathbf{Y} \end{array}}{\mathbf{s}_A^*, \mathbf{s}_F^*, \mathbf{Y} \sim_\delta \mathbf{s}_A, \mathbf{s}_F^*, \mathbf{Y}^*}$$

**Definition 25** (State Equivalence). The source configuration  $\langle \mathbf{H}, \mathbf{K}, \mathbf{A} \rangle$  and the target configuration  $\langle \mathbf{T}, \mathbf{A}, \mathbf{S} \rangle$  are equivalent with respect to an address map  $\delta$ , denoted as  $\langle \mathbf{H}, \mathbf{K}, \mathbf{A} \rangle \sim_\delta \langle \mathbf{T}, \mathbf{A}, \mathbf{S} \rangle$  if

- $\mathbf{H} \sim_\delta \mathbf{T}$
- $\mathbf{A} \sim_\delta \mathbf{A}$
- $\mathbf{K} \sim_\delta \mathbf{S}$

## 4.5 Runtime Compilation

Since the target language uses a stack-based semantics, we need to relate the runtime configurations for source and target. Note that this only proof artifact and does not exist during the actual runtime.

### Runtime Compilation of Values (Data)

$$\frac{\begin{array}{c} \text{(C-data-int)} \\ |n| = |\mathbf{n}| \\ n \sim_\delta \mathbf{n} \end{array}}{\quad} \quad \frac{\begin{array}{c} \text{(Heap-Value)} \\ \vdash \mathbf{v} : \tau \\ v \sim_\delta \text{unpack}(\mathbf{b}_0 \dots \mathbf{b}_{s-1}) \\ s = \text{sz}(\tau) \end{array}}{v \sim_\delta (\mathbf{b}, \mathbf{t})_0 \dots (\mathbf{b}, \mathbf{t})_{s-1}} \quad \frac{\text{(C-data-Ptr-corrupt)}}{a \sim_\delta \langle \mathbf{b}, \mathbf{n}_o, \mathbf{n}_l, \mathbf{0}, \mathbf{n}_{\text{id}} \rangle}$$

$$\begin{array}{c}
\text{(C-data-Valid-Ptr-Array)} \\
\frac{\delta(\mathbf{b}, \mathbf{n}_{\text{id}}) = \mathbf{b}, \mathbf{n}_{\text{id}} \quad \ell * \text{SZ}(\tau) = \mathbf{n}_l \quad (\mathbf{a} - \mathbf{b}) * \text{SZ}(\tau) = \mathbf{n}_o}{\mathbf{a}^{(\mathbf{b}, \ell, \tau, \mathbf{n}_{\text{id}})} \sim_{\delta} \langle \mathbf{b}, \mathbf{n}_o, \mathbf{n}_l, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle} \\
\text{(C-data-Valid-Ptr-Single)} \\
\frac{w \neq \text{array } \_ \quad \delta(\mathbf{b}, \mathbf{n}_{\text{id}}) = \mathbf{b}, \mathbf{n}_{\text{id}} \quad \text{SZ}(w) = \mathbf{n}_l \quad (\mathbf{a} - \mathbf{b}) = \mathbf{n}_o = 0}{\mathbf{a}^{(\mathbf{b}, \mathbf{1}, w, \mathbf{n}_{\text{id}})} \sim_{\delta} \langle \mathbf{b}, \mathbf{n}_o, \mathbf{n}_l, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle}
\end{array}$$

Runtime Compilation of Data:  $P, \Gamma \vdash v : w \sim_{\delta} v$

### Runtime Compilation of expressions

$$\begin{array}{c}
\text{(Rt-Value)} \\
\frac{v \sim_{\delta} \mathbf{v}}{P, \Gamma \vdash v : \tau \sim_{\delta} \langle [], \mathbf{v} \rangle} \\
\text{(Rt-Ptr-Arith)} \\
\frac{P, \Gamma \vdash v_1 : \text{ptr array } \tau \sim_{\delta} \langle [], \mathbf{v}_1 \rangle \quad P, \Gamma \vdash v_2 : \text{int} \sim_{\delta} \langle [], \mathbf{v}_2 \rangle \quad \mathbf{n} = \text{SZ}(\tau) * \mathbf{v}_2}{P, \Gamma \vdash v_1 \oplus v_2 : \text{array } \tau \sim_{\delta} \langle \text{handle.add}, \mathbf{n}; \mathbf{v}_1 \rangle} \\
\text{(Rt-Ptr-Arith-1)} \\
\frac{P, \Gamma \vdash v_1 : \text{ptr array } \tau \sim_{\delta} \langle [], \mathbf{v}_1 \rangle \quad P, \Gamma \vdash e_2 : \text{int} \sim_{\delta} \langle \mathbf{i}_2^*, \mathbf{v}_2^* \rangle \quad \mathbf{s} = \text{SZ}(\tau)}{P, \Gamma \vdash v_1 \oplus e_2 : \text{array } \tau \sim_{\delta} \langle \mathbf{i}_2^*; \text{u32.const } \mathbf{s}; \text{u32.}\times; \text{handle.add}, \mathbf{v}_2^*; \mathbf{v}_1 \rangle} \\
\text{(Rt-Ptr-Arith-2)} \\
\frac{P, \Gamma \vdash e_1 : \text{ptr array } w \sim_{\delta} \langle \mathbf{i}_1^*, \mathbf{v}_1^* \rangle \quad \llbracket P, \Gamma \vdash e_2 : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}_2^* \quad \mathbf{s} = \text{SZ}(\tau)}{P, \Gamma \vdash e_1 \oplus e_2 : \text{array } \tau \sim_{\delta} \langle \mathbf{i}_1^*; \mathbf{i}_2^*; \text{u32.const } \mathbf{s}; \text{u32.}\times; \text{handle.add}, \mathbf{v}_1^* \rangle} \\
\text{(Rt-BinOp)} \\
\frac{\llbracket \text{int} \rrbracket = \tau \quad P, \Gamma \vdash v_1 : \text{int} \sim_{\delta} \mathbf{v}_1 \quad P, \Gamma \vdash v_2 : \text{int} \sim_{\delta} \mathbf{v}_2}{P, \Gamma \vdash v_1 \oplus v_2 : \text{int} \sim_{\delta} \langle \tau.\otimes, \mathbf{v}_2; \mathbf{v}_1 \rangle} \\
\text{(Rt-BinOp-1)} \\
\frac{\llbracket \text{int} \rrbracket = \tau \quad P, \Gamma \vdash e_1 : \text{int} \sim_{\delta} \langle \mathbf{i}_1^*, \mathbf{v}_1^* \rangle \quad \llbracket P, \Gamma \vdash e_2 : \text{int} \rrbracket^{\text{exp}} = \mathbf{i}_2^*}{P, \Gamma \vdash e_1 \oplus e_2 : \text{int} \sim_{\delta} \langle \mathbf{i}_1^*; \mathbf{i}_2^* \tau.\otimes, \mathbf{v}_1^* \rangle} \\
\text{(Rt-BinOp-2)} \\
\frac{\llbracket \text{int} \rrbracket = \tau \quad P, \Gamma \vdash e_2 : \text{int} \sim_{\delta} \langle \mathbf{i}_2^*, \mathbf{v}_2^* \rangle \quad P, \Gamma \vdash v_1 : \text{int} \sim_{\delta} \langle [], \mathbf{v}_1 \rangle}{P, \Gamma \vdash v_1 \oplus e_2 : \text{int} \sim_{\delta} \langle \mathbf{i}_2^* \tau.\otimes, (\mathbf{v}_2^*, \mathbf{v}_1) \rangle}
\end{array}$$

$$\begin{array}{c}
\text{(Rt-Var-Assign)} \\
\frac{\llbracket \tau \rrbracket = \tau}{P, \Gamma \vdash e : \tau \sim_{\delta} \langle i^*, v^* \rangle} \\
\hline
P, \Gamma \vdash x := e : \tau \sim_{\delta} \langle \text{set } x, v^* \rangle \\
\text{(Rt-Var-Assign-1)} \\
\frac{\llbracket \tau \rrbracket = \tau}{P, \Gamma \vdash v : \tau \sim_{\delta} \langle [], v \rangle} \\
\hline
P, \Gamma \vdash x := v : \tau \sim_{\delta} \langle \text{set } x, v \rangle \\
\text{(Rt-Assign)} \\
\frac{\llbracket w \rrbracket = \tau}{P, \Gamma \vdash a^{(b, \ell, w, n_{id})} : \text{ptr } w \sim_{\delta} \langle [], a \rangle} \\
\frac{P, \Gamma \vdash v : w \sim_{\delta} \langle [], v \rangle}{P, \Gamma \vdash *a^{(b, \ell, w, n_{id})} := v : \text{int} \sim_{\delta} \langle \tau.\text{segstore}; \text{u32.const } 0, (v; a) \rangle} \\
\hline
\text{(Rt-Assign-forge)} \\
\frac{\llbracket w \rrbracket = \tau}{P, \Gamma \vdash a : \text{ptr } w \sim_{\delta} \langle [], a \rangle} \\
\frac{P, \Gamma \vdash v : w \sim_{\delta} \langle [], v \rangle}{P, \Gamma \vdash *a := v : \text{int} \sim_{\delta} \langle \tau.\text{segstore}; \text{u32.const } 0, (v; a) \rangle} \\
\hline
\text{(Rt-Assign-1)} \\
\frac{\llbracket w \rrbracket = \tau}{P, \Gamma \vdash a^{(b, \ell, w, n_{id})} : \text{ptr } w \sim_{\delta} \langle [], a \rangle} \\
\frac{P, \Gamma \vdash e : w \sim_{\delta} \langle i^*, v^* \rangle}{P, \Gamma \vdash *a^{(b, \ell, w, n_{id})} := e : \text{int} \sim_{\delta} \langle i^*; \tau.\text{segstore}; \text{u32.const } 0, v^*; a \rangle} \\
\hline
\text{(Rt-Assign-forge-1)} \\
\frac{\llbracket w \rrbracket = \tau}{P, \Gamma \vdash a : \text{ptr } w \sim_{\delta} \langle [], a \rangle} \\
\frac{P, \Gamma \vdash e : w \sim_{\delta} \langle i^*, v^* \rangle}{P, \Gamma \vdash *a := e : \text{int} \sim_{\delta} \langle i^*; \tau.\text{segstore}; \text{u32.const } 0, v^*; a \rangle} \\
\hline
\text{(Rt-Assign-2)} \\
\frac{\llbracket \tau \rrbracket = \tau}{P, \Gamma \vdash e_1 : \text{ptr } \tau \sim_{\delta} \langle i_1^*, v_1^* \rangle} \\
\frac{\llbracket P, \Gamma \vdash e_2 : \tau \rrbracket^{\text{exp}} = i_2^*}{P, \Gamma \vdash *e_1 := e_2 : \tau \sim_{\delta} \langle i_1^*; i_2^*; \tau.\text{segstore}; \text{u32.const } 0, v_1^* \rangle} \\
\hline
\text{(Rt-Arr-Assign)} \\
\frac{\llbracket \tau \rrbracket = \tau}{P, \Gamma \vdash a^{(b, \ell, w, n_{id})} : \text{ptr array } \tau \sim_{\delta} \langle [], a \rangle} \\
\frac{P, \Gamma \vdash n : \text{int} \sim_{\delta} \langle [], n \rangle}{P, \Gamma \vdash v : \tau \sim_{\delta} \langle [], v \rangle} \\
\frac{s = \text{sz}(\tau)}{\text{TODO: Incorrect order of operands}} \\
\hline
P, \Gamma \vdash a^{(b, \ell, w, n_{id})}[n] := v : \text{int} \sim_{\delta} \langle \text{u32}.\times; \text{handle.add}; \tau.\text{segstore}; \text{u32.const } 0, (s; v; a) \rangle
\end{array}$$

$$\begin{array}{c}
\text{(Rt-Arr-Assign1)} \\
\frac{\begin{array}{l} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash a^{(b, \ell, w, n_{id})} : \text{ptr array } \tau \sim_{\delta} \langle [], a \rangle \\ P, \Gamma \vdash n : \text{int} \sim_{\delta} \langle [], n \rangle \\ \llbracket P, \Gamma \vdash e_3 : \tau \rrbracket^{\text{exp}} = i_3^* \\ s = \text{sz}(\tau) \end{array}}{P, \Gamma \vdash a^{(b, \ell, w, n_{id})}[n] := e_3 : \text{int} \sim_{\delta} \langle \text{u32.const } s; \text{u32.}\times; \text{handle.add}; i_3^*; \tau.\text{segstore}; \text{u32.const } 0, n; a \rangle} \\
\text{(Rt-Arr-Assign2)} \\
\frac{\begin{array}{l} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash a^{(b, \ell, w, n_{id})} : \text{ptr array } \tau \sim_{\delta} \langle [], a \rangle \\ P, \Gamma \vdash e_2 : \text{int} \sim_{\delta} \langle i_2^*, v^* \rangle \\ \llbracket P, \Gamma \vdash e_3 : \tau \rrbracket^{\text{exp}} = i_3^* \\ s = \text{sz}(\tau) \end{array}}{P, \Gamma \vdash a^{(b, \ell, w, n_{id})}[e_2] := e_3 : \text{int} \sim_{\delta} \langle i_2^*; \text{u32.const } s; \text{u32.}\times; \text{handle.add}; i_3^*; \tau.\text{segstore}; \text{u32.const } 0, a \rangle} \\
\text{(Rt-Arr-Assign3)} \\
\frac{\begin{array}{l} \llbracket \tau \rrbracket = \tau \\ P, \Gamma \vdash e_1 : \text{ptr array } \tau \sim_{\delta} \langle i_1^*, v^* \rangle \\ \llbracket P, \Gamma \vdash e_2 : \text{int} \rrbracket^{\text{exp}} = i_2^* \\ \llbracket P, \Gamma \vdash e_3 : \tau \rrbracket^{\text{exp}} = i_3^* \\ s = \text{sz}(\tau) \end{array}}{P, \Gamma \vdash e_1[e_2] := e_3 : \text{int} \sim_{\delta} \langle i_1^*; i_2^*; \text{u32.const } s; \text{u32.}\times; \text{handle.add}; i_3^*; \tau.\text{segstore}; \text{u32.const } 0, v^* \rangle} \\
\text{(Rt-Seq)} \\
\frac{\llbracket P, \Gamma \vdash e_2 : w_2 \rrbracket^{\text{exp}} = i_2^*}{P, \Gamma \vdash v_1; e_2 : w \sim_{\delta} \langle i_2^*, [] \rangle} \\
\text{(Rt-Seq-1)} \\
\frac{\begin{array}{l} P, \Gamma \vdash e_1 : w \sim_{\delta} \langle i_1^*, v^* \rangle \\ \llbracket P, \Gamma \vdash e_2 : w_2 \rrbracket^{\text{exp}} = i_2^* \end{array}}{P, \Gamma \vdash e_1; e_2 : w \sim_{\delta} \langle i_1^*, \text{drop}; i_2^*, v^* \rangle} \\
\text{(Rt-Field)} \\
\frac{\begin{array}{l} P, \Gamma \vdash v : \text{ptr struct } s \sim_{\delta} \langle [], v \rangle \\ D(s) = \{f_1 : \tau_1, \dots, f_k : \tau_k \dots f_n : \tau_n\} \\ (o_1, o_2) = (\Sigma_i^{k-1} \text{sz}(\tau_i), (\text{sz}(\text{struct } s) - \text{sz}(\tau_k))) \end{array}}{P, \Gamma \vdash \&v \rightarrow f_k : \text{ptr } \tau_k \sim_{\delta} \langle \text{slice}, o_2; o_1; v \rangle} \\
\text{(Rt-Field-1)} \\
\frac{\begin{array}{l} P, \Gamma \vdash e : \text{ptr struct } s \sim_{\delta} \langle i^*, v^* \rangle \\ D(s) = \{f_1 : \tau_1, \dots, f : \tau_k \dots f_n : \tau_n\} \\ (o_1, o_2) = (\Sigma_i^{k-1} \text{sz}(\tau_i), (\text{sz}(\text{struct } s) - \text{sz}(\tau_k))) \end{array}}{P, \Gamma \vdash \&e \rightarrow f_k : \text{ptr } \tau_k \sim_{\delta} \langle i^*; \text{u32.const } o_1; \text{u32.const } o_2; \text{slice}, v^* \rangle} \\
\text{(Rt-Malloc-Single)} \\
\frac{n' = \text{sz}(w)}{P, \Gamma \vdash \text{malloc}(w) : \text{ptr } w \sim_{\delta} \langle \text{segalloc}, n' \rangle}
\end{array}$$

$$\begin{array}{c}
\text{(Rt-Malloc-Array-1)} \\
\frac{P, \Gamma \vdash n : \text{int} \sim_{\delta} \langle [], n \rangle \quad n' = n * \text{SZ}(\tau)}{P, \Gamma \vdash \text{malloc}(\tau, n) : \text{array } w \sim_{\delta} \langle \text{segalloc}, n' \rangle} \\
\text{(Rt-Malloc-Array)} \\
\frac{P, \Gamma \vdash e : \text{int} \sim_{\delta} \langle i^*, v^* \rangle \quad n' = \text{SZ}(\tau)}{P, \Gamma \vdash \text{malloc}(\tau, e) : \text{array } \tau \sim_{\delta} \langle i^*; \text{u32.const } n'; \text{u32.mult}; \text{segalloc}, v^* \rangle} \\
\text{(Rt-Deref)} \\
\frac{P, \Gamma \vdash v : \text{ptr } \tau \sim_{\delta} \langle [], v \rangle}{P, \Gamma \vdash *v : \tau \sim_{\delta} \langle \tau.\text{segload}, v \rangle} \\
\text{(Rt-Deref-1)} \\
\frac{P, \Gamma \vdash e : \text{ptr } \tau \sim_{\delta} \langle i^*, v^* \rangle}{P, \Gamma \vdash *e : \tau \sim_{\delta} \langle i^*; \tau.\text{segload}, v^* \rangle} \\
\text{(Rt-Arr-Deref)} \\
\frac{P, \Gamma \vdash n : \text{int} \sim_{\delta} \langle [], n \rangle \quad P, \Gamma \vdash a^{(b, \ell, \tau, n_{\text{id}})} : \text{ptr array } \tau \sim_{\delta} \langle [], a \rangle \quad n' = \text{SZ}(\tau) * n}{P, \Gamma \vdash a^{(b, \ell, \tau, n_{\text{id}})}[n] : \tau \sim_{\delta} \langle \text{handle.add}; \tau.\text{segload}, n'; a \rangle} \\
\text{(Rt-Arr-Deref1)} \\
\frac{P, \Gamma \vdash e : \text{int} \sim_{\delta} \langle i^*, v^* \rangle \quad P, \Gamma \vdash a^{(b, \ell, \tau, n_{\text{id}})} : \text{ptr array } \tau \sim_{\delta} \langle [], a \rangle \quad n = \text{SZ}(\tau)}{P, \Gamma \vdash a^{(b, \ell, \tau, n_{\text{id}})}[e] : \tau \sim_{\delta} \langle i^*; \text{u32.const } n; \text{u32.mult}; \text{handle.add}; \tau.\text{segload}, v^*; a \rangle} \\
\text{(Rt-Arr-Deref2)} \\
\frac{P, \Gamma \vdash e_1 : \text{int} \sim_{\delta} \langle i_1^*, v^* \rangle \quad P, \Gamma \vdash e_2 : \text{ptr array } \tau \sim_{\delta} i_2^* \quad n = \text{SZ}(\tau)}{P, \Gamma \vdash e_1[e_2] : \tau \sim_{\delta} \langle i_1^*; i_2^*; \text{u32.const } n; \text{u32.mult}; \text{handle.add}; \tau.\text{segload}, v^* \rangle} \\
\text{(Rt-Free)} \\
\frac{P, \Gamma \vdash a^{(b, \ell, w, n_{\text{id}})} : \text{ptr } w \sim_{\delta} \langle [], v \rangle}{P, \Gamma \vdash \text{free}(a^{(b, \ell, w, n_{\text{id}})}) : \text{int} \sim_{\delta} \langle \text{segfree}; \text{u32.const } 0, v \rangle} \\
\text{(Rt-Free-Forge)} \\
\frac{P, \Gamma \vdash a : \text{ptr } w \sim_{\delta} \langle [], v \rangle}{P, \Gamma \vdash \text{free}(a) : \text{int} \sim_{\delta} \langle \text{segfree}; \text{u32.const } 0, v \rangle} \\
\text{(Rt-Free-1)} \\
\frac{P, \Gamma \vdash e : \text{ptr } w \sim_{\delta} \langle i^*, v^* \rangle}{P, \Gamma \vdash \text{free}(e) : \text{int} \sim_{\delta} \langle i^*; \text{segfree}; \text{u32.const } 0, v^* \rangle}
\end{array}$$

$$\begin{array}{c}
\text{(Rt-Branch)} \\
\frac{
\begin{array}{l}
P, \Gamma \vdash v : \text{int} \sim_{\delta} \langle [], v \rangle \\
\llbracket P, \Gamma \vdash e_1 : w \rrbracket^{\text{exp}} = i_1^* \\
\llbracket P, \Gamma \vdash e_2 : w \rrbracket^{\text{exp}} = i_2^*
\end{array}
}{
P, \Gamma \vdash \text{if } v \text{ then } e_1 \text{ else } e_2 : w \sim_{\delta} \langle \text{branch } i_1^* i_2^*, v \rangle
} \\
\text{(Rt-Branch-1)} \\
\frac{
\begin{array}{l}
P, \Gamma \vdash e : \text{int} \sim_{\delta} \langle i^*, v^* \rangle \\
\llbracket P, \Gamma \vdash e_1 : w \rrbracket^{\text{exp}} = i_1^* \\
\llbracket P, \Gamma \vdash e_2 : w \rrbracket^{\text{exp}} = i_2^*
\end{array}
}{
P, \Gamma \vdash \text{if } e \text{ then } e_1 \text{ else } e_2 : w \sim_{\delta} \langle i^*; \text{branch } i_1^* i_2^*, v^* \rangle
} \\
\text{(Rt-Call)} \\
\frac{
\begin{array}{l}
F_{\text{map}}(g) = n_g \\
V_{\text{map}}(x) = n_x \\
\text{lookup}(P, g) = w_2 \ g(x : w_1) \mapsto e_g \\
P, \Gamma \vdash v_1 : w_1 \sim_{\delta} \langle [], v_1 \rangle
\end{array}
}{
P, \Gamma \vdash x := \text{call } g(v_1) : w' \sim_{\delta} \langle \text{call } n_g; \text{set } n_x, v_1 \rangle
} \\
\text{(Rt-Call-1)} \\
\frac{
\begin{array}{l}
F_{\text{map}}(g) = n_g \\
V_{\text{map}}(x) = n_x \\
\text{lookup}(P, g) = w_2 \ g(x : w_1) \mapsto e_g \\
P, \Gamma \vdash e : w_1 \sim_{\delta} \langle i^*, v^* \rangle
\end{array}
}{
P, \Gamma \vdash x := \text{call } g(e) : w' \sim_{\delta} \langle i^*; \text{call } n_g; \text{set } n_x, v^* \rangle
} \\
\text{Runtime Compilation of Expressions: } P, \Gamma \vdash e : w \sim_{\delta} \langle i^*, v^* \rangle
\end{array}$$

## 4.6 Configuration Compilation

$$\begin{array}{c}
\text{(Rt-Config)} \\
\frac{
\begin{array}{l}
P, \Gamma \vdash e : w \sim_{\delta} \langle i^*, v^* \rangle \\
\langle P, K, E^* \rangle \sim_{\delta} S^* \\
H \sim_{\delta} \Sigma.T \\
A \sim_{\delta} \Sigma.A \\
\theta_f \sim_{\delta} \theta_f
\end{array}
}{
P, (\theta_f; K), H, A \triangleright (e_f, E^*) \sim_{\delta} \langle \Sigma, (\theta_f, i^*, v^*) : S^* \rangle
} \\
\text{(Compile-Config)} \\
\frac{
\begin{array}{l}
\llbracket P, \Gamma_f \vdash e_f : w \rrbracket^{\text{exp}} = i^* \\
\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta \\
\langle P, K, E^* \rangle \sim_{\delta} S^* \\
H \sim_{\delta} \Sigma.T \\
A \sim_{\delta} \Sigma.A \\
\theta_f \sim_{\delta} \theta_f
\end{array}
}{
\Delta \vdash \langle \Sigma, (\theta_f, i^*, v^*) : S^* \rangle \\
\llbracket P, (\theta_f; K), H, A \triangleright (e_f, E^*), \delta \rrbracket = \langle \Sigma, (\theta_f, i^*, v^*) : S^* \rangle
}
\end{array}$$

## 4.7 Type Preserving Compilation

We prove that the compilation preserves well-typedness.

**Theorem 2** (Type-Preservation of a Compiled Program). If

- $\vdash P : \text{wt}$
- $\llbracket P \rrbracket^{\text{prog}} = M$

- $\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta$

then  $\Delta \vdash M$ .

*Proof.* From rule (Program), we have

$$\forall k, P.F[k] = f_k, \llbracket w_{rk} f_k(x : w_{ak}) \mapsto ((y_{jk} : w_{jk})^*, e_k) \rrbracket^{\text{fun}} = f_k \quad (45)$$

$$|s| = \text{sz}(P.n_{hs}) \quad (46)$$

$$M = \{\text{heap } 0, \text{segment } s, \text{funcs } f_0 f_1 \dots f_n\} \quad (47)$$

Applying Lemma 14 (Type-Preservation of a Compiled Function) to the (45), we have all the required premises for rule (Module).  $\square$

**Lemma 14** (Type-Preservation of a Compiled Function). If

- $P \vdash \tau_r f(x : \tau_a) \mapsto ((y_j : \tau_j)^*, e_f) : \tau_a \rightarrow \tau_r$
- $\llbracket \tau_a \rrbracket = \tau_a$
- $\llbracket \tau_r \rrbracket = \tau_r$
- $\llbracket \tau_j \rrbracket = \tau_j$
- $\llbracket \tau_r f(x : \tau_a) \mapsto ((y_j : \tau_j)^*, e) \rrbracket^{\text{fun}} = \langle \text{var } \tau_a \tau^v, i^* \rangle$

then  $\{\text{funcs } \rho^*, \text{locals} = \tau_a \tau_1 \tau_2 \dots, \text{return} = \tau_r\} \vdash \{\text{var } \tau_a \tau^v, \text{body } i^*\} : \tau^a \rightarrow \tau_r$ .

*Proof.* We have  $\Gamma_f = (\tau_r, \{x : \tau_a, y_1 : \tau_1, y_2 : \tau_2, \dots\})$ . By lifting the compilation of types to typing environments, we have  $\llbracket P, \Gamma_f \rrbracket^{\text{env}} = \Gamma_f$  where  $\Gamma_f = \{\text{funcs } \rho^*, \text{locals } \tau_a \tau_1 \tau_2 \dots, \text{return } \tau_r\}$ . Applying Corollary 1 (Type-Preservation of a Compiled Expression) we have the proof.  $\square$

**Lemma 15** (Type-Preservation of a Compiled Expression). If

- $P, \Gamma_f \vdash e : w$ ,
- $\llbracket P, \Gamma_f \rrbracket^{\text{env}} = \Gamma_f$
- $\llbracket w \rrbracket = \tau$
- $\llbracket P, \Gamma_f \vdash e : w \rrbracket^{\text{exp}} = i^*$ ,

then for all  $\tau'^*$ ,  $\Gamma_f \vdash i^* : \tau'^* \rightarrow \tau \tau'^*$ .

*Proof.* Proof is by induction on the compilation judgment.  $\square$

**Corollary 1** (Type-Preservation of a Compiled Expression). If

- $P, \Gamma_f \vdash e : w$ ,
- $\llbracket P, \Gamma_f \rrbracket^{\text{env}} = \Gamma_f$



- $\llbracket w \rrbracket = \tau$
- $\llbracket P, \Gamma \vdash e : w \rrbracket^{\text{exp}} = \mathbf{i}^*$ ,

we have  $\Gamma_f \vdash \mathbf{i} : \square \rightarrow \tau$ .

*Proof.* Instantiate  $\tau^*$  with  $\square$  in Lemma 15. □

## 4.8 Functional Correctness

We prove that the compilation preserves functional correctness.

### 4.8.1 Split MS Monitor

Before we prove functional correctness, we first need to establish that when the source takes multiple steps and results in MS monitor taking several steps, then they can be split. We will use this result to prove that when the source configuration (in related source and target configurations) takes multiple steps that are memory safe, then the compiled configuration takes one or more steps safely, and moreover the output source and target configurations will be in the same relation.

**Lemma 16** (MS Monitor Split). If

1.  $\Omega \xRightarrow{\alpha} \Omega'$  and
2.  $\Omega' \xRightarrow{\bar{\alpha}^*} \Omega''$  and
3.  $\delta \subseteq \delta''$  and
4.  $T =_{\delta} \Omega.A$  and
5.  $\alpha \cdot \bar{\alpha} =_{\delta''} \bar{\alpha}$  and
6.  $\vdash T \xRightarrow{\bar{\alpha}} T''$  and
7.  $T'' =_{\delta''} \Omega''.A$

then  $\exists \delta', \bar{\alpha}_1, \bar{\alpha}_2$  and  $T'$ ,

(a)  $\bar{\alpha} = \bar{\alpha}_1 \cdot \bar{\alpha}_2$

(b)

$$\begin{aligned}
&\delta \subseteq \delta' \\
&\delta \subseteq \delta' \\
&\alpha =_{\delta'} \bar{\alpha}_1 \\
&\vdash T \xRightarrow{\bar{\alpha}_1} T' \\
&T' =_{\delta'} \Omega'.A
\end{aligned}$$

(c)

$$\begin{aligned}
& \delta' \subseteq \delta'' \\
& \bar{\alpha} =_{\delta''} \bar{\alpha}_2 \\
& \vdash T' \overset{\bar{\alpha}_2}{\rightsquigarrow} T'' \\
& T'' =_{\delta''} \Omega''.A
\end{aligned}$$

*Proof.* Given:

$$\Omega \xRightarrow{\alpha} \Omega' \quad (48)$$

$$\Omega' \xRightarrow{\bar{\alpha}}^* \Omega'' \quad (49)$$

$$\delta \subseteq \delta'' \quad (50)$$

$$T =_{\delta} \Omega.A \quad (51)$$

$$\alpha \cdot \bar{\alpha} =_{\delta''} \bar{\alpha} \quad (52)$$

$$\vdash T \overset{\bar{\alpha}}{\rightsquigarrow} T'' \quad (53)$$

$$T'' =_{\delta''} \Omega''.A \quad (54)$$

We have to prove

$$\delta \subseteq \delta' \quad (55)$$

$$\alpha =_{\delta'} \bar{\alpha}_1 \quad (56)$$

$$\vdash T \overset{\bar{\alpha}_1}{\rightsquigarrow} T' \quad (57)$$

$$T' =_{\delta'} \Omega'.A \quad (58)$$

$$\delta' \subseteq \delta'' \quad (59)$$

$$\bar{\alpha} =_{\delta''} \bar{\alpha}_2 \quad (60)$$

$$\vdash T' \overset{\bar{\alpha}_2}{\rightsquigarrow} T'' \quad (61)$$

$$T'' =_{\delta''} \Omega''.A \quad (62)$$

If  $\bar{\alpha}$  is  $\epsilon$ , then  $\alpha \cdot \bar{\alpha}$  is  $\epsilon$  as well (Lemma 3 (Empty Source Trace)).

Let  $\bar{\alpha} \neq \epsilon$ . Then, applying Lemma 4 (Splitting Source Trace Equivalence), we have  $\bar{\alpha} = \bar{\alpha}_1 \cdot \bar{\alpha}_2$  for some  $\bar{\alpha}_1$  and  $\bar{\alpha}_2$ .

Inverting (52) using (Tr-Concatenate), we have

$$\alpha =_{\delta''} \bar{\alpha}_1 \quad (63)$$

$$\bar{\alpha} =_{\delta''} \bar{\alpha}_2 \quad (64)$$

Case analysis on (63)

Case **(Tr-Read-Authentic)**: Given:

$$\text{read}(a^{(b, \ell, w, n_{id})}, v) =_{\delta''} \text{read}(a^c) \cdot \text{read}((a+1)^c) \dots \text{read}((a+n-1)^c)$$

Thus  $\bar{\alpha}_1 = \text{read}(a^c) \cdot \text{read}((a+1)^c) \dots \text{read}((a+n-1)^c)$ . From **(Cons)** and **(MS-Read)**, we have  $\vdash T \xrightarrow{\bar{\alpha}_1} T'$ . Thus  $T = T'$ . From Lemma 1 (**Split Abstract MS Monitor**), we have

$$\vdash T' \xrightarrow{\bar{\alpha}_2} T'' \quad (65)$$

Since  $T = T'$ , it follows that  $\Omega.A = \Omega'.A$ . Let  $\delta' = \delta''$ . Thus

$$T' =_{\delta'} \Omega'.A \quad (66)$$

Thus, we have proved (67) to (74).

Case **(Tr-Write-Authentic)**: Similar to the above case.

Case **(Tr-SAlloc)**: Given:

$$\text{salloc}(a, n, w) =_{\delta''} \text{alloc}(n, a^c)$$

(We prove for  $w = \tau$ . The proof for the case  $w = \text{struct } s$  is analogous.)

We have  $\bar{\alpha}_1 = \text{alloc}(n, a^c, \phi)$ . We need to prove:

$$\delta \subseteq \delta' \quad (67)$$

$$\alpha =_{\delta'} \bar{\alpha}_1 \quad (68)$$

$$\vdash T \xrightarrow{\bar{\alpha}_1} T' \quad (69)$$

$$T' =_{\delta'} \Omega'.A \quad (70)$$

$$\delta' \subseteq \delta'' \quad (71)$$

$$\bar{\alpha} =_{\delta''} \bar{\alpha}_2 \quad (72)$$

$$\vdash T' \xrightarrow{\bar{\alpha}_2} T'' \quad (73)$$

$$T'' =_{\delta''} \Omega''.A \quad (74)$$

We first prove  $\vdash T \xrightarrow{\bar{\alpha}_1} T'$ . We are already given  $\vdash T \xrightarrow{\bar{\alpha}} T''$ . That is,  $\vdash T \xrightarrow{\text{alloc}(n, a^c, \phi) \cdot \bar{\alpha}_2} T''$ . From rule **(Cons)**, we have  $\vdash T \xrightarrow{\text{alloc}(n, a^c, \phi)} T'$ .

This implies  $\vdash T \xrightarrow{\bar{\alpha}_1} T'$ .

From **(Tr-SAlloc)**, we have that  $\delta''(a, n_{id}) = a^{(c, 0)}$ . Inverting **(MS-Alloc)**, we have that  $c$  is fresh. Let  $T' = T[a + i \mapsto A(c, \phi(i)) \mid i \in \{0..n-1\}]$  and  $T'.Y = T.Y \cdot \text{alloc}(n, a^c, \phi)$ . We are already given  $T =_{\delta} \Omega.A$ . Let  $\delta' = \delta''$ . Thus from Definition 4 (**Segment Agreement**  $T =_{\delta} A$ ), we just need

to prove that the newly allocated buffer agrees with the abstract memory. From (48) and (Src-Alloc), we have  $\Omega'.A.s_F^* = s_1^* \cdot (a + n, \ell' - \ell, w', n_{id}') \cdot s_2^*$  and  $\Omega'.A.s_A^* = (a, \ell, w, n_{id}) \cdot s_U^*$  where  $\text{fresh}(n_{id})$ . We thus have  $T' =_{\delta'} \Omega'.A$ .

Applying Lemma 1 (Split Abstract MS Monitor) to  $\vdash T \xrightarrow{\bar{\alpha}_1 \cdot \bar{\alpha}_2} T''$  and  $\vdash T \xrightarrow{\bar{\alpha}_1} T'$ , we have

$$\vdash T' \xrightarrow{\bar{\alpha}_2} T'' \quad (75)$$

Hence the proof.

**Case (Tr-SFree):** Given:

$$\text{free}(a^{(b, \ell, w, n_{id})}) =_{\delta} \text{sfree}(a^c)$$

We have  $\bar{\alpha}_1 = \text{sfree}(a^c)$ . We first need to prove  $\vdash T \xrightarrow{\bar{\alpha}_1} T'$  and then apply Lemma 1 (Split Abstract MS Monitor) to  $\vdash T \xrightarrow{\bar{\alpha}_1 \cdot \bar{\alpha}_2} T''$  and  $\vdash T \xrightarrow{\bar{\alpha}_1} T'$ . Since we are already given  $\vdash T' \xrightarrow{\bar{\alpha}_1 \cdot \bar{\alpha}_2} T''$  (from (53)), inverting (Cons), we have  $\vdash T \xrightarrow{\bar{\alpha}_1} T'$  and so  $\vdash T \xrightarrow{\bar{\alpha}_1} T'$  (trivially from (Empty)). The remaining proof is along the lines of the above alloc case.

□

#### 4.8.2 Functional Correctness—Multiple Steps

**Lemma 17** (Monotone  $\delta$ ). If  $\Omega \sim_{\delta} \Omega$  and  $\delta \subseteq \delta'$  then  $\Omega \sim_{\delta'} \Omega$ .

*Proof.* Follows from (Rt-Config). □

**Lemma 18** (Functional Correctness—Multiple Steps). If

1.  $\Omega \xrightarrow{\bar{\alpha}}^* \Omega'$  and
2.  $\llbracket P \rrbracket^{\text{prog}} = M$  and
3.  $\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta$  and
4.  $\Omega \sim_{\delta} \Omega$  and
5.  $\Delta \vdash \Omega$  and
6.  $\vdash \text{isValid } \Omega$  and
7.  $T =_{\delta} \Omega.A$
8.  $\delta \subseteq \delta'$  and

9.  $\bar{\alpha} =_{\delta'} \bar{\alpha}$  and
10.  $\vdash T \xrightarrow{\bar{\alpha}} T'$  and
11.  $T' =_{\delta'} \Omega'.A$

then

- (i)  $\exists \delta' \supseteq \delta$  and  $\Omega', \Omega' \sim_{\delta'} \Omega'$
- (ii)  $\exists \bar{\alpha}, \mathbf{M.funcs} \vdash \Omega \xrightarrow{\bar{\alpha}} \Omega'$  and
- (iii)  $\bar{\alpha} =_{\delta'} \bar{\alpha}$  and
- (iv)  $\vdash \mathbf{isValid} \Omega'$

*Proof.* Proof is by induction on the relation  $\Omega \xrightarrow{\bar{\alpha}} \Omega'$ .

**(S-Base):** Trivial.

**(S-Steps):** We have

$$\Omega \xrightarrow{\alpha} \Omega' \quad (76)$$

$$\Omega' \xrightarrow{\bar{\alpha}^*} \Omega'' \quad (77)$$

We are also given:

$$\llbracket P \rrbracket^{\text{prog}} = \mathbf{M} \quad (78)$$

$$\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta \quad (79)$$

$$\Omega \sim_{\delta} \Omega'' \quad (80)$$

$$\Delta \vdash \Omega \quad (81)$$

$$\vdash \mathbf{isValid} \Omega \quad (82)$$

$$T =_{\delta} \Omega.A \quad (83)$$

$$\delta \subseteq \delta'' \quad (84)$$

$$\bar{\alpha} =_{\delta''} \bar{\alpha} \quad (85)$$

$$\vdash T \xrightarrow{\bar{\alpha}} T'' \quad (86)$$

$$T'' =_{\delta'} \Omega''.A \quad (87)$$

Applying Lemma 16 (MS Monitor Split) to (76), (77) and (83) to (87), gives:

$$\bar{\alpha} = \bar{\alpha}_1 \cdot \bar{\alpha}_2 \quad (88)$$

$$\delta \subseteq \delta' \quad (89)$$

$$\alpha =_{\delta'} \bar{\alpha}_1 \quad (90)$$

$$\vdash T \xrightarrow{\bar{\alpha}_1} T' \quad (91)$$

$$T' =_{\delta'} \Omega'.A \quad (92)$$

$$\delta' \subseteq \delta'' \quad (93)$$

$$\bar{\alpha} =_{\delta''} \bar{\alpha}_2 \quad (94)$$

$$\vdash T' \xrightarrow{\bar{\alpha}_2} T'' \quad (95)$$

$$T'' =_{\delta''} \Omega''.A \quad (96)$$

That gives us necessary premises to invoke Lemma 21 (Functional Correctness—Single-step) on (76), (78) to (82) and (89) to (92). We get

$$\exists \delta' \supseteq \delta \text{ and } \Omega', \Omega' \sim_{\delta'} \Omega' \quad (97)$$

$$\mathbf{M.funcs} \vdash \Omega \xRightarrow{\alpha} \Omega' \quad (98)$$

$$\alpha =_{\delta'} \alpha \quad (99)$$

$$\vdash \mathbf{isValid} \Omega' \quad (100)$$

Applying Lemma 7 (Type and Validity Preservation for Configurations—Trace Semantics) to (76), gives

$$\Delta \vdash \Omega'$$

Above combined with (78), (79), (97) to (100), (93) to (96) gives us necessary premises to invoke I.H on (77). Applying I.H we have the proof.  $\square$

#### 4.8.3 Evaluation Context Lemmas

**Lemma 19** (Context Composition). If

1.  $e = E[e']$
2.  $P, \Gamma \vdash E : w' \Rightarrow w \sim_{\delta} \langle \mathbf{i}_{E[\cdot]}^*, \mathbf{v}_{E[\cdot]}^* \rangle$
3.  $P, \Gamma \vdash e' : w' \sim_{\delta} \langle \mathbf{i}'^*, \mathbf{v}'^* \rangle$

then  $P, \Gamma \vdash e : w \sim_{\delta} \langle \mathbf{i}^*; \mathbf{i}_{E[\cdot]}^*, \mathbf{v}'^*; \mathbf{v}_{E[\cdot]}^* \rangle$ .

*Proof.* Proof is by induction on the compilation of evaluation context, that is,  $P, \Gamma \vdash E : w' \Rightarrow w \sim_{\delta} \langle \mathbf{i}_{E[\cdot]}^*, \mathbf{v}_{E[\cdot]}^* \rangle$   $\square$

**Lemma 20** (Context Decomposition). If

1.  $e = E[e']$

$$2. \mathbf{P}, \Gamma \vdash \mathbf{e} : \mathbf{w} \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle$$

then there exists  $\mathbf{i}'^*, \mathbf{i}_{\mathbf{E}[\cdot]}^*, \mathbf{v}'^*, \mathbf{v}_{\mathbf{E}[\cdot]}^*$ , such that:

$$(i) \mathbf{i}^* = \mathbf{i}'^* ; \mathbf{i}_{\mathbf{E}[\cdot]}^*$$

$$(ii) \mathbf{v}^* = \mathbf{v}'^* ; \mathbf{v}_{\mathbf{E}[\cdot]}^*$$

$$(iii) \mathbf{P}, \Gamma \vdash \mathbf{E} : \mathbf{w}' \Rightarrow \mathbf{w} \sim_{\delta} \langle \mathbf{i}_{\mathbf{E}[\cdot]}^*, \mathbf{v}_{\mathbf{E}[\cdot]}^* \rangle$$

$$(iv) \mathbf{P}, \Gamma \vdash \mathbf{e}' : \mathbf{w}' \sim_{\delta} \langle \mathbf{i}'^*, \mathbf{v}'^* \rangle$$

*Proof.* Proof is by induction on  $\mathbf{P}, \Gamma \vdash \mathbf{e} : \mathbf{w} \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle$  . □

#### 4.8.4 Functional Correctness—Single-step

**Lemma 21** (Functional Correctness—Single-step). If

1.  $\Omega \xRightarrow{\alpha} \Omega'$  and
2.  $\llbracket \mathbf{P} \rrbracket^{\text{prog}} = \mathbf{M}$  and
3.  $\llbracket \mathbf{P}, \Delta \rrbracket^{\text{glob}} = \Delta$  and
4.  $\Omega \sim_{\delta} \Omega$  and
5.  $\Delta \vdash \Omega$  and
6.  $\vdash \text{isValid } \Omega$  and
7.  $T =_{\delta} \Omega.A$
8.  $\delta \subseteq \delta'$  and
9.  $\alpha =_{\delta'} \bar{\alpha}$  and
10.  $T \xrightarrow{\bar{\alpha}} T'$  and
11.  $T' =_{\delta'} \Omega'.A$

then

- (i)  $\exists \delta' \supseteq \delta$  and  $\Omega', \Omega' \sim_{\delta'} \Omega'$  and
- (ii)  $\exists \alpha, \mathbf{M.functs} \vdash \Omega \xRightarrow{\alpha} \Omega'$  and
- (iii)  $\alpha =_{\delta'} \alpha$  and
- (iv)  $\vdash \text{isValid } \Omega'$

*Proof.* Proof by induction on the  $\Omega \xRightarrow{\alpha} \Omega'$  . Interesting cases are those which emit non-empty events and manipulate pointers.

**Case (S-Step):** We are given:

$$P, (\theta; K), H, A \triangleright (E[e], E^*) \xRightarrow{\alpha} P, (\theta', K), H', A' \triangleright (E[e'], E^*)$$

First, we invert  $\Omega \sim_{\delta} \Omega = \langle \Sigma, (\theta, \mathbf{i}^*, \mathbf{v}^*) : \mathbf{S}^* \rangle$  via rule (Rt-Config), and obtain  $P, \Gamma \vdash E[e] : w \sim_{\delta} \langle \mathbf{i}^*, \mathbf{v}^* \rangle$ . Then, we apply Lemma 20 to the above judgment and decompose the target instruction list  $\mathbf{i}^*$  and value stack  $\mathbf{v}^*$  in the parts that are related to evaluation context  $E$  and to the redex  $e$  respectively, i.e., there exists  $\mathbf{i}'^*, \mathbf{i}_{E[\cdot]}^*, \mathbf{v}'^*, \mathbf{v}_{E[\cdot]}^*$ , such that:

$$\mathbf{i}^* = \mathbf{i}'^*; \mathbf{i}_{E[\cdot]}^* \quad (101)$$

$$\mathbf{v}^* = \mathbf{v}'^*; \mathbf{v}_{E[\cdot]}^* \quad (102)$$

$$P, \Gamma \vdash E : w' \Rightarrow w \sim_{\delta} \langle \mathbf{i}_{E[\cdot]}^*, \mathbf{v}_{E[\cdot]}^* \rangle \quad (103)$$

$$P, \Gamma \vdash e : w' \sim_{\delta} \langle \mathbf{i}'^*, \mathbf{v}'^* \rangle \quad (104)$$

Next, we invert the single source step (rule (S-Step)) and obtain the primitive reduction:

$$M \vdash P, \theta, H, A \triangleright e \xRightarrow{\alpha} P, \theta', H', A' \triangleright e' \quad (105)$$

Let  $\Omega_1 = P, K, H, A \triangleright e$ ,  $\Omega'_1 = P, K', H', A' \triangleright e'$ , and  $\Omega_1 = \langle \Sigma, (\theta, \mathbf{i}^*, \mathbf{v}^*) \rangle$ . Since  $P, \Gamma \vdash e : w' \sim_{\delta} \langle \mathbf{i}'^*, \mathbf{v}'^* \rangle$ , we have that  $\Omega_1 \sim_{\delta} \Omega_1$  (rule (Rt-Config)), thus we simulate the primitive reduction via Lemma 22 (Functional Correctness—Primitive Step), i.e., there exists an extended bijection  $\delta' \supseteq \delta$ , an event  $\alpha$ , such that  $\alpha =_{\delta'} \alpha$ , a target configuration  $\Omega'_1 = \langle \Sigma', (\theta', \mathbf{i}''^*, \mathbf{v}''^*) \rangle$ , such that  $\Omega'_1 \sim_{\delta'} \Omega'_1$  and  $M.\text{funcs} \vdash \Omega_1 \xRightarrow{\alpha} \Omega'_1$ .

Then, we plug the result of the primitive reduction in  $\Omega'_1$  into the hole of the target context identified above in  $\Omega$  (103), i.e., let  $\Omega' = \langle \Sigma', (\theta', \mathbf{i}''^*; \mathbf{i}_{E[\cdot]}^*, \mathbf{v}''^*; \mathbf{v}_{E[\cdot]}^*) \rangle$ . Since  $\Omega'_1 \sim_{\delta'} \Omega'_1$ , we have that  $P, \Gamma \vdash e' : w' \sim_{\delta'} \langle \mathbf{i}''^*, \mathbf{v}''^* \rangle$  (rule (Rt-Config)), thus, we can apply Lemma 19 to that and  $P, \Gamma \vdash E : w' \Rightarrow w \sim_{\delta'} \langle \mathbf{i}_{E[\cdot]}^*, \mathbf{v}_{E[\cdot]}^* \rangle$  (103 lifted by monotonicity of  $\delta' \supseteq \delta$ ), and obtain  $P, \Gamma \vdash E[e'] : w \sim_{\delta'} \langle \mathbf{i}''^*, \mathbf{i}_{E[\cdot]}^*, \mathbf{v}''^*, \mathbf{v}_{E[\cdot]}^* \rangle$ , from which have  $\Omega' \sim_{\delta'} \Omega'$ . Finally, notice that the target reduction  $M.\text{funcs} \vdash \Omega_1 \xRightarrow{\alpha} \Omega'_1$  is independent from the target context (just like in the source language), thus the same reduction can be lifted into the original configuration, giving us  $M.\text{funcs} \vdash \Omega \xRightarrow{\alpha} \Omega'$ , as desired.

**Case (S-Call):** Given  $P, K, H, A \triangleright (E_1 [x := \text{call } g(n')], E^*) \xRightarrow{\epsilon} P, K', H, A \triangleright (e_b, E_1 [x := [\cdot]] : E^*)$

. Inverting rule (S-Call), we have

$$(w \ g(x' : w')) \mapsto ((y_j : w_j)^*, e_b) \in P.F \quad (106)$$

$$K' = K; \{x' \mapsto n', y_0 \mapsto 0, y_1 \mapsto 0, \dots\} \quad (107)$$



From rule (C-Call), we have  $\llbracket P, \Gamma \vdash x := \text{call } g(n') : w' \rrbracket^{\text{exp}} = \mathbf{u32.const } n'; \text{call } n_g; \text{set } n_x.$

We are also given  $P, K, H, A \triangleright (E_1[x := \text{call } g(n')], E^*) \sim_\delta \langle \Sigma, (\theta, \mathbf{u32.const } n' : \text{call } n : i^*, v^*) : S^* \rangle$ . Inverting rule (Rt-Config), we have

$$P, \Gamma \vdash E_1[x := \text{call } g(n')] : w \sim_\delta \langle \mathbf{u32.const } n' : \text{call } n : i^*, v^* \rangle \quad (108)$$

$$\langle P, K[1 \dots], E^* \rangle \sim_\delta S^* \quad (109)$$

$$H \sim_\delta \Sigma.T \quad (110)$$

$$A \sim_\delta \Sigma.A \quad (111)$$

$$K[0] \sim_\delta \theta \quad (112)$$

From rule (Const) and rule (Call), we have

$$\mathbf{M.funcs} \vdash \langle \Sigma, \theta, \mathbf{u32.const } n' : \text{call } n : i^*, v^* \rangle \xrightarrow{\text{call}(\pi_1, i_1^*, v_1^*)} \langle \Sigma, \theta, i^*, v^* \rangle$$

where  $\Sigma_\phi(n) = \{\text{var } \tau^j, \text{body } i_1^*\} : \rho, \rho = \tau \rightarrow \tau^*$  and  $v_1^* = n' \circ^j$ . From rule (Ctx-Call), we further have

$$\mathbf{M.funcs} \vdash \langle \Sigma, \theta, \text{call } n : i^*, n' : v^* \rangle \rightarrow \langle \Sigma, ([k \mapsto v_k \mid v_k \in v_1^*], i_1^*, \epsilon) : (\theta, i^*, v^*) : S^* \rangle$$

We have to prove that

$$P, K', H, A \triangleright (e_b, E_1[x := [\cdot]] : E^*) \sim_\delta \langle \Sigma, ([k \mapsto v_k \mid v_k \in v_1^*], i_1^*, \epsilon) : (\theta, i^*, v^*) : S^* \rangle$$

From rule (Rt-Config), we have to prove

$$P, \Gamma \vdash e_b : w \sim_\delta \langle i_1^*, \epsilon \rangle \quad (113)$$

$$\langle P, K, E_1[x := [\cdot]] : E^* \rangle \sim_\delta (\theta, i^*, v^*) : S^* \quad (114)$$

$$H \sim_\delta \Sigma.T \quad (115)$$

$$A \sim_\delta \Sigma.A \quad (116)$$

$$\{x' \mapsto n', y_0 \mapsto 0, y_1 \mapsto 0, \dots\} \sim_\delta [k \mapsto v_k \mid v_k \in n' \circ^j] \quad (117)$$

We have (113) from rule (Program) and target step rule (Call). Invoking Lemma 20 (Context Decomposition) on (108) along with (112), (109) we have (114). Derivation of remaining premises from (110) and (111) is straightforward.

The step does not emit any trace and so we trivially have  $\alpha =_{\delta'} \alpha$ .

Heap is not modified during the target step. The new stack frame is valid from rule (empty-data-stack). From rule (cons-data-stack) we thus have  $\vdash \text{isValid } \Omega'$ .

**Case (S-Return):** Proof similar to the above case.

□

#### 4.8.5 Functional Correctness-Primitive Step

**Lemma 22** (Functional Correctness—Primitive Step). If

1.  $M \vdash \Omega \xrightarrow{\alpha} \Omega'$  and
2.  $\llbracket P \rrbracket^{\text{prog}} = M$  and
3.  $\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta$  and
4.  $\Omega \sim_{\delta} \Omega$  and
5.  $\Delta \vdash \Omega$  and
6.  $\vdash \text{isValid } \Omega$  and
7.  $T =_{\delta} \Omega.A$
8.  $\delta \subseteq \delta'$  and
9.  $\alpha =_{\delta'} \bar{\alpha}$  and
10.  $T \xrightarrow{\bar{\alpha}} T'$  and
11.  $T' =_{\delta'} \Omega'.A$

then  $\exists \delta', \Omega', \alpha$ , such that

- (i)  $\delta' \supseteq \delta$ ,
- (ii)  $\Omega' \sim_{\delta'} \Omega'$  and
- (iii)  $M.\text{funcs} \vdash \Omega \xRightarrow{\alpha} \Omega'$  and
- (iv)  $\alpha =_{\delta'} \alpha$  and
- (v)  $\vdash \text{isValid } \Omega'$

*Proof.* Proof by induction on the  $M \vdash \Omega \xrightarrow{\alpha} \Omega'$ . Interesting cases are those which emit non-empty events and manipulate pointers.

**Case (S-Ptr-Arith):** We have to prove

$$\exists \delta' \supseteq \delta \text{ and } \Omega', \Omega' \sim_{\delta'} \Omega' \quad (118)$$

$$M.\text{funcs} \vdash \Omega \xRightarrow{\alpha} \Omega' \quad (119)$$

$$\alpha =_{\delta'} \alpha \quad (120)$$

Given the following:

$$M \vdash P, K, H, A \triangleright a^{(b, \ell, w, n_{id})} \oplus n \xrightarrow{\epsilon} P, K, H, A \triangleright (a + n)^{(b, \ell, w, n_{id})} \quad (121)$$

$$\llbracket P \rrbracket^{\text{prog}} = M \quad (122)$$

$$\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta \quad (123)$$

$$\Omega \sim_{\delta} \Omega \quad (124)$$

$$\Delta \vdash \Omega \quad (125)$$

$$\vdash \text{isValid } \Omega \quad (126)$$

$$\text{MS++}(\alpha, \Omega.A) \quad (127)$$

From (Rt-Config), we have

$$\Omega = P, (\theta_f; K), H, A \triangleright (a^{(b, \ell, w, n_{id})} \oplus n, E^*) \quad (128)$$

$$\Omega' = P, (\theta_f; K), H, A \triangleright ((a + n)^{(b, \ell, w, n_{id})}, E^*) \quad (129)$$

$$P, \Gamma \vdash a^{(b, \ell, w, n_{id})} \oplus n : \text{array } \tau \sim_{\delta} \langle \text{handle.add}, n; \langle b, n_o, n_l, 1, n_{id} \rangle_F \rangle \quad (130)$$

$$\Omega = \langle \Sigma, (\theta_f, \text{handle.add}, n; \langle b, n_o, n_l, 1, n_{id} \rangle_F) : S^* \rangle \quad (131)$$

$$\langle P, K, E^* \rangle \sim_{\delta} S^* \quad (132)$$

$$H \sim_{\delta} \Sigma.T \quad (133)$$

$$A \sim_{\delta} \Sigma.A \quad (134)$$

$$\theta_f \sim_{\delta} \theta_f \quad (135)$$

From (Rt-Ptr-Arith), we have:

$$P, \Gamma \vdash a^{(b, \ell, w, n_{id})} : \text{array } \tau \sim_{\delta} \langle [], \langle b, n_o, n_l, 1, n_{id} \rangle_F \rangle \quad (136)$$

$$P, \Gamma \vdash n : \text{int} \sim_{\delta} \langle [], v \rangle \quad (137)$$

$$n = \text{sz}(\tau) * v \quad (138)$$

Inverting (137) using (C-data-int), we have  $|v| = |n|$ . Thus (138) becomes

$$n = \text{sz}(\tau) * n \quad (139)$$

**Proof for  $\Omega' \sim_{\delta'} \Omega'$  :** From (Handle-Add), we have

$$\Phi^* \vdash \langle \Sigma, (\theta_f, \text{handle.add}, n; \langle b, n_o, n_l, 1, n_{id} \rangle_F) : S^* \rangle \longrightarrow \langle \Sigma, (\theta_f, [], \langle b, n_o + n, n_l, 1, n_{id} \rangle_F) : S^* \rangle$$

Let  $\Omega' = \langle \Sigma, (\theta_f, [], \langle b, n_o + n, n_l, 1, n_{id} \rangle_F) : S^* \rangle$  and  $\delta' = \delta$ . From (Rt-Value), it suffices to show that

$$(a + n)^{(b, \ell, w, n_{id})} \sim_{\delta} \langle [], \langle b, n_o + n, n_l, 1, n_{id} \rangle_F \rangle$$

Inverting (136) using (Rt-Value), we have

$$a^{(b, \ell, w, n_{id})} \sim_{\delta} \langle b, n_o, n_l, 1, n_{id} \rangle_F \quad (140)$$

From (C-data-Valid-Ptr-Array) we have

$$\delta(b, n_{id}) = \langle b, n_{id} \rangle \quad (141)$$

$$\ell * \mathbf{sz}(\mathbf{w}) = \mathbf{n}_1 \quad (142)$$

$$(\mathbf{a} - \mathbf{b}) * \mathbf{sz}(\mathbf{w}) = \mathbf{n}_o \quad (143)$$

From (139), we have

$$((\mathbf{a} - \mathbf{b}) * \mathbf{sz}(\mathbf{w})) + \mathbf{n} * \mathbf{sz}(\mathbf{w}) = \mathbf{n}_o + \mathbf{n}$$

This implies (from (C-data-Valid-Ptr-Array)),  $(\mathbf{a} + \mathbf{n})^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{id})} \sim_\delta \langle \square, \langle \mathbf{b}, \mathbf{n}_o + \mathbf{n}, \mathbf{n}_1, \mathbf{1}, \mathbf{n}_{id} \rangle_{\mathbf{F}} \rangle$ .  
Since  $\delta' = \delta$ , we thus have  $\Omega' \sim_{\delta'} \Omega'$ .

**Proof for  $\mathbf{M.funcs} \vdash \Omega \xRightarrow{\alpha} \Omega'$ :** From (Handle-Add), we have that  $\mathbf{M.funcs} \vdash \Omega \xRightarrow{\epsilon} \Omega'$  and  $\alpha = \epsilon$ .

**Proof for  $\alpha =_{\delta'} \alpha$ :** We have  $\alpha = \epsilon$ . From (Empty), we have  $\alpha =_{\delta'} \alpha$ .

**Case (S-Assign):** Given the following:

$$\mathbf{M} \vdash \mathbf{P}, \mathbf{K}, \mathbf{H}, \mathbf{A} \triangleright *a^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{id})} := v \xrightarrow{\text{write}(a^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{id})}, v)} \mathbf{P}, \mathbf{K}, \mathbf{H}', \mathbf{A} \triangleright 0 \quad (144)$$

$$\llbracket \mathbf{P} \rrbracket^{\text{prog}} = \mathbf{M} \quad (145)$$

$$\llbracket \mathbf{P}, \Delta \rrbracket^{\text{glob}} = \Delta \quad (146)$$

$$\Omega \sim_\delta \Omega \quad (147)$$

$$\Delta \vdash \Omega \quad (148)$$

$$\vdash \text{isValid } \Omega \quad (149)$$

$$\text{MS++}(\alpha, \Omega.A) \quad (150)$$

From (Rt-Config) and (Rt-Assign), we have:

$$\Omega = \mathbf{P}, (\theta_f; \mathbf{K}), \mathbf{H}, \mathbf{A} \triangleright (*a^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{id})} := v, \mathbf{E}^*) \quad (151)$$

$$\Omega' = \mathbf{P}, (\theta_f; \mathbf{K}), \mathbf{H}', \mathbf{A} \triangleright (0, \mathbf{E}^*) \quad (152)$$

$$\llbracket \tau \rrbracket = \tau \quad (153)$$

$$\mathbf{P}, \Gamma \vdash *a^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{id})} := v : \mathbf{w} \sim_\delta \langle \tau.\text{segstore}; \mathbf{u32.const } 0, (v; \langle \mathbf{n}_b, \mathbf{n}_o, \mathbf{n}_1, \mathbf{1}, \mathbf{n}_{id} \rangle_{\mathbf{F}}) \rangle \quad (154)$$

$$\Omega = \langle \Sigma, (\theta, \tau.\text{segstore}; \mathbf{u32.const } 0, (v; \langle \mathbf{n}_b, \mathbf{n}_o, \mathbf{n}_1, \mathbf{1}, \mathbf{n}_{id} \rangle_{\mathbf{F}})) : \mathbf{S}^* \rangle \quad (155)$$

$$\langle \mathbf{P}, \mathbf{K}, \mathbf{E}^* \rangle \sim_\delta \mathbf{S}^* \quad (156)$$

$$\mathbf{H} \sim_\delta \Sigma.\mathbf{T} \quad (157)$$

$$\mathbf{A} \sim_\delta \Sigma.\mathbf{A} \quad (158)$$

$$\theta_f \sim_\delta \theta_f \quad (159)$$

Let

- $\delta' = \delta$ ,

- $\Omega' = \langle \Sigma', (\theta, \text{u32.const } 0, []) : S^* \rangle$ , and
- $\alpha = \text{write}_\tau (\langle n_b, n_o, n_l, 1, n_{id} \rangle_F)$ .

Then:

**Proof for  $M.\text{funcs} \vdash \Omega \xRightarrow{\alpha} \Omega'$ :** To show that the target store operation succeeds, we use the assumption that the source execution is memory safe. In particular, to apply rule (H-Store) (or (H-Store-Handle)), depending on the type  $\llbracket \tau \rrbracket = \tau$ , we only need to show that  $n_o > 0$ ,  $n_o + |\tau| < n_l$ , and  $n_{id} \in A.\text{allocated}$ . First, we invert  $\alpha =_{\delta'} \bar{\alpha}$  with rule (src-tr-write-auth) and deduce that  $\bar{\alpha} = \text{write}(a^{(c,s)}) \cdot \text{write}((a+1)^{(c,s)}) \dots \text{write}((a+n-1)^{(c,s)})$  where  $n = \text{sz}(\tau) = |\tau|$  and  $\delta(b, n_{id}) = b^{(c,s)}$ . Since  $T \xrightarrow{\bar{\alpha}} T'$ , we know that these memory accesses are memory safe, i.e.,  $T[a+i] = A(c,s)$  for  $i \in \{0, \dots, n-1\}$ . Thus, by  $T =_\delta \Omega.A$  (Definition 4), we know that the pointer  $a^{(b,\ell,w,n_{id})}$  is *not* dangling, i.e.,  $n_{id} \in A.\text{allocated}$ , and in-bounds, i.e.,  $a \geq b$  and  $(a-b) < \ell$ . We now leverage the source to target equivalence relation to derive corresponding facts about the target handle. First, the source pointer and target handle are related, thus we have  $\delta(b, n_{id}) = b, n_{id}$ ,  $\ell * \text{sz}(\tau) = n_l$  and  $(a-b) * \text{sz}(\tau) = n_o$ , from  $a^{(b,\ell,w,n_{id})} \sim_\delta \langle n_b, n_o, n_l, 1, n_{id} \rangle_F$  ((Rt-Value) and (C-data-Valid-Ptr-Array)). Then, we apply Lemma 25 to these,  $a \geq b$ , and  $(a-b) < \ell$ , and obtain  $o \geq 0$  and  $o + |\tau| < \ell$ . Lastly, since  $A \sim_\delta \Sigma.A$  and  $n_{id} \in A.\text{allocated}$ , we have  $n_{id} \in A.\text{allocated}$ . Thus, by rule (H-Store) and then (Const), we construct the reduction  $M.\text{funcs} \vdash \Omega \xRightarrow{\alpha} \Omega'$ , where  $\alpha = \text{write}_\tau (\langle n_b, n_o, n_l, 1, n_{id} \rangle_F)$ .

**Proof for  $\Omega' \sim_\delta \Omega'$ :** First, we relate the source value 0 resulting from the assignment to the target value 0 pushed on the stack by instruction **const 0**, via rule (C-data-int). Then, to show  $\Omega' \sim_\delta \Omega'$ , it suffices to show that the resulting stores are related, i.e.,  $H' \sim_\delta \Sigma'.T$ . The initial stores are related (157), therefore, by (Heap-Equiv) we only need to show that the byte-string encoding of value  $v$  is written in the target store at a related location. Formally, we have  $\Sigma'.T[(n_b + n_o) \dots (n_b + n_o) + s - 1] = (b, t)_0 \dots (b, t)_{s-1} = \tau.\text{pack}(v)$ , where  $s = \text{sz}(\tau)$ . Since the values written at the source and target are related, i.e.,  $v \sim_\delta v$  from  $\Omega \sim_\delta \Omega$ , we easily construct  $v \sim_\delta (b_0, t)_0 \dots (b_{s-1}, t)_{s-1}$  by rule (Heap-Value). Lastly, we invert  $a^{(b,\ell,w,n_{id})} \sim_\delta \langle n_b, n_o, n_l, 1, n_{id} \rangle$  ((154) and (Rt-Value)), and obtain  $\delta(b, n_{id}) = (n_b, n_{id} 0)$ , i.e., the source and target addresses that are written are related.

**Proof for  $\alpha =_{\delta'} \alpha$ :** We have  $\alpha = \text{write}(a^{(b,\ell,w,n_{id})}, v)$  and  $\alpha = \text{write}_\tau (\langle n_b, n_o, n_l, 1, n_{id} \rangle_F)$ . From  $a^{(b,\ell,w,n_{id})} \sim_\delta \langle n_b, n_o, n_l, 1, n_{id} \rangle$  and (153), we have all the premises required for (Tr-Rel-Write), and thus  $\alpha =_{\delta'} \alpha$ .

**Case (S-Arr-Assign):** Similar to above case.

Case **(S-Dereference)**: Given the following:

$$M \vdash P, K, H, A \triangleright *a^{(b, \ell, w, n_{id})} \xrightarrow{\text{read}(a^{(b, \ell, w, n_{id})}, v)} P, K, H, A \triangleright v \quad (160)$$

$$\llbracket P \rrbracket^{\text{prog}} = M \quad (161)$$

$$\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta \quad (162)$$

$$\Omega \sim_{\delta} \Omega \quad (163)$$

$$\Delta \vdash \Omega \quad (164)$$

$$\vdash \text{isValid } \Omega \quad (165)$$

$$T =_{\delta} \Omega.A \quad (166)$$

$$\delta \subseteq \delta' \quad (167)$$

$$\alpha =_{\delta'} \bar{\alpha} \quad (168)$$

$$T \xrightarrow{\bar{\alpha}} T' \quad (169)$$

$$T' =_{\delta'} \Omega'.A \quad (170)$$

From **(Rt-Config)** and **(Rt-Deref)**, we have:

$$\Omega = P, (\theta_f; K), H, A \triangleright (*a^{(b, \ell, w, n_{id})}, E^*) \quad (171)$$

$$\Omega' = P, (\theta_f; K), H', A \triangleright (v, E^*) \quad (172)$$

$$\llbracket \tau \rrbracket = \tau \quad (173)$$

$$P, \Gamma \vdash a^{(b, \ell, w, n_{id})} : \text{ptr } \tau \sim_{\delta} \langle [], \langle n_b, n_o, n_l, 1, n_{id} \rangle_{\bar{h}} \rangle \quad (174)$$

$$\Omega = \langle \Sigma, (\theta, \tau.\text{segload}, \langle n_b, n_o, n_l, 1, n_{id} \rangle_{\bar{h}}) : S^* \rangle \quad (175)$$

$$\langle P, K, E^* \rangle \sim_{\delta} S^* \quad (176)$$

$$H \sim_{\delta} \Sigma.T \quad (177)$$

$$A \sim_{\delta} \Sigma.A \quad (178)$$

$$\theta_f \sim_{\delta} \theta_f \quad (179)$$

**Proof for  $M.\text{funcs} \vdash \Omega \xrightarrow{\alpha} \Omega'$** : The configuration steps via rule **(H-Load)** (resp. **(H-Load-Handle)**), where the premises  $n_o > 0$ ,  $n_o + |\tau| < n_l$ , and  $n_{id} \in A.\text{allocated}$  hold following the same argument used in case **(S-Assign)**.

Thus, we have:  $\Phi^* \vdash \langle \Sigma, (\theta, \tau.\text{segload}, \langle n_b, n_o, n_l, 1, n_{id} \rangle_F) : S^* \rangle \xrightarrow{\alpha} \Omega'$  where

$$\Omega' = \langle \Sigma, (\theta, [], v) : S^* \rangle \quad (180)$$

$$v = \tau.\text{unpack}(b^*) \quad (181)$$

$$(b^*, t^*) = [T[a + j] \mid j \in \{0..|\tau| - 1\}] \quad (182)$$

$$a = n_b + n_o \quad (183)$$

$$\alpha = \text{read}_{\tau}(\langle n_b, n_o, n_l, 1, n_{id} \rangle_F) \quad (184)$$

**Proof for  $\Omega' \sim_{\delta'} \Omega'$ :** Let  $\Omega' = \langle \Sigma, (\theta, [], \mathbf{v}) : \mathbf{S}^* \rangle$  and  $\delta' = \delta$ . To prove  $\Omega' \sim_{\delta'} \Omega'$ , it suffices to show  $\mathbf{P}, \Gamma \vdash \mathbf{v} : \tau \sim_{\delta} \langle [], \mathbf{v} \rangle$ , where  $\mathbf{v} = \mathbf{H}[\mathbf{a}]$  from 160 (rule (S-Dereference)). Intuitively,  $\mathbf{v}$  is related to  $\mathbf{v}$  because  $\mathbf{v}$  is obtained by reading an handle related to the source pointer from a segment memory store that is related to the source heap. Formally, from the equivalence of the stores, i.e., (177) and rule (Heap-Equiv), we have that:

$$\mathbf{H}[\mathbf{a}] = \mathbf{v} \quad (185)$$

$$\vdash \mathbf{v} : \tau \quad (186)$$

$$\llbracket \tau \rrbracket = \tau \quad (187)$$

$$s = \mathbf{sz}(\tau) \quad (188)$$

$$(\mathbf{b}, \mathbf{n}, \tau, \mathbf{n}_{\text{id}}) \in \llbracket \mathbf{A} \rrbracket_1 \quad (189)$$

$$\mathbf{a} \in \llbracket \mathbf{b}.. \mathbf{b} + \mathbf{n} \rrbracket \quad (190)$$

$$\mathbf{a}, \mathbf{n}'_{\text{id}} = \delta(\mathbf{a}, \mathbf{n}_{\text{id}}) \quad (191)$$

$$\mathbf{T}[\mathbf{a}] \dots \mathbf{T}[\mathbf{a} + s - 1] = (\mathbf{b}, \mathbf{t})_0 (\mathbf{b}, \mathbf{t})_1 \dots (\mathbf{b}, \mathbf{t})_{s-1} \quad (192)$$

$$\mathbf{v} \sim_{\delta} (\mathbf{b}, \mathbf{t})_0 (\mathbf{b}, \mathbf{t})_1 \dots (\mathbf{b}, \mathbf{t})_{s-1} \quad (193)$$

Next, we need to show that the byte string corresponding to  $\mathbf{v}$  ((192) and (193)) is the same byte string read in the target step via handle  $\langle \mathbf{n}_{\mathbf{b}}, \mathbf{n}_{\mathbf{o}}, \mathbf{n}_{\mathbf{l}}, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle_F$ , i.e.  $\mathbf{a} = \mathbf{n}_{\mathbf{b}} + \mathbf{n}_{\mathbf{o}}$  and  $\mathbf{n}'_{\text{id}} = \mathbf{n}_{\text{id}}$ . Since the handle is valid (165), there exist an allocated slot in the target allocator state, i.e.,  $(\mathbf{n}_{\mathbf{b}}, \mathbf{n}_{\mathbf{l}}, \mathbf{n}_{\text{id}}) \in \llbracket \Omega.A \rrbracket_1$  by rule (cor-handle). Furthermore, since the source and target allocators are related (178), this slot is related to the slot from 189, i.e.,  $(\mathbf{b}, \mathbf{n}, \tau, \mathbf{n}_{\text{id}}) \sim_{\delta} (\mathbf{n}_{\mathbf{b}}, \mathbf{n}_{\mathbf{l}}, \mathbf{n}_{\text{id}})$ . By inverting this equivalence via rule (Slot-Eq-Prim), we obtain:

$$\forall i, 0 \leq i < \mathbf{n}, \delta(\mathbf{b} + i, \mathbf{n}_{\text{id}}) = (\mathbf{n}_{\mathbf{b}} + i * \mathbf{sz}(\tau), \mathbf{n}_{\text{id}})$$

which we instantiate with  $i = \mathbf{a} - \mathbf{b}$  and obtain  $\delta(\mathbf{a}, \mathbf{n}_{\text{id}}) = (\mathbf{n}_{\mathbf{b}} + (\mathbf{a} - \mathbf{b}) * \mathbf{sz}(\tau), \mathbf{n}_{\text{id}})$ .<sup>1</sup> Furthermore, since the pointer and the handle are related ((174) and rule (C-data-Valid-Ptr-Array)), we that  $(\mathbf{a} - \mathbf{b}) * \mathbf{sz}(\tau) = \mathbf{n}_{\mathbf{o}}$ , so we can rewrite the equation above to:

$$\delta(\mathbf{a}, \mathbf{n}_{\text{id}}) = (\mathbf{n}_{\mathbf{b}} + \mathbf{n}_{\mathbf{o}}, \mathbf{n}_{\text{id}}) = (\mathbf{a}, \mathbf{tid}')$$

which implies  $\mathbf{a} = \mathbf{n}_{\mathbf{b}} + \mathbf{n}_{\mathbf{o}}$  and  $\mathbf{n}_{\text{id}} = \mathbf{n}'_{\text{id}}$ , as desired. Finally, from (192), (193) and rule (Heap-Value), we have  $\mathbf{v} \sim_{\delta} \mathbf{unpack}(\mathbf{b}_0 \dots \mathbf{b}_{s-1}) = \mathbf{v}$ .

**Proof for  $\alpha =_{\delta} \alpha$ :** We have  $\alpha = \mathbf{read}(\mathbf{a}^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{\text{id}})}, \mathbf{v})$  and  $\alpha = \mathbf{read}_{\tau}(\langle \mathbf{n}_{\mathbf{b}}, \mathbf{n}_{\mathbf{o}}, \mathbf{n}_{\mathbf{l}}, \mathbf{1} \rangle_F)$ . From (173) and (174), we have all premises related to (Tr-Rel-Read). Thus, we have  $\alpha =_{\delta'} \alpha$ .

**Case (S-Arr-Dereference):** Similar to above case.

<sup>1</sup>Notice that  $0 \leq \mathbf{a} - \mathbf{b} < \mathbf{n}$  because the source execution is memory safe by assumption.

Case **(S-Struct-Field)**: Given the following:

$$M \vdash P, K, H, A \triangleright \&a \rightarrow f_k \xrightarrow{\epsilon} P, K, H, A \triangleright a + o_1^{(b+o_1, 1, \tau_k, n_{id})} \quad (194)$$

$$\llbracket P \rrbracket^{\text{prog}} = M \quad (195)$$

$$\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta \quad (196)$$

$$\Omega \sim_{\delta} \Omega \quad (197)$$

$$\Delta \vdash \Omega \quad (198)$$

$$\vdash \text{isValid } \Omega \quad (199)$$

$$\text{MS++}(\alpha, \Omega.A) \quad (200)$$

From **(Rt-Config)** and **(Rt-Field)**, we have:

$$\Omega = P, (\theta_f; K), H, A \triangleright (\&a \rightarrow f_k, E^*) \quad (201)$$

$$\Omega' = P, (\theta_f; K), H', A \triangleright (a + o_1^{(b+o_1, 1, \tau_k, n_{id})}, E^*) \quad (202)$$

$$P, \Gamma \vdash a^{(b, \ell, \text{struct } s, n_{id})} : \text{ptr struct } s \sim_{\delta} \langle [], \langle \mathbf{n}_b, \mathbf{n}_o, \mathbf{n}_l, 1, \mathbf{n}_{id} \rangle_F \rangle \quad (203)$$

$$(\mathbf{o}_1, \mathbf{o}_2) = (\Sigma_i^{k-1} \text{sz}(\tau_i), (\text{sz}(\text{struct } s) - \text{sz}(\tau_k))) \quad (204)$$

$$\Omega = \langle \Sigma, (\theta, \text{slice}; \mathbf{o}_2; \mathbf{o}_1; \langle \mathbf{n}_b, \mathbf{n}_o, \mathbf{n}_l, 1, \mathbf{n}_{id} \rangle_F) : S^* \rangle \quad (205)$$

$$\langle P, K, E^* \rangle \sim_{\delta} S^* \quad (206)$$

$$H \sim_{\delta} \Sigma.T \quad (207)$$

$$A \sim_{\delta} \Sigma.A \quad (208)$$

$$\theta_f \sim_{\delta} \theta_f \quad (209)$$

**Proof for  $M.\text{funcs} \vdash \Omega \xrightarrow{\alpha} \Omega'$** : Let  $\Omega' = \langle \Sigma, (\theta, [], \langle \mathbf{n}_b + \mathbf{o}_1, \mathbf{n}_o, \mathbf{n}_l - \mathbf{o}_2, 1, \mathbf{n}_{id} \rangle_F) : S^* \rangle$ .

To show that the target configuration steps with rule **(Slice)**, we first need to show that the offsets are not negative. From (204), we trivially have

$$\mathbf{o}_1 \geq \mathbf{0} \quad (210)$$

$$\mathbf{o}_2 \geq \mathbf{0} \quad (211)$$

Then, we only need to show that that offset  $\mathbf{o}_1$  is bounded by the length of the segment  $\mathbf{n}_l$ , i.e.,  $\mathbf{o}_1 < \mathbf{n}_l$ . Since 194 involves a struct, we know that  $\ell = 1$  and we can invert 203 with rule **(C-data-Valid-Ptr-Single)**, from which we obtain:

$$\delta(b, n_{id}) = \mathbf{n}_b, \mathbf{n}_{id} \quad (212)$$

$$\text{sz}(\text{struct } s) = \mathbf{n}_l \quad (213)$$

$$(a - b) = \mathbf{n}_o = 0 \quad (214)$$

Then, we rewrite the equation above using 213 and 204, from which we have:  $\mathbf{o}_1 = \Sigma_{i=1}^{k-1} \text{sz}(\tau_i) < \Sigma_{i=1}^n \text{sz}(\tau_i) = \text{sz}(\text{struct } s) = \mathbf{n}_l$ , as desired.



**Proof for  $\Omega' \sim_{\delta'} \Omega'$  :** To prove  $\Omega' \sim_{\delta'} \Omega'$ , it suffices to show that  $P, \Gamma \vdash a + o_1^{(b+o_1, 1, \tau_k, n_{id})} : \text{ptr } \tau_k \sim \langle [], \langle n_b + o_1, n_o, n_1 - o_2, 1, n_{id} \rangle_F \rangle$ . In order to apply (C-data-Valid-Ptr-Array), we need to show:

$$\delta(b + o_1, n_{id}) = (n_b + o_1, n_{id}) \quad (215)$$

$$\text{sz}(\tau_k) = n_1 - o_2 \quad (216)$$

$$(a + o_1 - b - o_1) * \text{sz}(\tau_k) = n_o \quad (217)$$

Since the source and target allocators are related (208), from rule (Slot-Eq-Struct), we have:

$$\delta(b, n_{id}) = (n_b, n_{id}) \quad (218)$$

$$D(s) = \{f_1 : \tau_1 \dots f_n : \tau_n\} \quad (219)$$

$$\forall i, 0 \leq i < n, \delta(b + i, n_{id}) = (n_b + \sum_{j=1}^i \text{sz}(\tau_j), n_{id}) \quad (220)$$

Then, to obtain (215), we instantiate (220) with  $i = o_1 = k - 1$  and obtain  $\delta(b + o_1, n_{id}) = (n_b + \sum_{j=1}^{k-1} \text{sz}(\tau_j), n_{id}) = (n_b + o_1, n_{id})$  by  $o_1 = \sum_{i=1}^{k-1} \text{sz}(\tau_i)$  (204). Equation (216) follows directly by rewriting with  $\text{sz}(\text{struct } s) = n_1$  (213) and  $o_2 = \text{sz}(\text{struct } s) - \text{sz}(\tau_k)$  (204):

$$\text{sz}(\tau_k) = n_1 - o_2 = \text{sz}(\text{struct } s) - (\text{sz}(\text{struct } s) - \text{sz}(\tau_k)) = \text{sz}(\tau_k)$$

(204), we have  $o_2 = \text{sz}(\text{struct } s) - \text{sz}(\tau_k)$ . Combined with (213), we have (216). Finally, we trivially have  $(a - b) * \text{sz}(\tau_k) = n_o$  (217) from  $(a - b) = n_o = 0$  (214).

**Proof for  $\alpha =_{\delta'} \alpha$  :** Since  $\alpha$  and  $\alpha$  are empty, they are trivially related.

**Case (S-Malloc-Array):** Given the following:

$$M \vdash P, K, H, A \triangleright \text{malloc}(\tau, n) \xrightarrow{\text{alloc}(a^{(a, n, \tau, n_{id})})} P, K, H', A' \triangleright a^{(a, n, \tau, n_{id})} \quad (221)$$

$$\llbracket P \rrbracket^{\text{prog}} = M \quad (222)$$

$$\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta \quad (223)$$

$$\Omega \sim_{\delta} \Omega \quad (224)$$

$$\Delta \vdash \Omega \quad (225)$$

$$\vdash \text{isValid } \Omega \quad (226)$$

$$\text{MS++}(\alpha, \Omega.A) \quad (227)$$

From (Rt-Config) and (Rt-Malloc-Array-1), we have:

$$\Omega = P, (\theta_f; K), H, A \triangleright (\text{malloc}(\tau, n), E^*) \quad (228)$$

$$\Omega' = P, (\theta_f; K), H', A \triangleright (a^{(a, n, \tau, n_{id})}, E^*) \quad (229)$$

$$\text{fresh}(\mathbf{n}_{\text{id}}) \quad (230)$$

$$\mathbf{P}, \Gamma \vdash \mathbf{n} : \text{int} \sim_{\delta} \langle \square, \mathbf{n} \rangle \quad (231)$$

$$\llbracket \tau \rrbracket = \tau \quad (232)$$

$$\Omega = \langle \Sigma, (\theta, \text{segalloc}, \mathbf{n}') : \mathbf{S}^* \rangle \quad (233)$$

$$\mathbf{n}' = \mathbf{n} * \mathbf{sz}(\tau) \quad (234)$$

$$\langle \mathbf{P}, \mathbf{K}, \mathbf{E}^* \rangle \sim_{\delta} \mathbf{S}^* \quad (235)$$

$$\mathbf{H} \sim_{\delta} \Sigma.\mathbf{T} \quad (236)$$

$$\mathbf{A} \sim_{\delta} \Sigma.\mathbf{A} \quad (237)$$

$$\theta_{\text{f}} \sim_{\delta} \theta_{\text{f}} \quad (238)$$

**Proof for  $\mathbf{M.funcs} \vdash \Omega \xRightarrow{\alpha} \Omega'$ :** From (H-Alloc), we have

$$\Phi^* \vdash \langle \Sigma, (\theta, \text{segalloc}, \mathbf{n}') : \mathbf{S}^* \rangle \xrightarrow{\text{salloc}(\mathbf{v})} \langle \Sigma', (\theta, \square, \mathbf{v}) : \mathbf{S}^* \rangle \text{ where } \Sigma' = (\Sigma.\mathbf{H}, \mathbf{T}', \mathbf{A}') \text{ such that}$$

$$\mathbf{v} = \langle \mathbf{n}_{\text{b}}, \mathbf{0}, \mathbf{n}', \text{true}, \mathbf{n}_{\text{id}} \rangle \quad (239)$$

$$\mathbf{a} + \mathbf{n}' < |\mathbf{T}| \quad (240)$$

$$\mathbf{T}' = \mathbf{T}[\mathbf{a} + \mathbf{j} \mapsto (\mathbf{0}, \mathbf{D}) \mid \mathbf{j} \in \mathbf{0}.. \mathbf{n}' - 1] \quad (241)$$

$$\mathbf{A}' = (\mathbf{s}_{\text{U}}^*, \mathbf{s}_{\text{F}}^*) \quad (242)$$

$$\mathbf{s}_{\text{F}}^* = \mathbf{s}_{\text{I}}^* \cdot (\mathbf{a} + \mathbf{n}', \mathbf{n}'' - \mathbf{n}', \_) \cdot \mathbf{s}_{\text{2}}^* \quad (243)$$

$$\mathbf{s}_{\text{U}}^* = (\mathbf{a}, \mathbf{n}', \mathbf{n}_{\text{id}}) \cdot \mathbf{s}_{\text{U}}^* \quad (244)$$

$$\text{fresh}(\mathbf{n}_{\text{id}}) \quad (245)$$

Note that (240) holds from rule (Program).

**Proof for  $\Omega' \sim_{\delta'} \Omega'$ :** Let  $\Omega' = \langle \Sigma', (\theta, \square, \langle \mathbf{n}_{\text{b}}, \mathbf{0}, \mathbf{n}', \text{true}, \mathbf{n}_{\text{id}} \rangle) : \mathbf{S}^* \rangle$  and  $\delta' = \delta[(\mathbf{a} + \mathbf{i}, \mathbf{n}_{\text{id}}) \mapsto (\mathbf{n}_{\text{b}} + \mathbf{i} \times \mathbf{sz}(\tau), \mathbf{n}_{\text{id}}) \mid \mathbf{i} \in \{0 \dots \mathbf{n} - 1\}]$ . Notice that  $\delta'$  is still a bijection because  $\mathbf{n}_{\text{id}}$  and  $\mathbf{n}_{\text{id}}$  are both fresh identifiers (245), (230) and that  $\delta' \supseteq \delta$ .

To prove  $\Omega' \sim_{\delta'} \Omega'$ , it suffices to show that  $\mathbf{P}, \Gamma \vdash \mathbf{a}^{(\mathbf{a}, \mathbf{n}, \tau, \mathbf{n}_{\text{id}})} : \text{array } \tau \sim_{\delta'} \langle \square, \langle \mathbf{n}_{\text{b}}, \mathbf{0}, \mathbf{n}', \text{true}, \mathbf{n}_{\text{id}} \rangle \rangle$ ,  $\mathbf{H}' \sim_{\delta'} \Sigma'.\mathbf{T}$  and  $\mathbf{A}' \sim_{\delta'} \Sigma'.\mathbf{A}$ .<sup>2</sup>

To prove  $\mathbf{P}, \Gamma \vdash \mathbf{a}^{(\mathbf{a}, \mathbf{n}, \tau, \mathbf{n}_{\text{id}})} : \text{array } \tau \sim_{\delta'} \langle \square, \langle \mathbf{n}_{\text{b}}, \mathbf{0}, \mathbf{n}', \text{true}, \mathbf{n}_{\text{id}} \rangle \rangle$  via (C-data-Valid-Ptr-Array), we must show (i)  $\delta'(\mathbf{a}, \mathbf{n}_{\text{id}}) = (\mathbf{n}_{\text{b}}, \mathbf{n}_{\text{id}})$ , which we have by definition from our choice of  $\delta'$ , (ii)  $\ell * \mathbf{sz}(\tau) = \mathbf{n}'$ , which we have by (234) and  $\mathbf{n} = \mathbf{n}$ , and (iii)  $(\mathbf{a} - \mathbf{a}) * \mathbf{sz}(\tau) = \mathbf{0}$ , which trivially holds.

We will now proceed to prove  $\mathbf{A}' \sim_{\delta'} \Sigma'.\mathbf{A}$ . We just need to prove the slot-equivalence for the newly allocated slot since the history equivalence is given by the relatedness of traces. That is,  $(\mathbf{a}, \mathbf{n}, \text{array } \tau, \mathbf{n}_{\text{id}}) \sim_{\delta'} (\mathbf{a}, \mathbf{n}', \mathbf{n}_{\text{id}})$ . From the construction of  $\delta'$ , (232), (234), we have

<sup>2</sup>The state components that are unchanged in this step are related up to  $\delta$  (e.g., 235), therefore they are also related up to  $\delta' \supseteq \delta$  by Lemma 17.

$$\delta'(\mathbf{a}, \mathbf{n}_{\text{id}}) = (\mathbf{a}, \mathbf{n}_{\text{id}}) \quad (246)$$

$$\forall 0 \leq i < n, \delta(\mathbf{a} + i, \mathbf{n}_{\text{id}}) = (\mathbf{a} + i * \mathbf{sz}(\tau), \mathbf{n}_{\text{id}}) \quad (247)$$

$$|n| * \mathbf{sz}(\tau) = |\mathbf{n}'| \quad (248)$$

We have all the premises for **(Slot-Eq-Prim)** and thus we have the required proof.

Finally, we prove  $\mathbf{H}' \sim_{\delta'} \Sigma'.\mathbf{T}$ . Notice that  $\mathbf{H} \sim_{\delta} \Sigma.\mathbf{T}$  from (236), therefore we only need to relate the final memories with respect to the initialization of the allocated buffer. In particular, we observe that the source allocator initializes the array as  $\mathbf{H}' = \mathbf{H}[\mathbf{a} + j \mapsto 0 \mid j \in 0..n - 1]$  via rule **(Src-Alloc)**, and similarly the target allocator initializes the corresponding bytes in segment memory via rule **(Seg-Alloc)**, i.e.,  $\mathbf{T}' = \mathbf{T}[\mathbf{a} + j \mapsto (0, \mathbf{D}) \mid j \in 0..n' - 1]$  (241). Thus, we apply rule **(Heap-Equiv)** to (232), (247), and  $0 \sim_{\delta} (0, \mathbf{D})_0 \dots (0, \mathbf{D})_{\mathbf{sz}(\tau)-1}$  (**(Heap-Value)**) to each element of the array and obtain  $\mathbf{H}' \sim_{\delta'} \Sigma'.\mathbf{T}$ , as desired.

**Proof for  $\alpha =_{\delta'} \alpha$ :** We show  $\text{alloca}^{(\mathbf{a}, n, \tau, \mathbf{n}_{\text{id}})} =_{\delta'} \text{salloc}(\langle \mathbf{n}_{\text{b}}, 0, \mathbf{n}', \text{true}, \mathbf{n}_{\text{id}} \rangle)$  via rule **(Tr-Rel-Allocate)**, where the pointer and the handle are allocated as shown above.

Case **(S-Malloc-Single)**: Similar to the above case.

Case **(S-Free)**: We first invert the judgment  $\Omega \sim_{\delta} \Omega$  via rule **(Rt-Config)** and rule **(Rt-Free)** and obtain that the source pointer and target handle are related, i.e.,  $\mathbf{v} = \mathbf{a}^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{\text{id}})} \sim_{\delta} \langle \mathbf{b}, \mathbf{n}_{\text{o}}, \mathbf{n}_{\text{l}}, 1, \mathbf{n}_{\text{id}} \rangle = \mathbf{v}$ , where

$$\begin{aligned} \delta(\mathbf{b}, \mathbf{n}_{\text{id}}) &= \mathbf{b}, \mathbf{n}_{\text{id}} \\ \ell * \mathbf{sz}(\tau) &= \mathbf{n}_{\text{l}} \\ (\mathbf{a} - \mathbf{b}) * \mathbf{sz}(\tau) &= \mathbf{n}_{\text{o}} \end{aligned}$$

and the next instructions in the target configuration are **free ; u32.const 0**.<sup>3</sup> Then, to show that the target steps via rule **(H-Free)**, we need to show that a segment with base address  $\mathbf{b}$  is allocated. Since the source configuration generates event  $\text{alloc}(\mathbf{b}^{(\mathbf{a}, n, \mathbf{w}, \mathbf{n}_{\text{id}})})$ , we know that the source pointer points to the first address of an allocated buffer, i.e.,  $\mathbf{b} = \mathbf{a}$  and  $(\mathbf{b}, n, \mathbf{w}, \mathbf{n}_{\text{id}}) \in \mathbf{A}.\text{Allocated}$ , by allocator rule **(Src-Free)**. Then, because source and target allocator are related, i.e.,  $\mathbf{A} \sim_{\delta} \mathbf{A}$ , we can invert the judgment via rule **(Alloc-Eq)** and rule **(Slot-Eq-Prim)** (the case for rule **(Slot-Eq-Struct)** is similar), and deduce that the corresponding target segment is also allocated, i.e.,  $(\mathbf{b}, n, \mathbf{n}_{\text{id}}) \in \mathbf{A}.\text{Allocated}$ . Thus, the target configuration steps first via rule **(H-Free)**, generating event  $\alpha = \text{sfree}(\mathbf{v})$ , which is related up to bijection  $\delta' = \delta$  to the source event  $\alpha = \text{sfree}(\mathbf{v})$ , by rule **(Tr-Rel-Free)** applied to  $\mathbf{v} \sim_{\delta} \mathbf{v}$ , which we have from

<sup>3</sup>The handle  $\mathbf{v}$  is valid because the source execution is memory safe.

$\Omega \sim_\delta \Omega$ . Finally, the allocators step with related events and the new histories are equivalent by the relatedness of traces, thus the resulting allocator states are related, i.e.,  $A' \sim_\delta A'$ , and the target configuration steps via rule **(Const)**, consuming instruction **u32.const 0** and pushing **0** on the stack, which leads to a related configuration  $\Omega' \sim_\delta \Omega'$ .

Case **(S-Assign-Forge)**: Impossible due to non-ms step.

Case **(S-Dereference-Forge)**: Impossible due to non-ms step.

Case **(Struct-Field-Forge)**: Impossible due to non-ms step.

Case **(S-Free-Forge)**: Impossible due to non-ms step.

Case **(Src-Free-Nop)**: Impossible due to non-ms step.  $\square$

## 4.9 Connection Lemma

**Lemma 23** (Connection Lemma). If  $\llbracket \Omega, \delta \rrbracket = \Omega$  then  $\exists \Omega'$  such that  $\Phi^* \vdash \Omega \xRightarrow{\epsilon} \Omega'$  and  $\Omega \sim_\delta \Omega'$ .

*Proof.* Invert rule **(Compile-Config)** and induct on  $\llbracket P, \Gamma_f \vdash e_f : w \rrbracket^{\text{exp}} = i^*$ .  $\square$

## 4.10 MS Preservation

We are now ready to prove that the compilation preserves memory safety: if the source configuration takes a memory safe step, then the compiled configuration also takes a memory safe step.

**Theorem 3** (MS Preservation—Weak). Let  $\vdash P : \text{wt}$ . If  $\llbracket P \rrbracket^{\text{prog}} = M$  then  $\vdash \text{RMS}(M)$ .

*Proof.* From Theorem 2 (**Type-Preservation of a Compiled Program**), we have  $\vdash M$ . Invoking Theorem 1 (**Robust Memory-Safety**), we have the proof.  $\square$

**Theorem 4** (MS Preservation—Strong). If

1.  $\vdash P : \text{wt}$
2.  $\llbracket P \rrbracket^{\text{prog}} = M$
3.  $\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta$
4.  $\Omega_0(P) \xRightarrow{\bar{\alpha}} \Omega$
5.  $\text{MS}(\bar{\alpha})$

then

- $M.\text{funcs} \vdash \Omega_0(M, n_f) \xRightarrow{\bar{\alpha}} \Omega$  and

- $\exists \delta, \Omega \sim_\delta \Omega$  and
- $\bar{\alpha} =_\delta \bar{\alpha}$  and
- $\text{MS}(\bar{\alpha})$

*Proof.* Applying Lemma 24 (Initial Configurations) to the first two given premises gives  $\llbracket \Omega_0(\mathbf{P}), \emptyset \rrbracket = \Omega_0(\mathbf{M}, \mathbf{n}_f)$ . Further applying Lemma 23 (Connection Lemma), we have  $\Phi^* \vdash \Omega_0(\mathbf{M}, \mathbf{n}_f) \xRightarrow{\epsilon} \bar{\Omega}$  and  $\Omega_0(\mathbf{P}) \sim_\emptyset \bar{\Omega}$  where  $\Phi^* = \mathbf{M}.\text{funcs}$ .

We have to prove

$$\Phi^* \vdash \Omega_0(\mathbf{M}, \mathbf{n}_f) \xRightarrow{\bar{\alpha}} \Omega \quad (249)$$

$$\exists \delta, \Omega \sim_\delta \Omega \quad (250)$$

$$\bar{\alpha} =_{\delta'} \bar{\alpha} \quad (251)$$

$$\text{MS}(\bar{\alpha}) \quad (252)$$

Applying Theorem 2 (Type-Preservation of a Compiled Program) to the first three premises in the hypothesis, we have  $\Delta \vdash \mathbf{M}$ . From the target config typing rule, ((Config)), we have  $\Delta \vdash \Omega_0(\mathbf{M}, \mathbf{n}_f)$ . Let  $T$ ,  $\delta^\circ$  and  $\delta^\circ$  be empty. Then, we trivially have  $T =_{\delta^\circ} \Omega_0.A$ . From  $\text{MS}(\bar{\alpha})$  we have

$$\bar{\alpha} =_\delta \bar{\alpha} \quad (253)$$

$$\text{MS}(\bar{\alpha}) \quad (254)$$

Expanding the definition of  $\text{MS}(\bar{\alpha})$  we have

$$\vdash T \xRightarrow{\bar{\alpha}} T' \quad (255)$$

Note that  $\delta \supseteq \emptyset$ . Also, (253) together with (255) and  $\Omega_0(\mathbf{P}) \xRightarrow{\bar{\alpha}^*} \Omega$  gives  $T' =_\delta \Omega.A$ . We now have the premises necessary to invoke Lemma 18 (Functional Correctness—Multiple Steps) and Lemma 28 (MS Preservation Strong—Multiple Steps). Thus,

$$\exists \delta \text{ and } \Omega, \Omega \sim_\delta \Omega \quad (256)$$

$$\Phi^* \vdash \Omega_0 \xRightarrow{\bar{\alpha}} \Omega \quad (257)$$

$$\bar{\alpha} =_\delta \bar{\alpha} \quad (258)$$

$$\vdash \text{isValid } \Omega \quad (259)$$

$$\delta = \delta \circ \delta^{-1} \quad (260)$$

$$\bar{\alpha} =_\delta \bar{\alpha} \quad (261)$$

$$T' =_\delta \Omega.A \quad (262)$$

To prove  $\text{MS}(\bar{\alpha})$ , we need

$$\bar{\alpha} =_{\delta} \bar{\alpha} \quad (263)$$

$$\vdash T \overset{\bar{\alpha}}{\rightsquigarrow} T' \quad (264)$$

which we have from (261) and (255). Hence proved.  $\square$

**Lemma 24** (Initial Configurations). If

1.  $\vdash P : \text{wt}$
2.  $\llbracket P \rrbracket^{\text{prog}} = M$

then  $\llbracket \Omega_0(P), \emptyset \rrbracket = \Omega_0(M, \mathbf{n}_f)$

*Proof.* Expand the definitions and the proof follows from (Compile-Config).  $\square$

**Corollary 2** (Functional Correctness). If

1.  $\vdash P : \text{wt}$
2.  $\llbracket P \rrbracket^{\text{prog}} = M$
3.  $\Omega_0(P) \overset{\bar{\alpha}}{\Longrightarrow^*} \Omega$
4.  $\text{MS}(\bar{\alpha})$

then

- $M.\text{funcs} \vdash \Omega_0(M, \mathbf{n}_f) \overset{\bar{\alpha}}{\Longrightarrow} \Omega$  and
- $\exists \delta, \Omega \sim_{\delta} \Omega$
- $\bar{\alpha} =_{\delta} \bar{\alpha}$

*Proof.* From Theorem 4 (MS Preservation—Strong).  $\square$

**Lemma 25** (Offsets out-of-bounds). Let  $a^{(b, \ell, w, n_{id})} \sim_{\delta} \langle b, \mathbf{n}_o, \mathbf{n}_l, 1, \mathbf{n}_{id} \rangle$ .

1. If  $a - b \geq \ell$  then  $\mathbf{n}_o \geq \mathbf{n}_l$ .
2. If  $a - b < \ell$  then  $\mathbf{n}_o < \mathbf{n}_l$ .
3. If  $a \geq b$  then  $\mathbf{n}_o \geq 0$ .
4. If  $a < b$  then  $\mathbf{n}_o < 0$ .

*Proof.* From the premises of **(C-data-Valid-Ptr-Array)**, we have

$$\llbracket w \rrbracket = \tau \quad (265)$$

$$\delta(\mathbf{b}, \mathbf{n}_{\text{id}}) = \mathbf{b}, \mathbf{n}_{\text{id}} \quad (266)$$

$$\ell * \mathbf{sz}(\tau) = \mathbf{n}_1 \quad (267)$$

$$(\mathbf{a} - \mathbf{b}) * \mathbf{sz}(\tau) = \mathbf{n}_0 \quad (268)$$

Let  $(\mathbf{a} - \mathbf{b}) \geq \ell$ . So,  $(\mathbf{a} - \mathbf{b}) * \mathbf{sz}(\tau) \geq \ell * \mathbf{sz}(\tau)$ . From (267) and (268), we have  $\mathbf{n}_0 \geq \mathbf{n}_1$ . Let  $(\mathbf{a} - \mathbf{b}) < \ell$ . So,  $(\mathbf{a} - \mathbf{b}) * \mathbf{sz}(\tau) \leq \ell * \mathbf{sz}(\tau)$ . From (267) and (268), we have  $\mathbf{n}_0 < \mathbf{n}_1$ . Proof for  $\mathbf{n}_0 \geq \mathbf{0}$  (resp.  $\mathbf{n}_0 < \mathbf{0}$ ) follows trivially from (268) and by the fact that  $\mathbf{sz}(\tau) > \mathbf{0}$ .  $\square$

#### 4.10.1 MS—Multiple Steps

**Lemma 26** (Trace Conversion). If

$$1. \ \overline{\alpha} =_{\delta} \overline{\alpha} \text{ and}$$

$$2. \ \overline{\alpha} =_{\delta} \overline{\alpha}$$

then  $\overline{\alpha} =_{\delta} \overline{\alpha}$  where  $\delta = \delta \circ \delta^{-1}$ .

*Proof.* Induction on  $\overline{\alpha} =_{\delta} \overline{\alpha}$ .

**Case (Empty):** Trivial.

**Case (Tr-Concat):** We have

$$\alpha \cdot \overline{\alpha} =_{\delta} \overline{\alpha} \quad (269)$$

$$\alpha \cdot \overline{\alpha}' =_{\delta} \alpha \cdot \overline{\alpha}' \quad (270)$$

We need to prove

$$\alpha \cdot \overline{\alpha} =_{\delta} \overline{\alpha}$$

Applying Lemma 4 (**Splitting Source Trace Equivalence**) to (269), we have

$$\overline{\alpha} = \overline{\alpha}' \cdot \overline{\alpha}'' \quad (271)$$

$$\alpha =_{\delta} \overline{\alpha}' \quad (272)$$

$$\overline{\alpha} =_{\delta} \overline{\alpha}'' \quad (273)$$

Inverting (270) using **(Tr-Concat)**, we have

$$\alpha =_{\delta} \alpha \quad (274)$$

$$\overline{\alpha} =_{\delta} \overline{\alpha} \quad (275)$$

Applying I.H to (273) we have that: If

$$\bullet \ \overline{\alpha} =_{\delta} \overline{\alpha}'' \text{ and}$$

- $\overline{\alpha} =_{\delta} \overline{\alpha}$

then  $\overline{\alpha} =_{\delta} \overline{\alpha}''$ .

Applying (273) and (275), we thus have

$$\overline{\alpha} =_{\delta} \overline{\alpha}'' \quad (276)$$

We still need to prove

$$\alpha =_{\delta} \overline{\alpha}' \quad (277)$$

Case analysis on (274) gives the following:

**Case (Tr-Rel-Read):** Given  $\text{read}(\mathbf{a}^{(b, \ell, w, n_{id})}, \mathbf{v}) =_{\delta} \text{read}_{\tau}(\langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{id} \rangle_{\mathbf{F}})$ .

From the premises, we have:

$$\vdash \mathbf{v} : \tau \quad (278)$$

$$\llbracket \tau \rrbracket = \tau \quad (279)$$

$$\mathbf{a}^{(b, \ell, w, n_{id})} \sim_{\delta} \langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{id} \rangle_{\mathbf{F}} \quad (280)$$

We have  $\alpha = \text{read}(\mathbf{a}^{(b, \ell, w, n_{id})}, \mathbf{v})$  and  $\alpha = \text{read}_{\tau}(\langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{id} \rangle_{\mathbf{F}})$ .  
From (292) and (Tr-Read-Authentic), we have

$$\text{read}(\mathbf{a}^{(b, \ell, w, n_{id})}, \mathbf{v}) =_{\delta} \text{read}(a^c) \cdot \text{read}((a+1)^c) \dots \text{read}((a+n-1)^c) \quad (281)$$

Inverting (281) using (Tr-Read-Authentic), we have

$$n = \text{sz}(\mathbf{v}) \quad (282)$$

$$\delta(\mathbf{b}) = b^c \quad (283)$$

$$\mathbf{o} = \mathbf{a} - \mathbf{b} \quad (284)$$

$$a = b + (\mathbf{o} * n) \quad (285)$$

Our goal is to prove

$$\text{read}_{\tau}(\langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1} \rangle_{\mathbf{F}}) =_{\delta} \text{read}(a^c) \cdot \text{read}((a+1)^c) \dots \text{read}((a+n-1)^c) \quad (286)$$

That is, inverting (286) using ((Tr-Read)), we need the following:

$$n = \text{sz}(\tau) \quad (287)$$

$$\delta(\mathbf{b}) = b^c \quad (288)$$

$$a = b + \mathbf{o} \quad (289)$$



From (278) and (279), we have (282) implies (287). Consider the following:

$$\delta(\mathbf{b}) \implies \delta \circ \delta^{-1}(\mathbf{b}) \implies \delta(\mathbf{b}) = b^c \quad (\text{from (283)})$$

Thus (288) holds.

Inverting (280) using rule (C-data-Valid-Ptr-Array), we have

$$\delta(\mathbf{b}) = \mathbf{b} \quad (290)$$

$$\ell * \mathbf{sz}(\mathbf{w}) = \ell \quad (291)$$

$$(\mathbf{a} - \mathbf{b}) * \mathbf{sz}(\mathbf{w}) = \mathbf{o} \quad (292)$$

Rewriting (285), we have

$$\begin{aligned} a &= b + (\mathbf{a} - \mathbf{b}) * n \\ \implies a &= b + \mathbf{o} \iff \mathbf{sz}(\mathbf{w}) = n \end{aligned} \quad (\text{from (292)})$$

It remains to prove that  $\mathbf{sz}(\mathbf{w}) = n$ . Note that from rule (Tr-Rel-Read), we have  $\mathbf{w} \neq \text{struct } \_$ . Then  $\mathbf{w} = \tau$ . (see rule (S-Malloc-Single) and rule (S-Malloc-Array)). Since  $\mathbf{sz}(\tau) = \mathbf{sz}(\mathbf{v})$ , we have  $\mathbf{sz}(\mathbf{w}) = n$ . Thus we have (289) which inturn implies that (286).

**Case (Tr-Rel-Write) :** Given  $\text{write}(\mathbf{a}^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{\text{id}})}, \mathbf{v}) =_{\delta} \text{write}_{\tau}(\langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle_{\mathbf{F}})$ .  
Proof similar to the above case.

**Case (Tr-Rel-Allocate):** Given  $\text{salloc}(\mathbf{a}, \mathbf{n}, \mathbf{w}, \mathbf{n}_{\text{id}}) =_{\delta} \text{salloc}(\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}})$ .  
From the premises, we have:

$$\delta(\mathbf{a}) = \mathbf{a} \quad (293)$$

$$\mathbf{n} * \mathbf{sz}(\mathbf{w}) = \mathbf{n} \quad (294)$$

Depending on whether  $\mathbf{w}$  is  $\text{struct } \_$ , we have:

$$\text{salloc}(\mathbf{a}, \mathbf{n}, \mathbf{w}, \mathbf{n}_{\text{id}}) =_{\delta} \text{alloc}(n, a^c, \phi_0)$$

or

$$\text{salloc}(\mathbf{a}, \mathbf{1}, \text{struct } \mathbf{s}, \mathbf{n}_{\text{id}}) =_{\delta} \text{alloc}(\text{srcsz}(\text{struct } \mathbf{s}), a^c, \phi)$$

For the first case, we need to prove

$$\text{salloc}(\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}}) =_{\delta} \text{alloc}(n, a^c, \phi)$$

For the second case, we need to prove

$$\text{salloc}(\mathbf{a}, \mathbf{n}, \mathbf{n}_{\text{id}}) =_{\delta} \text{alloc}(n, a^c, \phi)$$

These conclusions follow from (Tr-SAlloc) and (Tr-SAlloc) ((Tr-SAlloc-Struct) and (Tr-SAlloc), respectively).

Case **(Tr-Rel-Free)**: Given

$$\text{free}(a^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{\text{id}})}) =_{\delta} \text{sfree}(a^c) \quad (295)$$

$$\text{free}(a^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{\text{id}})}) =_{\delta} \text{free}(\langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle) \quad (296)$$

We need to prove

$$\text{sfree}(\mathbf{h}, \mathbf{n}_{\text{id}}) =_{\delta} \text{sfree}(a^c)$$

From (296) and **(Tr-Rel-Free)**, we have

$$(a^{(\mathbf{b}, \ell, \mathbf{w}, \mathbf{n}_{\text{id}})}) \sim_{\delta} \langle \mathbf{b}, \mathbf{o}, \ell, \mathbf{1}, \mathbf{n}_{\text{id}} \rangle$$

We have

$$\delta^{-1}(\mathbf{b}, \mathbf{n}_{\text{id}}) = (\mathbf{b}, \mathbf{n}_{\text{id}}) \quad (297)$$

$$\delta(\mathbf{b}, \mathbf{n}_{\text{id}}) = a^{(c, -)} \quad (298)$$

Rule **(Tr-Sfree)** is immediate from (298).

Case **(Tr-Rel-Read-Forge)** : Impossible case as there is no  $\bar{\alpha}$  such that  $\text{fread}(a, v) =_{\delta} \bar{\alpha}$ .

Case **(Tr-Rel-Write-Forge)**: Impossible case as there is no  $\bar{\alpha}$  such that  $\text{fwrite}(a, v) =_{\delta} \bar{\alpha}$ .

Case **(Tr-Rel-Free-Forge)**: Impossible case as there is no  $\bar{\alpha}$  such that  $\text{ffree } a =_{\delta} \bar{\alpha}$ .

□

**Lemma 27** (Alloc Conversion). If

1.  $T =_{\delta} \Omega.A$  and
2.  $\Omega \sim_{\delta} \Omega$  and

then  $T =_{\delta} \Omega.A$  where  $\delta = \delta \circ \delta^{-1}$ .

*Proof.* Given:

$$T =_{\delta} \Omega.A \quad (299)$$

$$\Omega \sim_{\delta} \Omega \quad (300)$$

$$\delta = \delta \circ \delta^{-1} \quad (301)$$

Case  $w \neq \text{struct } \_ :$  From (299), we have

$$\forall(a, n, w) \in [A]_1, n = \mathbf{n} * \mathbf{sz}(w) \quad (302)$$

$$\delta(a) = a^{(c, \perp)} \dots \delta(a + n - 1) = (a + n - 1)^{(c, \perp)} \quad (303)$$

$$T(a) = \dots T(a + n - 1) = (c, \perp) \quad (304)$$

From (300), we have  $\Omega.A \sim_{\delta} \Omega.A$ . From (Alloc-Eq), we have

$$\forall (\mathbf{a}, \mathbf{n}, \mathbf{w}) \in [\Omega.A]_1, (\mathbf{a}, \mathbf{n}, \mathbf{w}) \sim_{\delta} (\mathbf{a}, \mathbf{n}) \wedge (\mathbf{a}, \mathbf{n}) \in [\Omega.A]_1 \quad (305)$$

Thus, we have  $(\mathbf{a}, \mathbf{n}, \mathbf{w}) \sim_{\delta} (\mathbf{a}, \mathbf{n})$ . Inverting using (Slot-Eq-Prim) or ??,

$$\delta(\mathbf{a}) = \mathbf{a} \quad (306)$$

$$|\mathbf{n}| * \mathbf{sz}(\mathbf{w}) = |\mathbf{n}| \quad (307)$$

To prove  $T =_{\delta} \Omega.A$ , we need the following:

$$\forall (\mathbf{a}, \mathbf{n}) \in [\Omega.A]_1, n = \mathbf{n} \quad (308)$$

$$\forall i \in \{0, n - 1\}, \delta(\mathbf{a} + i) = a^{(c, -)} \quad (309)$$

$$T(\delta(\mathbf{a})) = \dots T(\delta(\mathbf{a}) + n - 1) = (c, \_ ) \quad (310)$$

This implies,

$$\forall (\mathbf{a}, \mathbf{n}) \in [\Omega.A]_1, n = \mathbf{n} \quad (311)$$

$$\forall i \in \{0, n - 1\}, \delta \circ \delta^{-1}(\mathbf{a} + i) = a^{(c, -)} \quad (312)$$

$$T(\delta \circ \delta^{-1}(\mathbf{a})) = \dots T(\delta \circ \delta^{-1}(\mathbf{a}) + n - 1) = (c, \_ ) \quad (313)$$

From (306), we have  $\delta^{-1}(\mathbf{a}) = \mathbf{a}$ . Thus,  $\delta \circ \delta^{-1}(\mathbf{a}) = \delta(\mathbf{a})$ . Rewriting the above, we need to prove:

$$\forall (\mathbf{a}, \mathbf{n}) \in [\Omega.A]_1, n = \mathbf{n} \quad (314)$$

$$\delta(\mathbf{a}) = a^{(c, -)} \dots \delta(\mathbf{a} + n - 1) = (a + n - 1)^{(c, -)} \quad (315)$$

$$T(a) = \dots T(a + n - 1) = (c, \_ ) \quad (316)$$

We already have (315) and (316) from (303) and (304). (302) and (307) together imply (314). Thus, we have the proof for  $T =_{\delta} \Omega.A$ .

**Case  $w = \text{struct } s$ :** From (299), we have

$$\forall (\mathbf{a}, 1, \text{struct } s) \in [A]_1, D(s) = \{f_1 : \tau_1, f_2 : \tau_2 \dots f_k : \tau_k\} \implies$$

$$n_0 = 0 \implies n_i = \mathbf{srcsz}(\tau_i) \implies$$

$$\delta(\mathbf{a} + i) = (a + \Sigma_i n_i)^{(c, s_i)} \wedge$$

$$T(a + \Sigma_i n_i) = T(a + \Sigma_i n_i + 1) = \dots = T(a + \Sigma_i n_{i+1} - 1) = (c, s_i)$$

Note that all fields have same color but different shades. When  $\delta = \delta \circ \delta^{-1}$ , we get different shades for the addresses corresponding to fields. However, the definition  $T =_{\delta} \Omega.A$  ignores the shades. Thus following an argument similar to the previous case, we have the proof.

□

**Lemma 28** (MS Preservation Strong—Multiple Steps). If

1.  $\Omega \xRightarrow{\bar{\alpha}}^* \Omega'$  and
2.  $\llbracket P \rrbracket^{\text{prog}} = \mathbf{M}$  and
3.  $\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta$  and
4.  $\Omega \sim_{\delta} \Omega$  and
5.  $\Delta \vdash \Omega$  and
6.  $\vdash \text{isValid } \Omega$  and
7.  $T =_{\delta} \Omega.A$  and
8.  $\delta \subseteq \delta'$  and
9.  $\bar{\alpha} =_{\delta'} \bar{\alpha}$  and
10.  $T \xrightarrow{\bar{\alpha}} T'$  and
11.  $T' =_{\delta'} \Omega'.A$

then

- (i)  $\delta' = \delta' \circ \delta'^{-1}$
- (ii)  $\bar{\alpha} =_{\delta'} \bar{\alpha}$  and
- (iii)  $T' =_{\delta'} \Omega'.A$

*Proof.* Given:

$$\Omega \xRightarrow{\bar{\alpha}}^* \Omega' \tag{317}$$

$$\llbracket P \rrbracket^{\text{prog}} = \mathbf{M} \tag{318}$$

$$\llbracket P, \Delta \rrbracket^{\text{glob}} = \Delta \tag{319}$$

$$\Omega \sim_{\delta} \Omega \tag{320}$$

$$\Delta \vdash \Omega \tag{321}$$

$$\vdash \text{isValid } \Omega \tag{322}$$

$$T =_{\delta} \Omega.A \tag{323}$$

$$\delta \subseteq \delta' \tag{324}$$

$$\bar{\alpha} =_{\delta'} \bar{\alpha} \tag{325}$$

$$T \xrightarrow{\bar{\alpha}} T' \tag{326}$$

$$T' =_{\delta'} \Omega'.A \tag{327}$$

We have to prove

$$\delta' = \delta' \circ \delta'^{-1} \quad (328)$$

$$\bar{\alpha} =_{\delta'} \bar{\alpha} \quad (329)$$

$$T' =_{\delta'} \Omega'.A \quad (330)$$

Applying Lemma 18 (Functional Correctness—Multiple Steps) from premises (317) to (327), we have

$$\exists \delta' \supseteq \delta \text{ and } \Omega', \Omega' \sim_{\delta'} \Omega' \quad (331)$$

$$\mathbf{M.funcs} \vdash \Omega \xRightarrow{\bar{\alpha}} \Omega' \quad (332)$$

$$\bar{\alpha} =_{\delta'} \bar{\alpha} \quad (333)$$

Let  $\delta = \delta \circ \delta^{-1}$  and  $\delta' = \delta' \circ \delta'^{-1}$ .

Applying Lemma 26 (Trace Conversion) to (325) and (333), we have (328).

Applying Lemma 27 (Alloc Conversion) to (327) and (331), we have (329).  $\square$

#### 4.11 Non-MS Preservation

We also prove that the compilation preserves non-memory safety in a safe way: if the source configuration takes a step that is memory unsafe, then the compiled configuration traps.

**Theorem 5** (Non-MS Preservation—Top Level). If

1.  $\vdash P : \mathbf{wt}$  and
2.  $\llbracket P \rrbracket^{\text{prog}} = \mathbf{M}$  and
3.  $\Omega_0(P) \xRightarrow{\bar{\alpha}}^* \Omega$  and
4.  $\mathbf{MS}(\bar{\alpha})$  and
5.  $\Omega \xRightarrow{\alpha} \Omega'$  and
6.  $\neg \mathbf{MS}(\bar{\alpha} \cdot \alpha)$

then

- $\mathbf{M.funcs} \vdash \Omega_0(\mathbf{M}, \mathbf{n}_f) \xRightarrow{\bar{\alpha}} \Omega$  and
- $\exists \delta, \Omega \sim_{\delta} \Omega'$  and
- $\bar{\alpha} =_{\delta} \bar{\alpha}$  and
- $\mathbf{MS}(\bar{\alpha})$  and

- $\mathbf{M.functs} \vdash \Omega \xRightarrow{\text{trap}} \Omega'$

*Proof.* Given:

$$\vdash P : \text{wt} \quad (334)$$

$$\llbracket P \rrbracket^{\text{prog}} = \mathbf{M} \quad (335)$$

$$\Omega_0(P) \xRightarrow{\bar{\alpha}^*} \Omega \quad (336)$$

$$\text{MS}(\bar{\alpha}) \quad (337)$$

$$\Omega \xRightarrow{\alpha} \Omega' \quad (338)$$

$$\neg \text{MS}(\bar{\alpha} \cdot \alpha) \quad (339)$$

Invoke Theorem 4 (MS Preservation—Strong) on (334) to (337) we have

$$\mathbf{M.functs} \vdash \Omega_0(\mathbf{M}, \mathbf{n}_f) \xRightarrow{\bar{\alpha}} \Omega \quad (340)$$

$$\exists \delta, \Omega \sim_{\delta} \Omega \quad (341)$$

$$\bar{\alpha} =_{\delta} \bar{\alpha} \quad (342)$$

$$\text{MS}(\bar{\alpha}) \quad (343)$$

Now induct on on  $\Omega \xRightarrow{\alpha} \Omega'$ . Cases (S-Call) and (S-Return) are invalid as they do not emit any relevant events. Only (S-Step) is valid. Invoke Lemma 29 (Non-MS Preservation—Primitive Step) on the premise of (S-Step) to get the required proof.  $\square$

**Lemma 29** (Non-MS Preservation—Primitive Step). If

1.  $\llbracket P \rrbracket^{\text{prog}} = \mathbf{M}$  and
2.  $\mathbf{M} \vdash \Omega \xRightarrow{\alpha} \Omega'$  and
3.  $T =_{\delta} \Omega.A$  and
4. (a)  $\forall \bar{\alpha}, \alpha \neq_{\delta} \bar{\alpha}$  or  
 (b)  $\exists \bar{\alpha}, \alpha =_{\delta} \bar{\alpha}$  and  $\vdash T \not\xRightarrow{\bar{\alpha}}$   
 and
5.  $\Omega \sim_{\delta} \Omega$  and
6.  $\vdash \text{isValid } \Omega$

then

- $\mathbf{M.functs} \vdash \Omega \xRightarrow{\text{trap}} \Omega'$

*Proof.* We prove by case analysis on  $M \vdash \Omega \xrightarrow{\alpha} \Omega'$ . Interesting cases are (S-Assign), (S-Arr-Assign), (S-Assign-Forge), (S-Dereference), (S-Arr-Dereference), (S-Dereference-Forge), (S-Free), (Src-Free-Nop) and (S-Free-Forge). We prove for assignment and free; proof for remaining cases (including dereference) follows analogously.

Case **(S-Assign)**:

$$M \vdash P, \theta_f; K, H, A \triangleright *a^{(b, \ell, w, n_{id})} := v \xrightarrow{\text{write}(a^{(b, \ell, w, n_{id})}, v)} P, \theta_f; K, H', A \triangleright v$$

We have  $\alpha = \text{write}(a^{(b, \ell, w, n_{id})}, v)$ . Let  $T$  and  $\delta$  be such that  $T =_{\delta} A$  and  $\text{write}(a^{(b, \ell, w, n_{id})}, v) =_{\delta} \bar{\alpha}$ . Inverting (Tr-Write-Authentic), we have  $\bar{\alpha} = \text{write}(a^{c, s}) \cdot \text{write}((a+1)^{c, s}) \dots \text{write}((a+n-1)^{c, s})$  where  $n = \text{sz}(v)$  and  $\delta(b, n_{id}) = b^{c, s}$  and  $a = b + (\circ * n)$ .

We also have that  $\vdash T \xrightarrow{\bar{\alpha}}$ . From (MS-Write), we then have that there exists  $a'$  such that

$$T(a') \neq A(c, s) \quad (344)$$

This implies either  $a'$  is allocated or freed. Consider the first case. Without loss of generality assume that  $a' = a + n - 1$ . From the given condition  $T =_{\delta} A$ , we have  $T(b) = \dots = T(b + (\ell * n) - 1) = (c, s)$ . This implies,  $a' \notin (b, b + (\ell * n) - 1)$ . Thus

$$a' < b$$

or

$$a' > b + (\ell * n) - 1$$

Once again, without loss of generality, assume that the latter holds. Thus,

$$\begin{aligned} a + n - 1 &> b + (\ell * n) - 1 \\ \implies b + (\circ * n) + n - 1 &> b + (\ell * n) - 1 \\ \implies \circ &> \ell - 1 \\ \implies \circ &\geq \ell \end{aligned}$$

We are already given  $P, \theta_f; K, H, A \triangleright *a^{(b, \ell, w, n_{id})} := v \sim_{\delta} \langle \Sigma, (\theta_f, \tau.\text{segstore}; \text{u32.const } 0, (v; a)) : S^* \rangle$ . From (Rt-Config) and (Rt-Assign), we have

$$P, \Gamma \vdash a^{(b, \ell, w, n_{id})} : \text{ptr } w \sim_{\delta} \langle [], a \rangle \quad (345)$$

$$P, \Gamma \vdash v : w \sim_{\delta} \langle [], v \rangle \quad (346)$$

$$A \sim_{\delta} \Sigma.A \quad (347)$$

From (C-data-Valid-Ptr-Array), we have  $a^{(b, \ell, w, n_{id})} \sim_{\delta} \langle b, n_o, n_1, 1, n_{id} \rangle$ . Applying Lemma 25 (Offsets out-of-bounds) to  $\circ \geq \ell$ , we have that

$\mathbf{n}_o \geq \mathbf{n}_l$ . This implies that  $\mathbf{n}_o + |\tau| > \mathbf{n}_l$  and thus takes the target step (H-Store-Trap).

Consider the latter case where  $a'$  is freed. That is,

$$T(a') = F(c', s') \quad (348)$$

This implies  $a$  belongs to a freed slot in source allocator  $A$ . From (C-data-Valid-Ptr-Array), we have  $a^{(b, \ell, w, n_{id})} \sim_{\delta} \langle b, \mathbf{n}_o, \mathbf{n}_l, 1, \mathbf{n}_{id} \rangle$ . From (347) and rule (Alloc-Eq), we get that the target handle belongs to a free slot in the target allocator  $\Sigma.A$ . Thus, the target takes the step (H-Store-Trap) due to the premise  $\mathbf{n}_{id} \in A.\text{freed}$ .

Hence proved.

**Case (S-Assign-Forge):** Given:

$$M \vdash P, \theta_f; K, H, A \triangleright *a := v \xrightarrow{\text{fwrite}(a, v)} P, \theta_f; K, H', A \triangleright v$$

We have  $\alpha = \text{fwrite}(a, v)$ . Thus,  $\exists \bar{\alpha}$ , such that  $\text{fwrite}(a, v) =_{\delta} \bar{\alpha}$ .

We are already given  $P, \theta_f; K, H, A \triangleright *a := v \sim_{\delta} \langle \Sigma, (\theta_f, \tau.\text{segstore}; \mathbf{u32.const } 0, (v; a)) : S^* \rangle$ . From (Rt-Config) and (Rt-Assign-forge), we have

$$P, \Gamma \vdash a : \text{ptr } w \sim_{\delta} \langle [], a \rangle \quad (349)$$

$$P, \Gamma \vdash v : w \sim_{\delta} \langle [], v \rangle \quad (350)$$

$$A \sim_{\delta} \Sigma.A \quad (351)$$

Consider (349). From (C-data-Ptr-corrupt), we have  $a = \langle b, o, l, 0, \mathbf{n}_{id} \rangle$ .

Hence, from (H-Store-Trap), we thus have  $M.\text{funcs} \vdash \Omega \xrightarrow{\text{trap}} \Omega'$ .

**Case (S-Free-Forge):** Given:

$$M \vdash P, K, H, A \triangleright \text{free}(a) \xrightarrow{\text{ffree } a} P, K, H', A' \triangleright 0$$

Let  $\alpha = \text{ffree } a$  and  $\alpha = \text{trap}$ . From (Rt-Free-Forge), we have

$$P, \Gamma \vdash \text{free}(a) : \text{int} \sim_{\delta} \langle \text{segfree}; \mathbf{u32.const } 0, v \rangle$$

with premises:

$$P, \Gamma \vdash a : \text{ptr } w \sim_{\delta} \langle [], v \rangle$$

and inturn from (C-data-Ptr-corrupt), we have

$$a \sim_{\delta} \langle b, \_, \_, 0, \mathbf{n}_{id} \rangle$$

This implies  $v = \langle b, \_, \_, 0, \mathbf{n}_{id} \rangle$ . This satisfies the premise of (H-Free-trap) and thus

$$\Phi^* \vdash \langle \Sigma, \theta, \text{segfree} : i^*, v : v^* \rangle \xrightarrow{\text{trap}} \langle \Sigma, \theta, \epsilon, \epsilon \rangle$$



**Case (Src-Free-Nop):** This handles the case when an authentic pointer is freed in an incorrect way—either the pointer is in the middle of an allocated slot or has already been freed. Given:

$$M \vdash P, K, H, A \triangleright \text{free}(a^{(b, \ell, w, n_{id})}) \xrightarrow{\text{free}(a^{(b, \ell, w, n_{id})})} P, K, H, A \triangleright 0$$

From  $\Omega \sim_\delta \Omega$  and (Rt-Free), we have

$$P, \Gamma \vdash \text{free}(a^{(b, \ell, w, n_{id})}) : \text{int} \sim_\delta \langle \text{segfree}; \text{u32.const } 0, v \rangle$$

From the premises of (Rt-Free), we have

$$P, \Gamma \vdash a^{(b, \ell, w, n_{id})} : \text{ptr } w \sim_\delta \langle [], v \rangle$$

We have two cases:  $a \neq b$  and  $a = b$ .

**Case  $a \neq b$ :** From (C-data-Valid-Ptr-Array) and (C-data-Valid-Ptr-Single), we have

$$a^{(b, \ell, \tau, n_{id})} \sim_\delta \langle b, n_o, n_1, 1, n_{id} \rangle$$

From the premises of (C-data-Valid-Ptr-Array), we have  $(a - b) * \text{sz}(w) = n_o$ . Since  $a \neq b$ , we have  $n_o \neq 0$ . Thus, target takes the step (H-Free-trap).

From the premises of (C-data-Valid-Ptr-Single), we have  $(a - b) = n_o = 0$ . Thus  $a = b$ —a contradiction. Thus (C-data-Valid-Ptr-Single) does not hold.

**Case  $a = b$ :** From the premises of (C-data-Valid-Ptr-Array) and (C-data-Valid-Ptr-Single), we have  $n_o = 0$ . Assume (H-Free). Then,

$$\langle T, A \rangle \xrightarrow{\text{sfree}(a, n_{id})} \langle T', A' \rangle$$

From the premises of (Seg-Free),

$$s_A^* = s_1^* \cdot (b, \_, n_{id}) \cdot s_2^*$$

Given  $\Omega \sim_\delta \Omega$ . This implies, from (Alloc-Eq), we have that  $s_A^* \sim_\delta s_A^*$ . Thus,  $(b, \_, n_{id}) \in s_A^*$  implies  $(b, \ell, w, n_{id}) \in s_A^*$ . However, from (Src-Free-Nop), we have that  $(b, \ell, w, n_{id}) \notin s_A^*$ —a contradiction. This is because we assumed that the allocator state changes in (H-Free). Thus,

$$\langle T, A \rangle \xrightarrow{\text{sfree}(a, n_{id})} \langle T, A \rangle$$

This implies (H-Free-trap). Hence proved.  $\square$