

### STM32L5 series

## Excellence in ultra-low-power MCUs with more security





### The STM32 portfolio

### Five product categories



Short- and long-range connectivity









32- and 64-bit microprocessors













**Enabling edge AI solutions** 

32-bit general-purpose microcontrollers: from 75 to 3,224 CoreMark score



Scalable security







### First STM32 based on Arm® Cortex®-M33

#### STM32L5 is the answer



#### More security with TrustZone and ST security implementation

• HW to increase resistance to logical and board level attack



#### **Lower Power consumption**

STM32 ultra-low-power technology



#### Integration, performance, ecosystem

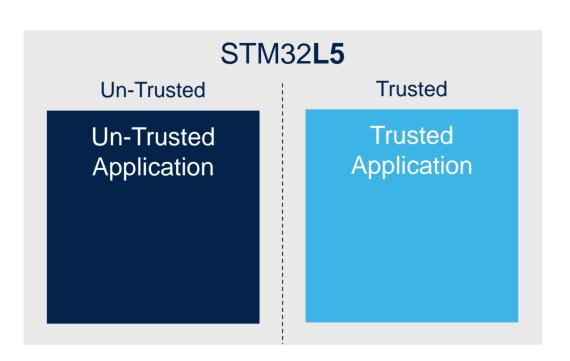
More performance, choice of packages and wide ecosystem





### Security: TrustZone for isolation

#### ST implementation provides a high granularity of isolation



- Each GPIO or peripheral, DMA channel, clock configuration register, ART or small part of Flash memory or SRAM can be configured as Trusted or un-Trusted
- Full isolation of trusted and non-trusted worlds

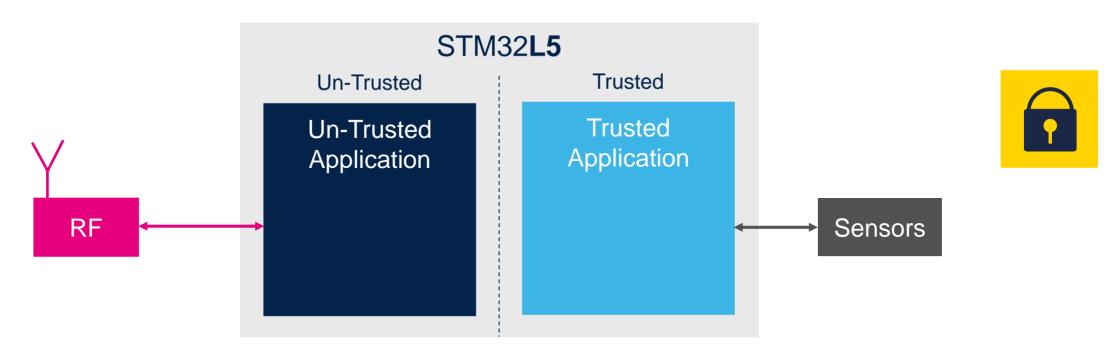




### Security: TrustZone for isolation

#### TrustZone provides full isolation

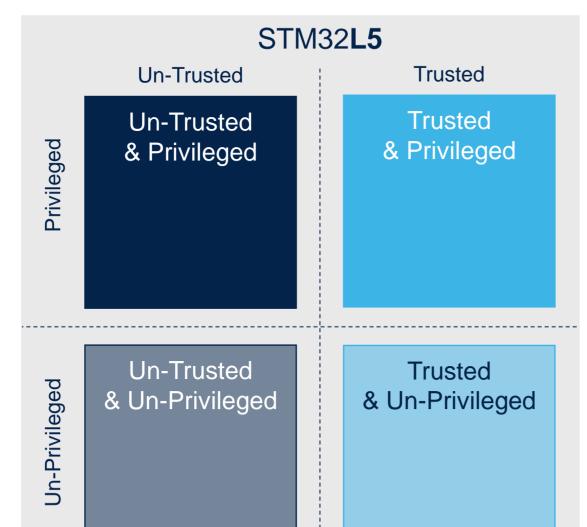
#### Example of IoT application implementation







### Security: TrustZone and privileged zones

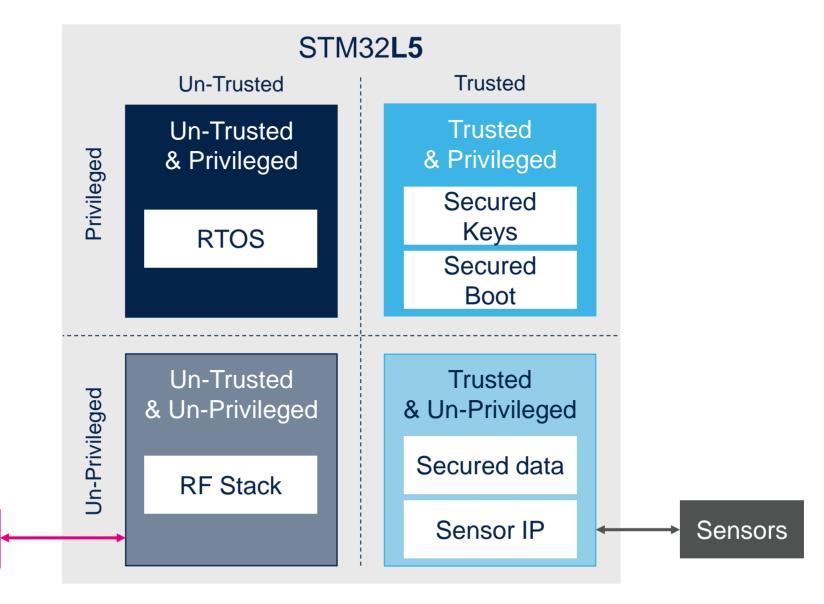


- More partitioning
- Possibility to separate the trusted and un-trusted area with privileged and un-privileged zone
- Strong **granularity** to define each part of memory or each peripheral, DMA channel as privileged or un-privileged





### TrustZone: example





RF



### A full set of security

## Encryption Decryption Authentication



- AES-128/256 Encryption
- SHA-256 Authentication
- Public Key Acceleration (PKA): for RSA, Diffie-Hellmann or ECC (Elliptic Curve Cryptography)
- Certified Crypto library
- True Random Number Generator
- Unique ID
- OTP Zone





Memory & IP Protection

- Active and static Anti-tamper detection
- Memory Protection Unit (MPU)
- Secure Boot
- Read and Write Protection
- HDP (Hide Protect)
- Unique Boot Entry
- OTFDEC (On-the-fly decryption) on Octo SPI to protect external memory
- JTAG fuse
- TrustZone
- SFI (Secure Firmware Installation)

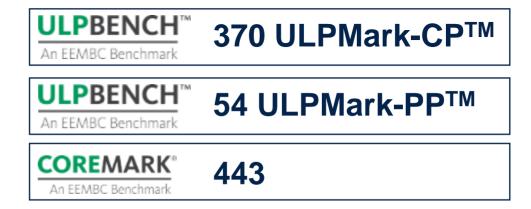




### Extends battery lifetime

- STM32L5 reuses the STM32L4/L4+ technology achieving best-in-class power consumption
- STM32L5 integrates an optional SMPS (DC/DC buck voltage regulator) which can be enabled/disabled on the fly to avoid external noise for external RF or data acquisition.

Proven by EEMBC test results:







### Ultra-low-power modes

#### Best power consumption numbers with full flexibility

Wake-up time **V**BAT 3 nA / 187 nA\* Tamper detection: 3 I/Os. RTC 250 μs Shutdown 17 nA / 122 nA\* Wake-up sources: reset pin, 5 I/Os, RTC 14 µs **Standby** 108 nA / 222 nA\* Wake-up sources: + BOR. IWDG 14 µs Standby + 4-Kbyte RAM 272 nA / 386nA\* Wake-up sources: + all I/Os, PVD, COMPs, I<sup>2</sup>C. 5 µs Stop 2 (full retention: 256-Kbyte RAM) 3.0  $\mu$ A / 3.1  $\mu$ A\* LPUART. LPTIM 6 cycles 26 μA / MHz Sleep Wake-up sources: any interrupt or event Run up to 110 MHz Down to 62 µA / MHz



Note: \* without RTC / with RTC



### More performance

#### Better responsiveness of the application

• New Arm® Cortex®-M33 performance: +20% versus Cortex-M4

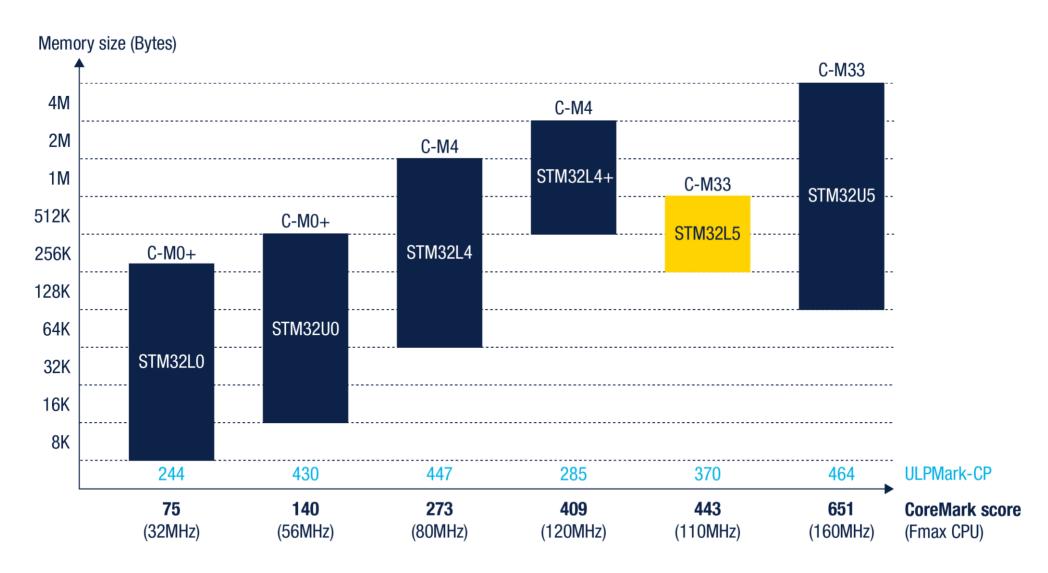
1.5 DMIPS/MHz
4.02 CoreMark/MHz
165 DMIPS
443 CoreMark

- New ST ART Accelerator™: working both on internal and <u>external</u> Flash
  - 8 Kbytes of instruction cache





### STM32L5 ultra-low-power benchmark





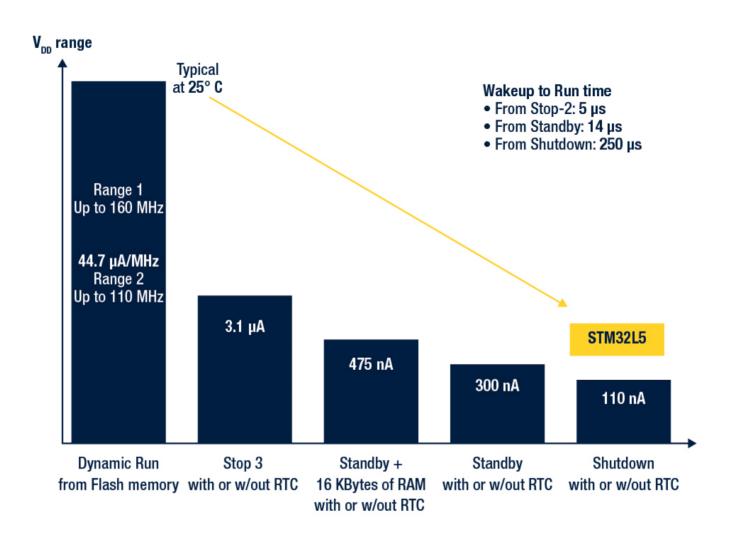
### STM32L5 MCU series

<ul> <li>ART Accelerator™</li> <li>USART, SPI, I²C</li> <li>Octo-SPI</li> <li>16- and 32-bit timers</li> <li>SAI + audio PLL</li> </ul>	Product line	Flash (KB)	RAM (KB)	Memory I/F	2x Op-Amp	2х Сотр	4ch / 2x Sigma Delta Interface	12-bit ADC 5 Msps 16-bit HW oversampling	USB Type-C	CAN- FD	AES, PKA, OTFDEC 128-/256-bit
SHA, TRNG  2x 12-bit DAC  Temperature sensor  Low voltage 1.71V  to 3.6V	STM32L552 USB Device & CAN-FD	512 to 256	256	SDIO FSMC Octo SPI	•	•	•	2	•	•	
V <sub>bat</sub> mode Unique ID Capacitive Touch sensing	STM32L562 USB Device & CAN-FD & AES	512 to 256	256	SDIO FSMC Octo SPI		•	•	2	•	•	•





### STM32L5 ULTRA-LOW-POWER





### Large portfolio

#### 7 packages, several options

Flash memory size / RAM size (bytes)





**Legend:** without HW crypto

with HW crypto



### High integration and innovation

#### Large memory, USB Type-C™ w/ power delivery controller, CAN FD

#### **Parallel interface**

FSMC 8-/16-bit (TFT-LCD, SRAM, NOR, NAND)

#### Digital

2x SAI, DFSDM (4 channels)

#### **Timers**

14 timers including: 2x 16-bit advanced motor control timers 2x LPUART timers 3x 16-bit-timers 2 x 32-bit timers

#### 1/0s

Up to 115 I/Os Touch-sensing controller Arm® Cortex®-M33 CPU 110 MHz TrustZone® FPU

MPU

ETM

#### DMA

ART Accelerator™

Up to 512-Kbyte Flash memory Dual Bank

> 256-Kbyte RAM

#### Connectivity

USB Device Crystal-less, USB Type-C and PD, 1x SD/SDIO/MMC, 3 x SPI, 4 x I<sup>2</sup>C, 1x CAN FD, 1 x Octo-SPI, 5 x USART + 1 x LPUART

#### **Encryption**

AES (256-bit), PKA, SHA-1, SHA-256,TRNG, CRC, OTFDEC

#### **Analog**

2 x 12-bit ADC 12/16 bits 5 MSPS, 2 x DAC, 2 x comparators, 2 x op amps 1 x temperature sensor



### A complete ecosystem





## STM32CubeL5 One-stop-shop software package





#### **STM32Cube Middleware**

#### **Generic Middleware**

- FreeRTOS
- FatFS file system
- mbedTLS and mbedCrypto
- USB Device stacks

#### **Dedicated Middleware**

- Secure Boot and Secure Firmware Update
- TF-M for trusted execution environment
- USB-PD device driver
- STM32 Touch Sensing library

#### **Peripheral drivers**

#### HAL API

Hardware Abstraction Layer, highly portable and easy to use

#### LL APIs

Low-Layer APIs, light weight and highly optimized for runtime efficiency

#### **Project Examples**

#### STM32CubeMX ready

More than 300 project examples for KEIL, IAR and STM32CubeIDE toolchains, with a STM32CubeMX configuration file

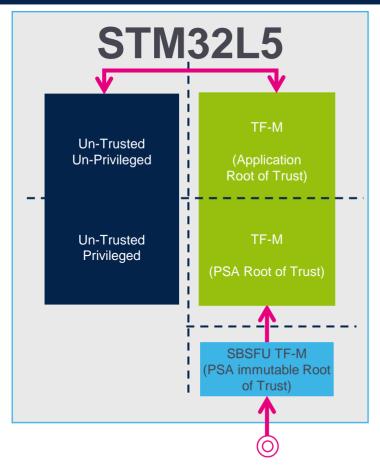


www.st.com/stm32cubel5

### SBSFU and TF-M in STM32CubeL5

#### Reference code framework for a trusted Execution Environment





#### **TF-M Framework**

- Isolation and Secure execution
- Secure services (crypto, initial attestation, secure storage)
- Easy addition of user secure services
- Leveraging STM32L5 security features

#### SBSFU TF-M

- Secure Boot
- Secure Firmware Update



## STM32L5 is one of the first MCU PSA Level 2 certified





### STM32CubeIDE

#### All-in-1 STM32 development tool





#### Configure and generate code

STM32CubeMX integrated



#### Develop code, Compile and Link

#### **TrustZone support**

- TrueSTUDIO / SW4STM32 importer
- Advanced editor
- GNU C/C++ for Arm® toolchain

#### **Program and Debug**

#### **TrustZone support**

- GDB and OpenOCD debugger
- Support of ST-Link and J-Link debug probes



### Partners IDEs development flow

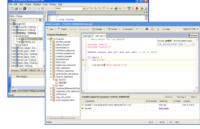
#### **Arm® V8-M TrustZone architecture support**

















#### STM32CubeMX

#### STM32CubeMX enhanced for TrustZone

- Peripherals/middleware configuration
- Resources allocation to security domains

Optional step

### IDEs Compile and Debug

#### **TrustZone Support**

- Partners IDE
- STM32CubeIDE based on Eclipse
- TrustZone debugging

#### **STM32 Programming Tool**

#### STM32CubeProgrammer

- Device and memory configuration
- Program the application
- Secure Firmware Install



### Configuration tool



Power Consumption Calculator

MCU or board Selector





**Code Generation** 

TrustZone support



Middleware Parameters

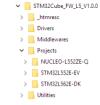
FreeRTOS
FatFS
USB device





macOS<sub>®</sub>

Load an Example .ioc file



**Pinout Configuration** 



**Clock Tree Initialization** 



Peripherals Configuration

TrustZone configuration and GPIOs, memories, DMA, peripherals allocation to security domains





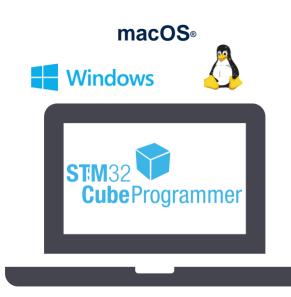


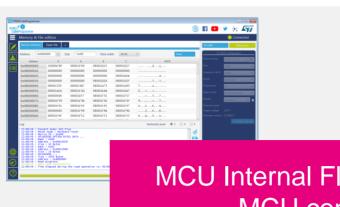






### All-in-one programming software tool





MCU Internal Flash and external Flash services MCU configuration (Option bytes)

Intuitive GUI
Command Line Interface for scripting
API DLL for Custom Integration

STLink (JTAG, SWD)
STM32 Bootloader Interface (USB, UART, SPI, I2C, CAN)
Secure Firmware install (SFI)





### STM32L5 hardware solutions

#### Speed-up evaluation prototyping and design







#### **Evaluation Boards**

Full feature STM32L5 evaluation

• <u>STM32L552E-EV</u>

#### **Discovery Kit**

Flexible prototyping & demo

• STM32L562E-DK

#### **Nucleo Boards**

Affordable and quick prototyping

• NUCLEO-L552ZE-Q



### Discovery kit

#### Prototype your wearable or sensor application with STM32L562E-DK

STM32L562 MCU with AES and PKA

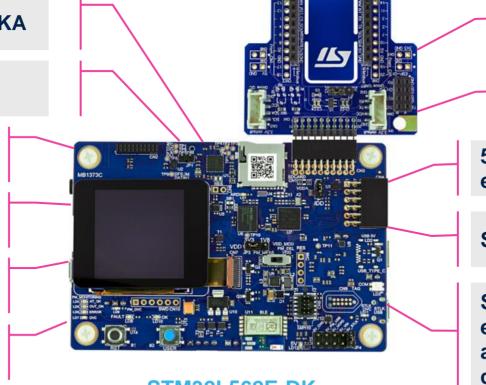
240 x 240 pixel-TFT color Display

state-of-the-art Energy Meter

3D accelerometer and 3D gyroscope

Bluetooth® V4.1 low energy module

**Audio Codec and Headphone amplifier** 



**Digital microphone** 

**USB Type-C™ Sink device FS** 

**512Mbit Octal Flash memory** extension

ST-Link V3

STMod+ connector with fan-out expansion board for Wi-Fi®, Grove and mikroBUS™ compatible connectors



Fan-out expansion board included \$76.22



### STM32CubeMonitor-power

#### State-of-the-art on-board power consumption measurement





#### STM32L562E-DK

On-board Energy Meter 300 nA to 150 mA measurement range

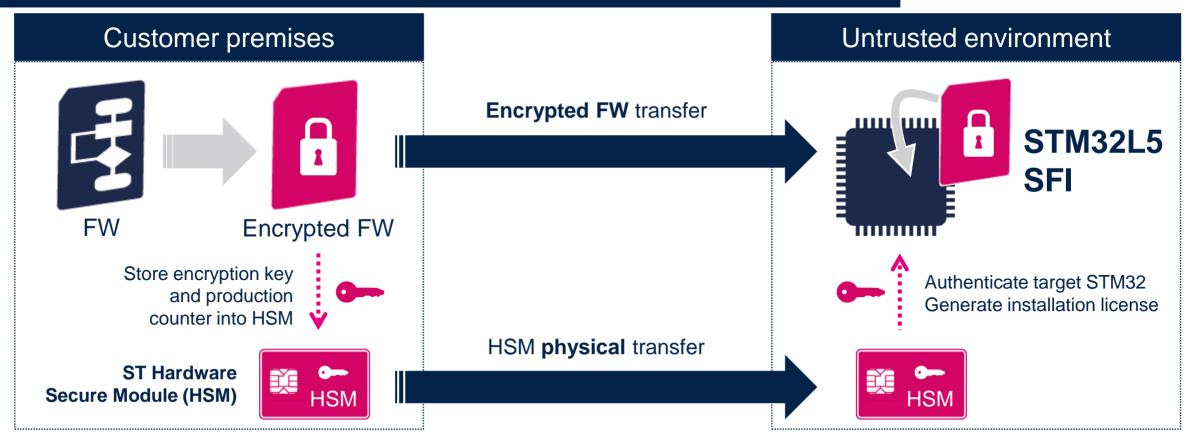






## Secure your production flow with Secure Firmware Install (SFI)

#### Protect your code and control the number of products manufactured







### Conclusion

#### STM32L5 helps designers to answer IoT challenges



**More security** 



Lower power consumption



Integration, performance, ecosystem





### Releasing your creativity



@STM32



@ST\_World





community.st.com



www.st.com/stm32l5



STM32L5 Online Training



wiki.st.com/stm32mcu



github.com/stm32-hotspot



STM32L5 blog articles

# Our technology starts with You



Find out more at <a href="http://www.st.com/STM32L5">http://www.st.com/STM32L5</a>

© STMicroelectronics - All rights reserved.

ST logo is a trademark or a registered trademark of STMicroelectronics International NV or its affiliates in the EU and/or other countries. For additional information about ST trademarks, please refer to <a href="https://www.st.com/trademarks">www.st.com/trademarks</a>.
All other product or service names are the property of their respective owners.

