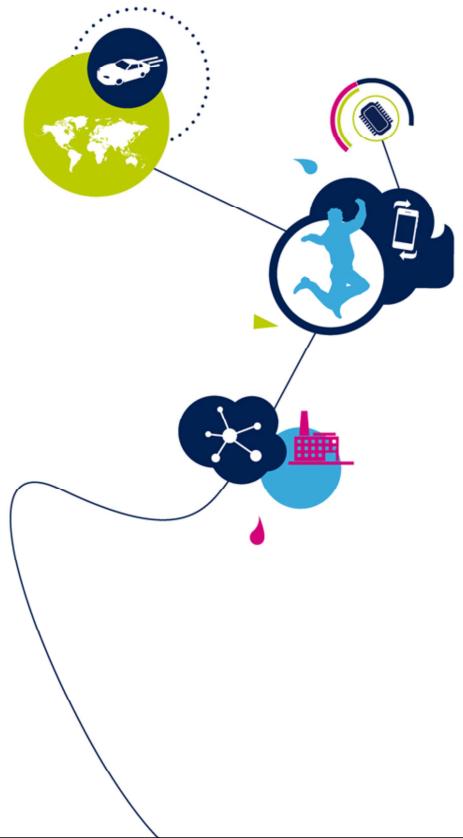


STM32L5 – STM32CubeMX

STM32CubeMX graphical software configuration tool

Revision 1.0

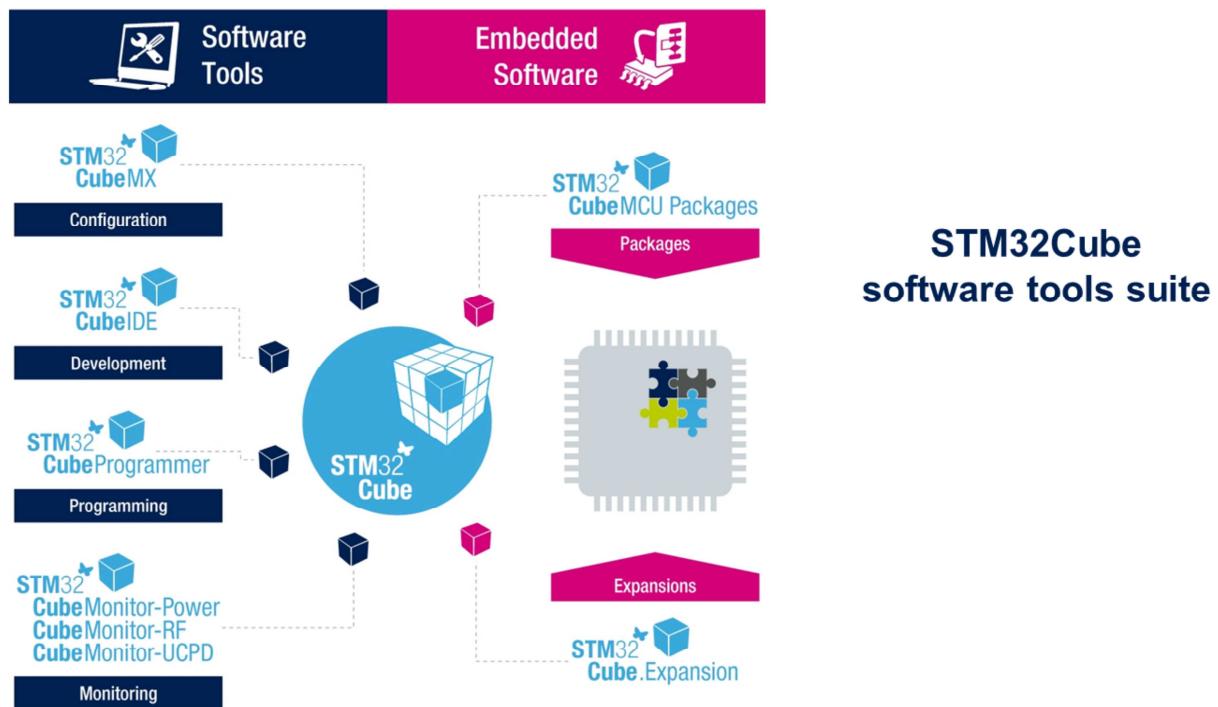


Hello, and welcome to this presentation of the STM32CubeMX Code Generation Tool. It covers the main features of this tool, which is used to configure and generate code, compile and debug, and estimate power consumption for the STM32 family of microcontrollers.



Introduction

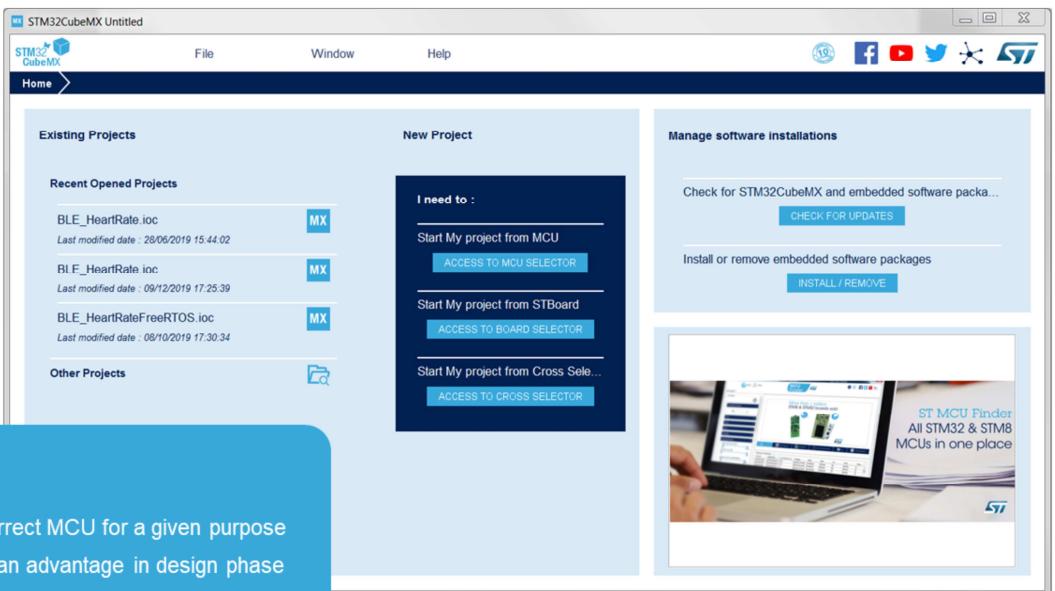
2



While this presentation is having some specific parts related to the STM32L5 series of microcontrollers, STM32CubeMX is a common platform for any STM32 device and is part of the STM32Cube software tools suite.

- Choose ideal MCU and simply configure
 - Pinouts
 - Clocks and oscillators
 - Peripherals
 - Low-power modes
 - Middleware

- Helps choose the correct MCU for a given purpose
- Simulation provides an advantage in design phase
- Boosts development speed with a headstart



The STM32CubeMX application helps developers using STM32 microcontrollers through a user interface that guides the initial configuration of a firmware project.

It provides the means to configure pin assignments, the clock tree, integrated peripherals, and simulate the power consumption of the resulting project. It uses a rich library of data from the STM32 microcontroller portfolio.

The application is intended to ease the initial phase of development, by helping developers select the best product with regards to features and power.

Key features

4

- Peripheral and middleware parameters
- Power consumption calculator
- Code generation
 - Possible to re-generate code while keeping user code intact.
- Option of command-line and batch operation
- Expandable by plugins
- MCU selector
 - Filter by family, package, peripherals or memory sizes.
 - Search for similar product.
- Pinout configuration
 - Choose peripherals to use and assign GPIO and alternate functions to pins.
- Configure NVIC and DMA
- Clock tree initialization
 - Choose oscillator and set PLL and clock dividers.



The user interface is built around a natural workflow of choosing a suitable MCU, selecting the required peripherals and assigning pin configurations.

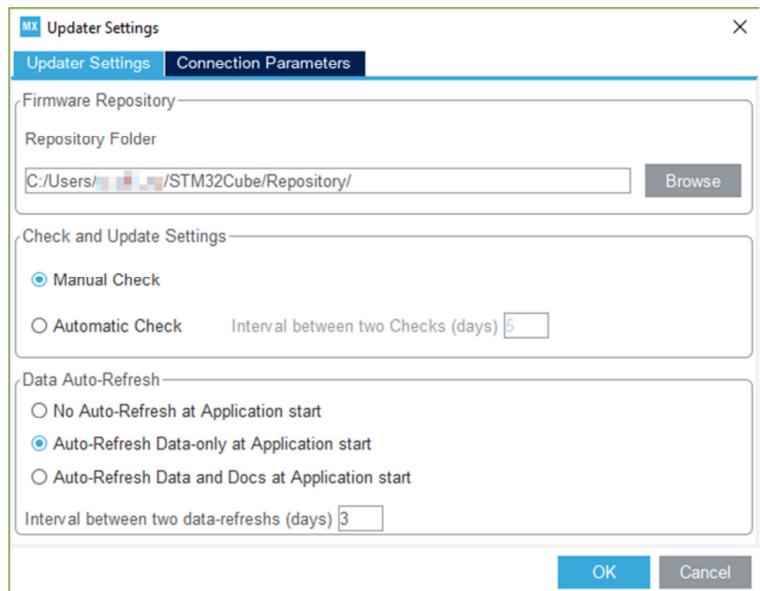
The power consumption calculator aids in designing an efficient system.

Finally, the project initialization code can be generated and, potentially, re-generated while keeping the user code intact.

Prerequisites and settings

5

- STM32CubeMX needs Java RE
 - Check release notes of the particular version for additional requirements.
 - Multiplatform tool runs on Windows, Linux and macOS
- After installation, hit Alt+S to configure the updater – not only for the GUI but also for Cube FW libraries.
- Select SW library placement.



Download the STM32CubeMX installer for free from the ST website and install it.

Then, set your preferences in the Settings menu:

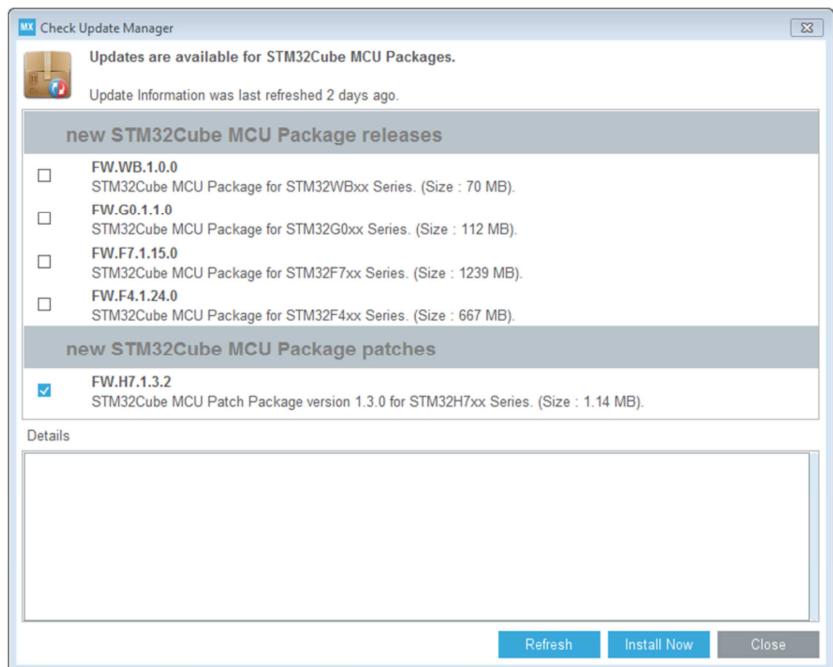
- one menu for the updater and library download (Alt+S),
- the other menu for code generation and integration with development toolchains (Alt+P).

Once this setup is completed, a new project can be created.

Updater

6

- Updates are accessible from the Help menu
- The tool updater can detect new releases of the tool and the associated Cube library.
- Use the libraries manager to download new library packages.



If the internet connection is configured correctly, the tool can update itself as well as the code libraries used for generating the project workspaces.

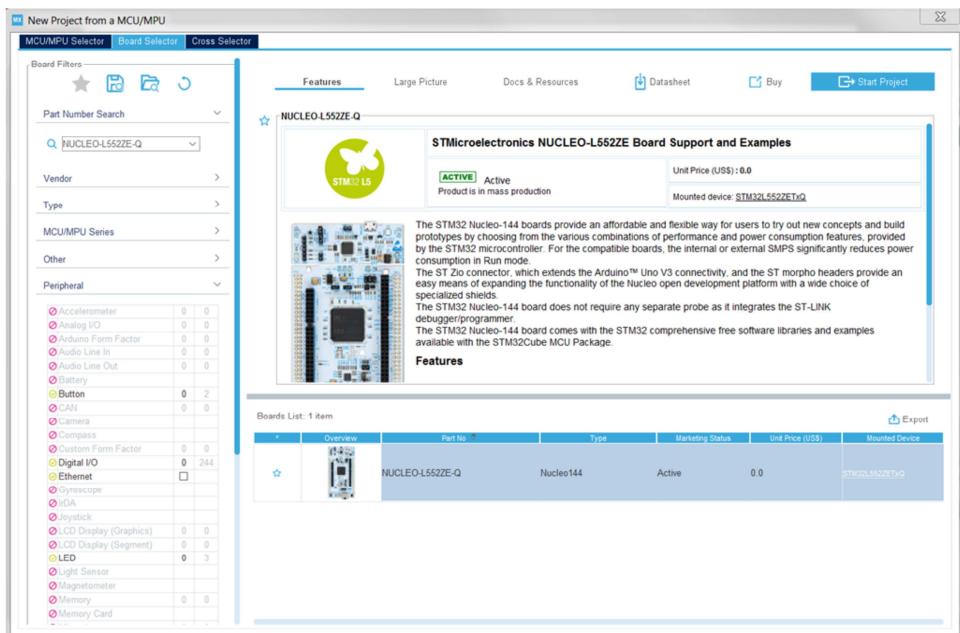
Use the “Install new libraries” option (Alt+U) to download additional STM32Cube libraries, or retrieve older versions for interoperability reasons.

However, note that the STM32CubeMX tool is not tested with all historical library releases, and, new library releases may not work correctly with old tool versions.

MCU selector

7

- Find MCU by name ...
 - Quickly locate by Series and Lines
- ... or application needs
 - Package (pin count)
 - RAM size
 - NV memory requirements
 - Embedded peripherals
 - Number and type of interfaces
 - Core and frequency
 - Price
- Convenient links to documentation
- Export table to Excel file



The MCU selector window will come up after selecting the “New Project” option. If the user knows which MCU to use, it can be found quickly.

If not, the available products can be filtered based on the specific requirements.

Configurations for existing STM32 boards are available under the “Board Selector” tab. If, for example, a NUCLEO-L552ZE board is selected, then the I/Os for the LCD, buttons, audio, and communication interfaces are loaded. However, some communication interfaces on the board are only accessible as options after reconfiguring jumpers or with solder bridges. These are not pre-defined in the STM32CubeMX tool.

Once the MCU and its related board are selected, click on « Start Project » and the configuration interface will show up. For the STM32L5 series, a pop up window appears to select whether the TrustZone option is enabled.

MCU Cross Selector

8

The screenshot shows the 'Cross Selector' tab of the MCU/MPU Selector software. A search bar at the top has 'STM32L552ZETx-QFP144' entered. Below it, a table compares the STM32L552ZETx with other STM32 MCUs based on various parameters like core, package, and memory. Two blue callout bubbles highlight features: one pointing to the part number search bar with the text 'STM32 MCU sorted by similarity', and another pointing to the comparison table with the text 'Detailed comparison to selected products'. The ST logo and 'life. augmented' tagline are visible in the bottom left corner.

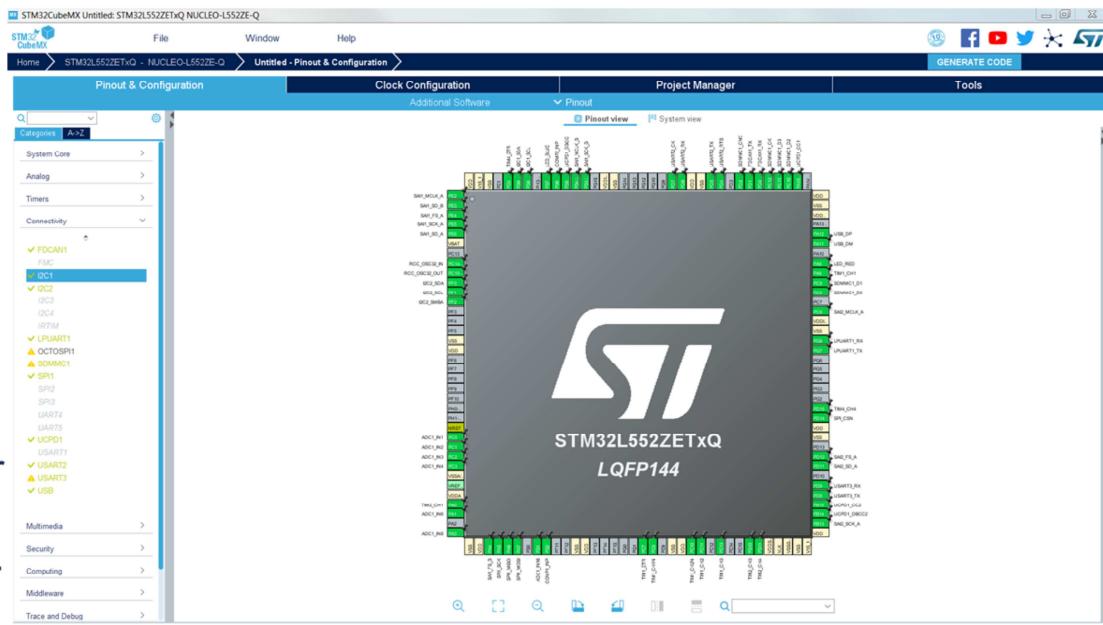
Product	Public Price	no	no	no	5.457 USD (for 10K)
busArch	no	no	no	no	32 bit
core	ARM Cortex-M33 at 110 MHz	ARM Cortex-M33 at 110 MHz	ARM Cortex-M33 at 110 MHz	ARM Cortex-M4 at 80 MHz	
package	QFP144	QFP144	QFP144	QFP144	
System Core	GPIO	115 io	115 io	111 io	115 io
Temperature range	-40 °C to 85 °C				
Voltage range	1.71 V to 3.60 V				
RAM	256 KB	256 KB	256 KB	320 KB	
eeprom	no	no	no	no	
flash	512 KB	512 KB	512 KB	512 KB	
Touch Sensing	no	no	no	yes	
ADC	4xAADC 24-bit 16xAADC 12-bit	4xAADC 24-bit 16xAADC 12-bit	4xAADC 24-bit 16xAADC 12-bit	4xAADC 24-bit 24xAADC 12-bit	
Comparator (COMP)	2	2	2	2	
OPAMP	no	no	no	2	
Timer	2xTimer 32-bit 12xTimer 16-bit	2xTimer 32-bit 12xTimer 16-bit	2xTimer 32-bit 12xTimer 16-bit	2xTimer 32-bit 12xTimer 16-bit	
Timer (HRTM)	no	no	no	no	
Timer (LPTM)	no	no	no	yes	

The cross selector feature helps identify a replacement or alternatives for many common MCUs on the market, not only from ST Microelectronics. The tool allows choosing important parameters to provide sensible results that reflect the actual needs.

Pin assignment (1/3)

9

- Pinout from:
 - Peripheral tree
 - Manually
- Automatic signal remapping
- Management of dependencies between peripherals and/or middleware (FatFS, USB, AI ...)



The next step is to select the peripherals to be used and, where applicable, assign pins to their inputs and outputs. Independent GPIOs can also be configured.

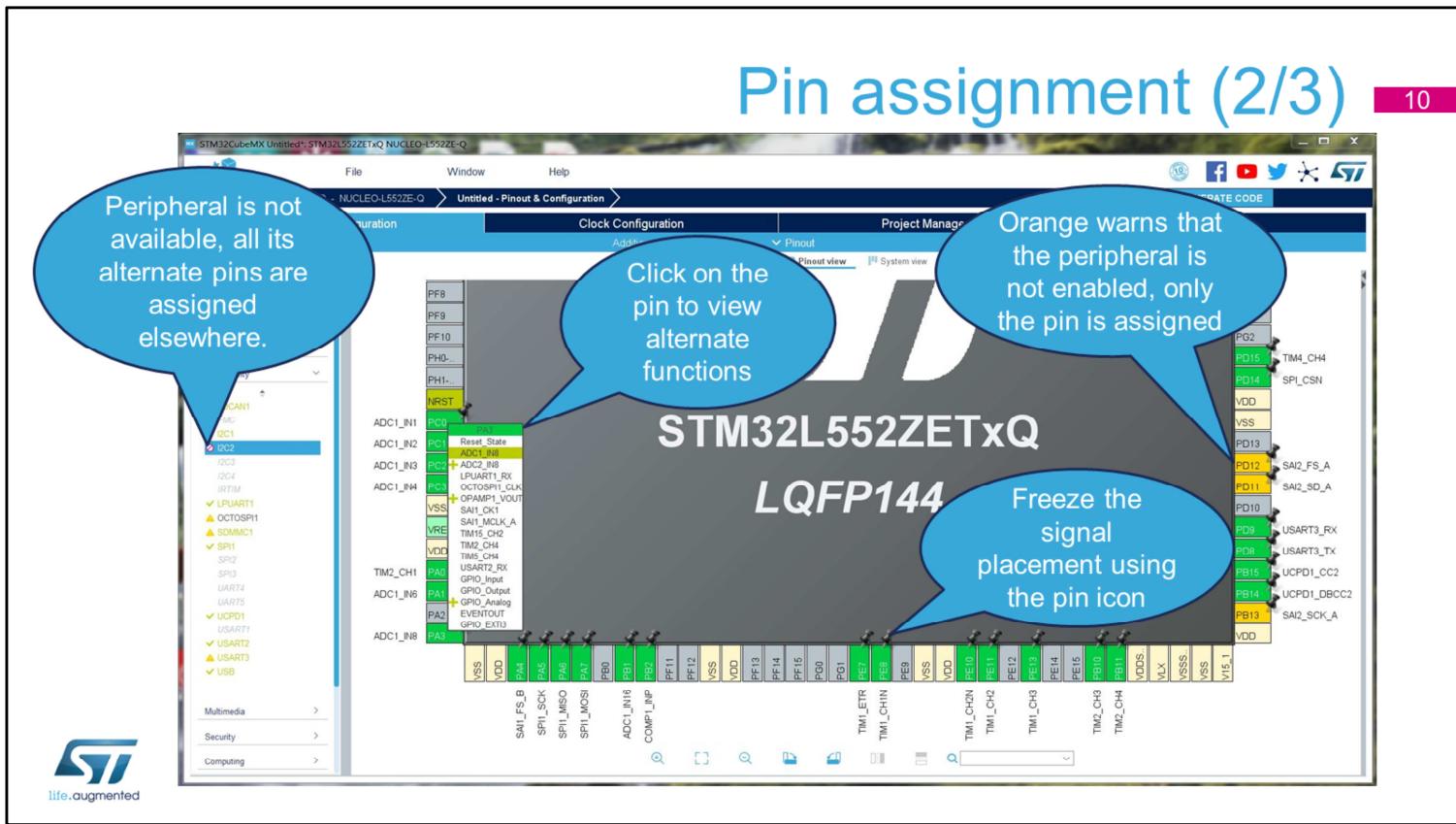
Signals are assigned to default pins, but they can be transferred to alternate locations, which are displayed by CTRL-clicking on the pin.

For example, when the I2C1 peripheral is enabled, the tool automatically assigns it to the default pins.

The tool automatically takes into account most bonds between the peripherals and software components it manages.

Pin assignment (2/3)

10



As more pins are reserved for their alternate functions, the choice of remaining configurations for other peripherals decreases.

The limitations are indicated by icon changes on other peripheral nodes.

Left-click on the pin to display its alternate functions.

Right-click on the pin to name or select the pin assignment.

If a pinout is selected without a particular peripheral enabled or if there is any other problem with the pinout, the pin turns orange instead of green.

Pin assignment (3/3)

11

- Different possible states for peripheral modes.
 - Dimmed: the mode is not available because it requires another mode to be set.
 - Yellow: The mode is available with limitations.
 - Red: Signals required for this mode can't be mapped to the pinout.
- Signals can be set/moved directly from the pinout view.
 - Click on the pin to see the list of possible signals and select one.
 - To see alternate pins for a signal, CTRL+click on the signal and drag it elsewhere.
 - Ignore unused pins since the code generator can set them to power-saving analog mode.



There are different possible states for peripheral modes:

Dimmed: The mode is not available because it requires another mode to be set. Place the mouse pointer over the dimmed mode to see the reason – it may require a disabled clock source or may have other peripheral dependencies.

Yellow: The mode is available with limitations because some options are blocked by conflicts. For example, the USART may not be configured to synchronous mode because all the selectable clock pins are taken.

Red: Signals required for this mode cannot be mapped to the pinout. This may occur, for example, if a crucial signal has all its alternate pins used by other peripherals.

Signals can be set/moved directly from the pinout view.

Click on the pin to display the list of possible signals and select one. This works for GPIOs which have no peripherals assigned.

To see alternate pins for a signal, hold the CTRL key and click on the signal. You can then drag and drop the signal to

the new pin (while holding the CTRL key).

It is not necessary to manually set all unused pins to analog.

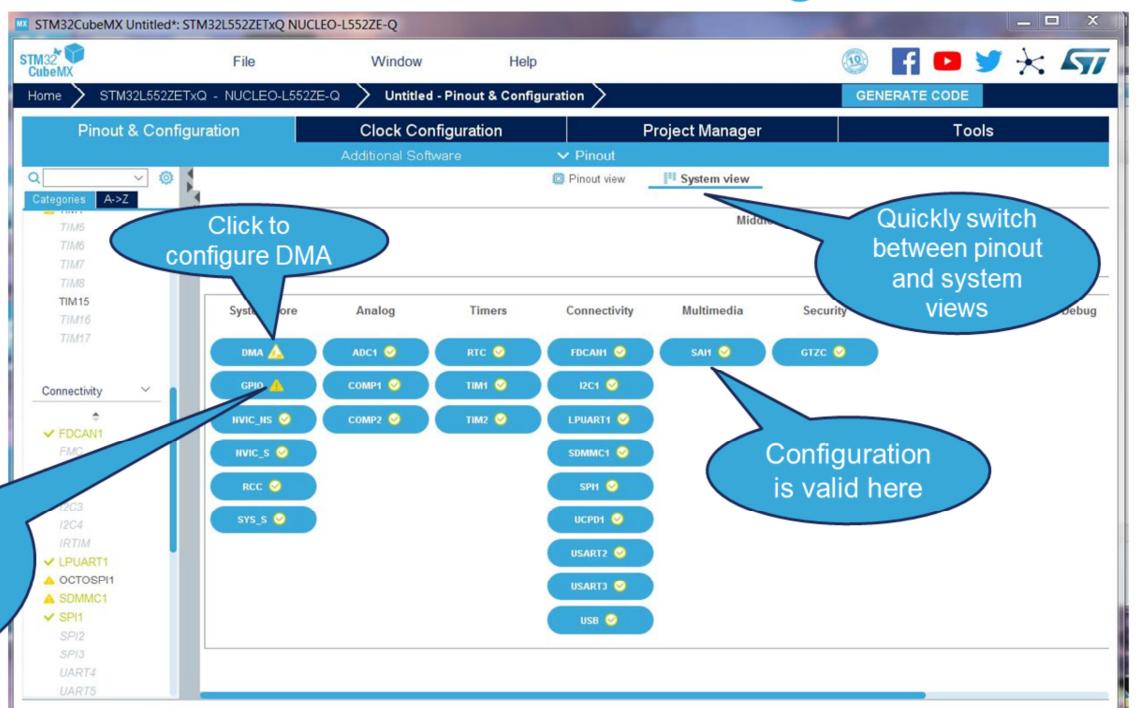
There is a semi-automated step that does this.

Peripheral and Middleware configuration

12

- Global view of used peripherals and middleware.
- Highlight of configuration state
 - ⚠ Not configured
 - ✓ OK
 - ⚠ Problem
 - ✗ Blocking error

GPIO configuration is considered incorrect, but code may be generated

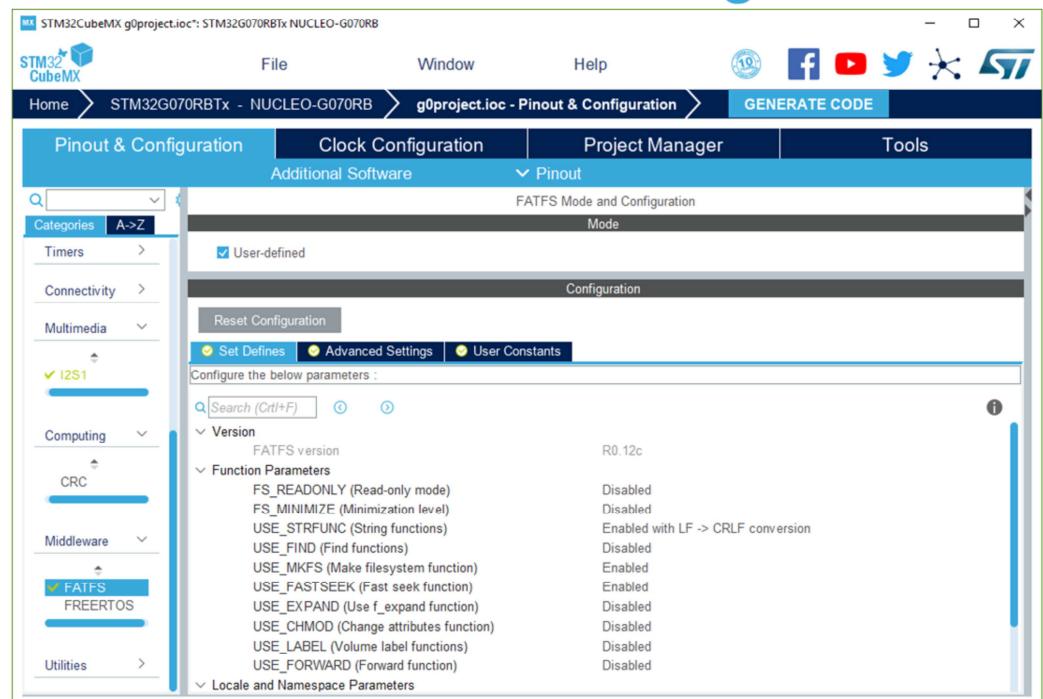


The Configuration tab of the main window provides an overview of all the configurable hardware and software components that STM32CubeMX can help set up. Each button with access to configuration options is displayed with a small icon indicating the configuration state. The default state is not configured. Clicking on a button for a peripheral or middleware displays its configuration options. Even when configured correctly, further modifications are possible. Warning signs provide notifications about incorrect configurations, and the peripheral will not work if code is generated in this state. Critical errors are represented by a red “X” and the configuration must be modified to continue. To add more peripherals and components, return to the Pinout tab.

Middleware configuration

13

- Presents options specific to each supported software component.
- All settings are organized in logical groups.
- Description and constraints are available for quick reference.

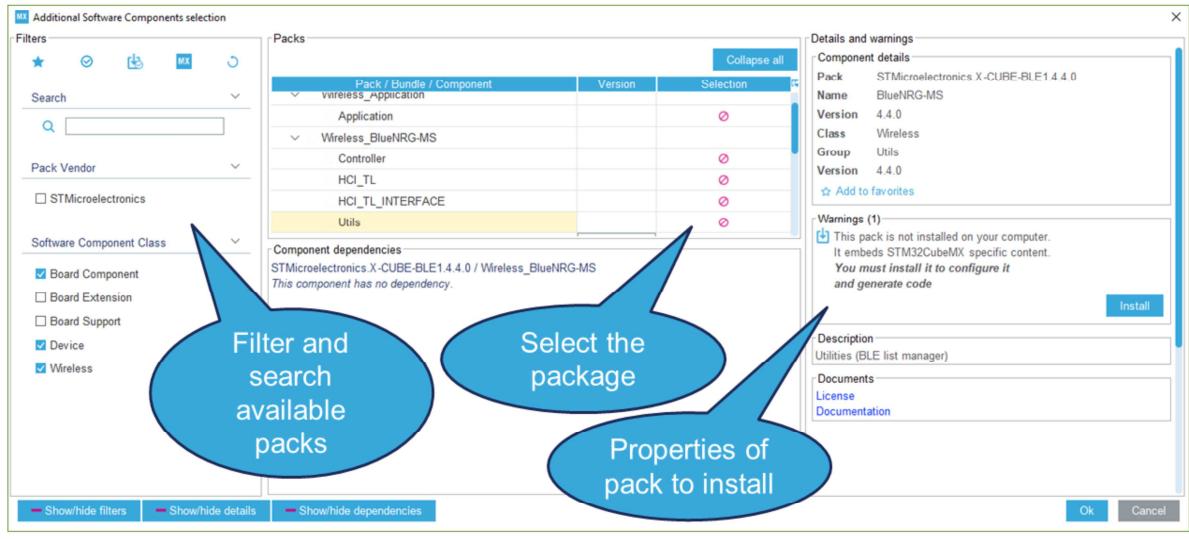


Each middleware software component has options that are different, but they are all presented in a similar fashion, giving easy access to initialization options and providing informative descriptions.

Additional Middleware Packs

14

- Most middleware packs are not included in the base package, but downloadable optionally.

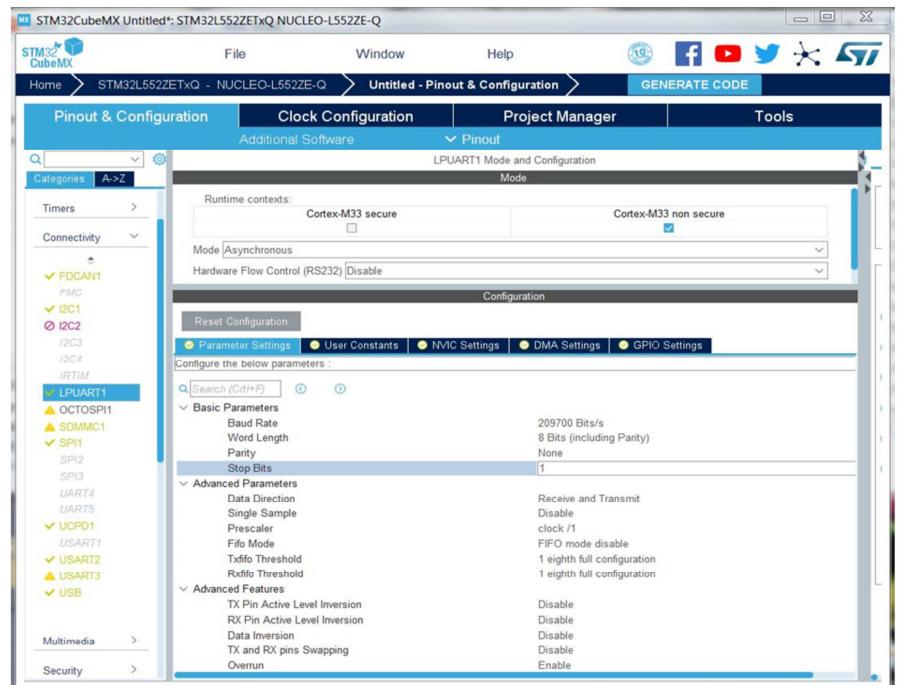


The vast amount of middleware packs available for STM32 makes it most impractical to distribute as a single package. Clicking the “Additional Softwares” link in the Pinout & configuration tab presents a dialog window where most middleware packages available for the selected MCU can be found and installed.

Peripheral configuration

15

- All available initialization parameters are presented with short description and options.
- Interrupt may be assigned to peripherals.
- DMA may be associated, where applicable.
- GPIO settings for peripherals with input and/or output.



When configuring a peripheral, the dialogue window shows basic parameters, dependencies and constraints. Simple drop-down menus are used when applicable.

Interrupts priorities can only be set in the “NVIC settings” tab. The peripheral window can only be used to enable or disable each interrupt.

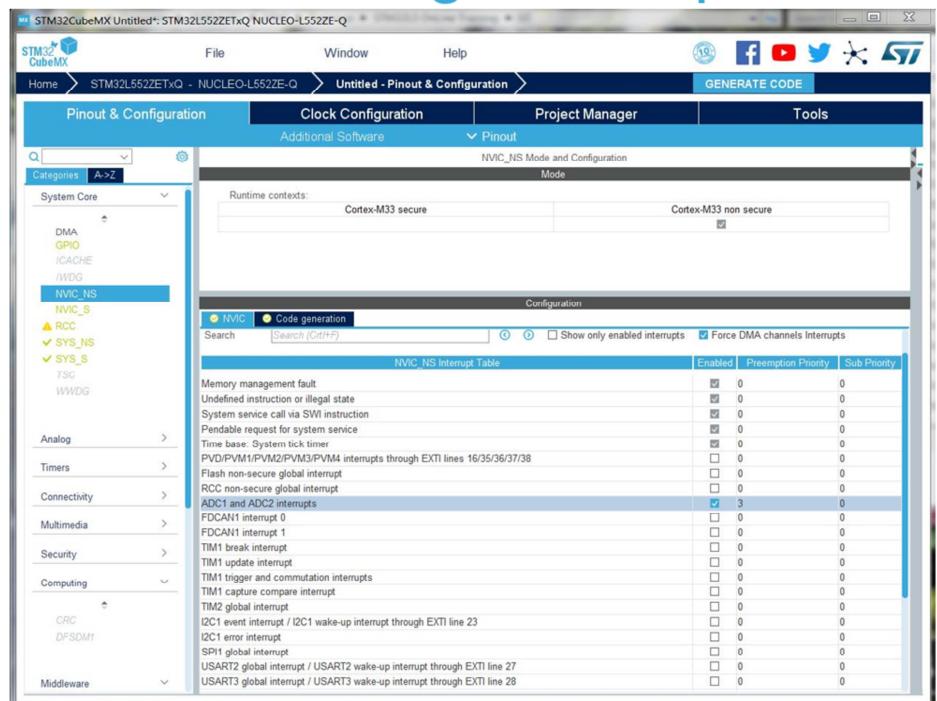
The DMA settings tab contains all the parameters for DMA requests relevant for initialization, but run-time parameters (start address, ...) are not managed here.

The GPIO settings tab is used to define GPIO parameters and features, pin filtering and the possibility to label each signal for easy identification.

NVIC configuration panel

16

- Single control panel for all interrupts.
- Manage priorities and sub-priorities.
- Searching, filtering and sorting interrupts in the list.
- Code generation tab allows to customize interrupt initialization.



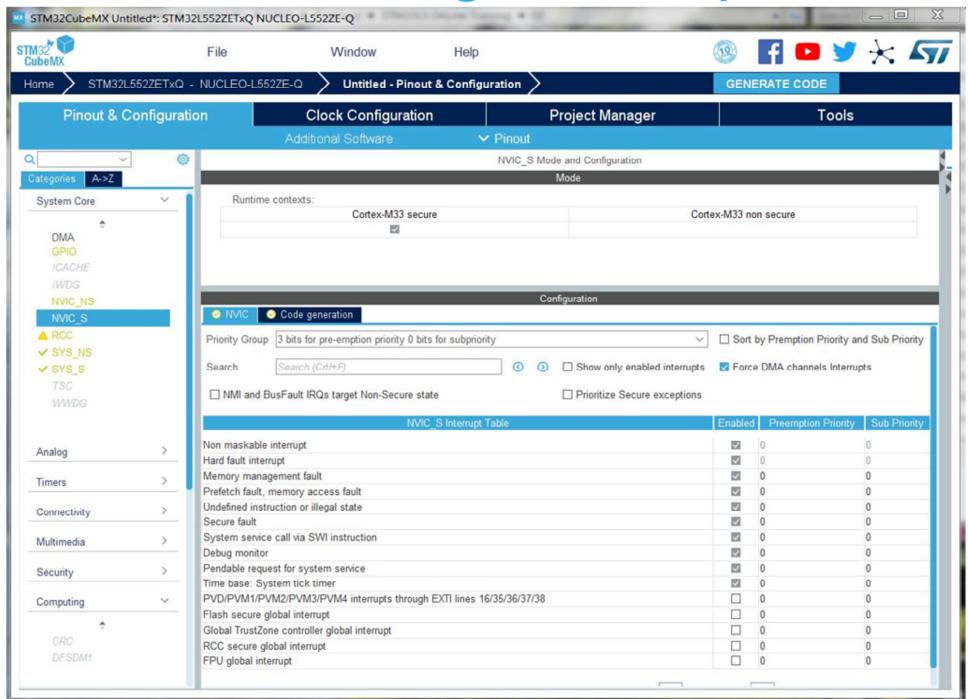
A central location with easy-to-understand overview of available and enabled interrupts, along with their priorities, is another advantage of STM32CubeMX. This window is used to enable interrupts for selected peripherals and to configure the priorities.



NVIC secure configuration panel

17

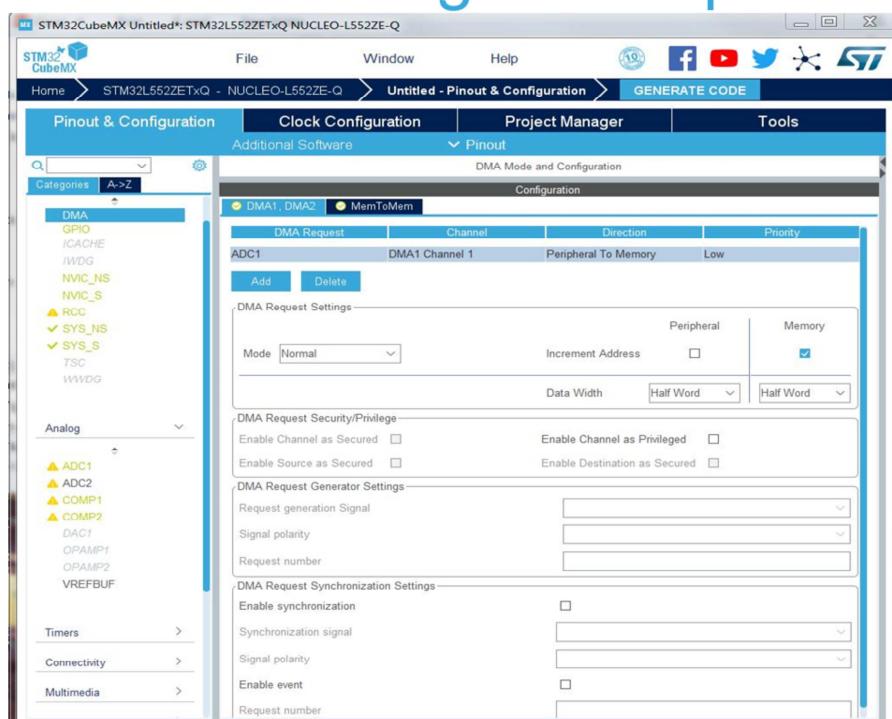
- Configure all security aspects offered by the STM32L5.
- Manage interrupts priorities and sub-priorities.
- Enable the interrupts for the secure Flash, the global TrustZone controller and the secure RCC.



The STM32L5 Series devices offer security foundation. When TrustZone is enabled, firmware hardware isolation is supported thanks to securable peripherals, memories and I/Os. The panel enables the configuration of all interrupts related to security events such as hard faults, secure memory management or TrustZone events.

DMA configuration panel

- Manages all DMA requests including memory to memory.
- Configure direction, priority and other settings.

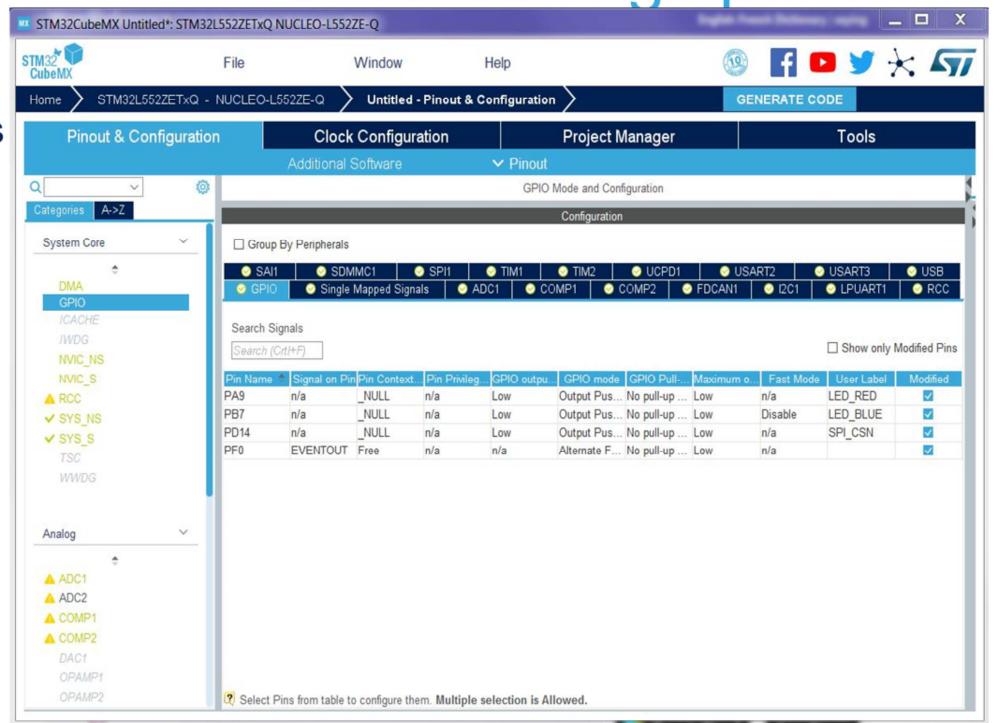


Select the tab for the corresponding DMA channel and click the “Add” button to add a DMA request for the specified peripherals. Verify all configuration options. Note that this configures a DMA channel, but does not fully describe a DMA transfer. This must be done in the application code.

GPIO settings panel

19

- The application attempts to set sensible default values for most GPIO parameters.
- Default values are chosen, as low-speed and no pull-up.
- Multiple pins can be selected to set them to the same configuration.



The GPIO tab in the Pin configuration window facilitates the configuration and initialization settings for each pin.

Each pin is listed in table format which provides an overview of the pin configuration along with its user label.

Sort, search, and apply modifications to selected pins using drop-down menus.

Default values assigned by the tool are safe but may not work with certain peripheral configurations.

Check that the GPIO speed selected by the tool is sufficient for the peripheral communication speed, and that an internal pull-up is selected where needed.

To assign the settings faster, try selecting groups of pins rather than configuring pins individually. Use tabs to get pin groups dedicated to specific peripherals.

Note that settings applied during initialization can be modified during runtime, but that is outside the scope of the STM32CubeMX tool.

Code generation

20

- Generates all the initialization code in C.
- Generates project file for any supported development toolchain.
- User code can be added in dedicated sections and will be kept upon regeneration.
- Option to use the latest library version or keep the same even if re-generating.

```
main.c
22  /*
23   */
24  /* Includes -----
25  #include "stm32f4xx_hal.h"
26  #include "cmsis_os.h"
27  #include "lwip.h"
28  #include "usb_device.h"
29
30  /* Define structures */
31  ADC_HandleTypeDef hadc1;
32
33
34  /* USER CODE BEGIN 0 */
35
36  /* USER CODE END 0 */
37  /* Private function prototypes -----
38  static void SystemClock_Config(void);
39  static void StartThread(void const * argument);
40  static void MX_GPIO_Init(void);
41  static void MX_ADC1_Init(void);
42  static void MX_NVIC_Init(void);
43
44  int main(void)
45  {
46  /* USER CODE BEGIN 1 */
47
48  /* USER CODE END 1 */
49  /* MCU Configuration-
50  /* Reset of all peripherals, Initializes the Flash interface
51  HAL_Init();
52  /* Configure the system clock */
```



When all inputs, outputs, and peripherals are configured, the code is ready to be generated.

First, check the settings in the Project menu of the main window.

One of the several supported development tools can be selected to take over the generated project, including toolchains from Keil, IAR, and ST CubeIDE.

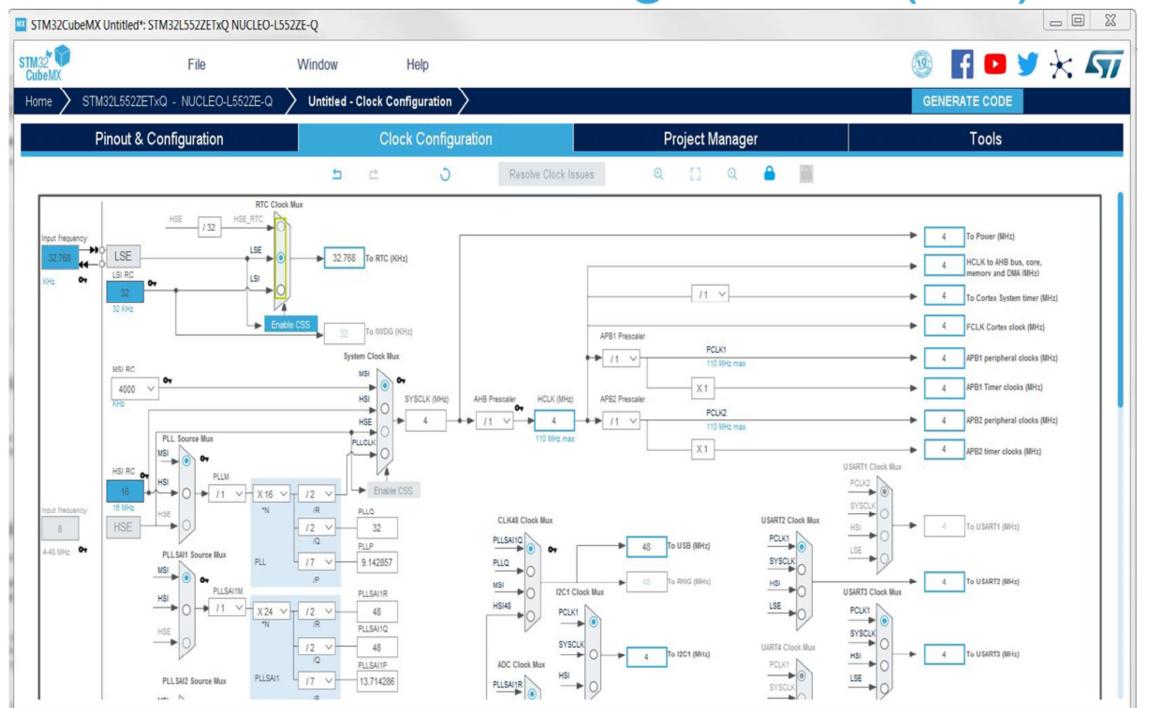
User code must be kept between the constraints of the “USER CODE“ comment blocks in order for the initialization settings to be modified using STM32CubeMX without affecting the custom code.

See the next slide to see how to activate this option.

Clock configuration (1/2)

21

- Immediate display of all clock values.
- Active and inactive clock paths are differentiated.
- Management of clock constraints and features.

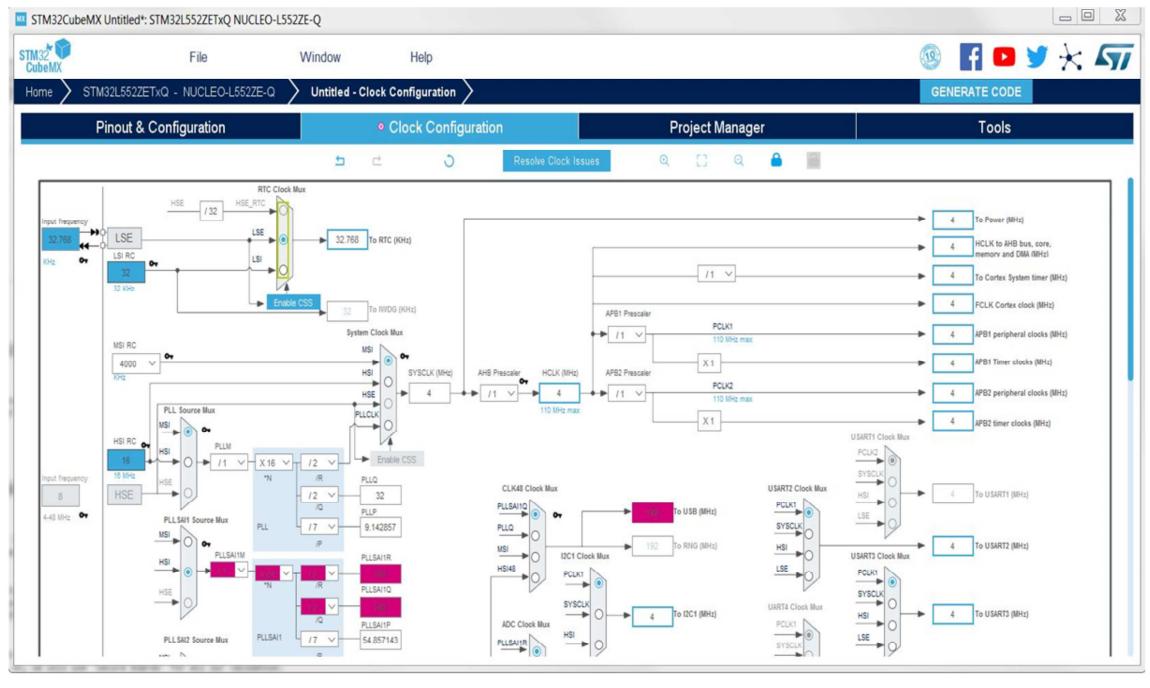


The clock configuration tab provides a schematic overview of the clock paths, along with all clock sources, dividers, and multipliers. Actual clock speeds are visible. Active and enabled clock signals are highlighted in blue. Drop-down menus and buttons serve to modify the actual clock configuration.

Clock configuration (2/2)

22

- Highlight of errors – instantly turns red.
- Enter the value in the blue frame and let the tool adjust the dividers and multipliers.
- Lock a value to prevent the tool from modifying it.



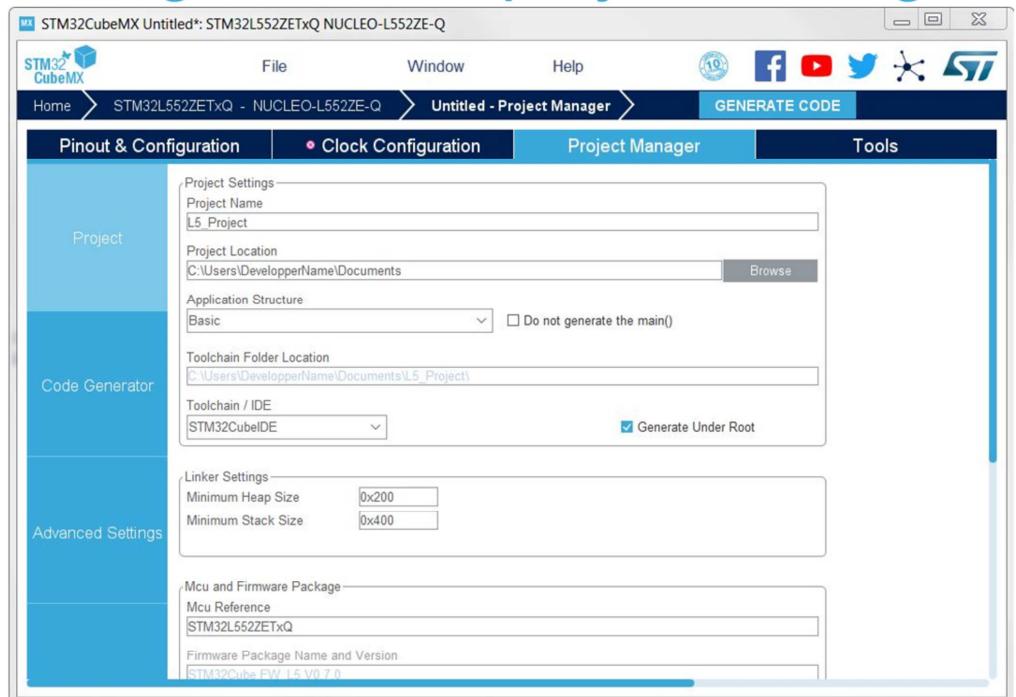
If a configured value is out of bounds, it immediately turns red to highlight a problem.

It also works the other way; enter the required clock speed in a blue frame and the software will attempt to reconfigure multipliers and dividers to provide the requested value. Right-click on a clock value in blue to lock it to prevent modifications.

Code generation project settings

23

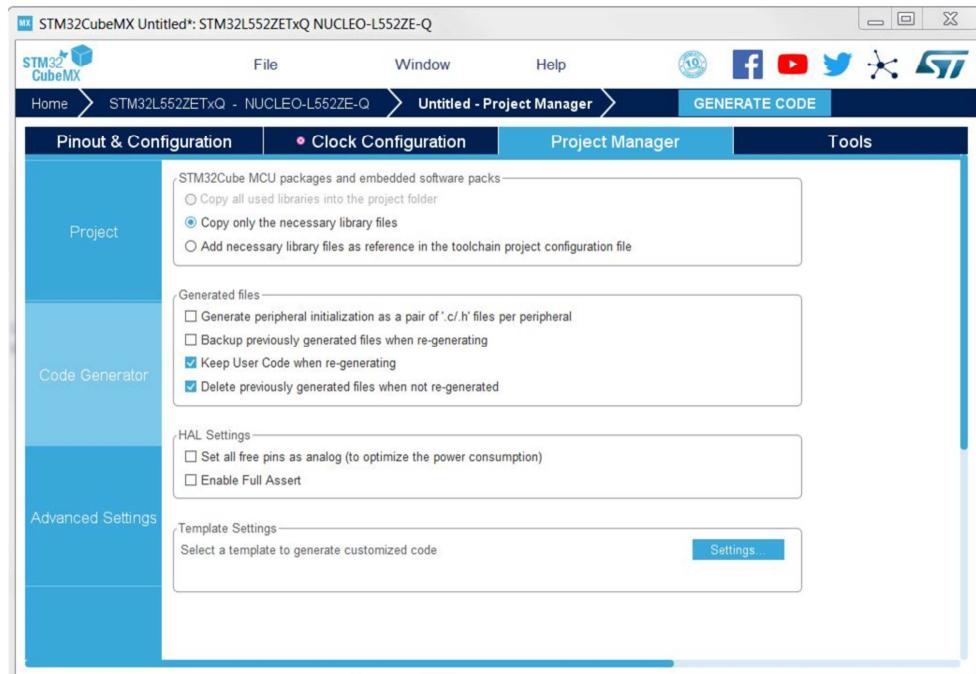
- Name your project when saving
- Browse for project location
- Pick the preferred toolchain
- Review the exact MCU type and library version



This window is available when saving the project (Save as...). The toolchain folder refers to where the workspace for the toolchain will be placed, not the actual toolchain application location. A limited version of this dialog window is also available using the Alt+P shortcut to display Project settings.

Code generation options

24



- Library package
 - Whole library or the necessary part may be copied to the generated project folder.
 - Or keep the library in original place and refer to it from all projects.
- Generated files
 - Each peripheral initialized in separate file or in common source file.
 - Options for working with old files.
 - **The option to keep user code intact is here.**
- HAL settings
 - Setting available pins to analog reduces power consumption, but be careful to **explicitly select SWD/JTAG in pinout**.
 - Full assert is useful for debugging.

The STM32Cube HAL library may be associated with the project in various ways. Select the Copy option if the project should be migrated as a compact package or if there is need to customize the library code. Keeping the library in the original location makes it easier to share the latest version of the library among several projects.

It can also generate the initialization code for all peripherals together in the `stm32xxxx_hal_msp.c` file, or generate one file per peripheral.

Options to back up or delete old files are a matter of the preferred workflow. Keep in mind that the options are tied to the re-generation function. This is also where the “keep user code when re-generating” option is enabled.

The “Set all free pins as analog” setting helps lower power consumption, but if the SWD/JTAG interface is not specifically selected in the Pinout tab, this option will disable the debug interface.

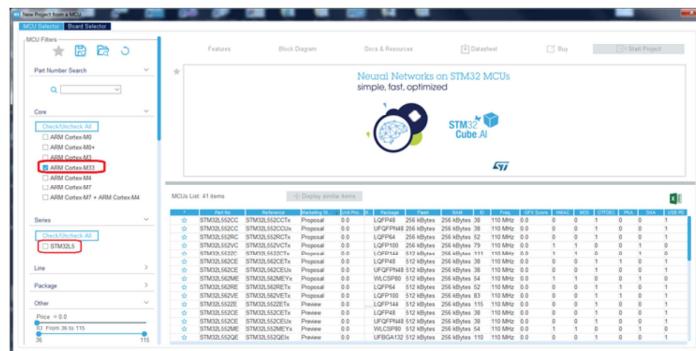
“Full assert” enables checking the parameters passed to the

HAL functions, and may help reveal some bugs in the user code without an excessive debugging effort.

STM32L5 microcontroller selection

25

- MCU Selector
 - New Core M33
 - New Series L5
 - New Part Numbers STM32L552xx/L562xx
- Board Selector
 - New boards
 - STM32L552ZE-EVAL
 - NUCLEO-L552ZE-Q
 - STM32L552E-DISCO



To start a project based on a L5 microcontroller, you can either select the M33 core as embedded core or one of the available part numbers.

You can also start from the selection of one of the 3 board families: EVAL, NUCLEO or DISCO Board.

Once you click on Start Project, a pop up window appears to select whether the TrustZone option is enabled.

STM32L5 Support in STM32CubeMX

26

- Start a new project from MCU or Board
 - Select whether the project is secure ($\text{TZEN} = 1$) or non-secure ($\text{TZEN} = 0$)
- Configure pinout and peripherals with regards to security requirements
 - New peripherals to be taken into account: GTZC, SYS iCache,
 - GTZC (Global Trust Zone controller) peripheral
 - support of secure & privileged peripheral attribute on peripherals (GTZC-TZSC)
 - support of internal SRAM memory partitioning (GTZC-MPCBB)
 - support of external memories partitioning (GTZC-MPCWM)
 - Impact of TZ: Securable IPs, TZ aware IPs
 - Trust Zone aware peripherals: RTC, DMA, DMAMUX, GPIO, EXTI, OTFDEC, PWR, RCC, SYSCFG
 - Assign a peripheral or resource to Secure or Non-Secure context
 - Generate Code for specific IDE
 - New project structure for secure and non secure contexts with IDE options to activate



CubeMX tool supports the new security architecture of the STM32L5 microcontroller.

It enables the secure configuration of TrustZone aware peripherals, the assignment of a resource either to the secure or the non-secure context and provides advanced settings for security specific peripherals such as the Global Trust Zone controller (GTZC) peripheral.

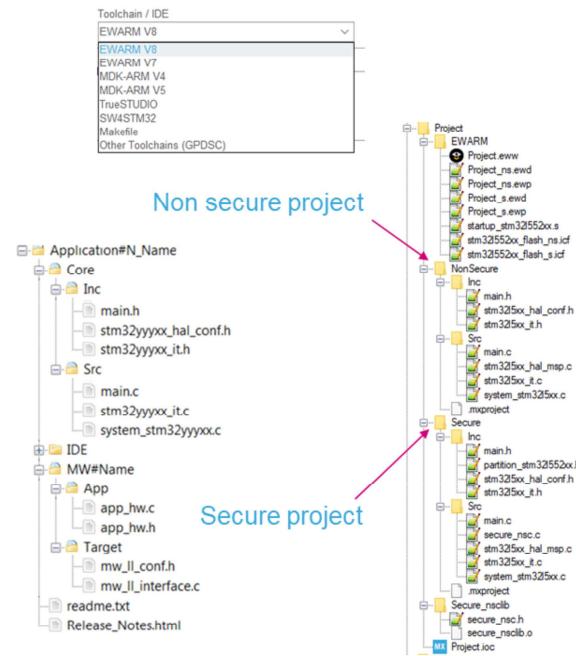
This GTZC peripheral support Secure and privileged peripheral attribute on peripherals, internal SRAM memory and external memories partitioning.

In the end it generates specific code for both secure and non secure projects.

Code generation

27

- Project Generation
 - New IDEs versions support new TZ options to be taken into account (IAR 8.30, Keil 5.25, CubeIDE V1.2.0).
 - Possibility to choose project type secure/non secure
 - Project with TZEN = 0 (default configuration)
 - Legacy project structure
 - Project with TZEN = 1
 - New project structure to be aligned with Firmware specifications
 - Request to generate only Secure or Only NonSecure or both
 - Examples in MX format are planned, are delivered in the Firmware MCU package



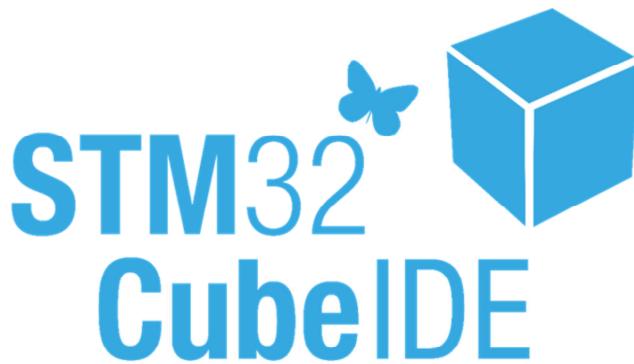
Many new versions of IDEs support TrustZone options.
When TrustZone is enabled ($TZEN = 1$), the generated project structure is made of two sets: a secure and a non secure projects.

Both projects can be downloaded in Flash memory for execution.

The secure project provides the APIs of non-secure entries to the non-secure project.

STM32CubeIDE integration

28



- Free IDE for STM32 MCU
- Integrates STM32CubeMX with all features
- Natural workflow
 - New project starts with MCU selector
 - All CubeMX features are accessible in tab next to source files
 - Code generation accesses sections of source code within one application



While it's still possible to use the STM32CubeIDE normally and generate code for all supported development environments, with STM32CubeIDE there is a shortcut. The new CubeIDE includes an embedded version of the CubeMX, easy to use and full featured. Both MX and IDE users will find an ideal blend of functionality in this merge.

Warning and disclaimer

29

- Designed for use with the entire STM32 family of microcontrollers, at times the STM32CubeMX GUI tool is unable to focus on a specific feature of a particular device.
- **The STM32CubeMX GUI tool is not a replacement for the reference manual or datasheet**
 - Always refer to written documentation for further information!
 - Important features are often available on the product or in the HAL but not in the GUI.
- The GUI helps to start a project and to initialize a working starting configuration – but the configuration can be dynamically changed at runtime (i.e. GPIO, NVIC priority or clock settings).



The user interface is a great tool, it is a universal assistant for all STM32 microcontrollers. However it cannot tackle all the details of each product while providing a useful overview of the diversified STM32 portfolio.

In case of doubt, please refer to the reference manual or datasheet for more detailed and accurate information. Do not hesitate to read the application notes and examples to learn more. It is common practice to start an application with STM32CubeMX to quickly get a prototype working, and then modify the code when dynamic changes are needed (typically to support a different clock or GPIO configuration in the same application).

If the user writes the code within the user areas defined by the STM32CubeMX generator, they can return to the initial STM32CubeMX setup if some modifications need to be applied at the top level of user interface. This typically involves adding GPIO pin configurations, changing the clock, or changing the NVIC Priority, for example.



Power consumption calculator plug-in



When developing embedded applications, low power consumption is often the primary design goal. Extracting power consumption levels from datasheets is a time-consuming and tedious job. The power consumption calculator attempts to simplify the task by extracting datasheet values to a smart User Interface tool, producing informative estimates from configurable scenarios.

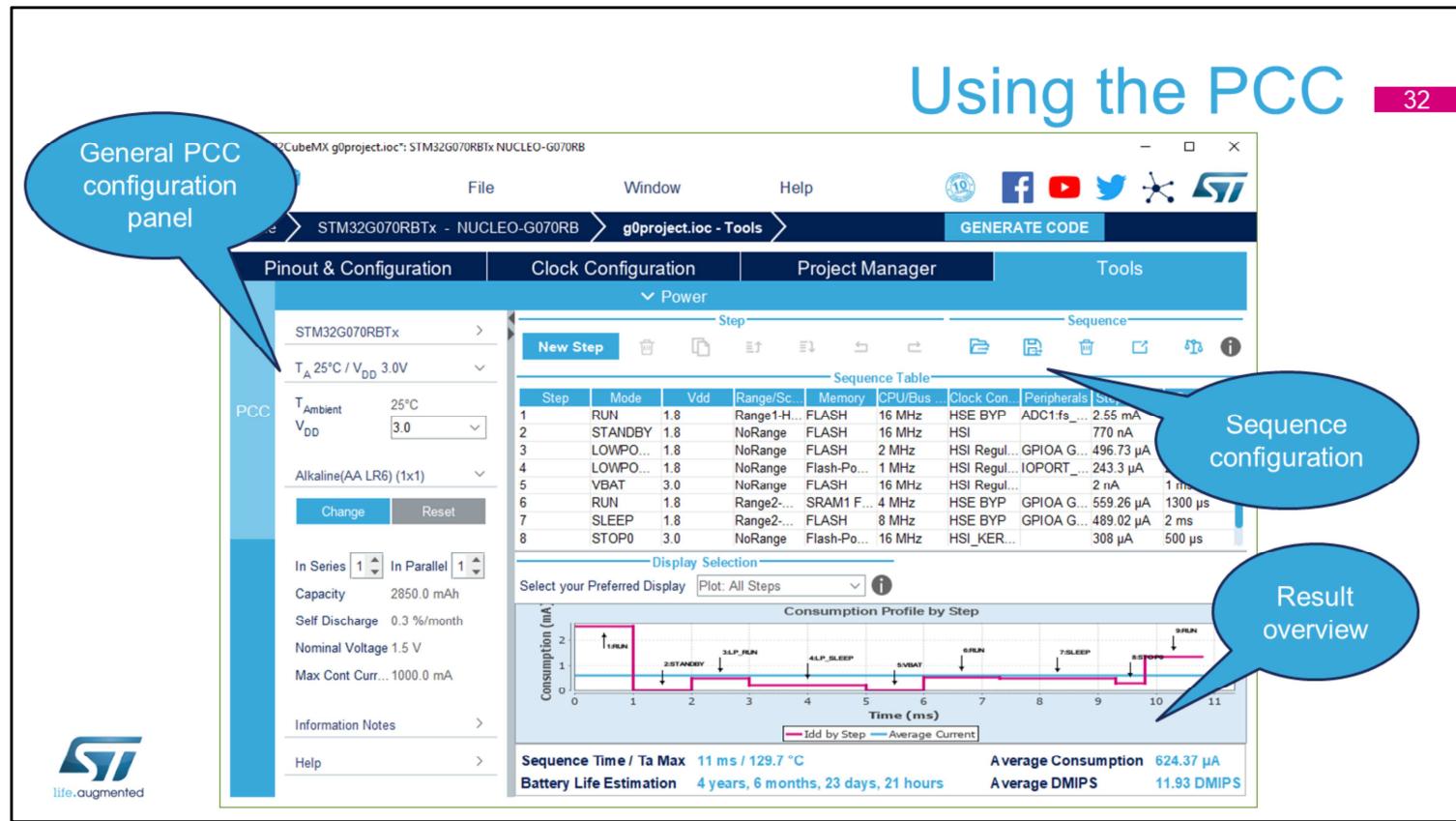
- The Power Consumption Calculator (PCC) uses a database of typical values to estimate power consumption, DMIPS, and battery life of STM32 MCUs.
- It includes a GUI tool integrated into the STM32CubeMX
- Highly configurable scenarios with validity check are available
- Use the battery selector or define a custom battery
- Facilitates comparison with other MCUs or other power options
- Import, export and generate reports



The Power Consumption Calculator can be used to estimate battery lifetime used as either main or supplementary power supplies. Sequences can easily be imported and exported. Illegal state transitions are detected too. It is even possible to compare sequence executions of two different MCUs and generate a report.

Using the PCC

32



The Power Consumption Calculator is the fourth tab in the STM32CubeMX main window. The window is divided into several panes.

The general configuration pane summarizes the typical operating conditions and the MCU type currently selected. The second pane displays the simulation sequence and its controls.

There is no button to execute the simulation; the results are available instantly.

General PCC parameters

33

- MCU selection taken from STM32CubeMX
 - Use the direct link to the datasheet to get more detailed information.
- Parameter selection
 - Temperature and voltage choice may be limited, depending on the selected MCU.
- Battery selection – select existing or define your own
 - Battery is defined by capacity, voltage, self discharge and current limitations.
- Information notes
 - Purpose is to warn about estimation limitations.

STM32G070RBTx

Series STM32G0
Line STM32G0x0 Value Line
[Datasheet](#)

T_A 25°C / V_{DD} 1.8V

T_{Ambient} 25°C
V_{DD} 1.8

Li-MnO2(CR2477) (1x1)

In Series 1 In Parallel 1

Capacity 850.0 mAh
Self Discharge 0.12 %/month
Nominal Voltage 3.0 V
Max Cont Current 2.0 mA

Information Notes >

Help >



The general PCC configuration pane is mostly informative, summarizing the selected MCU and the default power source.

Parameters such as temperature and voltage may even be defined, depending on the MCU selected and the available power consumption data.

The Battery selection pane is used to select or define a battery type. The battery source is optional and, if defined, may be used in only selected sequence steps, simulating a device that works both independently and connected to an external power source.

Information and help sections include useful notes for the user.

Building a sequence

34

- A sequence is a set of ordered steps.

The screenshot shows a software interface for building sequences. At the top, there are three blue speech bubbles with text: 'Create new steps by adding or duplicating existing ones', 'Load existing sequences and adapt them', and 'Compare sequences, even with different MCUs'. Below the bubbles is a toolbar with icons for 'New Step' (highlighted), delete, save, and other sequence management functions. A horizontal line separates the toolbar from the 'Sequence Table'. The table has columns for Step, Mode, Vdd, Range/Scale, Memory, CPU/Bus Freq, Clock Config, Peripherals, Step Current, and Duration. The data in the table is as follows:

Step	Mode	Vdd	Range/Scale	Memory	CPU/Bus Freq	Clock Config	Peripherals	Step Current	Duration
1	RUN	1.8	Range1-High	FLASH	16 MHz	HSE BYP	ADC1:fs_10...	2.55 mA	1 ms
2	STANDBY	1.8	NoRange	FLASH	16 MHz	HSI		770 nA	1 ms
3	LOWPOWER...	1.8	NoRange	FLASH	2 MHz	HSI Regulato...	GPIOA GPI...	496.73 µA	1 ms
4	LOWPOWER...	1.8	NoRange	Flash-Power...	1 MHz	HSI Regulato...	IOPORT_Bu...	243.3 µA	2 ms
5	VBAT	3.0	NoRange	FLASH	16 MHz	HSI Regulato...		2 nA	1 ms
6	RUN	1.8	Range2-Medi...	SRAM1 Flas...	4 MHz	HSE BYP	GPIOA GPI...	559.26 µA	1300 µs
7	SLEEP	1.8	Range2-Medi...	FLASH	8 MHz	HSE BYP	GPIOA GPI...	489.02 µA	2 ms
8	STOP0	3.0	NoRange	Flash-Power...	16 MHz	HSI_KERON		308 µA	500 µs
9	RUN	1.8	Range1-High	FLASH	8 MHz	HSE BYP	ADC1:fs_10...	1.35 mA	1 ms



The “Sequence Table” defines a series of steps, with different durations and configurations. Its length is virtually unlimited.

Sequences can be loaded, modified, and reused.

Individual steps can be duplicated and repositioned within the sequence using the User Interface.

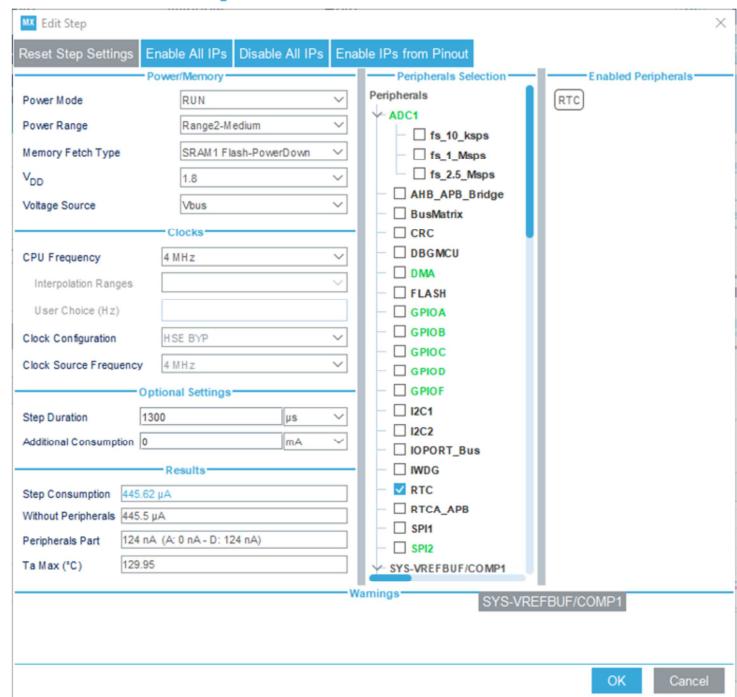
If enabled, all state transitions are checked against basic validity rules to prevent illegal jumps in frequency or power ranges. Problematic steps are instantly highlighted in the sequence table.

Click the “Show log” button to display a detailed explanation. The “Compare” feature displays a comparison of the power and performance in the current scenario with a saved sequence. Different configurations, including different MCUs, can be evaluated against each other.

Power consumption step definition 1/2

35

1. Power mode selection determines availability for peripherals.
2. Regulator setting balances performance and consumption.
3. Select the memory from which the code is executed, as well as prefetch and bus options.
4. Vdd – several typical settings available.
5. This option is present for battery life calculation purposes.



A power step can be added or edited in this dialog window. If the transition checker is enabled, it will preset the new step with allowed values.

The power step is determined by several characteristics, with the power mode being the most important. The availability and characteristics of each power mode are described in the specific reference manual or datasheet. Power mode selection has the most significant impact on the availability of other settings, interfaces and power/performance balance.

The voltage regulator sets the core voltage. At lower voltages, the system clock frequency is limited, but the power consumption is often drastically reduced. Refer to the datasheet for more details.

The address from which the instruction is fetched and the related settings can also influence the power consumption and available clock speeds.

The supply voltage for which power consumption is

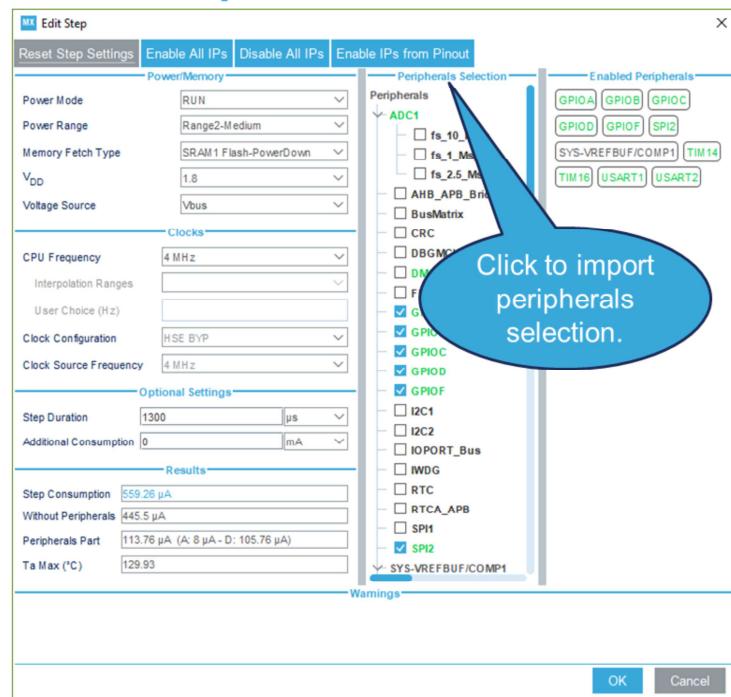
calculated. Use the nearest possible value if the actual voltage is not available.

The last option is present to exclude cases when the device is, for example, connected to the USB in battery drain model. To learn more about power modes, refer to the system power control module training presentation.

Power consumption step definition 2/2

36

- Clock
 - Frequency choice is limited by power consumption range
 - Available clock configurations depend on available data and other settings
- Peripherals
 - Choose clock gating to peripherals
 - Import selection from pinout tab
- Optional settings
 - Additional consumption is represented by estimated pin load



Clock settings may be limited by both power and memory settings, and by available measurement data. See the documentation for a complete list of options.

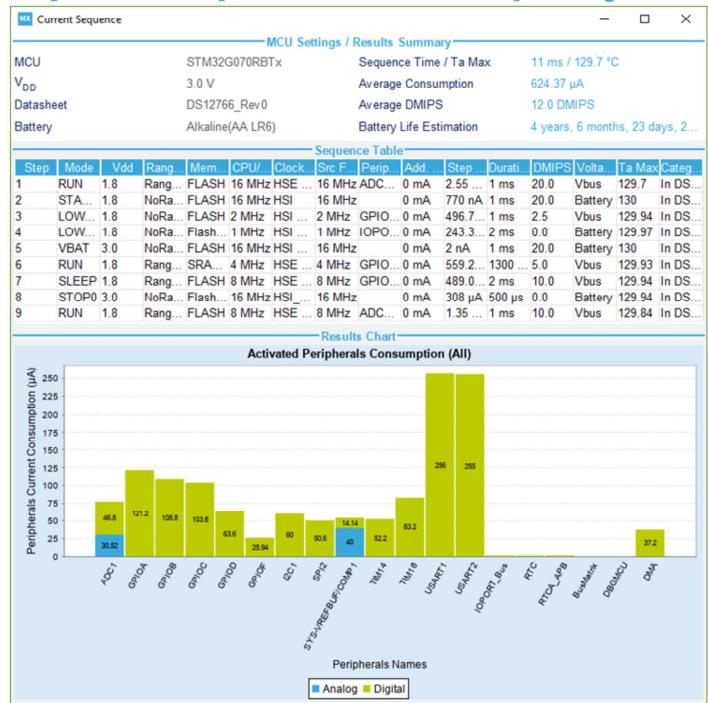
Disabling the clock for an unused peripheral is a sure way to conserve energy. Check peripherals that will be powered during the step duration. By clicking on the “Import” button, all the peripherals that will be initialized by the generated code will be selected.

Finally, define the step duration and any additional consumption settings. Additional consumption may be represented by various loads attached to pins, such as LEDs, buttons, or communication interfaces.

Sequence consumption profile display

37

- It's possible to detach the charts to external display for presentation purposes.
- Several different views selectable
 - Plot current vs time
 - Pie chart
 - Consumption of peripherals



The power consumption calculator features powerful presentation tools. Click on “Ext. Display” button to display the report in a separate window. There are many different ways available to plot the current consumption estimates in graphical form. The default method is based on the power step sequence and the consumption over time.

Alternatively, the percentage of energy spent in different modes can be charted. The pie chart may show the share of each mode, or split to only display Run and Low-power modes.

It is also possible to split the power consumption of peripherals, and plot their power requirements in a graph. You can plot digital peripherals only, analog peripherals only, or a mixed view with both.

Output and generating report

38

- An optional step is to generate a PDF report.
- The PDF report is also available without PCC.
- Complete saved project work includes:
 - Project.ioc
 - Project.pcs
 - Project.pdf
 - Project.txt
 - Project.jpg
 - ... and the generated project for a supported development environment.

myproject Project
Configuration Report

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32G0
Line	STM32G0x0 Value line
MCU	STM32G070RBTx
Datasheet	DS12766 Rev0

6.2. Parameter Selection

Temperature	25
Vdd	1.8

6.3. Battery Selection

Battery	Li-MnO ₂ (CR2477)
Capacity	850.0 mAh
Self Discharge	0.12 %/month
Nominal Voltage	3.0 V
Max Cont Current	2.0 mA
Max Pulse Current	10.0 mA
Cells in series	1
Cells in parallel	1



A file with the extension .ioc contains the static initialization settings. The power sequence is saved using the .pcs extension. A PDF report is generated, along with simplified text and a separate JPG image file with pinout.

- For more details, please refer to the following resources
 - UM1718 – User manual
 - DB2163 – Product specifications
 - TN0072 – Product technical note
 - RN0094 – Product release note
- Download the tool from ST website www.st.com



For more information about using the STM32CubeMX Code Generation Tool, the documents listed in this slide are available for download on www.st.com.

Thank you.