able 22. Alternate function AF0 to AF7<sup>(1)</sup>

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	AF7	USART1/2/3	USART3_CK	USART3_RTS_ DE	1	USART1_RTS_ DE	USART1_CTS_ NSS	USART1_CK	USART1_TX	USART1_RX	1	ı	USART3_TX	USART3_RX	USART3_CK	USART3_CTS_ NSS	USART3_RTS_ DE	
	AF6	SPI3/I2C3/DFS DM1/COMP1/	1	DFSDM1_DATI N0	DFSDM1_CKI N0	SPI3_SCK	SPI3_MISO	SPI3_MOSI	1	1	1		1	-	DFSDM1_DATI	DFSDM1_CKI	DFSDM1_DATI N2	DFSDM1_CKI
(nan)	AF5	SPI1/2/3/12C4/ DFSDM1/ OCTOSPI1	SPI1_NSS	1	1	SPI1_SCK	SPI1_MISO	SPI1_MOSI	I2C4_SCL	I2C4_SDA	DFSDM1_CKOU	SPI2_NSS	SPI2_SCK	1	SPI2_NSS	SPI2_SCK	SPI2_MISO	SPI2 MOSI
to Ar/ ' (contil	AF4	I2C1/2/3/4	1	1	I2C3_SMBA	1	I2C3_SDA	I2C1_SMBA	I2C1_SCL	I2C1_SDA	I2C1_SCL	I2C1_SDA	I2C2_SCL	I2C2_SDA	I2C2_SMBA	I2C2_SCL	I2C2_SDA	1
Table 22. Alternate function AFU to AF7 (continued)	AF3	SPI2/SAI1/I2C4/ USART2/TIM1/8/ OCTOSPI1	TIM8_CH2N	TIM8_CH3N	1	ı	1	OCTOSPI1_NCLK	TIM8_BKIN2	TIM8_BKIN	SAI1_CK1	SAI1_D2	I2C4_SCL	I2C4_SDA	TIM1_BKIN	ı	TIM8_CH2N	TIM8 CH3N
lable 22. Alter	AF2	TIM1/2/3/4/5/ LPTIM3	тімз_снз	TIM3_CH4	1	ı	TIM3_CH1	TIM3_CH2	TIM4_CH1	TIM4_CH2	TIM4_CH3	TIM4_CH4	LPTIM3_OUT	1	-	LPTIM3_IN1	LPTIM3_ETR	1
•	AF1	TIM1/2/5/8/L PTIM1	TIM1_CH2N	TIM1_CH3N	LPTIM1_OUT	TIM2_CH2	1	LPTIM1_IN1	LPTIM1_ETR	LPTIM1_IN2	1	IR_OUT	TIM2_CH3	TIM2_CH4	TIM1_BKIN	TIM1_CH1N	TIM1_CH2N	TIM1 CH3N
	AF0	SYS_AF	ı	ı	ı	JTDO/TRACE SWO	NJTRST	ı	ı	ı	ı		ı	ı	ı	ı	ı	RTC REFIN
		Port	PB0	PB1	PB2	PB3	PB4	PB5	PB6	PB7	PB8	PB9	PB10	PB11	PB12	PB13	PB14	PB15
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Table 22. Alternate function AF0 to AF7<sup>(1)</sup> (continued)

				ומטופ 22. אונפ	Table 44. Aitelliate Iuliction Al V to Al V . (continued)	יוורט) ויי סוף	iliaca)		
		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7
<u> </u>	Port	SYS_AF	TIM1/2/5/8/L PTIM1	TIM1/2/3/4/5/ LPTIM3	SPI2/SAI1/I2C4/ USART2/TIM1/8/ OCTOSPI1	I2C1/2/3/4	SPI1/2/3/I2C4/ DFSDM1/ OCTOSPI1	SPI3/I2C3/DFS DM1/COMP1/	USART1/2/3
	PC0	ı	LPTIM1_IN1	1	OCTOSPI1_IO7	I2C3_SCL	-	ı	1
	PC1	TRACED0	LPTIM1_OUT	1	SPI2_MOSI	I2C3_SDA	-	•	1
	PC2		LPTIM1_IN2	1	1		SPI2_MISO	DFSDM1_CKO UT	1
	РСЗ	ı	LPTIM1_ETR	LPTIM3_OUT	SAI1_D1	1	SPI2_MOSI	1	1
	PC4	,	1	1	-	1	-	1	USART3_TX
	PC5	ı	ı	1	SAI1_D3	ı	-	1	USART3_RX
	PC6	-	-	тімз_сн1	тім8_сн1	•	-	DFSDM1_CKI N3	1
Port	PC7	,	1	TIM3_CH2	TIM8_CH2	,	,	DFSDM1_DATI N3	1
	PC8		ı	тімз_снз	TIM8_CH3	1	ı	ı	1
	PC9	TRACED0	TIM8_BKIN2	TIM3_CH4	TIM8_CH4	1	ı	1	1
	PC10	TRACED1	ı	LPTIM3_ETR	1	ı	ı	SP13_SCK	USART3_TX
	PC11	1	ı	LPTIM3_IN1	1	ı	OCTOSPI1_NCS	SPI3_MISO	USART3_RX
	PC12	TRACED3	1	ı	1	1	1	SPI3_MOSI	USART3_CK
	PC13		1	1	1	ı	-	1	1
	PC14	ı	ı	1	1	ı	-	1	1
	PC15		1		1	1	1	1	ı

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	AF7	USART1/2/3	-	1	USART3_RTS_ DE	USART2_CTS_ NSS	USART2_RTS_ DE	USART2_TX	USART2_RX	USART2_CK	USART3_TX	USART3_RX	USART3_CK	USART3_CTS_ NSS	USART3_RTS_ DE	-	-	ı
-	AF6	SPI3/I2C3/DFS DM1/COMP1/	1	ı	٠	DFSDM1_DATI U	DFSDM1_CKI U	1	DFSDM1_DATI	DFSDM1_CKI	1	-	-	<u> </u>	٠ -	1	-	•
ned)	AF5	SPI1/2/3/I2C4/ DFSDM1/ OCTOSPI1	SPI2_NSS	SPI2_SCK	ı	SPI2_MISO	SPI2_MOSI	1	SPI3_MOSI	-	1	-	-	-	-	-	-	-
to AF7''' (contin	AF4	I2C1/2/3/4	ı	ı	1	1	1	1	-	1	1	-	-	I2C4_SMBA	I2C4_SCL	I2C4_SDA	-	1
lable 22. Alternate function AFU to AF (**) (continued)	AF3	SPI2/SAI1/I2C4/ USART2/TIM1/8/ OCTOSPI1	ı	ı	ı	SPI2_SCK	1	ı	SAI1_D1	1	ı	-	-		1	1	-	-
lable 22. Alter	AF2	TIM1/2/3/4/5/ LPTIM3	1	1	TIM3_ETR	1	1	1		1	1	-	-		TIM4_CH1	TIM4_CH2	TIM4_CH3	TIM4_CH4
•	AF1	TIM1/2/5/8/L PTIM1	ı	1	1	1	,	1	1	ı	1	1	-		1	1	1	ı
•	AF0	SYS_AF	ı	ı	TRACED2	1	1	ı	ı	ı	ı	1	1	ı	1	ı	1	ı
-		Port	PD0	PD1	PD2	PD3	PD4	PD5	PD6	Port PD7	PD8	PD9	PD10	PD11	PD12	PD13	PD14	PD15



Table 22. Alternate function AF0 to AF7<sup>(1)</sup> (continued)

	AF7	USART1/2/3	,	1	1	ı	1	ı	ı	1	ı	ı	1	1	1	1	-	
	AF6	SPI3/I2C3/DFS DM1/COMP1/	-	-		-	DFSDM1_DATI N3	DFSDM1_CKI N3		DFSDM1_DATI	DFSDM1_CKI N2	DFSDM1_CKO UT	-	-	-	-	-	
ned)	AF5	SPI1/2/3/12C4/ DFSDM1/ OCTOSPI1	1	1	1	1	1	,	1	ı	ı	1	1	1	SPI1_NSS	SPI1_SCK	SPI1_MISO	10014 1100
lable 22. Alternate function AFU to AF (**) (continued)	AF4	I2C1/2/3/4	1	1	1	1	1	1	1		1	1	1	1	1	1	1	
rnate tunction AFU	AF3	SPI2/SAI1/I2C4/ USART2/TIM1/8/ OCTOSPI1	1	1	SAI1_CK1	OCTOSPI1_DQS	SAI1_D2	SAI1_CK2	SAI1_D1	1	ı	1	1	1	1	1	TIM1_BKIN2	1417 G
lable 22. Alter	AF2	TIM1/2/3/4/5/ LPTIM3	TIM4_ETR	1	TIM3_ETR	TIM3_CH1	TIM3_CH2	TIM3_CH3	TIM3_CH4	1	1	1	1	1	1	1	TIM1_BKIN2	
	AF1	TIM1/2/5/8/L PTIM1	,	ı	ı	ı	ı	1	ı	TIM1_ETR	TIM1_CH1N	TIM1_CH1	TIM1_CH2N	TIM1_CH2	TIM1_CH3N	TIM1_CH3	TIM1_CH4	MIZIG FINIT
	AF0	SYS_AF	ı	1	TRACECK	TRACED0	TRACED1	TRACED2	TRACED3	ı	ı	1	1	1	1	1	ı	
		Port	PE0	PE1	PE2	PE3	PE4	PE5	PE6	PE7	PE8	PE9	PE10	PE11	PE12	PE13	PE14	Ĭ
		<u> </u>								Port	Ц							

(continued)
AF7 <sup>(</sup>
5
AF0
function
<b>Alternate</b>
Table 22.

		AF0	AF1	AF2	AF2 AF3 AF4	AF4	AF5	AF6	AF7
Δ.	Port	SYS_AF	TIM1/2/5/8/L PTIM1	) IF	SPI2/SAI1/I2C4/ USART2/TIM1/8/ OCTOSPI1	I2C1/2/3/4	SPI1/2/3/I2C4/ DFSDM1/ OCTOSP11	SPI3/I2C3/DFS DM1/COMP1/	USART1/2/3
	PF0	1	1		1	I2C2_SDA		•	1
	PF1	ı	,	1	1	I2C2_SCL	1	1	
	PF2	ı		1	1	I2C2_SMBA	1	1	
	PF3	ı	ı	LPTIM3_IN1	1	1	1	1	
	PF4	ı	1	LPTIM3_ETR	ı	ı	ı	1	
	PF5	ı		LPTIM3_OUT	1	1	1	1	
	PF6	ı	TIM5_ETR	TIM5_CH1	1	1	ı	1	
t	PF7	ı	ı	TIM5_CH2	1	ı	ı	1	ı
Ē	PF8	ı	1	TIM5_CH3	1	1	ı	1	
	PF9	ı	ı	TIM5_CH4	1	ı	ı	1	ı
	PF10	1	1	1	OCTOSP11_CLK	1	1	DFSDM1_CKO UT	ı
	PF11	ı	ı	1	OCTOSPI1_NCLK	1	ı	1	ı
	PF12	-	ı	-	1	1	1	1	ı
	PF13	1	1	-	1	I2C4_SMBA	-	1	
	PF14	-	-	_	-	I2C4_SCL	-	1	•
	PF15	-	1	-	-	I2C4_SDA	1	1	-



Table 22. Alternate function AF0 to AF7<sup>(1)</sup> (continued)

Ī			ומטופ 22. אוונ	lable 22. Aitelliate Iuliction AF ( Collinaeu)		lided)		
	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7
•	SYS_AF	TIM1/2/5/8/L PTIM1	TIM1/2/3/4/5/ LPTIM3	SPI2/SAI1/I2C4/ USART2/TIM1/8/ OCTOSPI1	12C1/2/3/4	SP11/2/3/12C4/ DFSDM1/ OCTOSP11	SPI3/I2C3/DFS DM1/COMP1/	USART1/2/3
PG0		-	1	ı	1	1		-
PG1			1	ı	ı	1	1	1
PG2			1	1	1	SPI1_SCK	ı	1
PG3			1	1	1	SPI1_MISO	1	1
PG4			ı	ı	ı	SPI1_MOSI	ı	1
PG5			1	1	1	SPI1_NSS	1	1
PG6			1	OCTOSPI1_DQS	I2C3_SMBA	1	1	1
PG7	ı	1	1	SAI1_CK1	I2C3_SCL	1	DFSDM1_CKO UT	1
PG8			1	ı	I2C3_SDA	1	1	1
PG9	ı	1	ı	ı	1	1	SPI3_SCK	USART1_TX
PG1 0	ı	LPTIM1_IN1	1	1	1	1	SPI3_MISO	USART1_RX
PG11		LPTIM1_IN2	1	OCTOSPI1_105	1	1	SPI3_MOSI	USART1_CTS_ NSS
PG1 2		LPTIM1_ETR	1	1	1	1	SPI3_NSS	USART1_RTS_ DE
PG1 3	ı	1	1	ı	I2C1_SDA	ı	ı	USART1_CK
PG1 4	ı	1	ı	1	I2C1_SCL	ı	1	-
PG1 5	ı	LPTIM1_OUT	1	1	I2C1_SMBA	1	,	-

Table 22. Alternate function AF0 to AF7<sup>(1)</sup> (continued)

AF3 AF4 AF5 AF6 AF7	SPI2/SAI1/12C4/		1	
AF3	SPI2/SAI1/I2C4/ USART2/TIM1/8/ OCTOSPI1	1	1	1
AF2	TIM1/2/3/4/5/ LPTIM3	ı	1	ı
AF1	TIM1/2/5/8/L PTIM1	,	1	1
AF0	SYS_AF	ı	ı	ı
	Port	ort PH0	PH1	PH3

1. Refer to Table 23 for AF8 to AF15.

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Table 23. Alternate function AF8 to AF15<sup>(1)</sup>

				lable 23. A	lable 23. Alternate function And to Anio	AF6 to AF15			
		AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
<b>-</b>	Port	UART4/5/LPUA RT1/SDMMC1	FDCAN1/ TSC	USB/OCTOSP11	UCPD1	SDMMC1/COMP1 /2/TIM1/8/FMC	SAI1/2/TIM8	TIM2/8/15/16/17/ LPTIM2	EVENTOUT
	PA0	UART4_TX	1	ı	1	1	SAI1_EXTCLK	TIM2_ETR	EVENTOUT
	PA1	UART4_RX	ı	OCTOSPI1_DQS	1	ı	ı	TIM15_CH1N	EVENTOUT
	PA2	LPUART1_TX	,	OCTOSP11_NCS	UCPD1_FRSTX1	1	SAI2_EXTCLK	TIM15_CH1	EVENTOUT
	PA3	LPUART1_RX	1	OCTOSPI1_CLK	1	1	SAI1_MCLK_A	TIM15_CH2	EVENTOUT
	PA4	ı	ı	ı	1	ı	SAI1_FS_B	LPTIM2_OUT	EVENTOUT
	PA5	ı	1	ı	1	ı	ı	LPTIM2_ETR	EVENTOUT
	PA6	LPUART1_CTS _NSS	ı	OCTOSP11_103		TIM1_BKIN	TIM8_BKIN	TIM16_CH1	EVENTOUT
	PA7	ı	1	OCTOSP11_102	1	1	ı	TIM17_CH1	EVENTOUT
Port	PA8	ı	ı	ı	1	ı	SAI1_SCK_A	LPTIM2_OUT	EVENTOUT
(	PA9	1	ı	1	1	ı	SAI1_FS_A	TIM15_BKIN	EVENTOUT
	PA10	ı	1	CRS_SYNC	1	ı	SAI1_SD_A	TIM17_BKIN	EVENTOUT
	PA11		FDCAN1_ RX	USB_DM	,	TIM1_BKIN2	1	,	EVENTOUT
	PA12	-	FDCAN1_ TX	USB_DP	1		ı	ı	EVENTOUT
	PA13	1	1	USB_NOE	1	ı	SAI1_SD_B	-	EVENTOUT
	PA14	-	-	1	1	-	SAI1_FS_B	-	EVENTOUT
	PA15	UART4_RTS_D E	ı	1	•		SAI2_FS_B	•	EVENTOUT

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	AF15	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT
	AF14	TIM2/8/15/16/17/ LPTIM2	1	LPTIM2_IN1	1	-	TIM17_BKIN	TIM16_BKIN	TIM16_CH1N	TIM17_CH1N	TIM16_CH1	TIM17_CH1		1	TIM15_BKIN	TIM15_CH1N	TIM15_CH1	TIM15_CH2
(pən	AF13	SAI1/2/TIM8	SAI1_EXTCLK	1	1	SAI1_SCK_B	SAI1_MCLK_B	SAI1_SD_B	SAI1_FS_B	TIM8_BKIN	SAI1_MCLK_A	SAI1_FS_A	SAI1_SCK_A	1	SAI2_FS_A	SAI2_SCK_A	SAI2_MCLK_A	SAI2_SD_A
o AF15 <sup>(1)</sup> (contin	AF12	SDMMC1/COMP1 /2/TIM1/8/FMC	COMP1_OUT	1	1	-	1	COMP2_OUT	TIM8_BKIN2	FMC_NL	SDMMC1_D4	SDMMC1_D5	COMP1_OUT	COMP2_OUT	1	1	1	-
Table 23. Alternate function AF8 to AF15 $^{(1)}$ (continued)	AF11	UCPD1	ı	1	UCPD1_FRSTX1	1	1	1	ı	1	1	1	1	ı	1	UCPD1_FRSTX2	1	-
Table 23. Alterna	AF10	USB/OCTOSP11	OCTOSPI1_I01	OCTOSPI1_100	OCTOSPI1_DQS	CRS_SYNC	1	ı	ı	ı	ı	ı	OCTOSPI1_CLK	OCTOSPI1_NCS	OCTOSPI1_NCLK	1	ı	-
	AF9	FDCAN1/ TSC	ı	ı	ı	ı	TSC_G2_ IO1	TSC_G2_ IO2	TSC_G2_ IO3	TSC_G2_ IO4	FDCAN1_ RX	FDCAN1_ TX	TSC_SY NC	ı	TSC_G1_ IO1	TSC_G1_ IO2	TSC_G1_ IO3	1
	AF8	UART4/5/LPUA RT1/SDMMC1	1	LPUART1_RTS_ DE	1	1	UART5_RTS_D E	UART5_CTS_N SS	ı	UART4_CTS_N SS	SDMMC1_CKIN	SDMMC1_CDIR	LPUART1_RX	LPUART1_TX	LPUART1_RTS_ DE	LPUART1_CTS _NSS	1	-
		Port	PB0	PB1	PB2	PB3	PB4	PB5	PB6	PB7	B PB8	PB9	PB10	PB11	PB12	PB13	PB14	PB15



Table 23. Alternate function AF8 to AF15<sup>(1)</sup> (continued)

	AF15	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	
	AF14	TIM2/8/15/16/17/ LPTIM2	LPTIM2_IN1	1	ı	LPTIM2_ETR	1	1	ı	ı	ı	TIM8_BKIN2		ı	ı	1	1	
ned)	AF13	SAI1/2/TIM8	SAI2_FS_A	SAI1_SD_A	1	SAI1_SD_A	1	1	SAIZ_MCLK_A	SAIZ_MCLK_B	1	SAI2_EXTCLK	SAI2_SCK_B	SAI2_MCLK_B	SAI2_SD_B	1	1	
o AF15''' (contin	AF12	SDMMC1/COMP1 /2/TIM1/8/FMC	SDMMC1_D5	-	1	1	1	1	SDMMC1_D6	SDMMC1_D7	SDMMC1_D0	SDMMC1_D1	SDMMC1_D2	SDMMC1_D3	SDMMC1_CK	ı	1	
te tunction AF8 to	AF11	UCPD1	1	-	1	1	1	ı	1	1	ı			UCPD1_FRSTX2	1	ı	-	
Table 23. Alternate function AF8 to AF1511 (continued)	AF10	USB/OCTOSPI1	-	OCTOSP11_IO4	OCTOSPI1_105	OCTOSPI1_106	OCTOSPI1_I07	1	-	-	1	USB_NOE	-	1	-	1	-	
	AF9	FDCAN1/ TSC	,	-	TSC_G3_ IO1	TSC_G1_ IO4	1	ı	TSC_G4_ IO1	TSC_G4_ IO2	TSC_G4_ IO3	TSC_G4_ IO4	TSC_G3_ IO2	TSC_G3_ IO3	TSC_G3_ IO4	ı	1	
	AF8	UART4/5/LPUA RT1/SDMMC1	LPUART1_RX	LPUART1_TX		1	1	1	SDMMC1_D0DI R	SDMMC1_D123 DIR	ı	•	UART4_TX	UART4_RX	UART5_TX	ı	1	
		Port	PC0	PC1	PC2	PC3	PC4	PC5	PC6	PC7	C PC8	PC9	PC10	PC11	PC12	PC13	PC14	1

(continued)
8 to AF15 <sup>(1)</sup>
tion AF8 to A
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23. Alter
Table

			lable 25. Aiteilla	מווכנוסוו או ס				
	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
3 ~	UART4/5/LPUA RT1/SDMMC1	FDCAN1/ TSC	USB/OCTOSPI1	UCPD1	SDMMC1/COMP1 /2/TIM1/8/FMC	SAI1/2/TIM8	TIM2/8/15/16/17/ LPTIM2	EVENTOUT
	1	FDCAN1_ RX	ı	,	FMC_D2	1	1	EVENTOUT
	,	FDCAN1_ TX	ı	1	FMC_D3	1	ı	EVENTOUT
	UART5_RX	TSC_SY NC	1	1	SDMMC1_CMD	1	1	EVENTOUT
<u></u>		1	1	1	FMC_CLK	1	1	EVENTOUT
<u></u>		1	OCTOSPI1_I04	1	FMC_NOE	ı	1	EVENTOUT
		1	OCTOSPI1_105	1	FMC_NWE	1	1	EVENTOUT
	1	1	OCTOSP11_106	1	FMC_NWAIT	SAI1_SD_A	1	EVENTOUT
	1	ı	OCTOSPI1_I07	ı	FMC_NCE/FMC_ NE1	1	ı	EVENTOUT
	1	ı	ı	1	FMC_D13	1	1	EVENTOUT
	1	-	-	1	FMC_D14	SAI2_MCLK_A	_	EVENTOUT
	ı	TSC_G6_ IO1	-		FMC_D15	SAI2_SCK_A	•	EVENTOUT
	ı	TSC_G6_ IO2	ı	ı	FMC_A16	SAI2_SD_A	LPTIM2_ETR	EVENTOUT
	1	TSC_G6_ IO3	-	-	FMC_A17	SAI2_FS_A	LPTIM2_IN1	EVENTOUT
	ı	TSC_G6_ IO4	-		FMC_A18	1	LPTIM2_OUT	EVENTOUT
	1	-	-	-	FMC_D0	1	-	EVENTOUT
		-	-	•	FMC_D1	-	-	EVENTOUT



Table 23. Alternate function AF8 to AF15<sup>(1)</sup> (continued)

				lable 23. Alterna	te runction AF8	lable 23. Alternate function AF8 to AF15'' (continued)	nea)		
		AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
т.	Port	UART4/5/LPUA RT1/SDMMC1	FDCAN1/ TSC	USB/OCTOSPI1	UCPD1	SDMMC1/COMP1 /2/TIM1/8/FMC	SAI1/2/TIM8	TIM2/8/15/16/17/ LPTIM2	EVENTOUT
	DE0	1	,	1	1	FMC_NBL0	1	TIM16_CH1	EVENTOUT
. —	PE1	1	1	1	1	FMC_NBL1	1	TIM17_CH1	EVENTOUT
	PE2	•	TSC_G7_ IO1	•	•	FMC_A23	SAI1_MCLK_A	-	EVENTOUT
	PE3	•	TSC_G7_ IO2	•	•	FMC_A19	SAI1_SD_B	•	EVENTOUT
	PE4	•	TSC_G7_ IO3	-	•	FMC_A20	SAI1_FS_A	-	EVENTOUT
	PE5	•	TSC_G7_ IO4	•	•	FMC_A21	SAI1_SCK_A	-	EVENTOUT
	PE6	1	-	-	-	FMC_A22	SAI1_SD_A	-	EVENTOUT
Port	PE7	1	1		•	FMC_D4	SAI1_SD_B	-	EVENTOUT
Ш	PE8	1	1		-	FMC_D5	SAI1_SCK_B	-	EVENTOUT
	PE9	1	1	OCTOSP11_NCLK	•	FMC_D6	SAI1_FS_B	-	EVENTOUT
	PE10	ı	TSC_G5_ IO1	OCTOSPI1_CLK		FMC_D7	SAI1_MCLK_B		EVENTOUT
	PE11	•	TSC_G5_ IO2	OCTOSP11_NCS	•	FMC_D8	-	•	EVENTOUT
	PE12	•	TSC_G5_ IO3	OCTOSP11_100	•	FMC_D9	-		EVENTOUT
	PE13	•	TSC_G5_ IO4	OCTOSP11_101	•	FMC_D10	-	-	EVENTOUT
	PE14	1	1	OCTOSP11_102	'	FMC_D11	-	-	EVENTOUT
	PE15	ı	ı	OCTOSP11_103	1	FMC_D12		-	EVENTOUT

Table 23. Alternate function AF8 to AF15<sup>(1)</sup> (continued)

	ıc	JUT	JUC	TUC	TÜC	TUC	Ţ	Ţ	TÜC	JUC	Ţ	JUC	TUC	JUC	TUC	JUT	JUT	JUT
	AF15	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT	EVENTOUT
	AF14	TIM2/8/15/16/17/ LPTIM2	-	ı	ı	1	ı	ı	1	ı	ı	TIM15_CH1	TIM15_CH2	-	1	-	-	-
nen)	AF13	SAI1/2/TIM8	-	ı	ı	ı	ı	ı	SAI1_SD_B	SAI1_MCLK_B	SAI1_SCK_B	SAI1_FS_B	SAI1_D3	1	ı	-	-	-
ומטופ בט: אונפווומנפ ומווכנוסוו או ס נס או וא (כסוונווומפת)	AF12	SDMMC1/COMP1 /2/TIM1/8/FMC	FMC_A0	FMC_A1	FMC_A2	FMC_A3	FMC_A4	FMC_A5	1	ı	1	ı	1	ı	FMC_A6	FMC_A7	FMC_A8	FMC_A9
	AF11	UCPD1	1	1	1	1	1	1	1	1	1	1	1	1	1	-		•
ומטוכ בס. הונכו וופ	AF10	USB/OCTOSP11	ı	ı	ı	1	ı	ı	OCTOSPI1_103	OCTOSPI1_IO2	OCTOSPI1_IO0	OCTOSP11_101	1	ı	1	1	•	1
	AF9	FDCAN1/ TSC	,	,	ı	1	1	ı	1	,	ı		1	ı	1	-	TSC_G8_ IO1	TSC_G8_ IO2
	AF8	UART4/5/LPUA RT1/SDMMC1	-	1	1	ı	1	1	1	1	1	1	1	1	ı	-	-	-
		Port	PF0	PF1	PF2	PF3	PF4	PF5	PF6	PF7	PF8	PF9	PF10	PF11	PF12	PF13	PF14	PF15
											Port	_						



Table 23. Alternate function AF8 to AF15<sup>(1)</sup> (continued)

AF	α	δ <u>Η</u> Δ	AE10	AF10 AF11 AF12	ΔΕ12		AE14	AE15
Ar8	AF9		AF10	AFTT	AF12	AF13	AF14	AF15
UART4/5/LPUA FDCAN1/ USB/ RT1/SDMMC1 TSC	_	USB/	USB/OCTOSP11	UCPD1	SDMMC1/COMP1 /2/TIM1/8/FMC	SAI1/2/TIM8	TIM2/8/15/16/17/ LPTIM2	EVENTOUT
TSCG8 	TSC_G8_ IO3		ı	1	FMC_A10	1		EVENTOUT
TSC_G8_ - 104	TSC_G8_ IO4		ı	1	FMC_A11	ı		EVENTOUT
	1		ı	1	FMC_A12	SAI2_SCK_B	1	EVENTOUT
ı	1		ı	1	FMC_A13	SAI2_FS_B	ı	EVENTOUT
1	ı			1	FMC_A14	SAI2_MCLK_B	ı	EVENTOUT
LPUART1_CTS	-	1		1	FMC_A15	SAI2_SD_B		EVENTOUT
LPUART1_RTS DE -	1	1		UCPD1_FRSTX1	1	ı	ı	EVENTOUT
LPUART1_TX		1		UCPD1_FRSTX2	FMC_INT	SAI1_MCLK_A	ı	EVENTOUT
LPUART1_RX		1		1	-	1	ı	EVENTOUT
	1	1		1	FMC_NCE/FMC_ NE2	SAI2_SCK_A	TIM15_CH1N	EVENTOUT
1		1		1	FMC_NE3	SAI2_FS_A	TIM15_CH1	EVENTOUT
1	1	1		1	-	SAI2_MCLK_A	TIM15_CH2	EVENTOUT
-	-	-		1	FMC_NE4	SAI2_SD_A	-	EVENTOUT
	-	-		1	FMC_A24	-	-	EVENTOUT
				1	FMC_A25	1	1	EVENTOUT
1	-	•		1	-	•	•	EVENTOUT

Table 23. Alternate function AF8 to AF15<sup>(1)</sup> (continued)

							, ca,		
		AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
<b>-</b>	Port	UART4/5/LPUA FDCAN1/ RT1/SDMMC1 TSC	FDCAN1/ TSC	USB/OCTOSP11	UCPD1	SDMMC1/COMP1 /2/TIM1/8/FMC	SAI1/2/TIM8	TIM2/8/15/16/17/ LPTIM2	EVENTOUT
	0НА	-	-	-	•	-	1	-	EVENTOUT
HOT H	PH1	-	ı	1	•	1	1	-	EVENTOUT
	БНЗ	-	ı	-	1	1	1	-	EVENTOUT

1. Refer to Table 22 for AF0 to AF7.