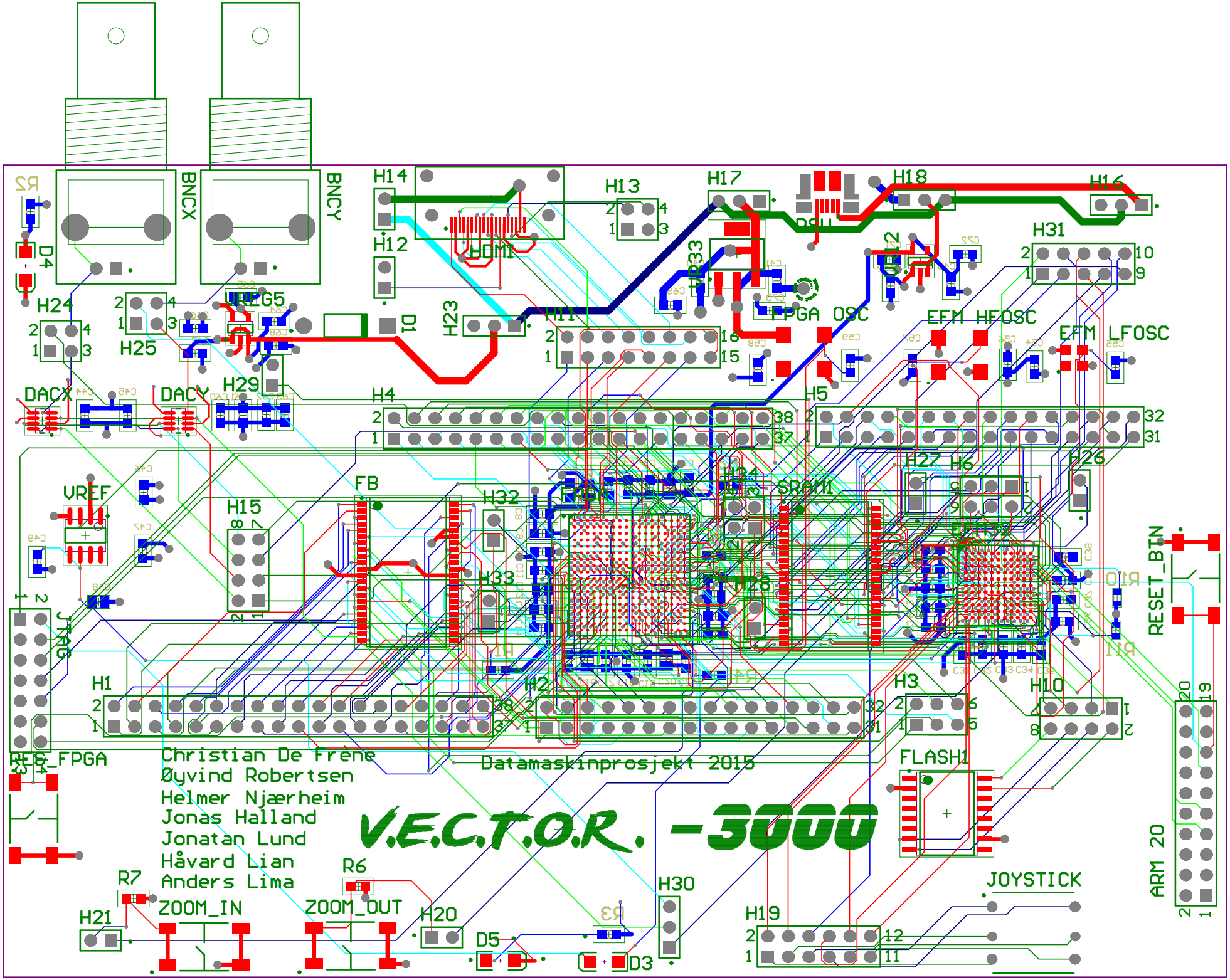
BNCX
BNCY
H14
HDMI
H13
H17
PSU
H18
H16
H12
H31
D4
H24
H25
VREG5
D1
H23
H11
FPGA OSC
EFM HFOSC
EFM LFOSC
DACX
DACY
H29
H4
H5
VREF
H15
FB
H32
H33
SRAM
H27
H6
H26
RESET_BTN
JTAG
H1
H2
H3
H10
RLS_FPGA
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V.E.C.T.O.R. -3000
FLASH1
ARM 20
R7
R6
H21
ZOOM_IN
ZOOM_OUT
H20
D5
D3
H30
H19
JOYSTICK