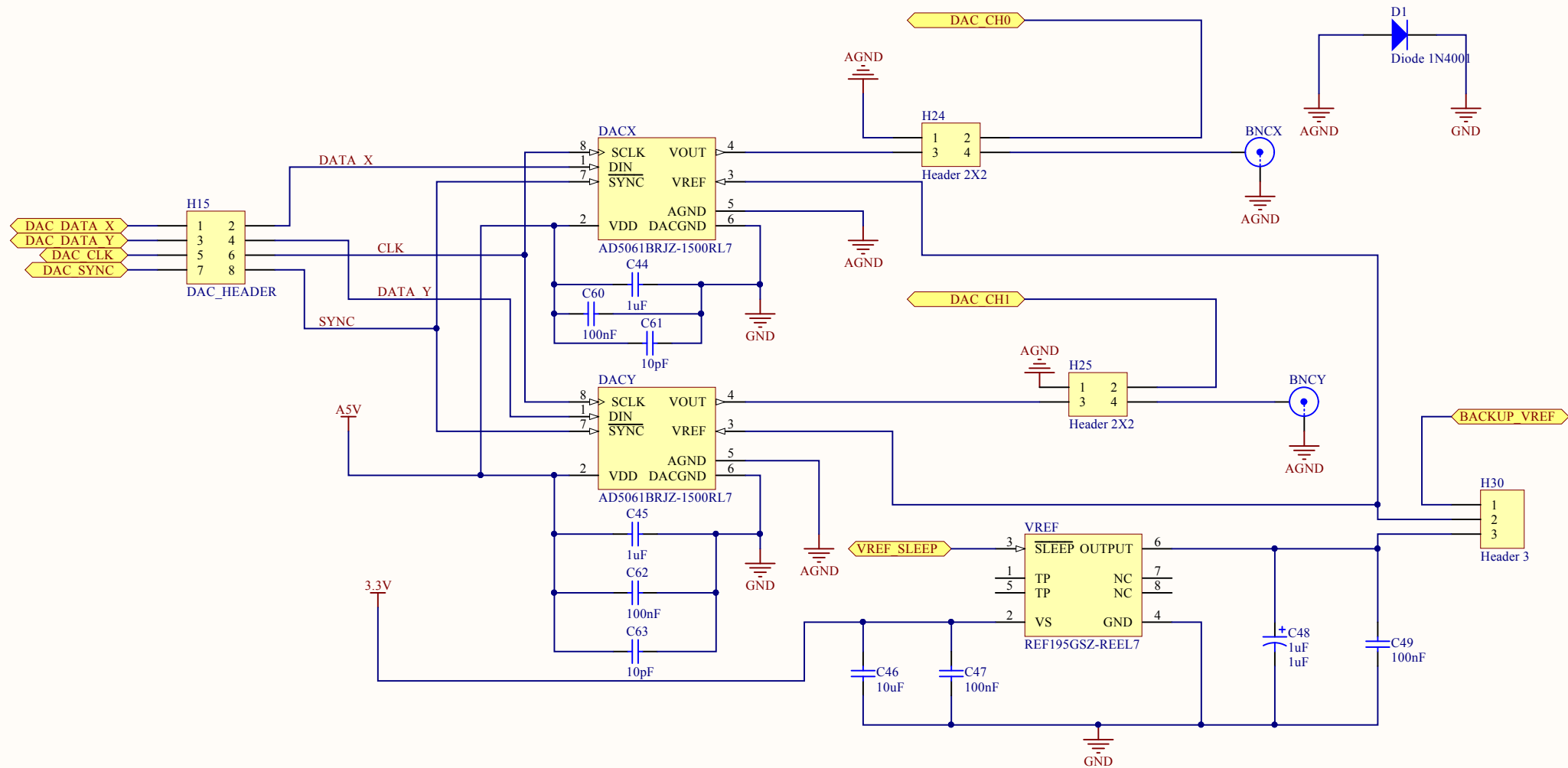
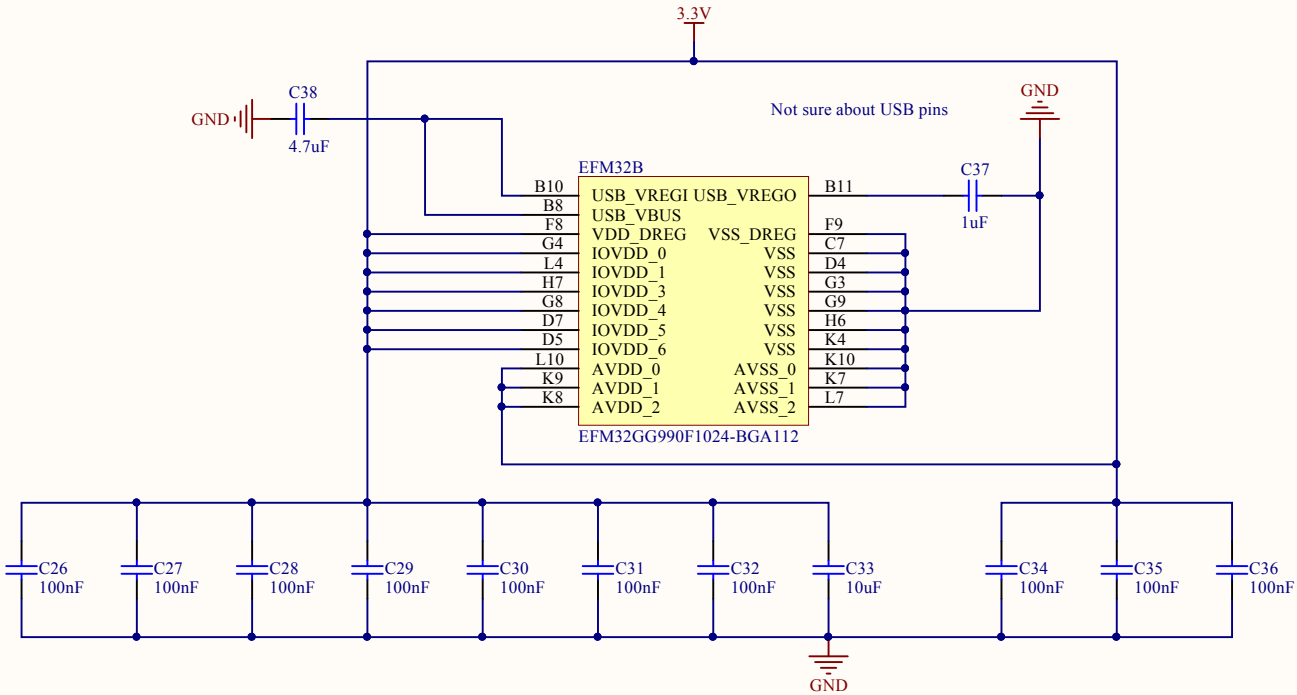


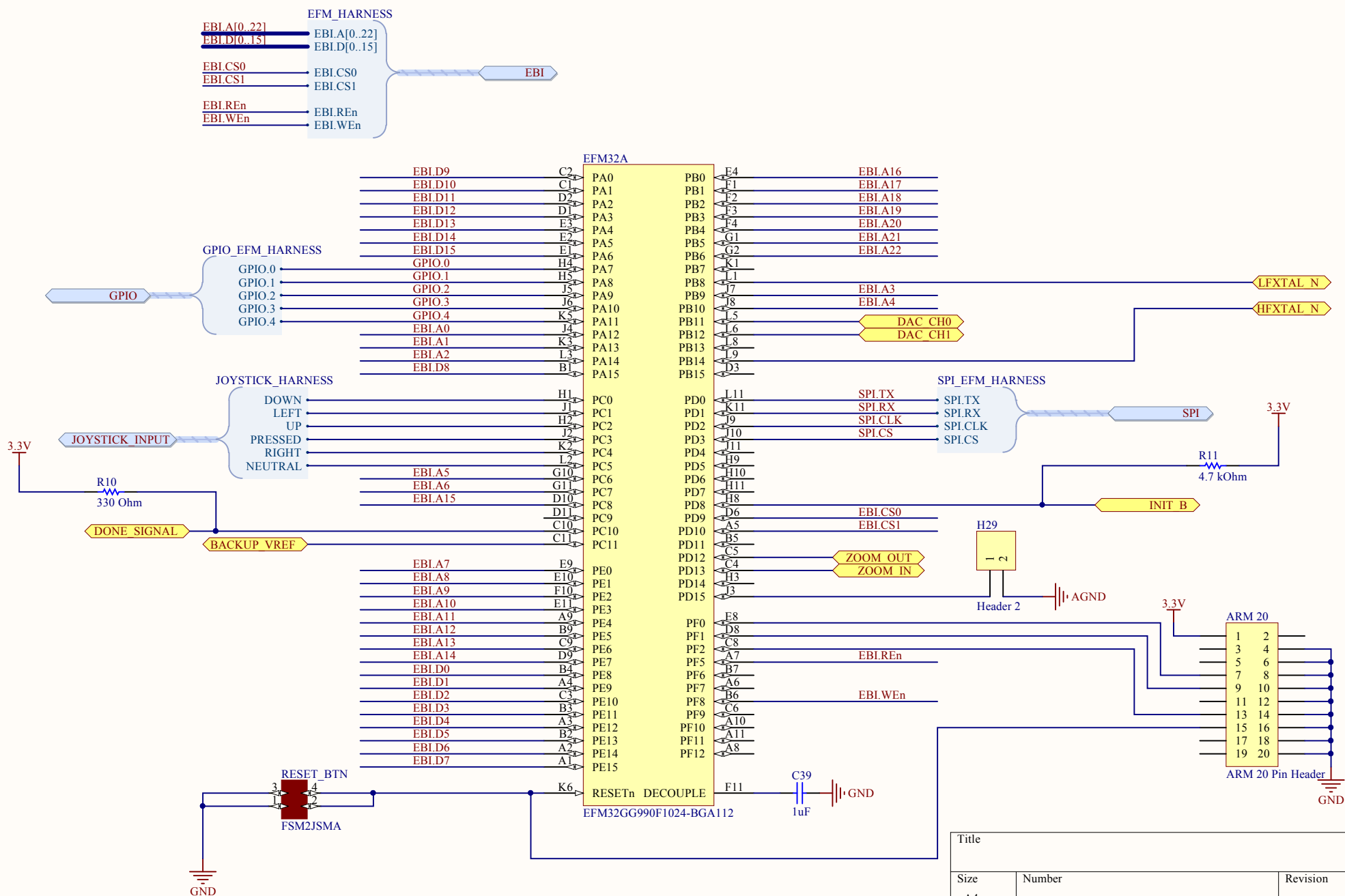
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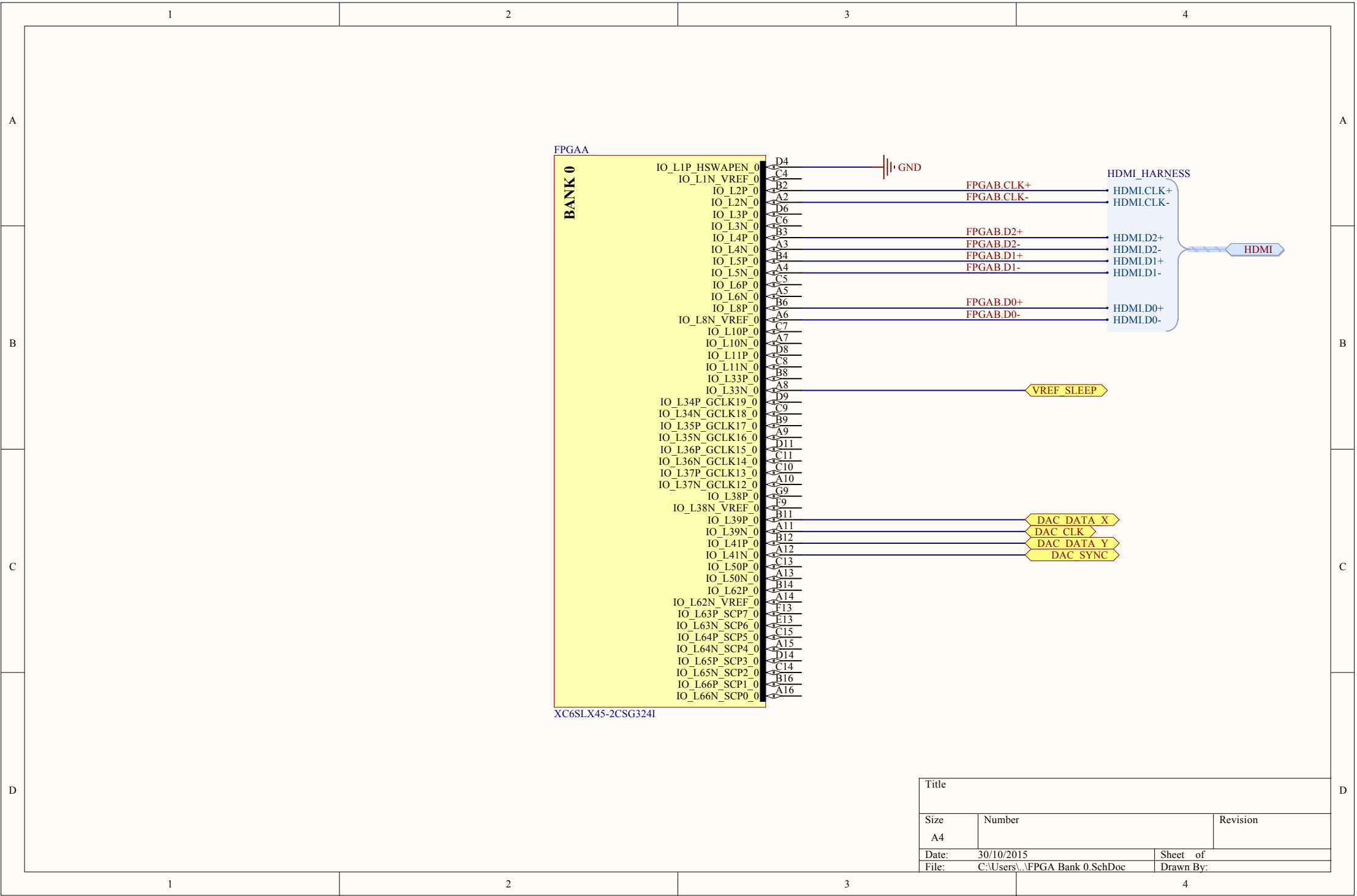
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File:	C:\Users\...\EFM Power.SchDoc	Drawn By:



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FPGAB

BANK 1

IO_L1P_A25_1	F15
IO_L1N_A24_VREF_1	F16
IO_L29P_A23_M1A13_1	C17
IO_L29N_A22_M1A14_1	C18
IO_L30P_A21_M1RESET_1	F14
IO_L30N_A20_M1A11_1	G14
IO_L31P_A19_M1CKE_1	D17
IO_L31N_A18_M1A12_1	D18
IO_L32P_A17_M1A8_1	H12
IO_L32N_A16_M1A9_1	G13
IO_L33P_A15_M1A10_1	E16
IO_L33N_A14_M1A4_1	E18
IO_L34P_A13_M1WE_1	K12
IO_L34N_A12_M1BA2_1	K13
IO_L35P_A11_M1A7_1	F17
IO_L35N_A10_M1A2_1	F18
IO_L36P_A9_M1BA0_1	H13
IO_L36N_A8_M1BA1_1	H14
IO_L37P_A7_M1A0_1	H15
IO_L37N_A6_M1A1_1	H16
IO_L38P_A5_M1CLK_1	G16
IO_L38N_A4_M1CLKN_1	G18
IO_L39P_M1A3_1	J13
IO_L39N_M1ODT_1	K14
IO_L40P_GCLK11_M1A5_1	L12
IO_L40N_GCLK10_M1A6_1	L13
IO_L41P_GCLK9_IRDY1_M1RASN_1	K15
IO_L41N_GCLK8_M1CASN_1	K16
IO_L42P_GCLK7_M1UDM_1	L15
IO_L42N_GCLK6_TRDY1_M1LDM_1	L16
IO_L43P_GCLK5_M1DQ4_1	H17
IO_L43N_GCLK4_M1DQ5_1	H18
IO_L44P_A3_M1DQ6_1	J16
IO_L44N_A2_M1DQ7_1	J18
IO_L45P_A1_M1LDQS_1	K17
IO_L45N_A0_M1LDQSN_1	K18
IO_L46P_FCS_B_M1DQ2_1	L17
IO_L46N_FOE_B_M1DQ3_1	L18
IO_L47P_FWE_B_M1DQ0_1	M16
IO_L47N_LDC_M1DQ1_1	M18
IO_L48P_HDC_M1DQ8_1	N17
IO_L48N_M1DQ9_1	N18
IO_L49P_M1DQ10_1	P17
IO_L49N_M1DQ11_1	P18
IO_L50P_M1UDQS_1	N15
IO_L50N_M1UDQSN_1	N16
IO_L51P_M1DQ12_1	T17
IO_L51N_M1DQ13_1	T18
IO_L52P_M1DQ14_1	U17
IO_L52N_M1DQ15_1	U18
IO_L53P_1	M14
IO_L53N_VREF_1	N14
IO_L61P_1	L14
IO_L61N_1	M13
IO_L74P_AWAKE_1	P15
IO_L74N_DOUT_BUSY_1	P16

XC6SLX45-2CSG324I

FPGAB.A0

FPGAB.A1

FPGAB.A2

FPGAB.A3

FPGAB.A4

FPGAB.A5

FPGAB.A6

FPGAB.A7

FPGAB.A8

FPGAB.A9

FPGAB.A10

FPGAB.A11

FPGAB.A12

FPGAB.A13

FPGAB.A14

FPGAB.A15

FPGAB.A16

FPGAB.A17

FPGAB.A18

FPGAB.A19

FPGAB.A20

FPGAB.A21

FPGAB.A22

FPGAB.D0

FPGAB.D1

FPGAB.D2

FPGAB.D3

FPGAB.D4

FPGAB.D5

FPGAB.D6

FPGAB.D7

FPGAB.D8

FPGAB.D9

FPGAB.D10

FPGAB.D11

FPGAB.D12

FPGAB.D13

FPGAB.D14

FPGAB.D15

FPGAB.WEn

FPGAB.REn

FPGAB.CS1

FPGAB_HARNESS

FPGAB.A[0..22] EBI.A[0..22]

FPGAB.D[0..15] EBI.D[0..15]

FPGAB.REn EBI.REn

FPGAB.WEn EBI.WEn

FPGAB.CS1 EBI.CS1

EBI

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FPGAC

BANK 2

IO_L1P_CCLK_2
IO_L1N_M0_CMPMISO_2
IO_L2P_CMPCLK_2
IO_L2N_CMPMISO_2
IO_L3P_D0_DIN_MISO_MISO1_2
IO_L3N_MOSI_CSI_B_MISO0_2
IO_L5P_2
IO_L5N_2
IO_L12P_D1_MISO2_2
IO_L12N_D2_MISO3_2
IO_L13P_M1_2
IO_L13N_D10_2
IO_L14P_D11_2
IO_L14N_D12_2
IO_L15P_2
IO_L15N_2
IO_L16P_2
IO_L16N_VREF_2
IO_L19P_2
IO_L19N_2
IO_L20P_2
IO_L20N_2
IO_L22P_2
IO_L22N_2
IO_L23P_2
IO_L23N_2
IO_L29P_GCLK3_2
IO_L29N_GCLK2_2
IO_L30P_GCLK1_D13_2
IO_L30N_GCLK0_USERCCLK_2
IO_L31P_GCLK31_D14_2
IO_L31N_GCLK30_D15_2
IO_L32P_GCLK29_2
IO_L32N_GCLK28_2
IO_L40P_2
IO_L40N_2
IO_L41P_2
IO_L41N_VREF_2
IO_L43P_2
IO_L43N_2
IO_L44P_2
IO_L44N_2
IO_L45P_2
IO_L45N_2
IO_L46P_2
IO_L46N_2
IO_L47P_2
IO_L47N_2
IO_L48P_D7_2
IO_L48N_RDWR_B_VREF_2
IO_L49P_D3_2
IO_L49N_D4_2
IO_L62P_D5_2
IO_L62N_D6_2
IO_L63P_2
IO_L63N_2
IO_L64P_D8_2
IO_L64N_D9_2
IO_L65P_INIT_B_2
IO_L65N_CSO_B_2

XC6SLX45-2CSG324I

R15
T15
U16
V16
R13
T13
U15
V15
T14
V14
N12
P12
U13
V13
M11
N11
R11
T11
T12
V12
N10
P11
M10
N9
U11
V11
R10
T10
U10
V10
R8
T8
T9
V9
M8
N8
U8
V8
U7
V7
N7
P8
T6
V6
R7
T7
N6
P7
R5
T5
U5
V5
R3
T3
T4
V4
N5
P6
U3
V3

EFM CLK

EFM RX

SPI_FPGA_HARNESS

SPI.RX

SPI.CLK

SPI

H34
1 2
3 4
Header 2X2

3.3V

R9
10 kOhm

3.3V

R8
10 kOhm

H31

1 2
3 4
5 6
7 8
9 10
Header 5X2

GPIO_FPGA_HARNESS

GPIO.0

GPIO.1

GPIO.2

GPIO.3

GPIO.4

GPIO

CLK IN

H33

1 2
Header 2

INIT B

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A4		
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A

A

B

B

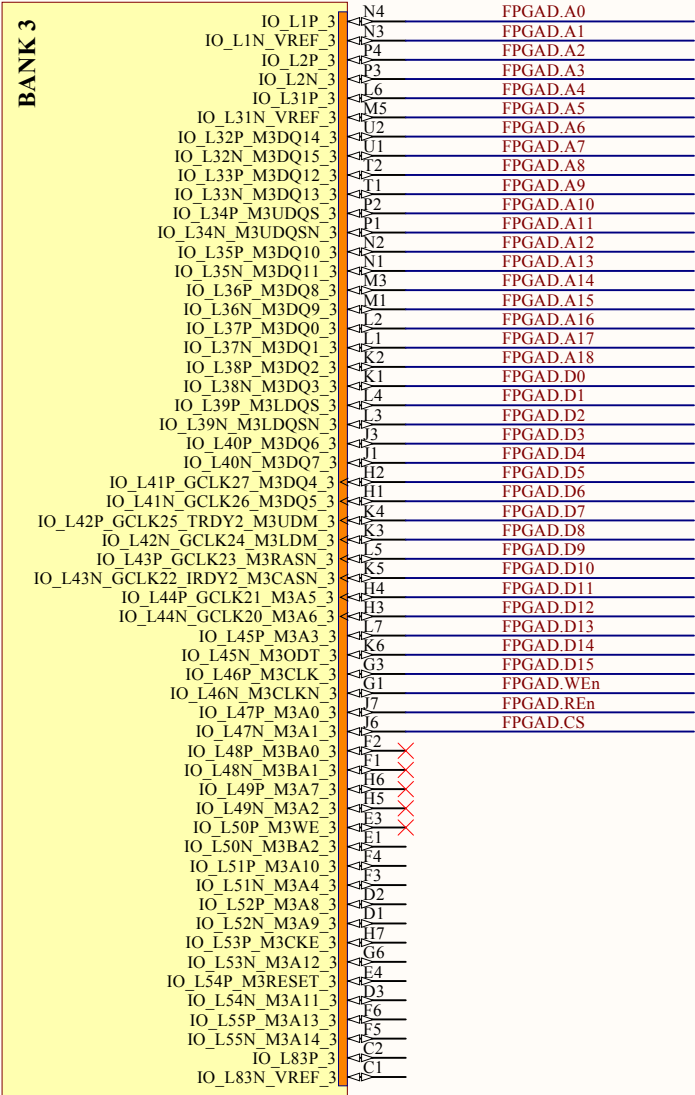
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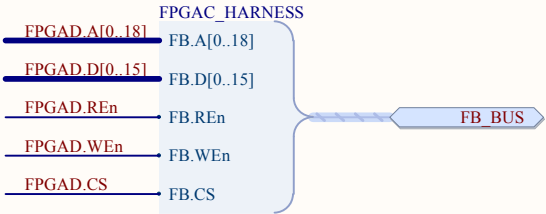
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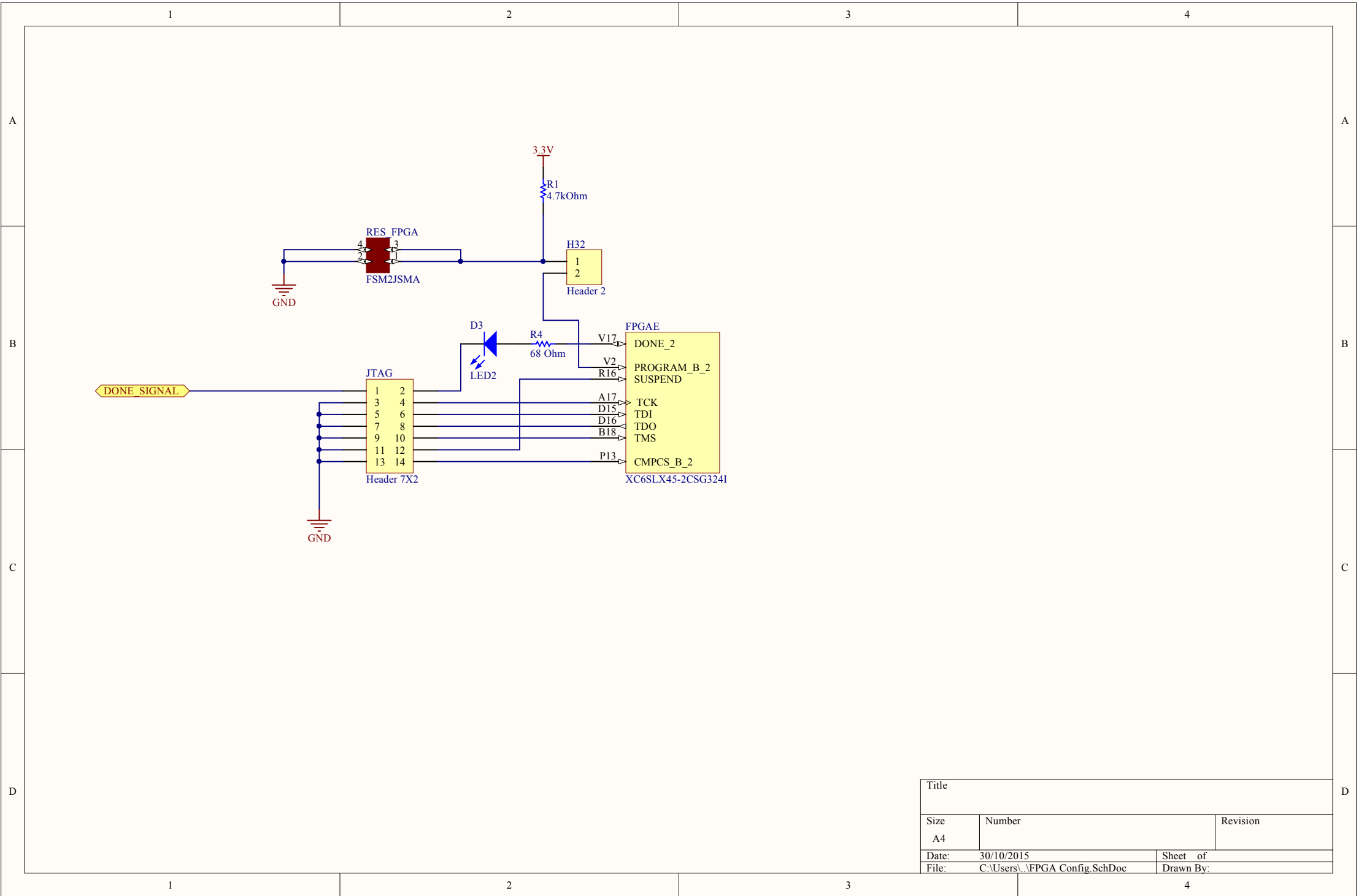
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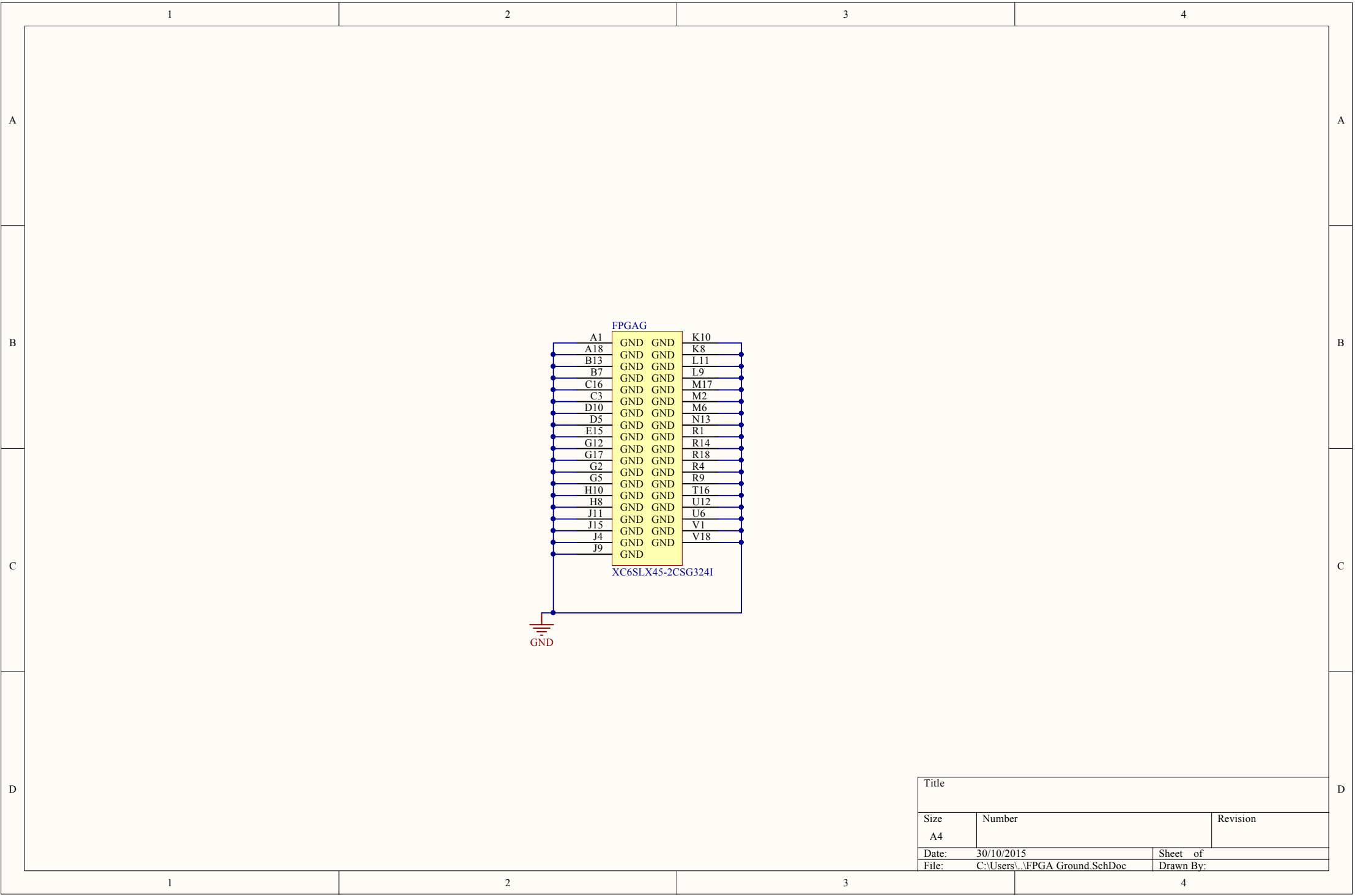
XC6SLX45-2CSG324I



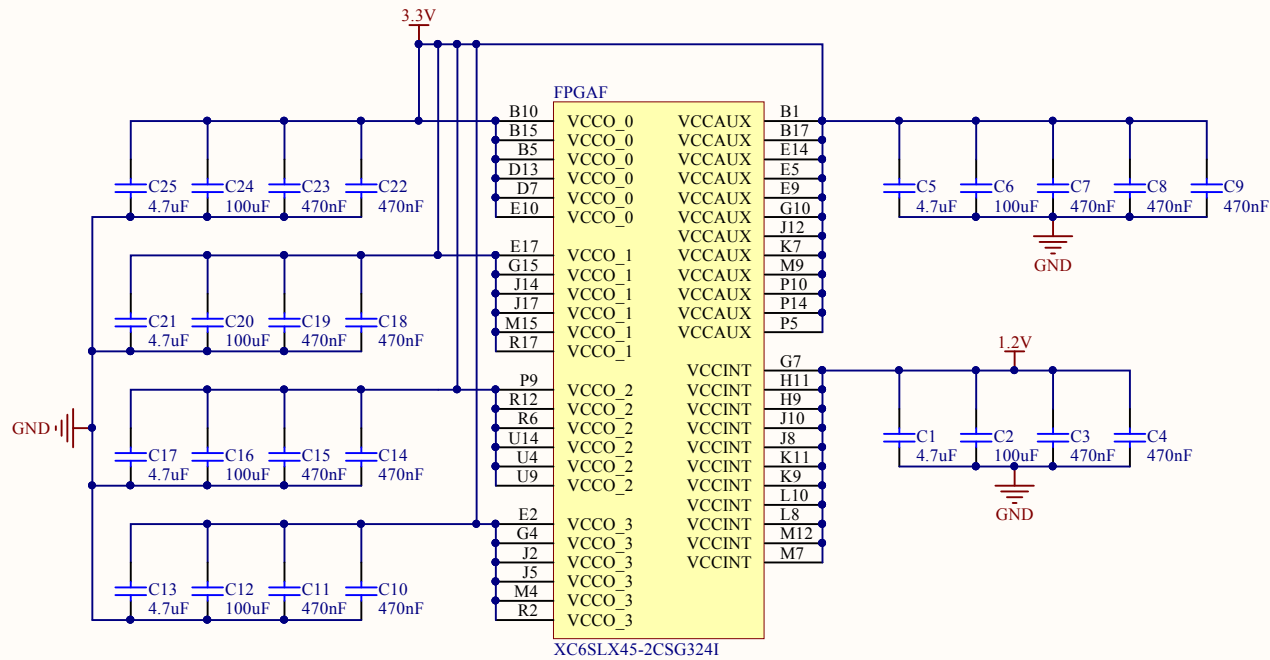
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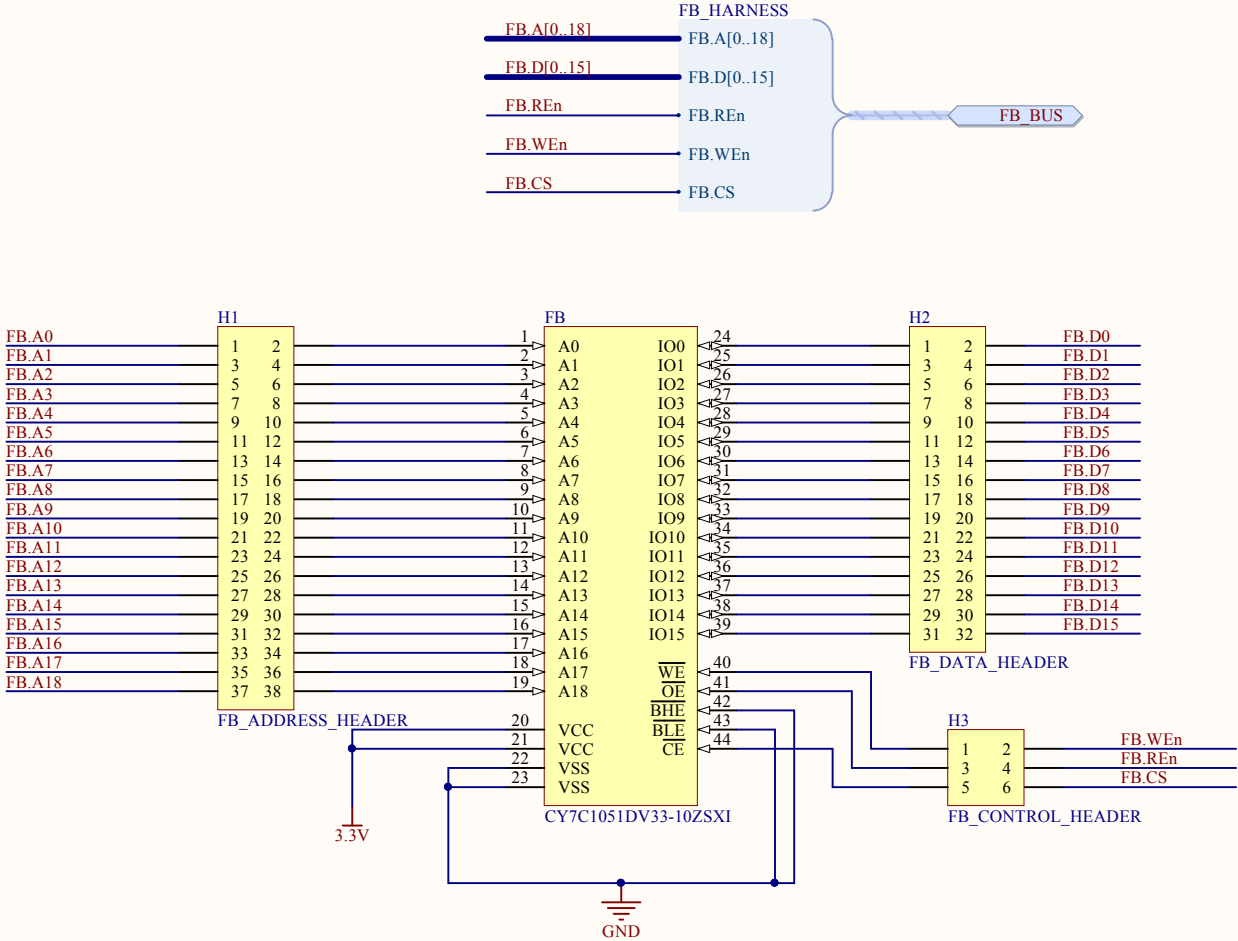


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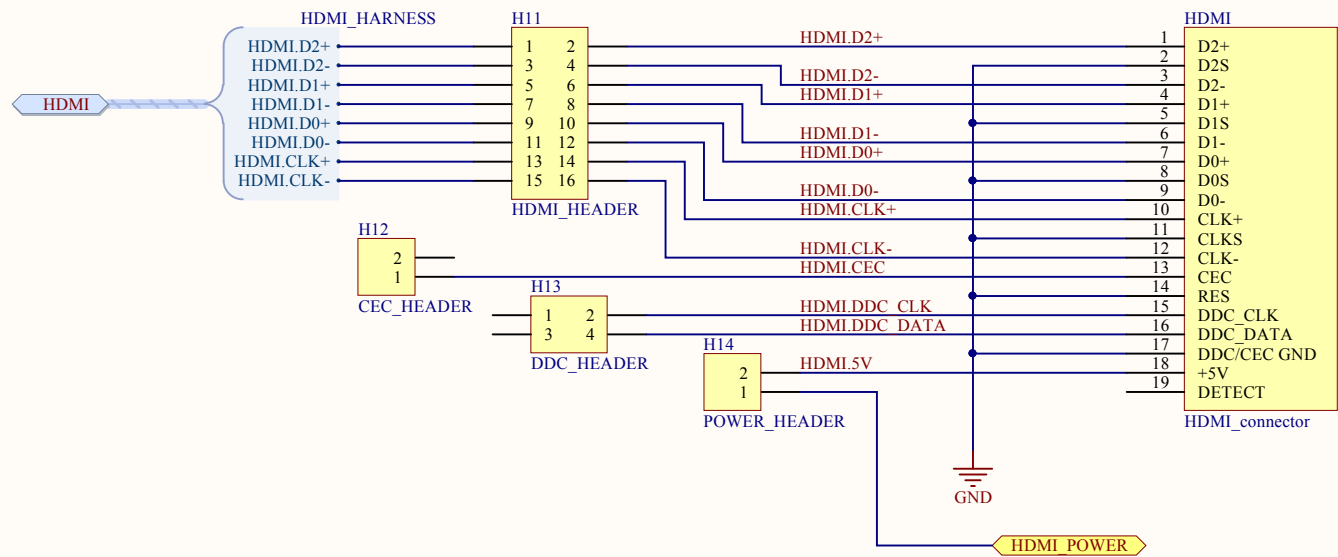


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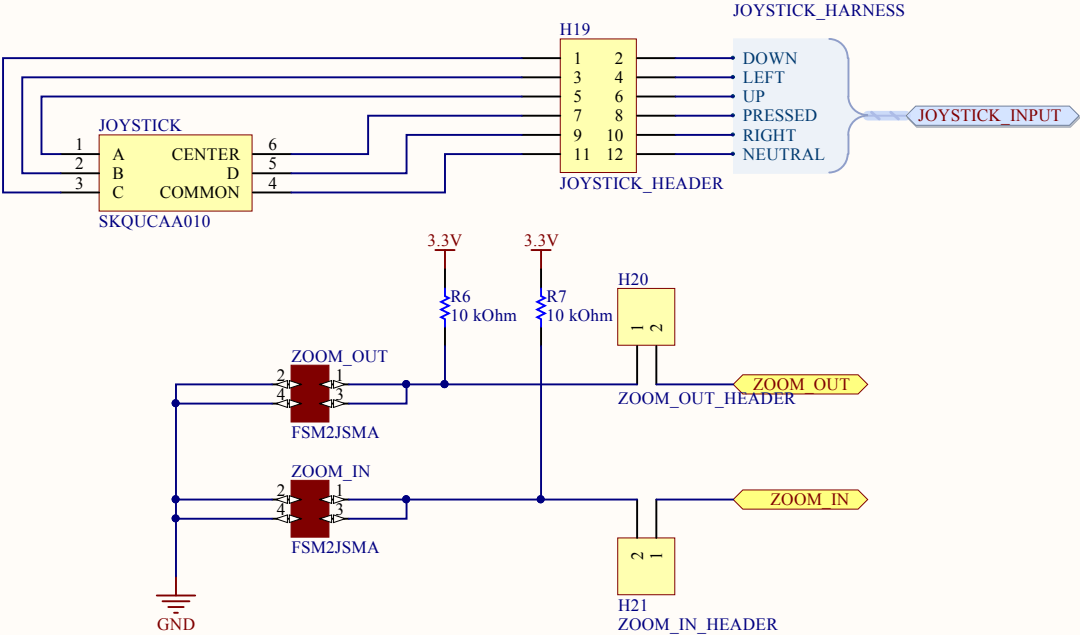




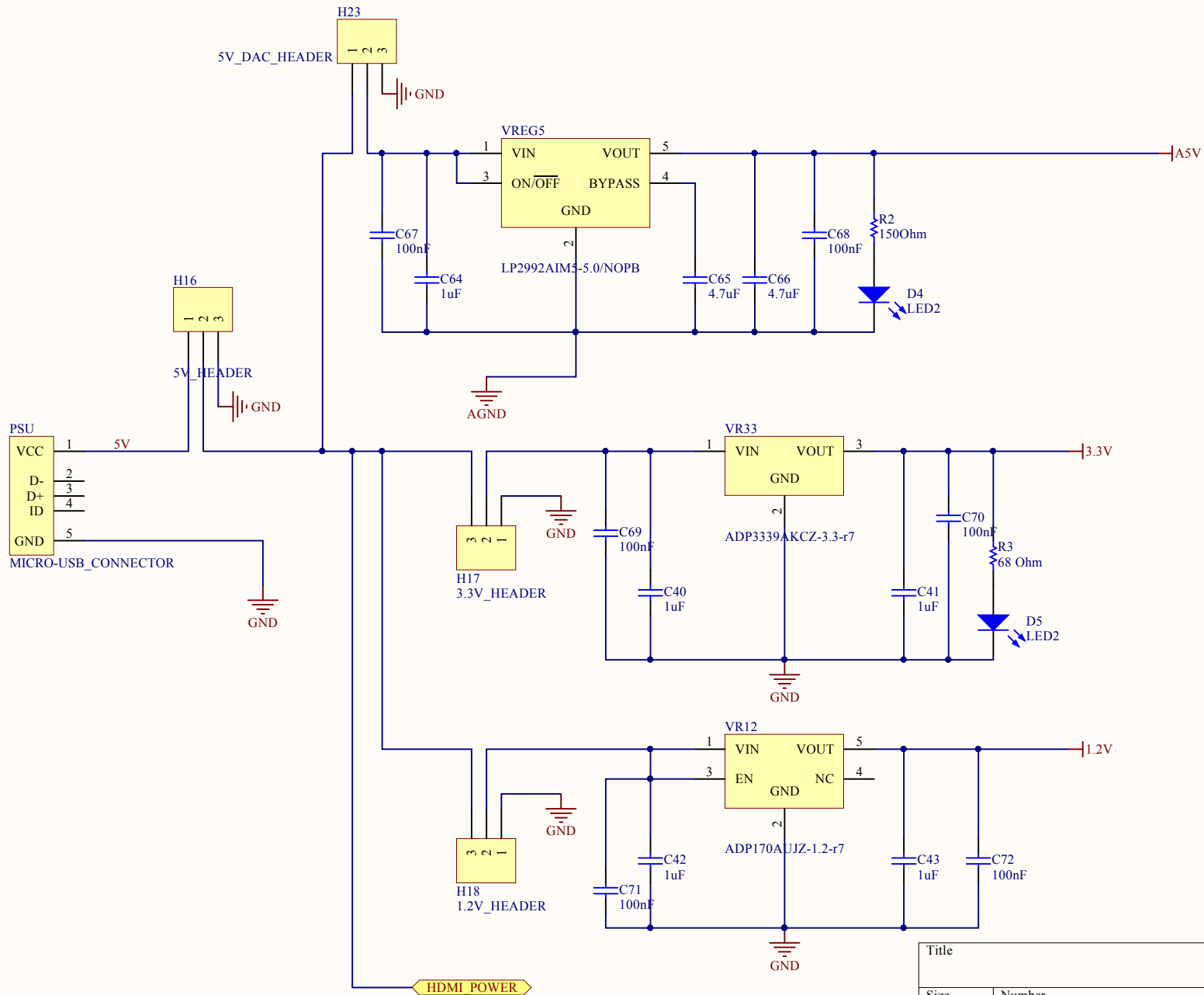
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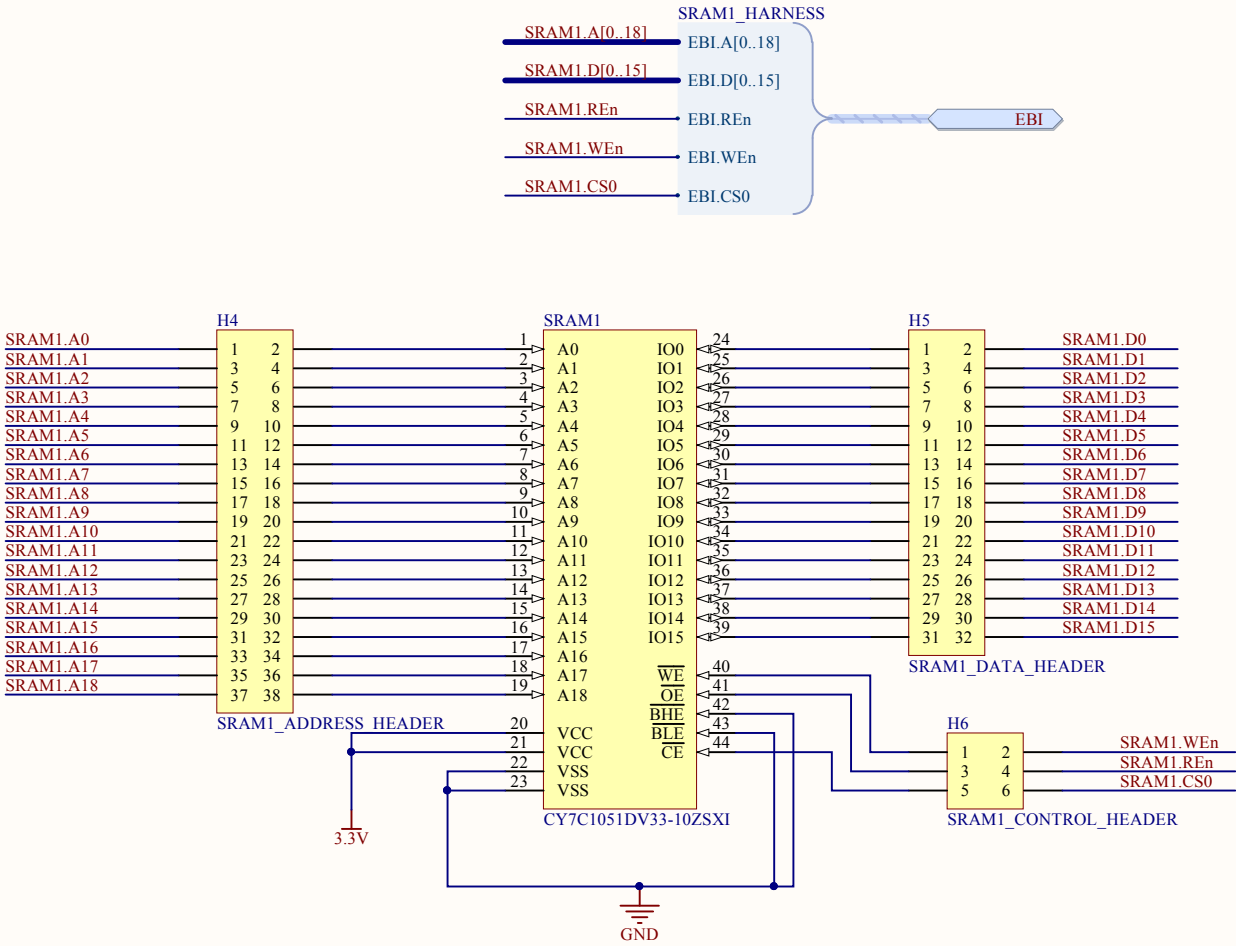
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File:	C:\Users\...\SRAM.SchDoc	Drawn By: