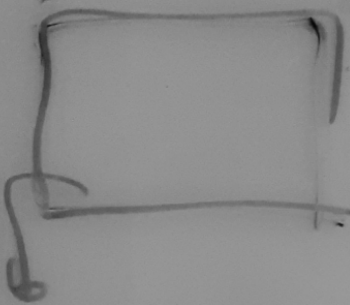


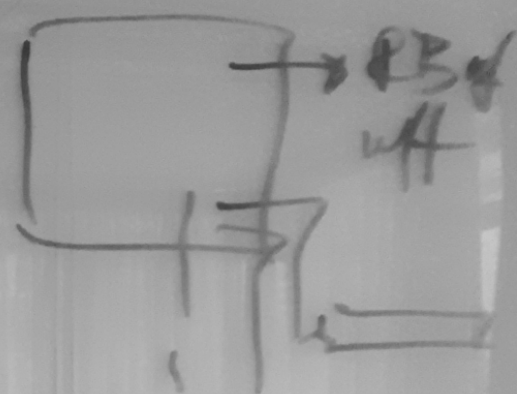
A

NET/OS

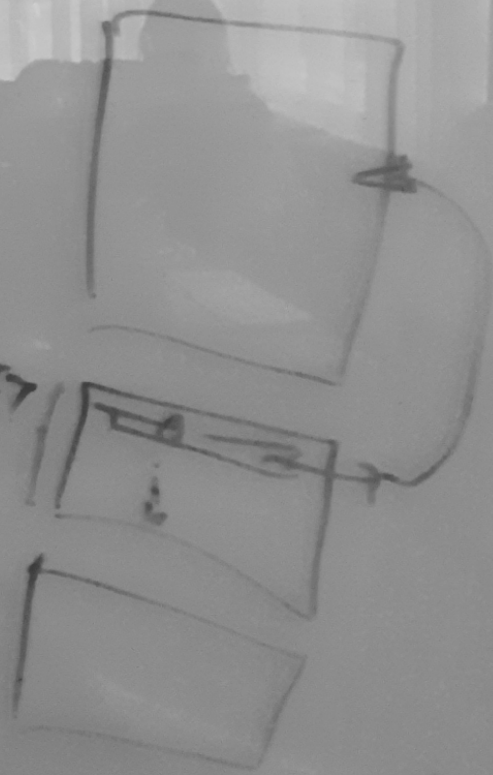
control

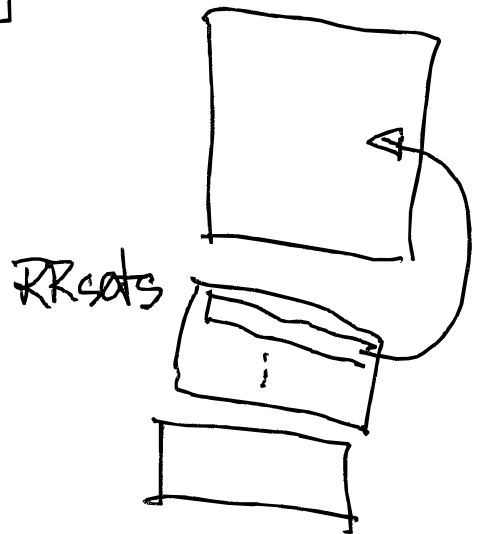
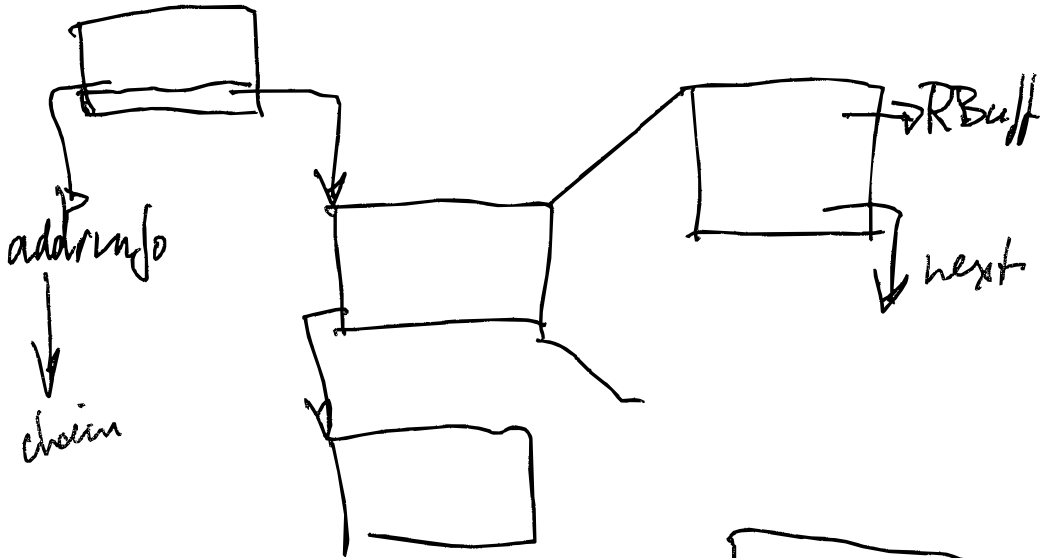


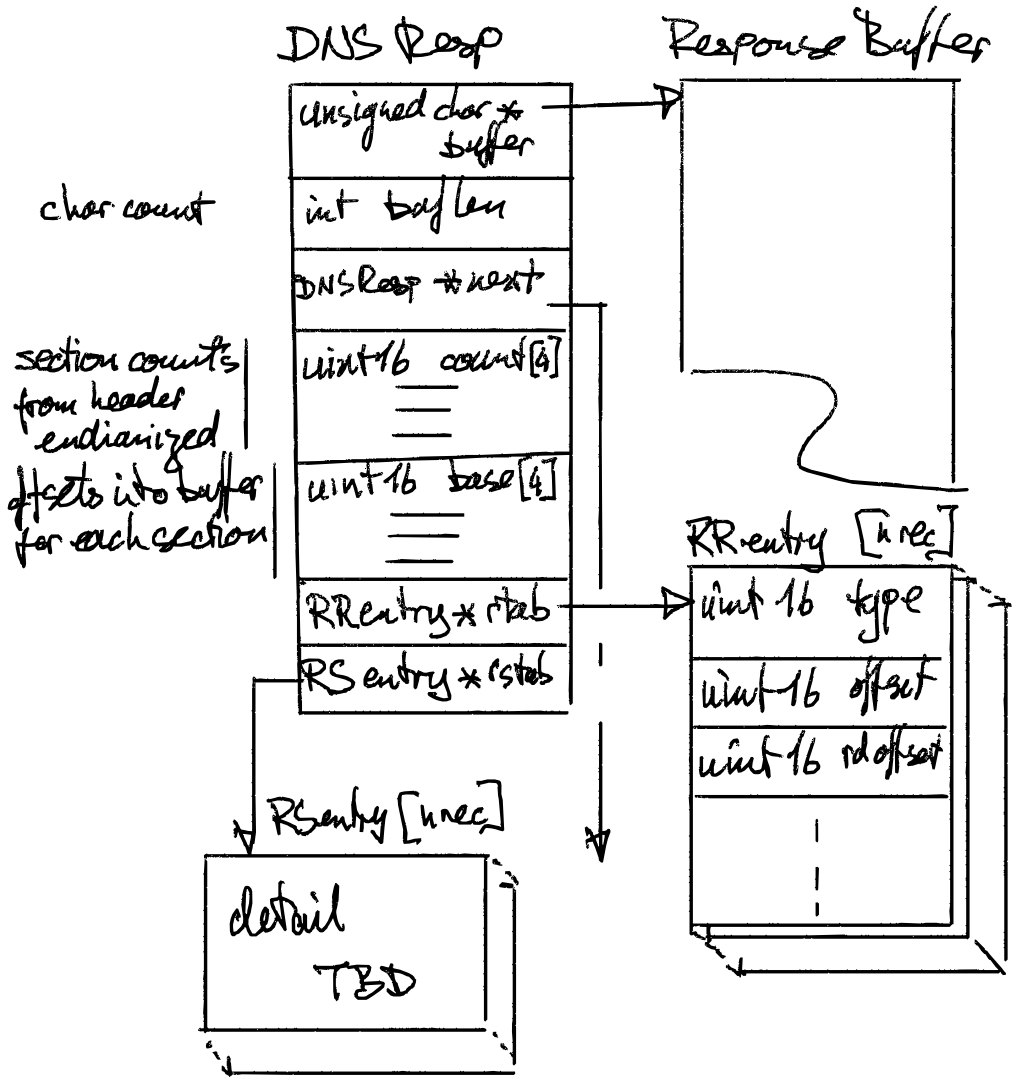
...



RESET







Notes (1)  $nrec = \sum_{n=0}^3 count[n]$

(aggregate count of RRs in buffer)

(2) RRentry, RSEntry arrays inline?