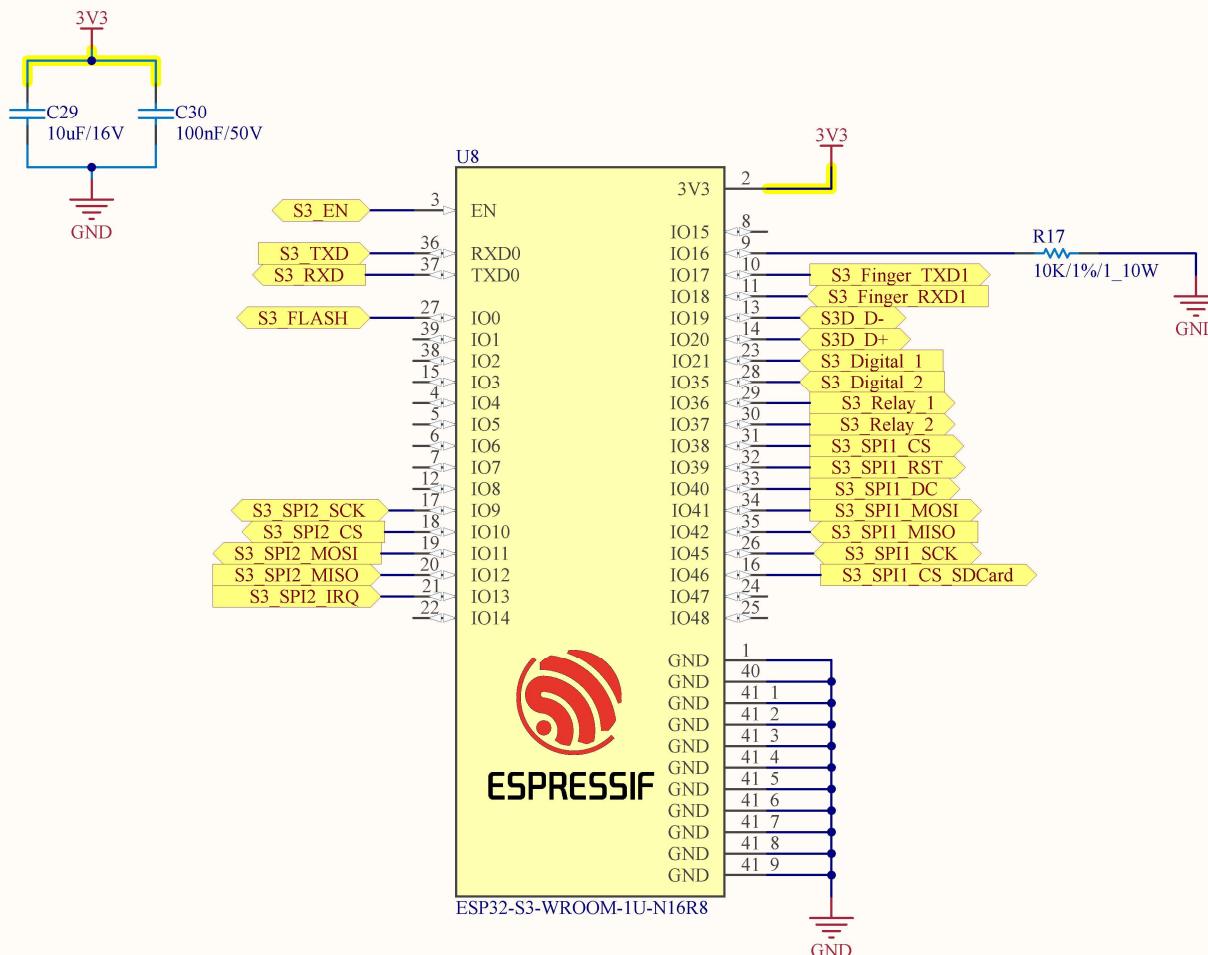


A



B

A

B

C

C

D

D

Project Name

*



Size

A4

Revision

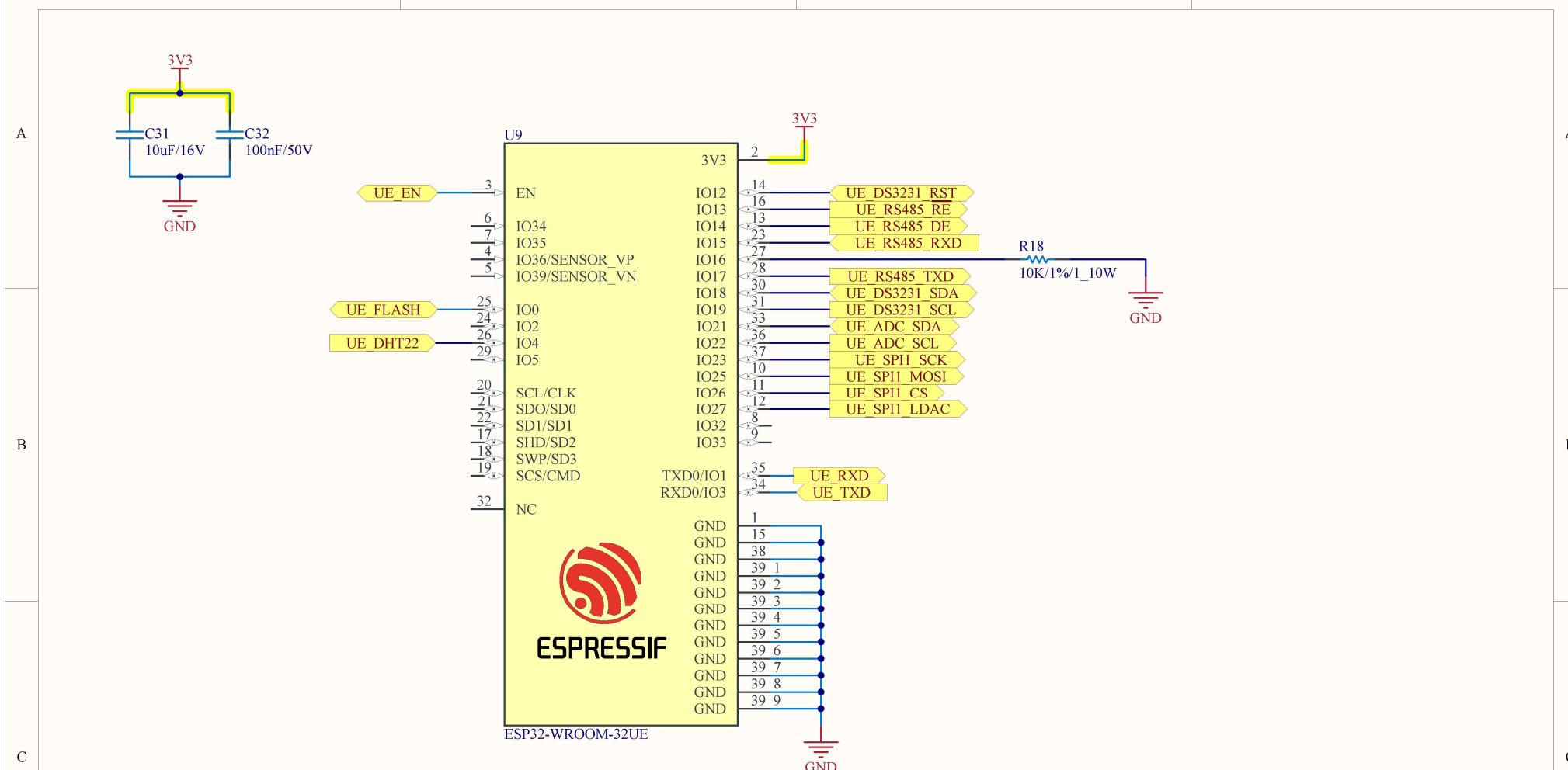
Date: 1/21/2025

Last Modified: 1/20/2025

Drawn By: *

Sheet 3 of 14 Checked By: *

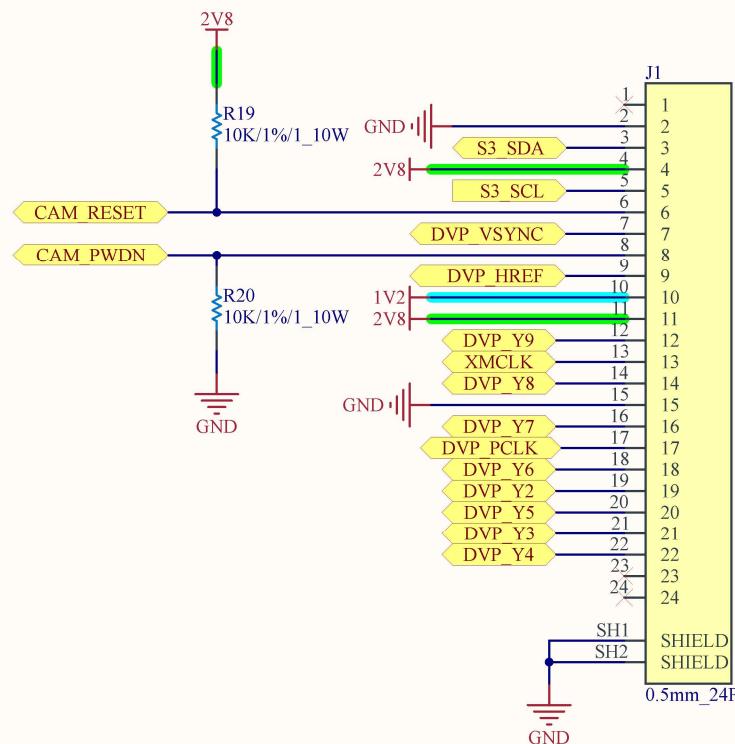
File: ESP32_BLE.SchDoc



Project Name * Size A4		Revision	 
Date: 1/21/2025	Last Modified: 1/20/2025		
Drawn By: *	Sheet 4 of 14	Checked By: *	
File: ESP32_WiFi.SchDoc			

A

A



B

B

C

C

D

D

Project Name

*



Size

A4

Revision

Date: 1/21/2025

Last Modified: 1/20/2025

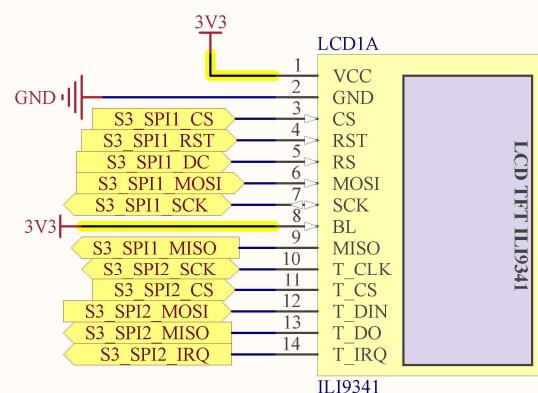
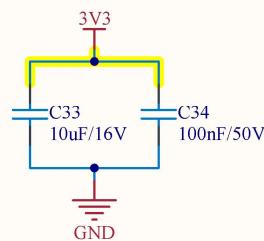
Drawn By: *

Sheet 5 of 14

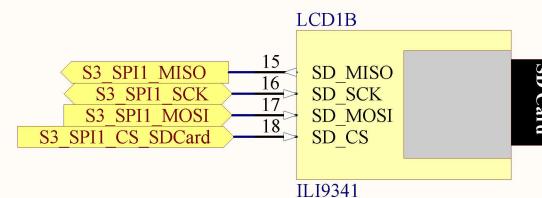
Checked By: *

File: Camera.SchDoc

A



B



C

D

Project Name

*



Size

A4

Revision

Date: 1/21/2025

Last Modified: 1/20/2025

Drawn By: *

Sheet 6 of 14

Checked By: *

File: Display.SchDoc

A

A

B

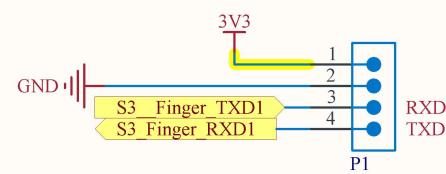
B

C

C

D

D



Project Name

*



Size

A4

Revision

Date: 1/21/2025

Last Modified: 1/20/2025

Drawn By: *

Sheet 7 of 14

Checked By: *

File: FingerPrint.SchDoc

A

A

B

B

C

C

D

D

Project Name

*



Size

A4

Revision

Date: 1/21/2025

Last Modified: 1/18/2025

Drawn By: *

Sheet 8 of 14

Checked By: *

File: Ethernet.SchDoc

A

B

C

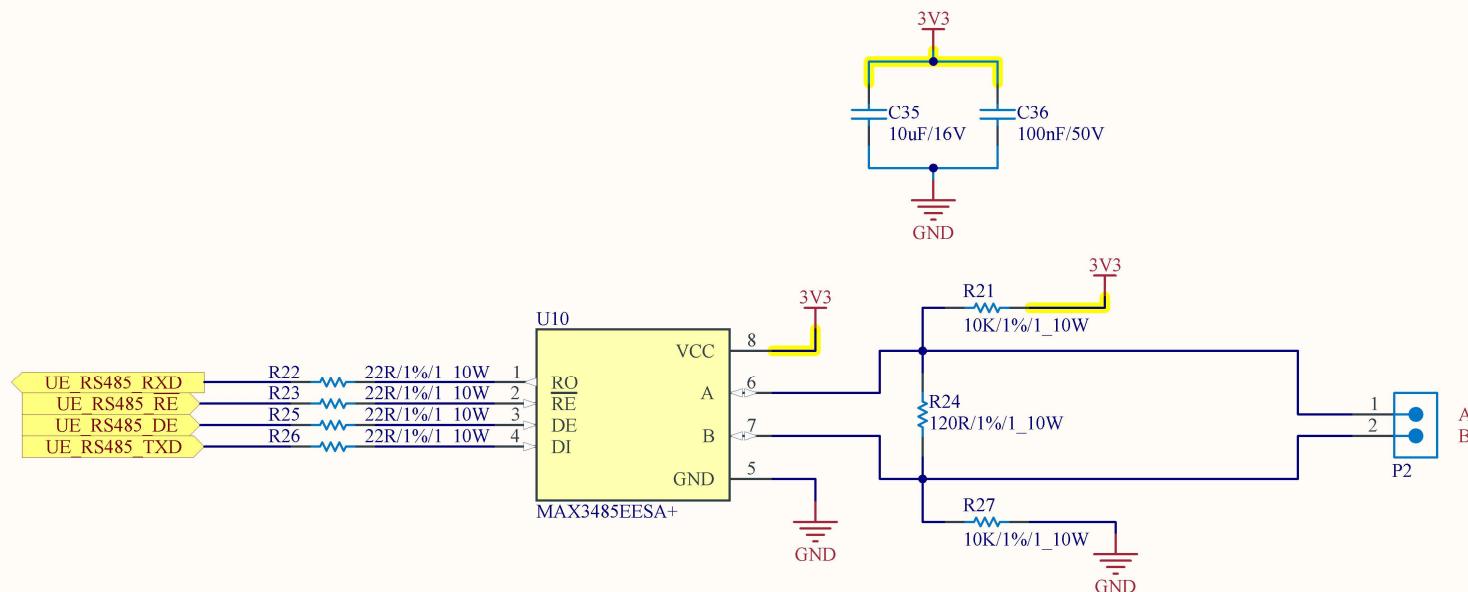
D

A

B

C

D



Project Name

*



Size

A4

Revision

Date: 1/21/2025

Last Modified: 1/20/2025

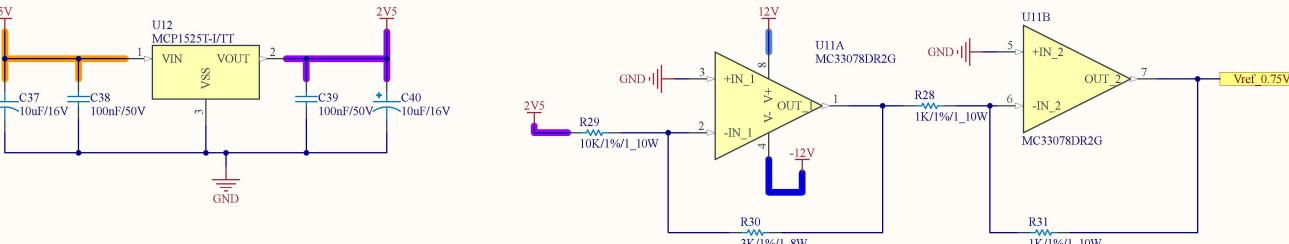
Drawn By: *

Sheet 9 of 14 | Checked By: *

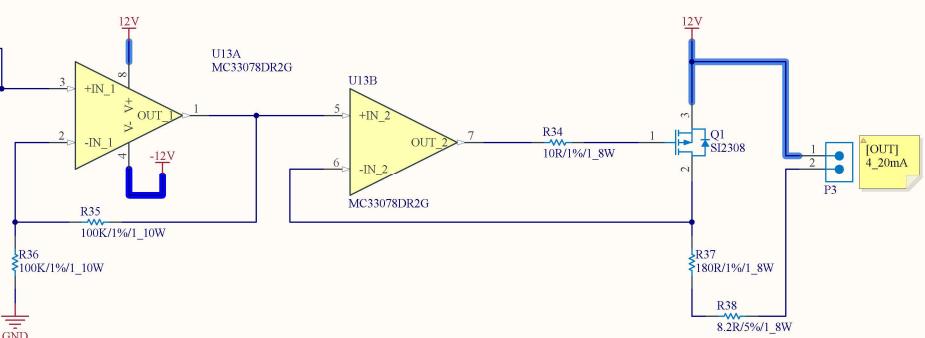
File: UART_RS485.SchDoc

1 2 3 4 5 6 7 8

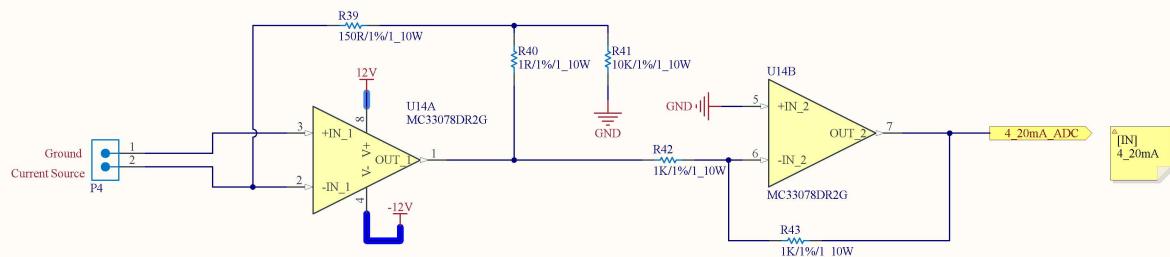
A



B



C

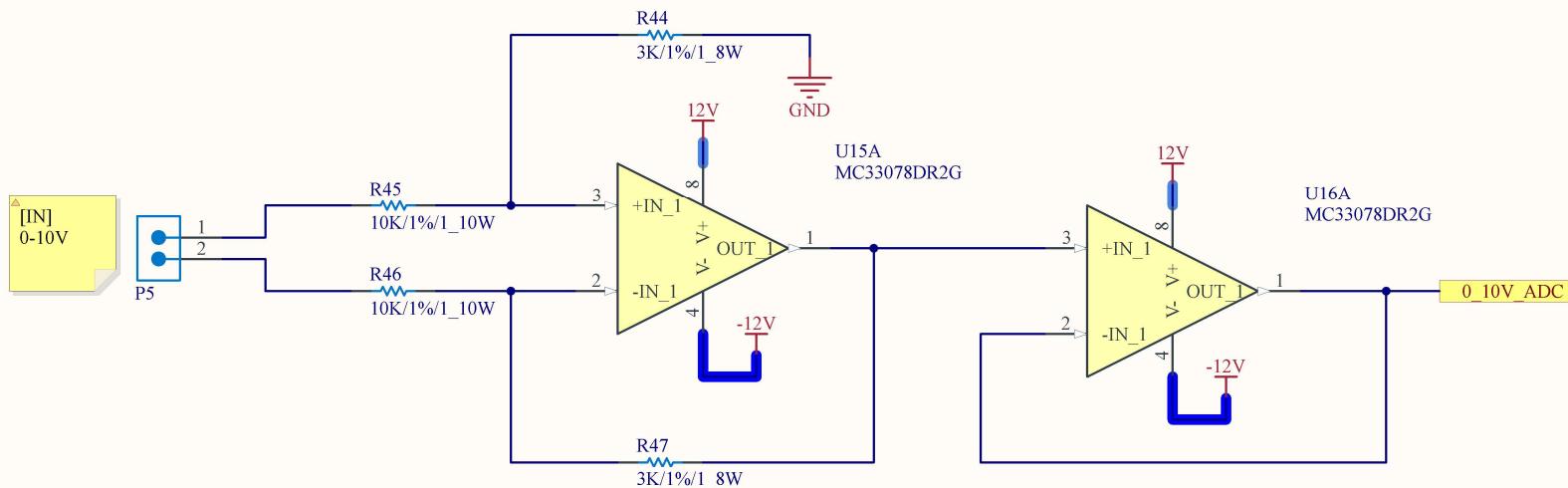


D

Project Name *		
Size	A3	Revision
Date:	1/21/2025	Last Modified: 1/20/2025
Drawn By:	Sheet 10 of 14	Checked By:
File: 4-20mA_Converter.SchDoc		

1 2 3 4 5 6 7 8

A



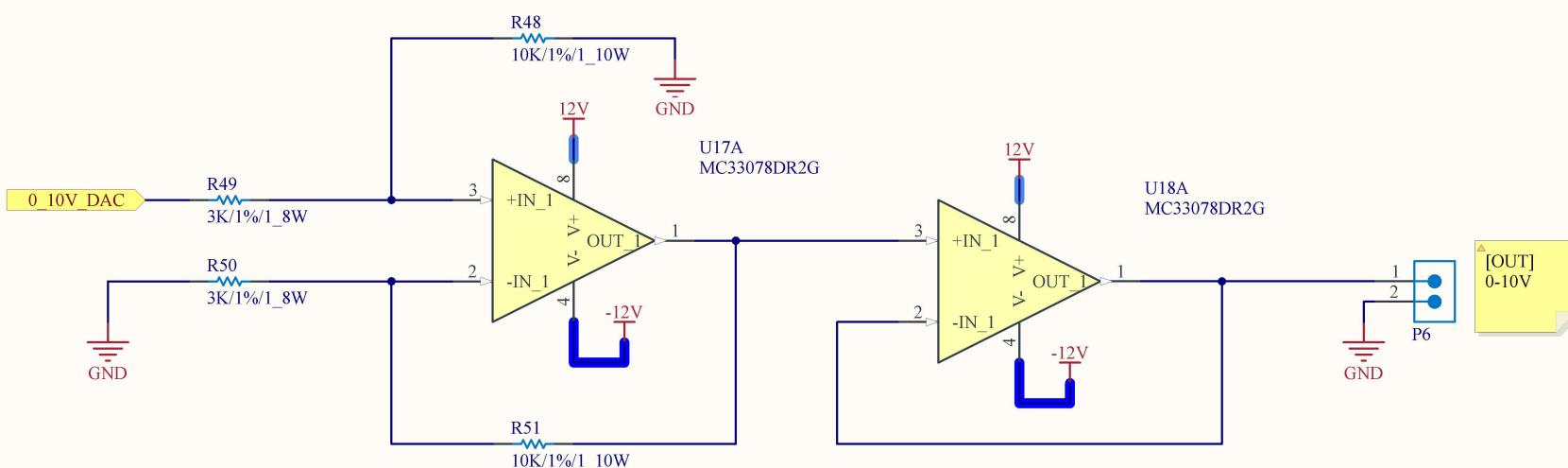
B

A

B

C

C



D

C

D

Project Name

*



Size

A4

Revision

Date: 1/21/2025

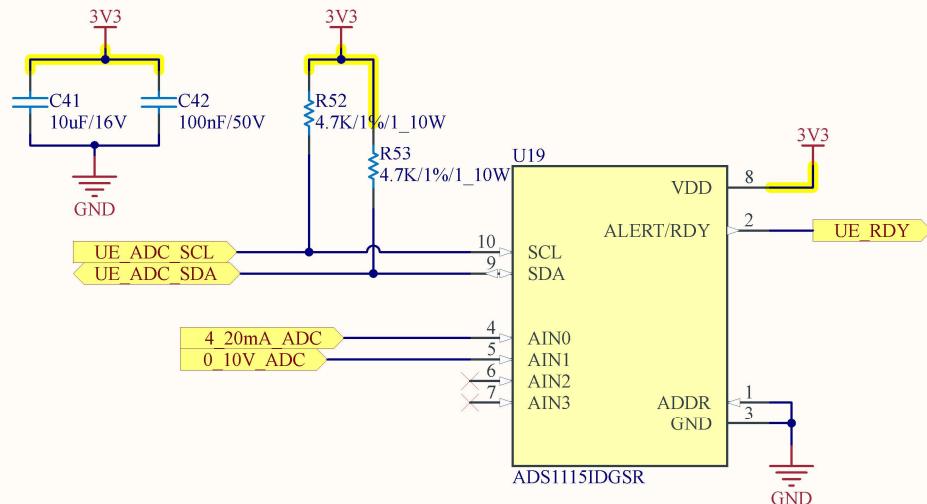
Last Modified: 1/20/2025

Drawn By: *

Sheet 11 of 14 Checked By: *

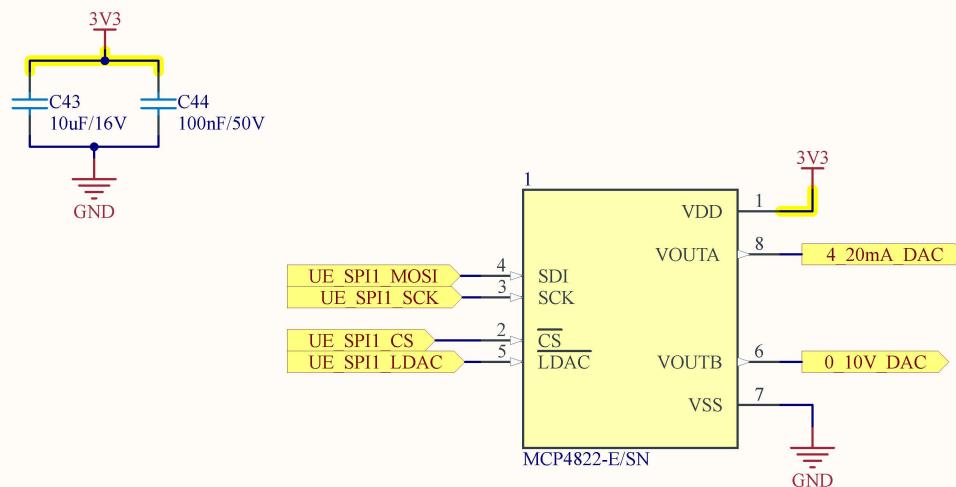
File: 0-10V_Converter.SchDoc

A



B

A



C

B

D

C

Project Name

*



Size

A4

Revision

Date: 1/21/2025

Last Modified: 1/20/2025

Drawn By: *

Sheet 12 of 14 Checked By: *

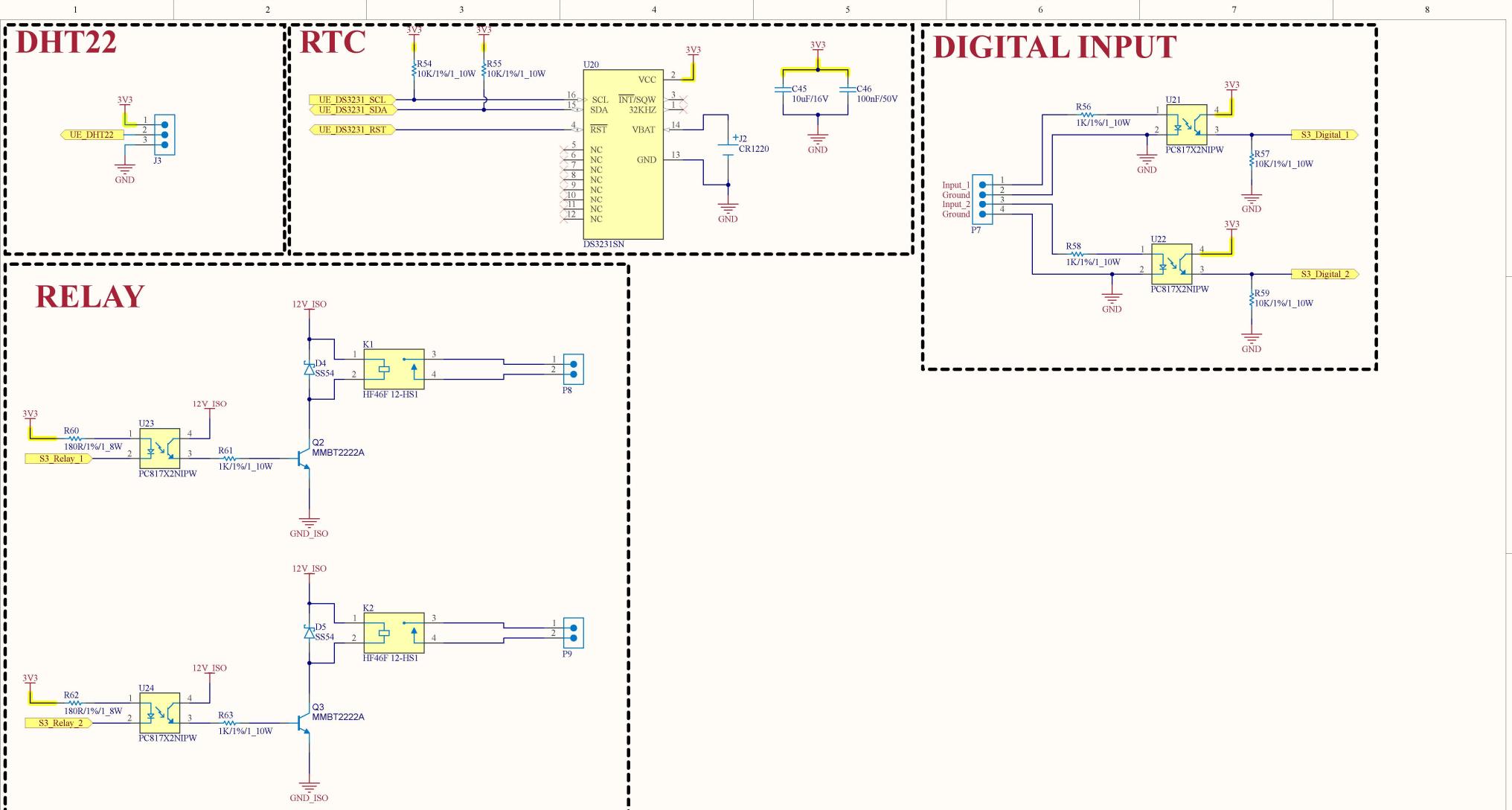
File: DAC_ADC_External.SchDoc

DHT22

RTC

DIGITAL INPUT

RELAY



Project Name

*



PIF

Size

A3

Revision

Date: 1/21/2025

Last Modified: 1/20/2025

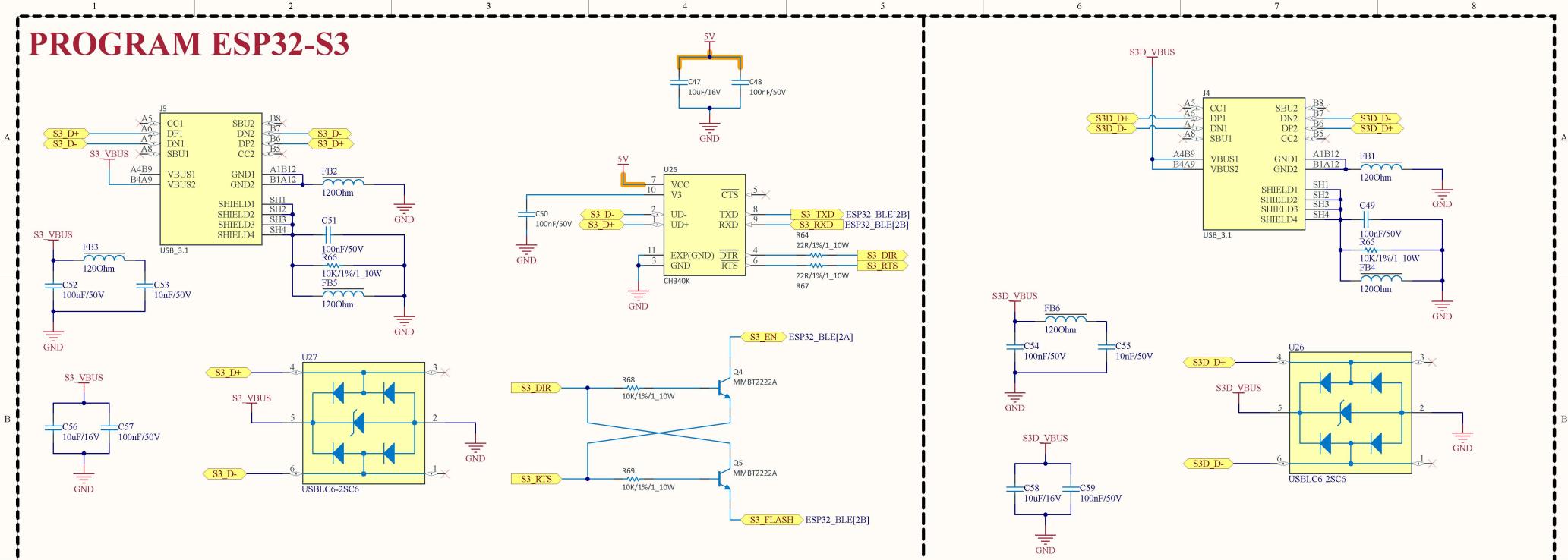
Drawn By: *

Sheet 13 of 14

Checked By: *

File: Interface_IO.SchDoc

PROGRAM ESP32-S3



PROGRAM ESP32-WROOM-32UE

