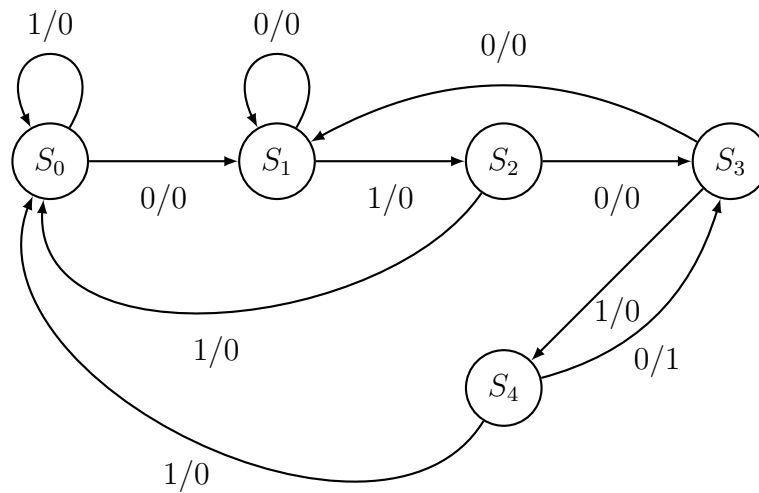


Gate : EC-2020,39

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1. The state diagram of a sequence detector is shown below. state S_0 is the initial state of the sequence detector. If the output is 1, then



- (a) the sequence 01010 is detected.
- (b) the sequence 01011 is detected.
- (c) the sequence 01110 is detected.
- (d) the sequence 01001 is detected.