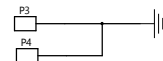


[illegible]

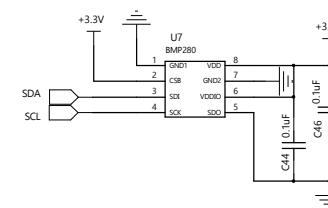
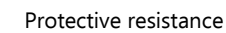
## A



## C

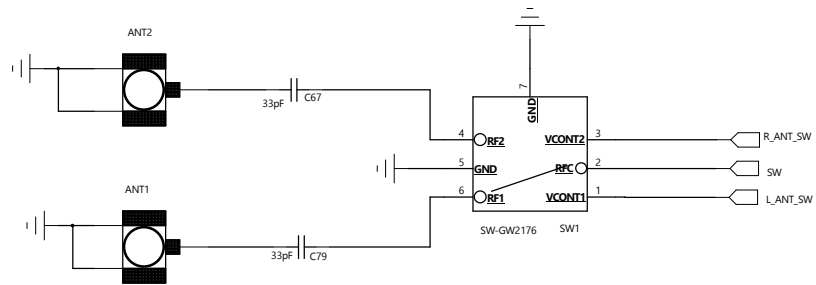


## EEPROM

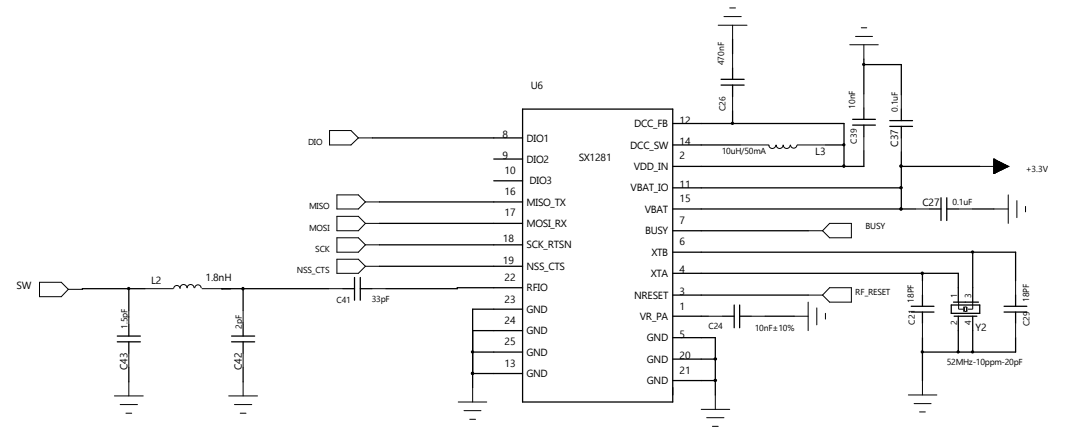


Title: INr6-HS	
Drawing No:	Version: V1.3
Drawn By: zhuanghaibo	Date: 2020-08-14
Size	Sheet: 1/2

## ANT



## RF Circuit



Title: <b>INr6-HS</b>	
Drawing No:	Version: <b>V1.3</b>
Drawn By: <b>zhuanghaibo</b>	Date: 2020-08-14
Size	Sheet: <b>1/2</b>