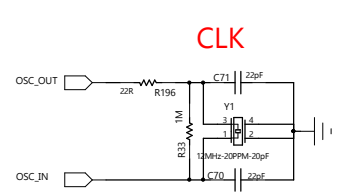
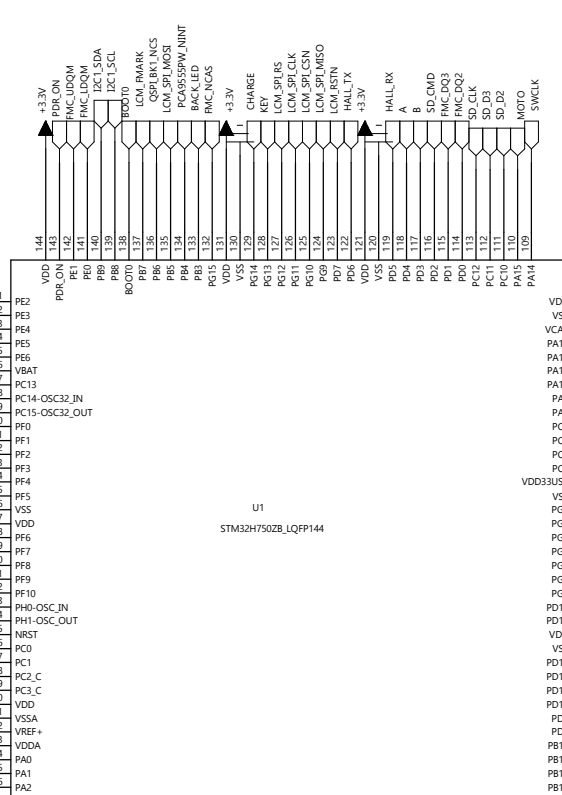
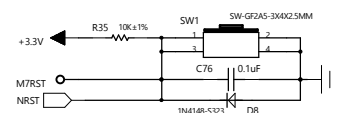


| | |
|-------------------|----------------|
| Title: MAIN-Poart | |
| Drawing No: | Version: V1.0 |
| Drawn By: 郑晓 | Date: 20240614 |
| Size: A3 | Sheet: 1 of 3 |



The diagram shows a circuit for the DFU (Reserve) mode. It features a push-button labeled PDR_ON. When pressed, it connects to a node that branches to two resistors: R91 (1K) and R92 (1K). R91 is connected to a positive supply rail (+), and R92 is connected to ground. The output of this network is indicated by an arrow pointing to the right.

| | |
|------------------|--|
| CLK | 阻抗控制在50Ω, 线径10mil, 线间距≥20mil, 线长≤1000mil |
| CS CE RAS CAS WE | 线径5mil, 线间距≥10mil, 与CLK等长, 误差 $\leq \pm 100\text{mil}$ |
| A0-A11 BS0 BS1 | 线径5mil, 线间距≥10mil, 与CLK等长, 误差 $\leq \pm 100\text{mil}$ |
| DQ0-DQ7 LDQM | 线径5mil, 线间距≥8mil, 数据组内等长, 误差 $\leq \pm 20\text{mil}$ |
| DQ0-DQ7 UDQM | 线径5mil, 线间距≥8mil, 数据组由短长, 误差 $\leq \pm 20\text{mil}$ |

The circuit diagram shows a 3.3V supply connected to a 10k resistor. The other end of the resistor is connected to a series of capacitors labeled C39 through C40. Each capacitor is connected to ground. The capacitors are labeled with their values: C39 (100pF), C40 (0.1uF), C41 (0.1uF), C42 (0.1uF), C43 (0.1uF), C44 (0.1uF), C45 (0.1uF), C46 (0.1uF), C47 (0.1uF), C48 (0.1uF), and C49 (100pF).

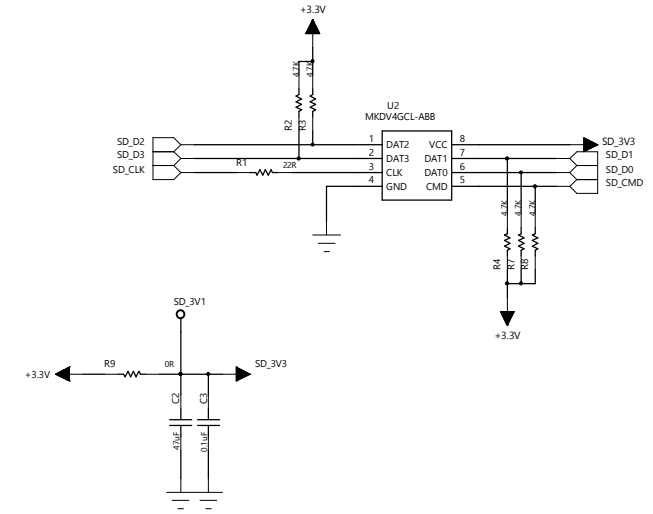
The diagram shows a pull-up circuit for the HW_VER pin. A 3.3V supply is connected to the NC pin through a resistor R99 (10k ±1%). The NC pin is also connected to ground through a resistor R100. The current I is indicated flowing into the NC pin.

| 电压 | 版本 | 电压 | 版本 |
|--------|--------|--------|--------|
| +3.3V | Ver1.0 | +1.43V | Ver1.5 |
| +3V | Ver1.1 | +1.1V | Ver1.6 |
| +2.64V | Ver1.2 | +0.57V | Ver1.7 |
| +2.18V | Ver1.3 | +0V | Ver1.8 |
| +1.81V | Ver1.4 | | |

Below the table, the title "MAIN-MC" is displayed in red, and the text "Drawing No:" is shown in a box.

SD card

SD card



BACK Lamp

