## A-C4E6 FPGA board PIN

CLK 50M	PIN 23
not user pack	PIN 24
net aber paen	1 111,1
K2	PIN 90
K2 K3	PIN 91
	_
K4	PIN_87
K5	PIN_86
swith_1	PIN_58
swith_2	PIN_59
swith_3	PIN_60
swith_4	PIN_64
swith_5	PIN_65
swith_6	PIN_66
swith 7	PIN 67
swith 8	PIN 68
LED	
LED D3	PIN 72
LED D4	PIN 73
LED D5	PIN 74
LED_D6	PIN 80
LED_DO LED_D7	PIN 83
_	_
LED_D8	PIN_84
LED_D9	PIN_77
LED_D10	PIN_76
LED_D11	PIN_75
LED_D12	PIN_71
LED_D13	PIN_70
LED_D14	PIN_69
LCD1602/LCD12864	
date_DO	PIN_101
date_D1	PIN_103
date D2	PIN 104
date_D3	PIN 105
date D4	PIN 106
date D5	PIN 110
date_D6	PIN 111
1-4- D7	DIN_III

PIN\_112 PIN\_85

PIN\_99

PIN\_100

## digit LED

 $date_D7$ 

rs

wr

en

LED_A LED_B LED_C LED_D LED_E LED_F LED_G LED_H	PIN_127 PIN_126 PIN_125 PIN_124 PIN_121 PIN_120 PIN_119 PIN_115	
LED_BITO LED_BIT1 LED_BIT2 LED_BIT3 LED_BIT4 LED_BIT5 LED_BIT6 LED_BIT7	PIN_128 PIN_129 PIN_132 PIN_133 PIN_135 PIN_136 PIN_137 PIN_138	right left
RS232 RX TX	PIN_114 PIN_113	
AT24C04 SDA SCK	PIN_3 PIN_7	
BELL	PIN_141	
VGA R G B VS HS	PIN_2 PIN_1 PIN_144 PIN_143 PIN_142	
PS/2 DATE SDCK	PIN_10 PIN_11	