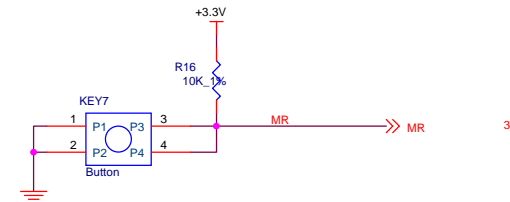
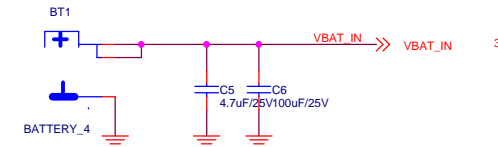
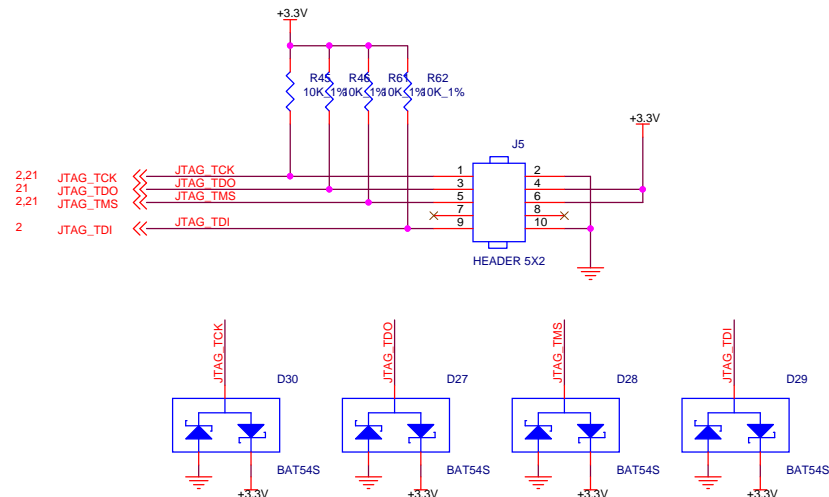
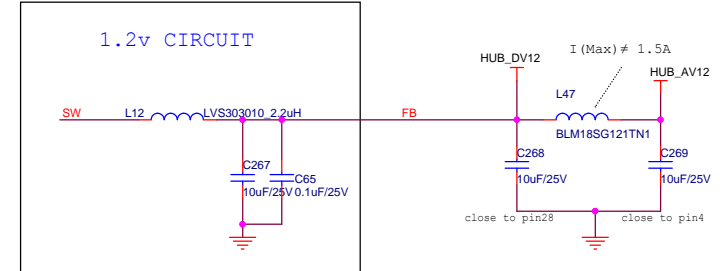
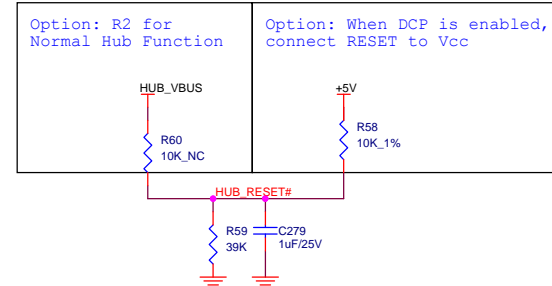
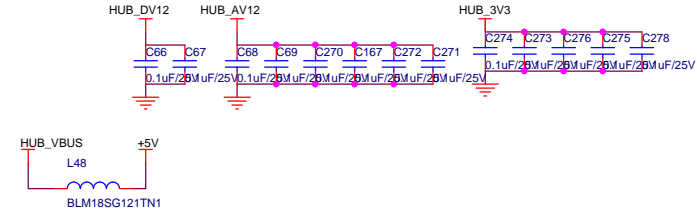
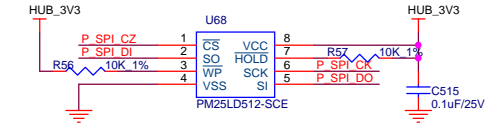
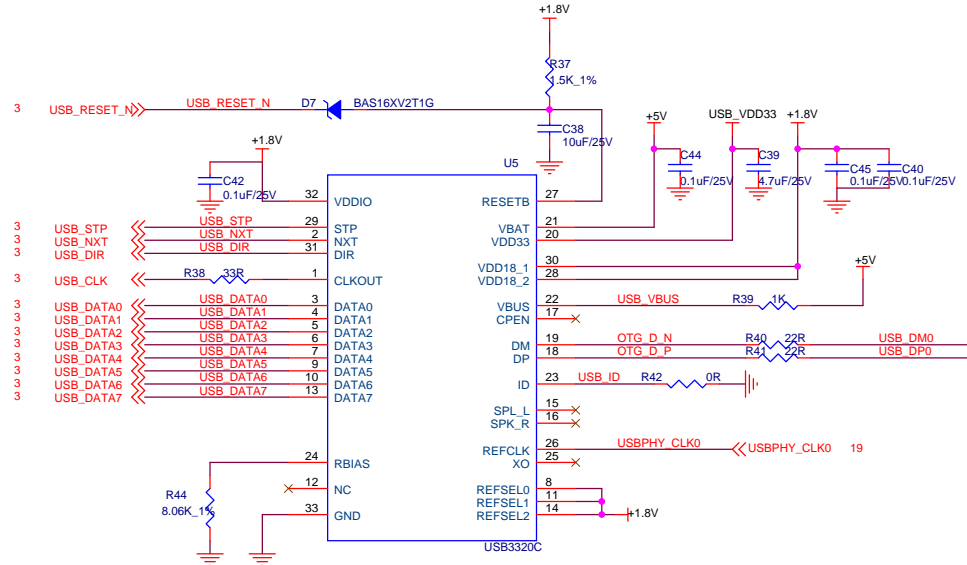
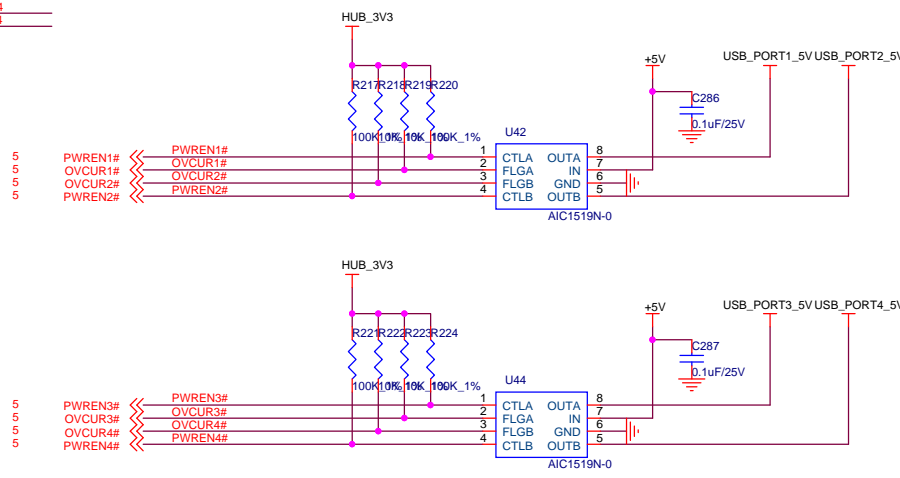
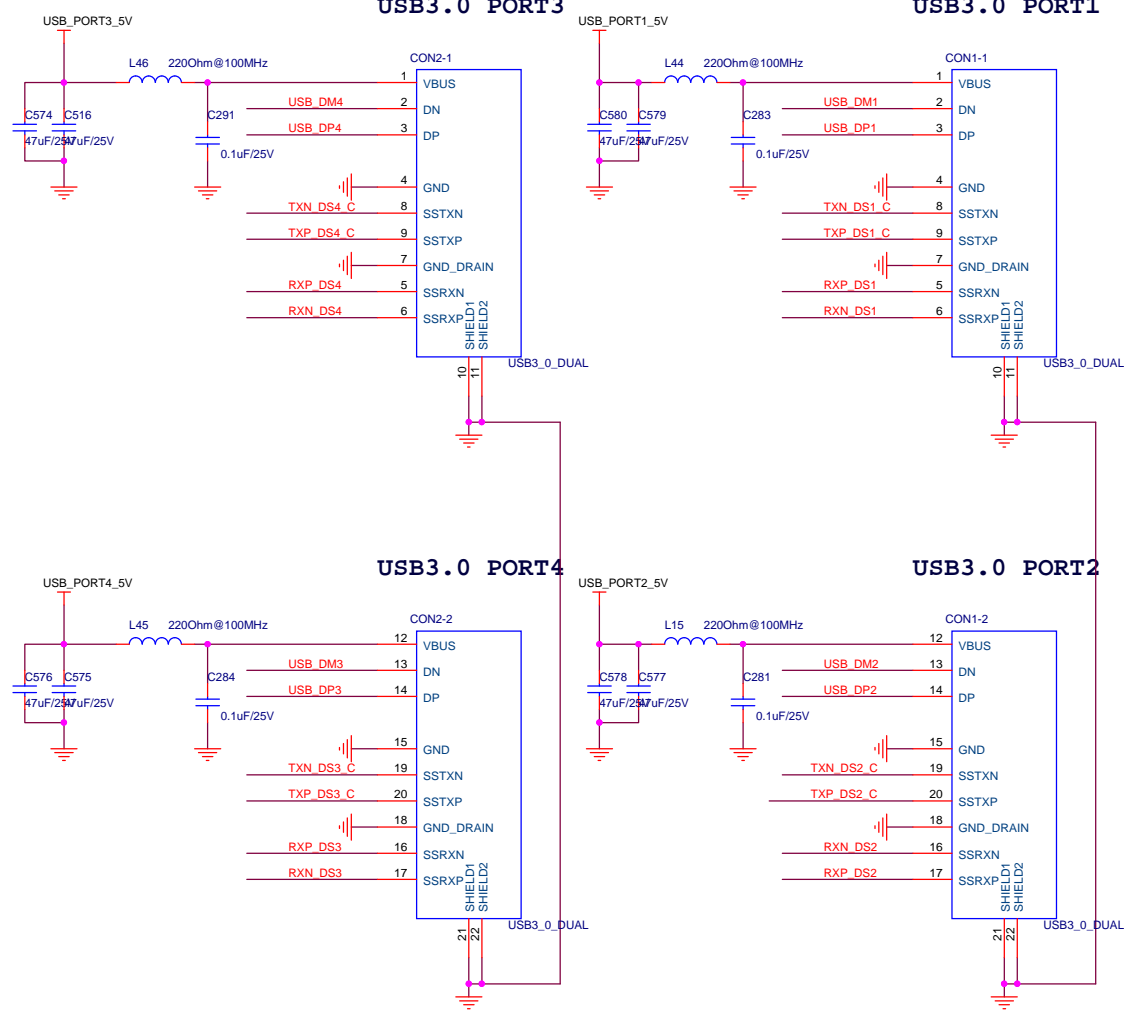
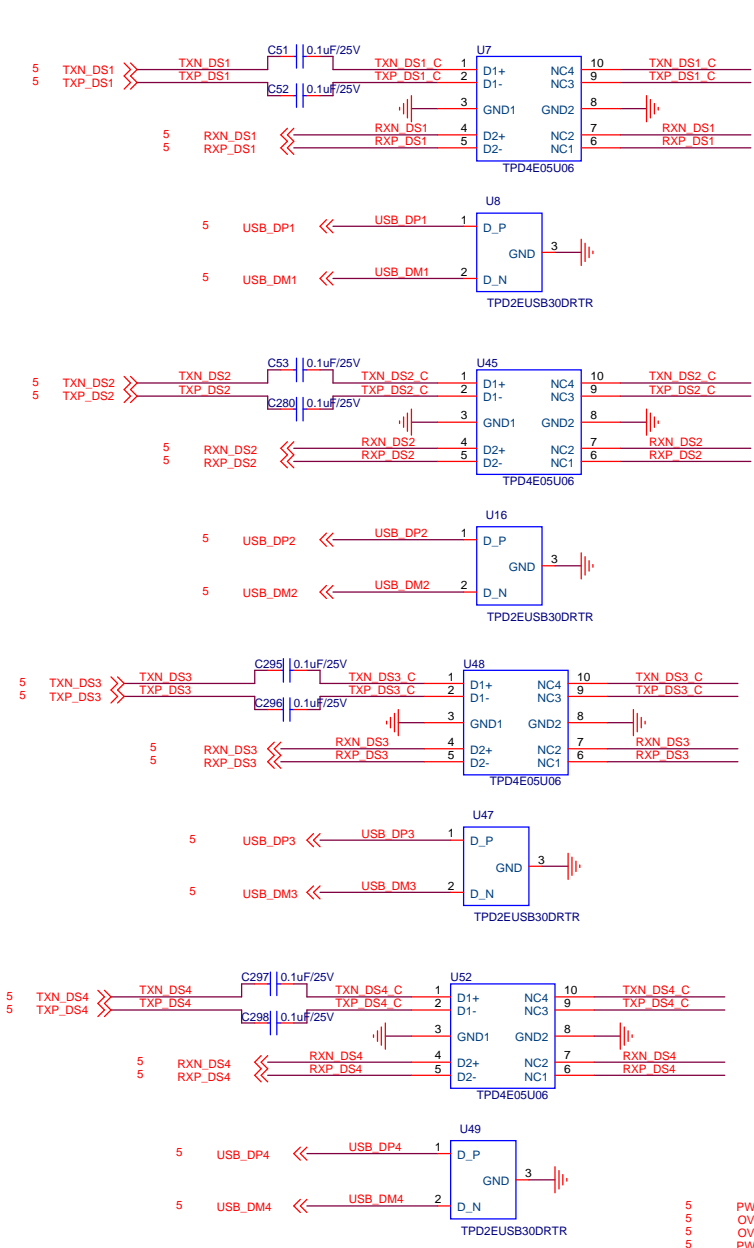


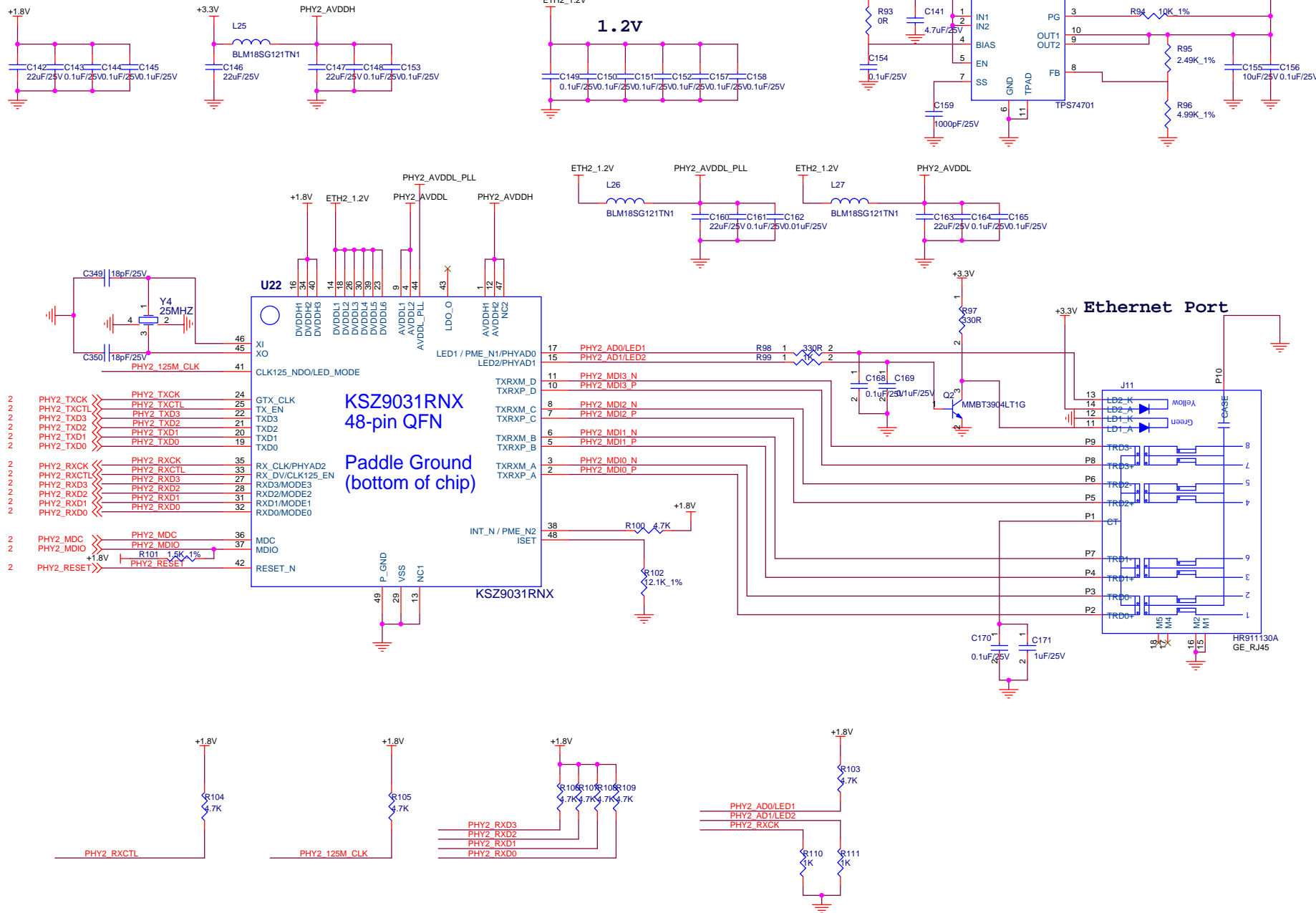
MODE[3:0]	BOOT MODE	Descritpion
0000	PS JTAG	PS JTAG Interface
0001	Quad SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0100	NAND	Requires 8-bit data bus width
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1000	PJTAG(MIO #0)	PJTAG connection 0 option
1001	PJTAG(MIO #1)	PJTAG connection 1 option
1110	SD1 LS(3.0)	SD 3.0

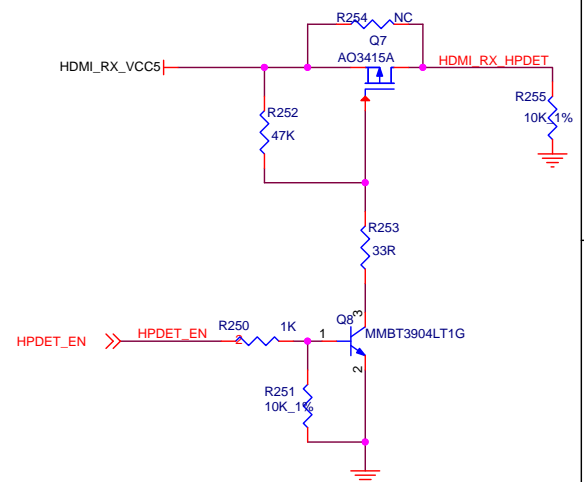
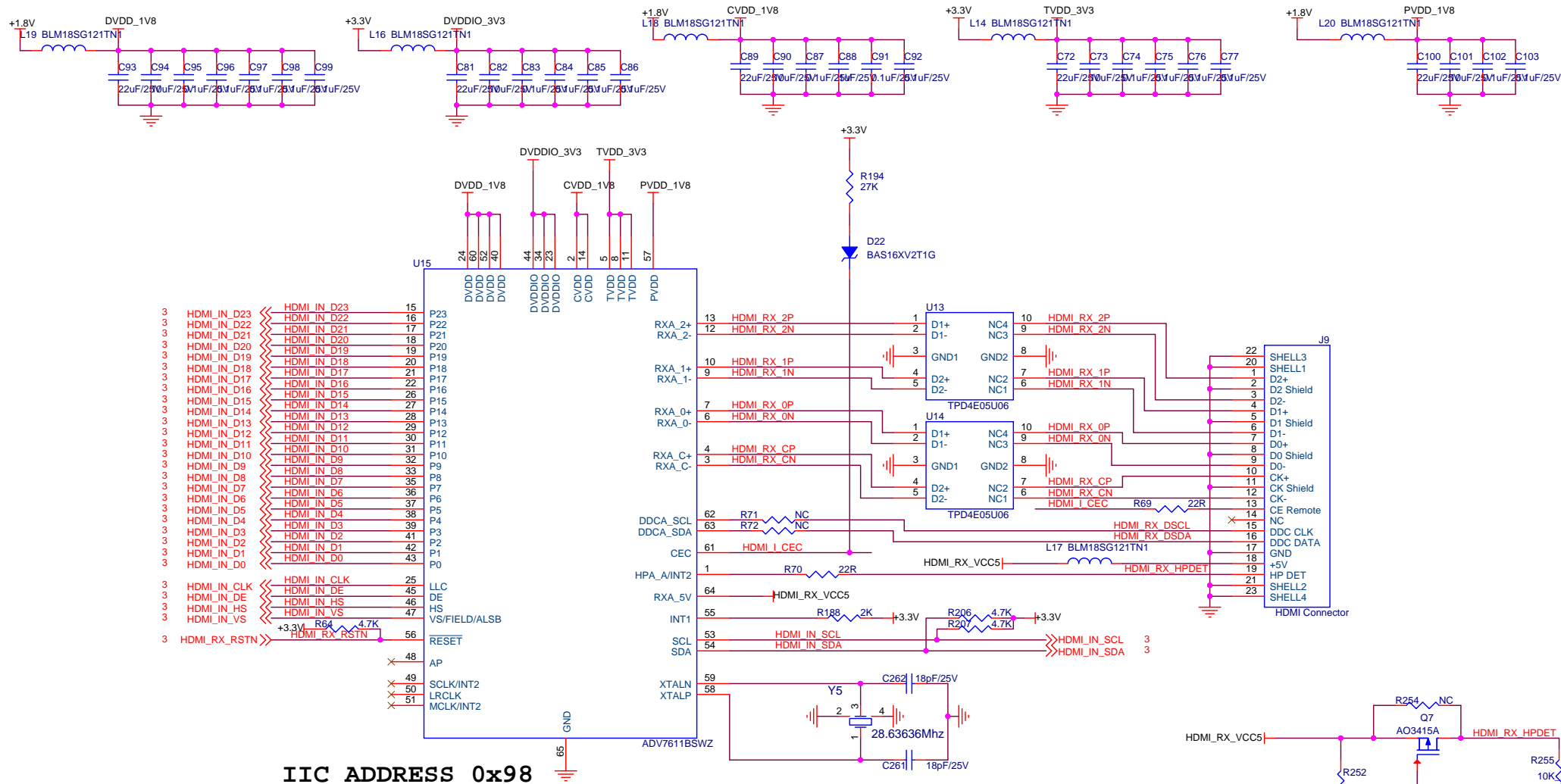
JTAG Connector

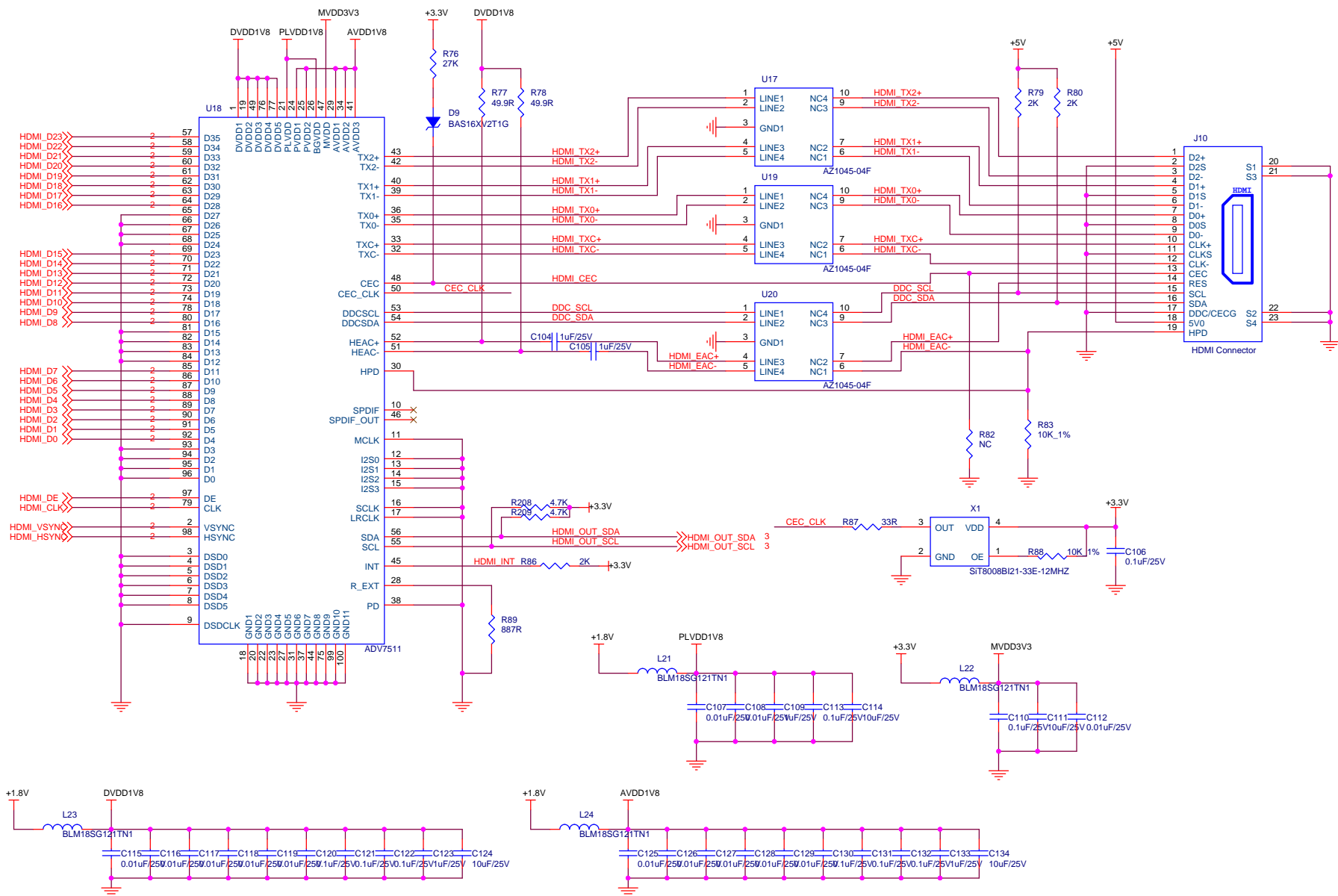


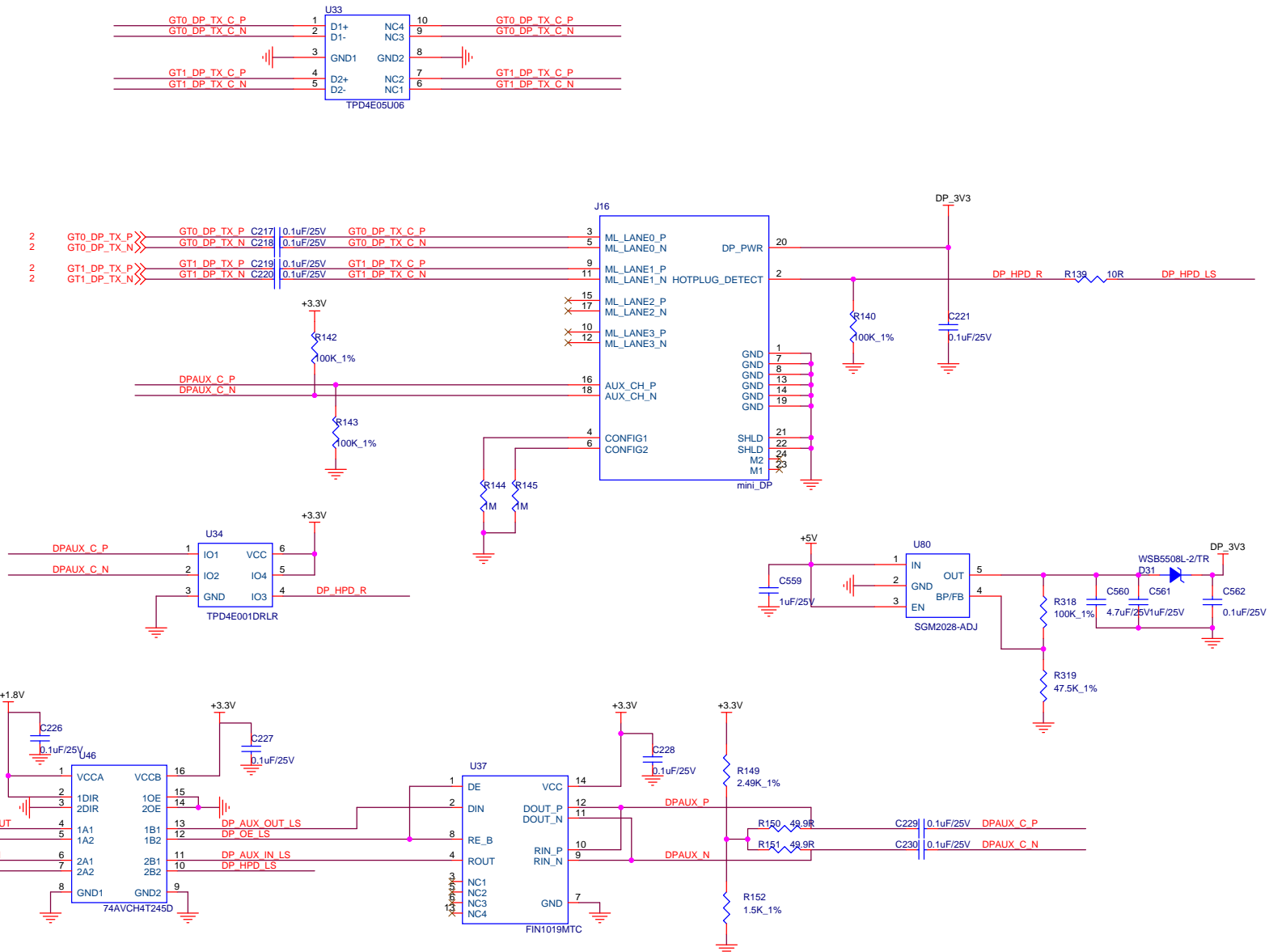




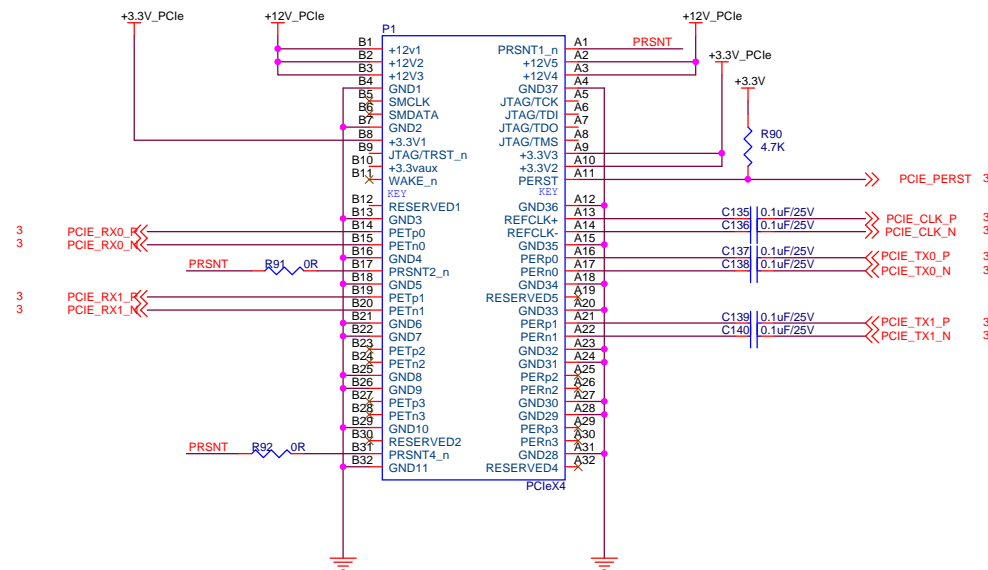


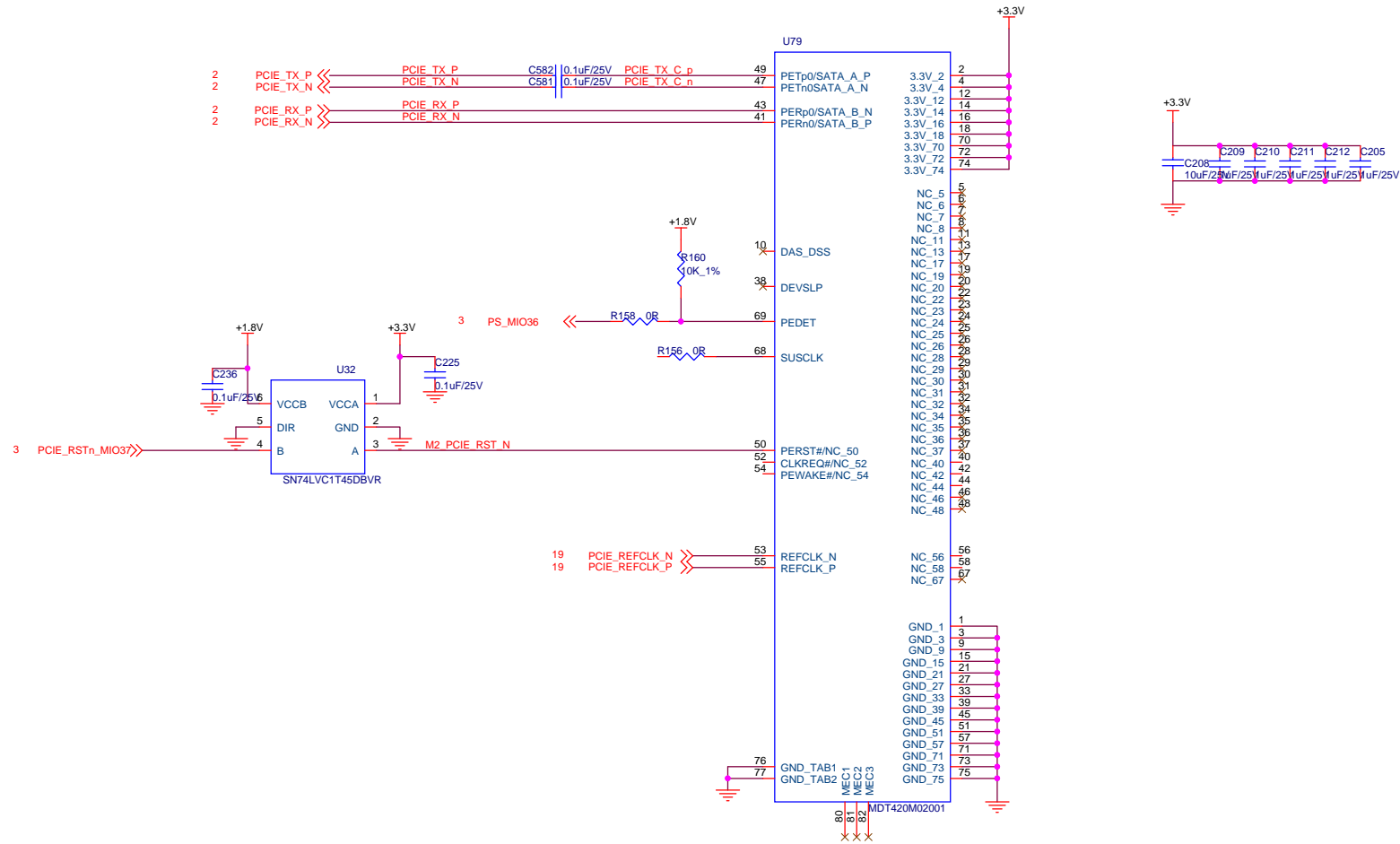




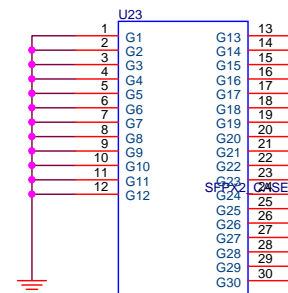
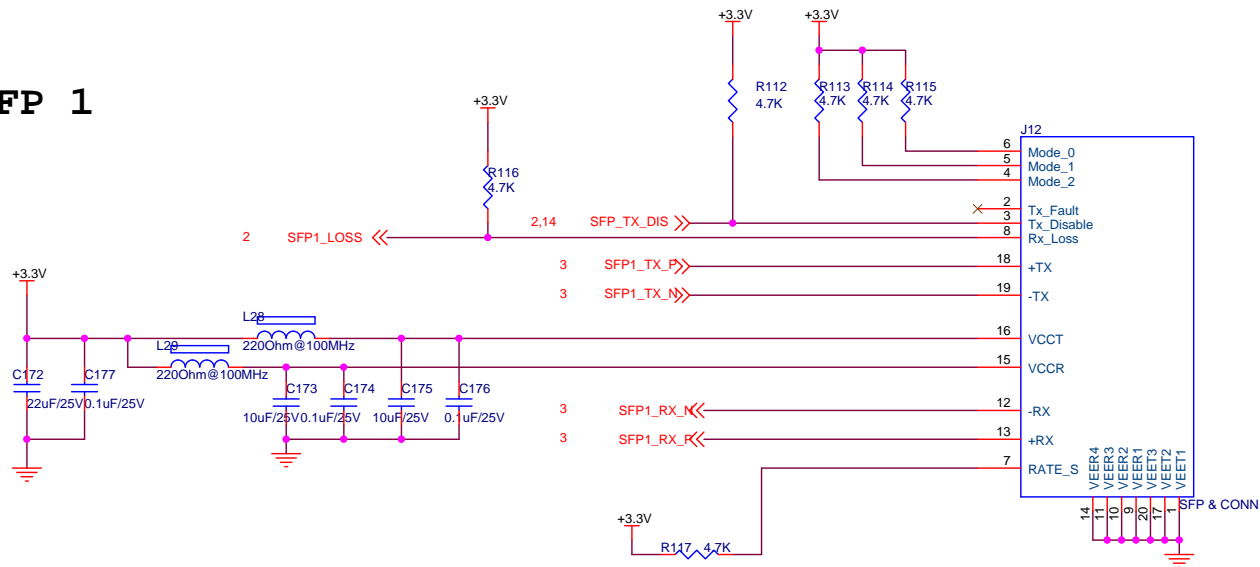


PCIE X2 SLOT

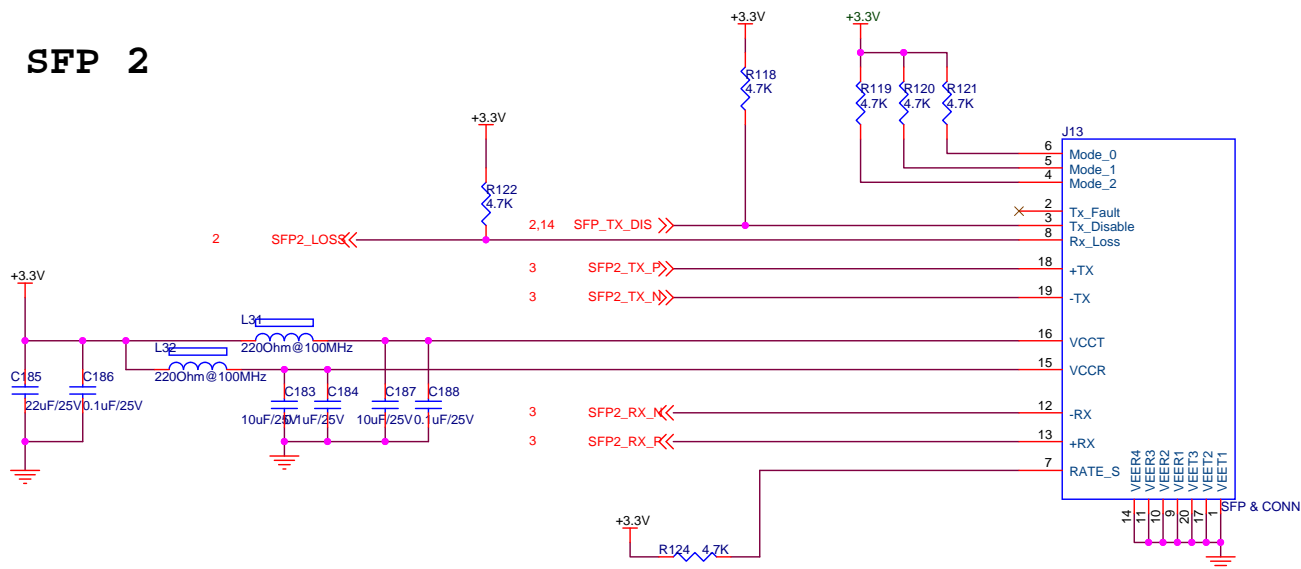




SFP 1

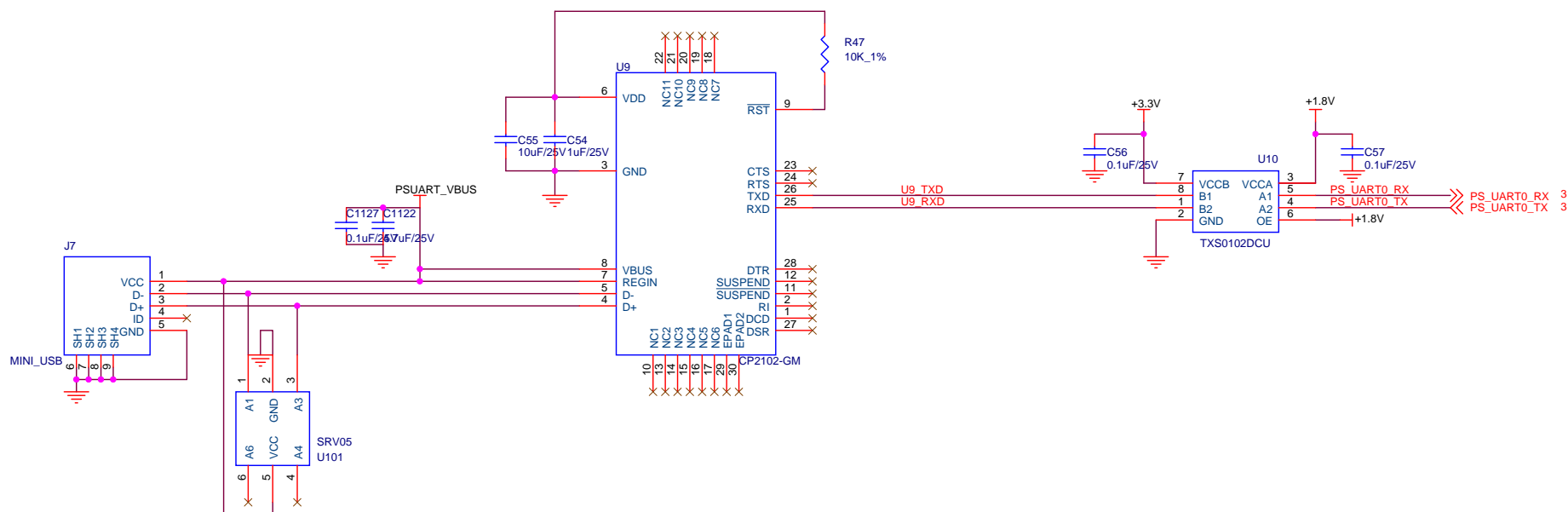


SFP 2

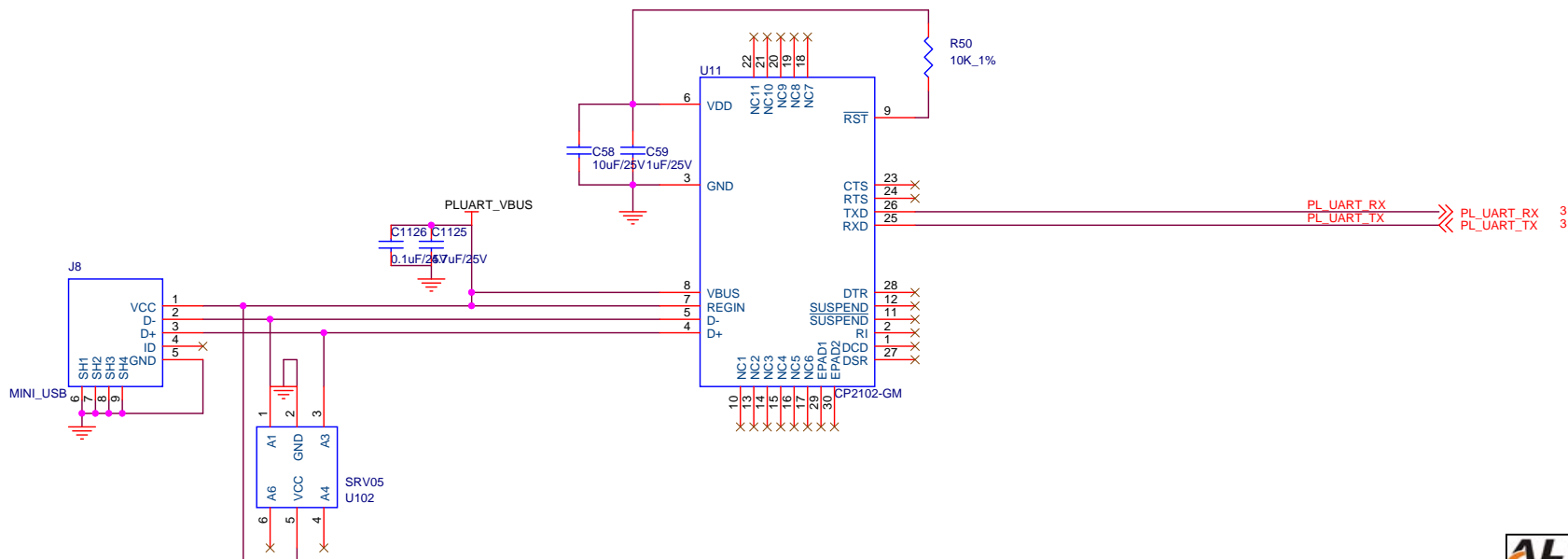


ALINX Confidential

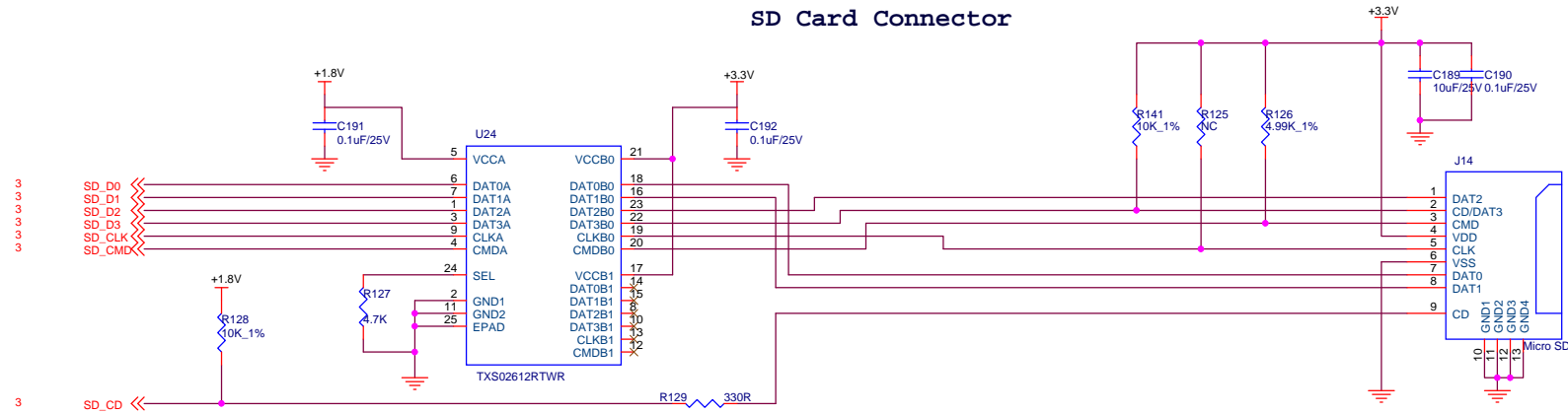
PS UART PORT



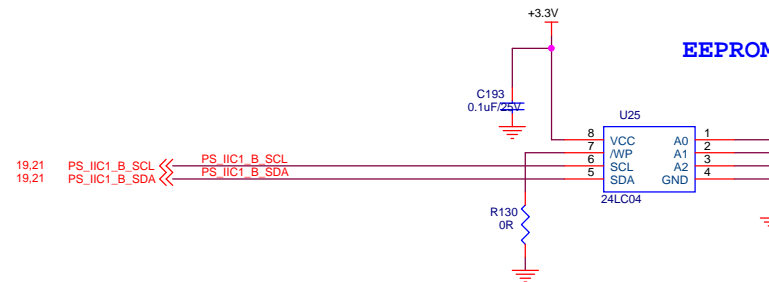
PL UART PORT



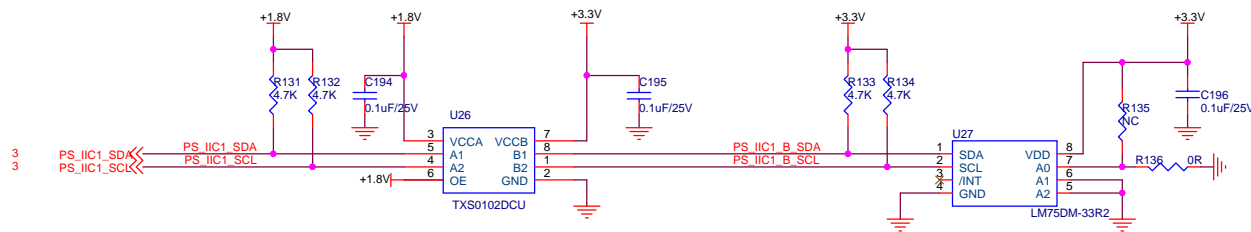
SD Card Connector

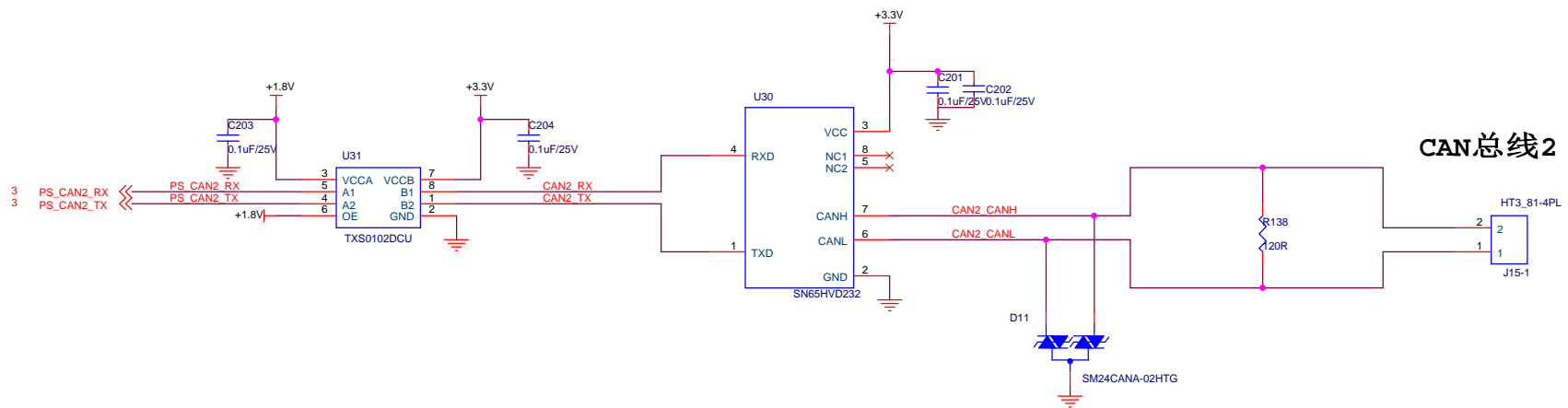
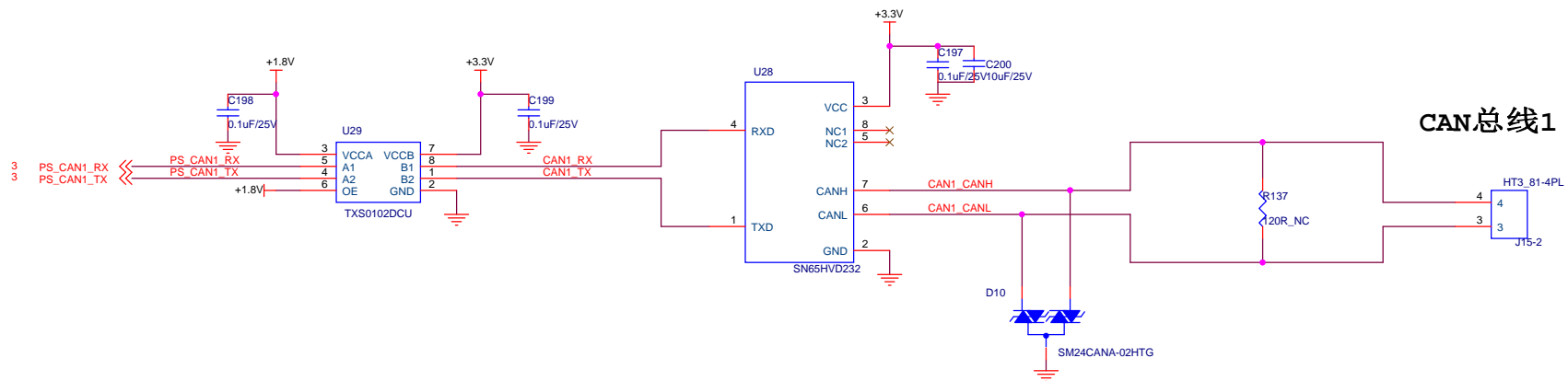


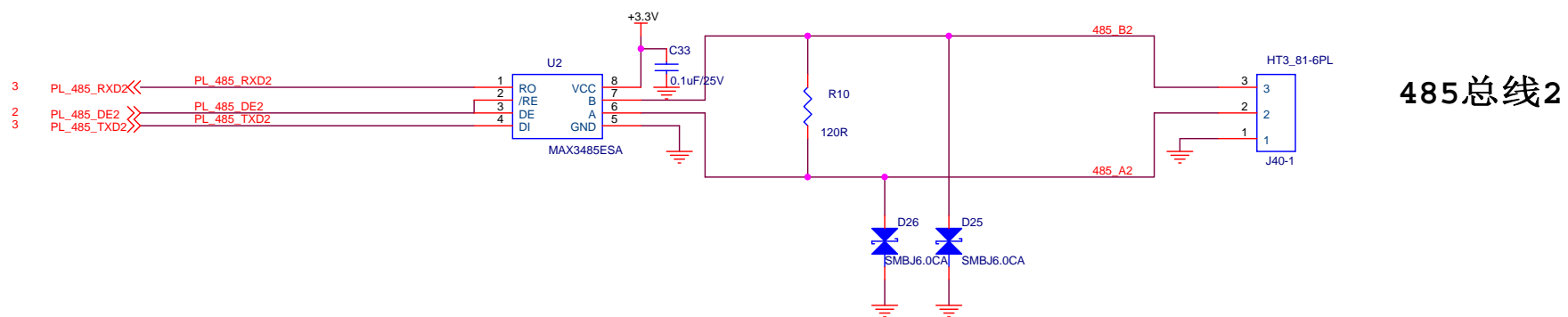
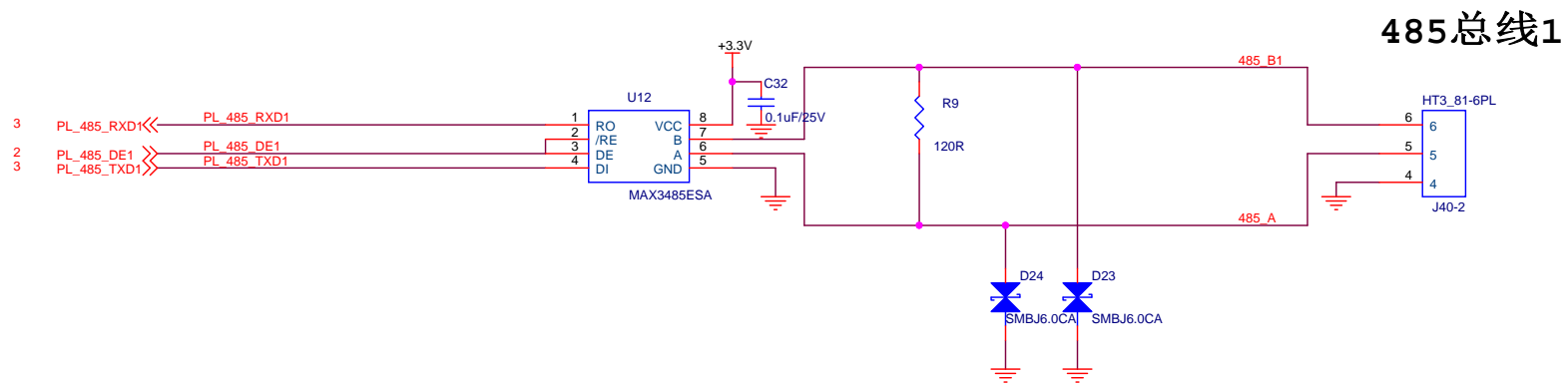
EEPROM

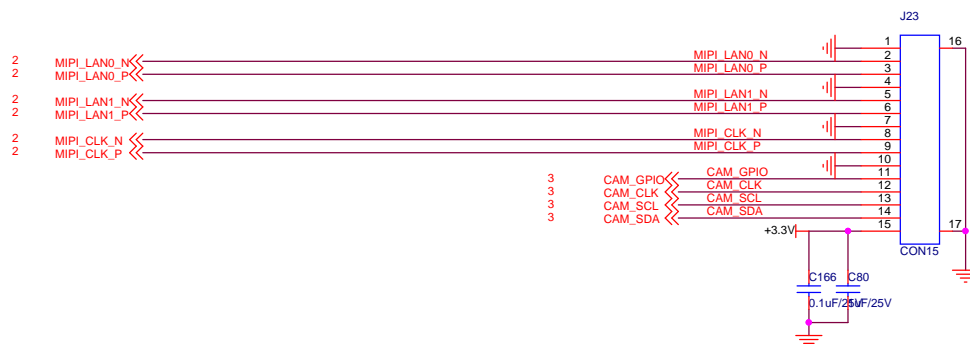
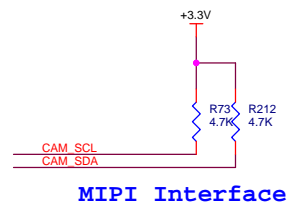
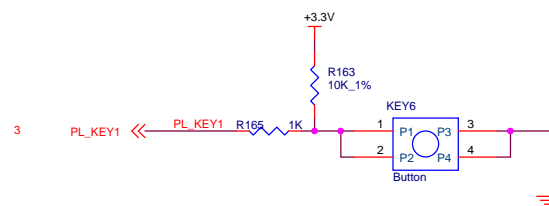
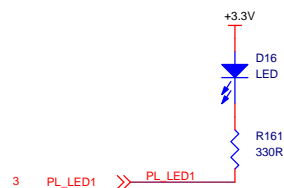
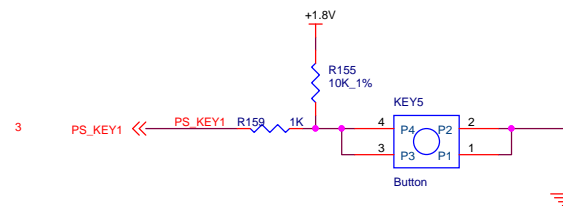
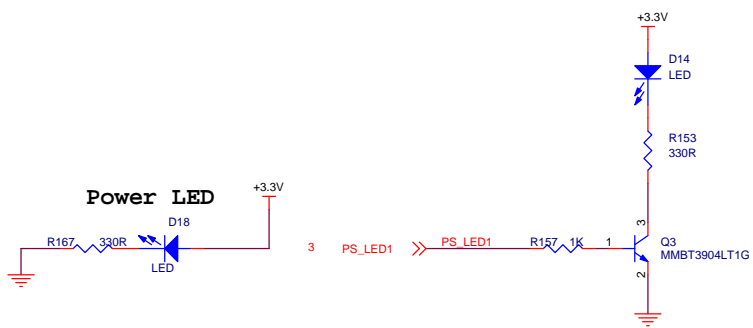


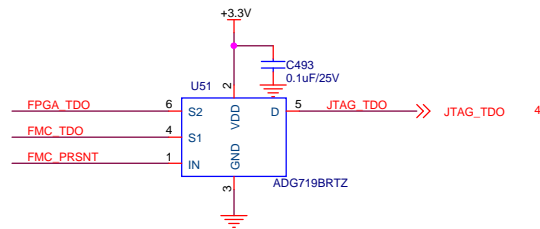
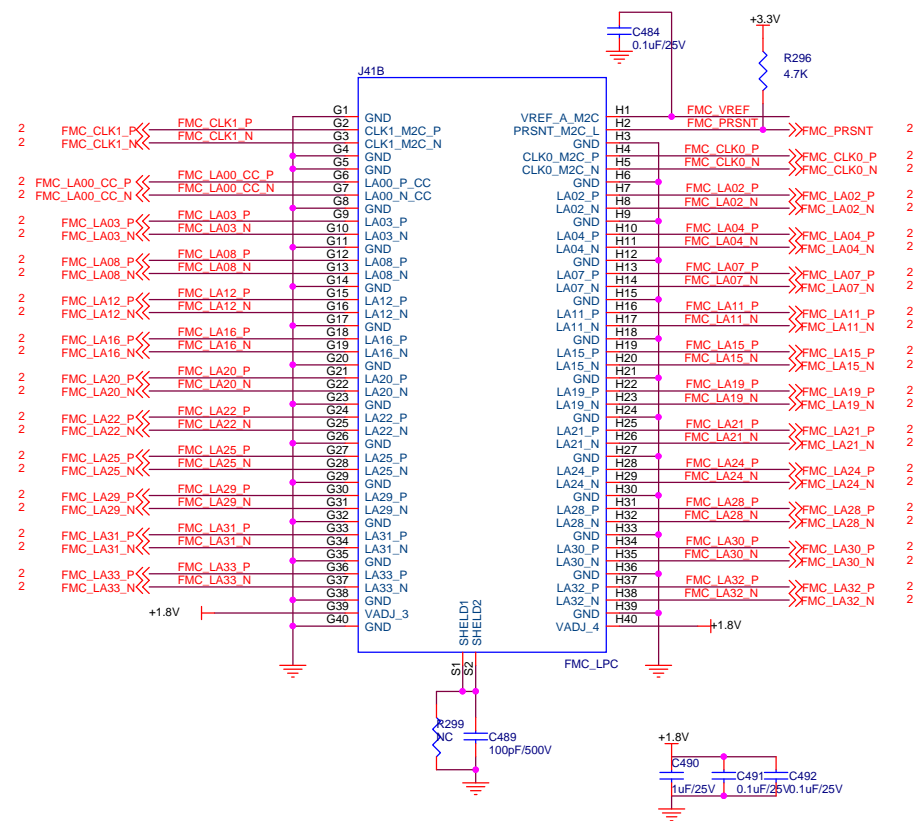
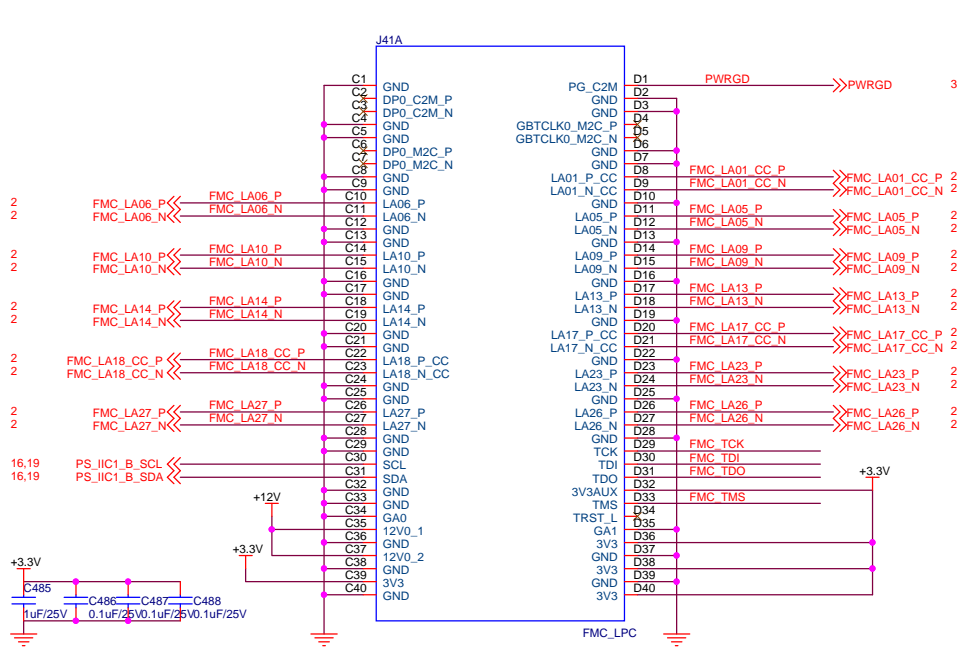
SENSOR



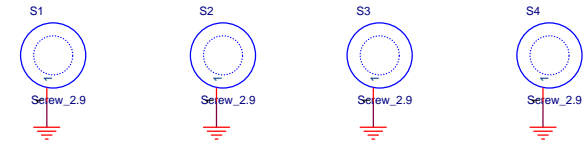








FMC_PRSENT = 1: JTAG_TDO = FPGA_TDO
FMC_PRSENT = 0: JTAG_TDO = FMC_TDO

[illegible]

+1.8V POWER

The schematic diagram illustrates the +1.8V power supply circuit. It uses an MP1482DS step-down converter. The input is +12V, which is filtered by capacitor C243 (10uF/25V) and resistor R179 (10K). The EN pin is connected to PS_POR_B via resistor R180. The SW pin is connected to the output, which is filtered by capacitor C242 (0.01uF/25V) and inductor L34 (CMLE051E-2R2MS). The output is also filtered by a pi-network consisting of resistor R183 (9.53K), resistor R185 (10K_1%), and a parallel combination of capacitors C247 (10uF/25V), C248 (10uF/25V), C249 (10uF/25V), C250 (10uF/25V), C251 (10uF/25V), and C252 (10uF/25V). The feedback pin (FB) is connected to the output via resistor R183. The ground pin (GND) is connected to ground via resistor R187 (2.2K) and capacitor C246 (0.1uF/25V). The compensation pin (COMP) is connected to ground via capacitor C259 (33nF/25V).