HW-Z1-ZCU104 Evaluation Board (XCZU7EV-2FFVC1156)

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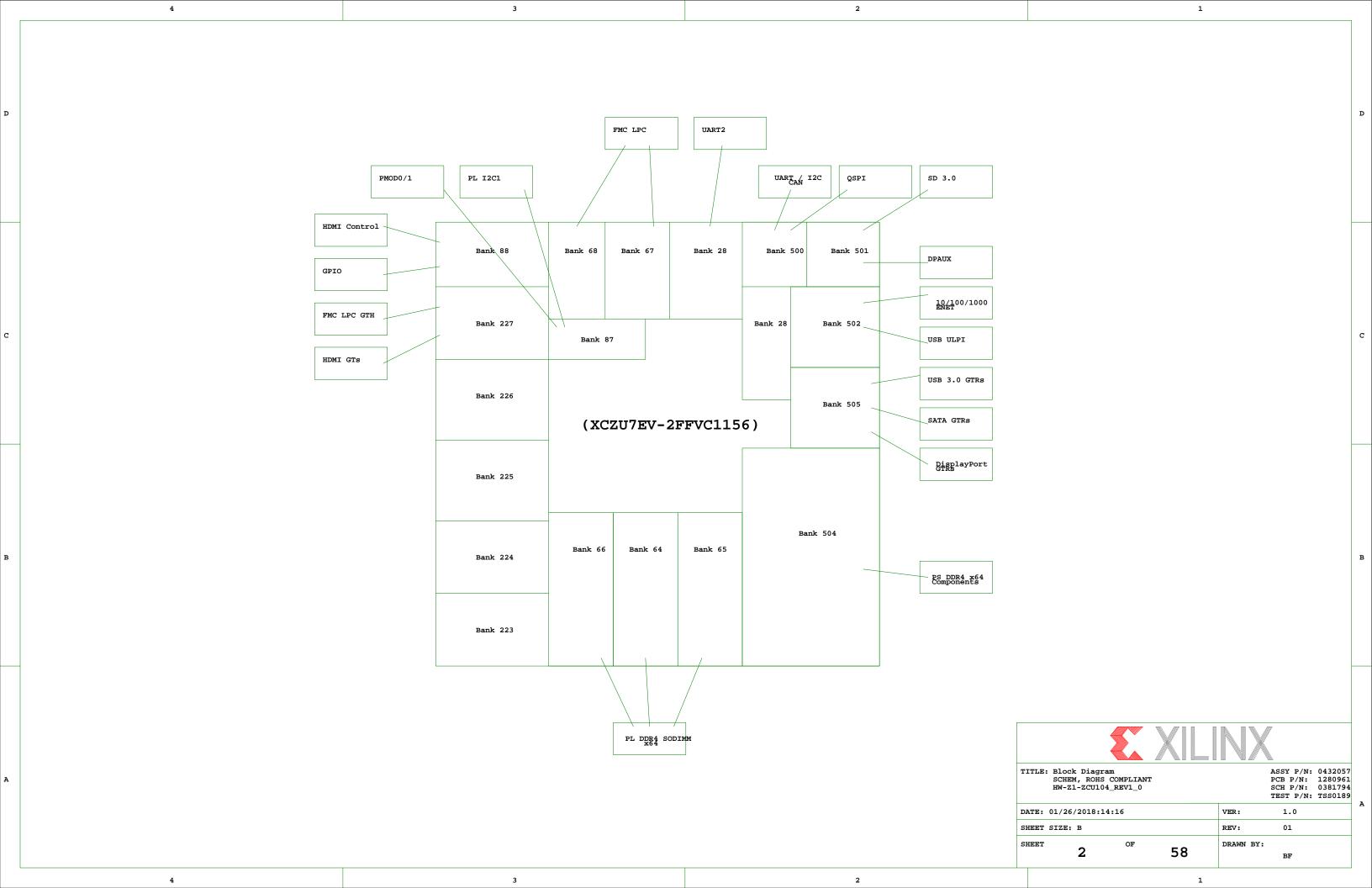
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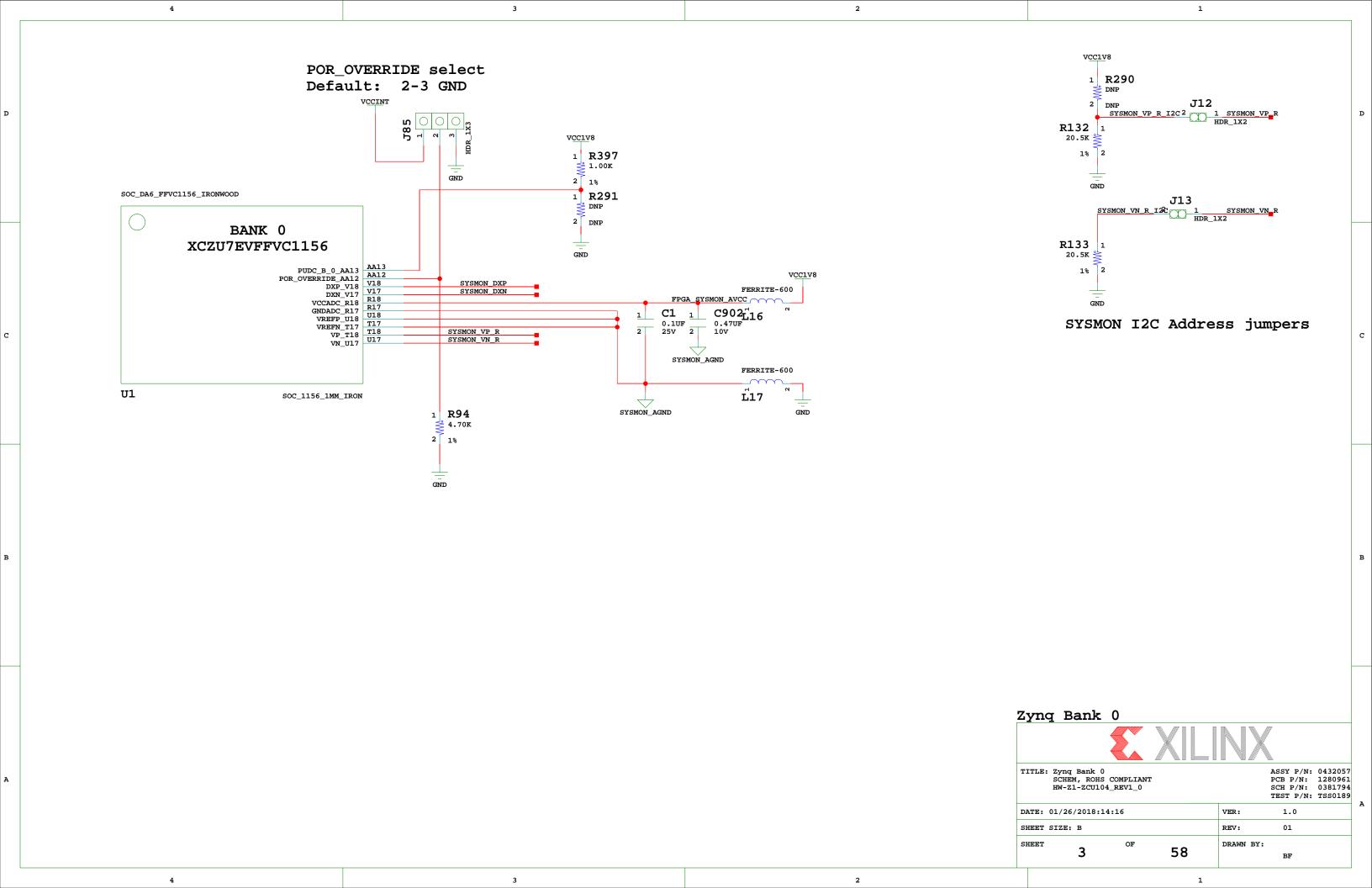
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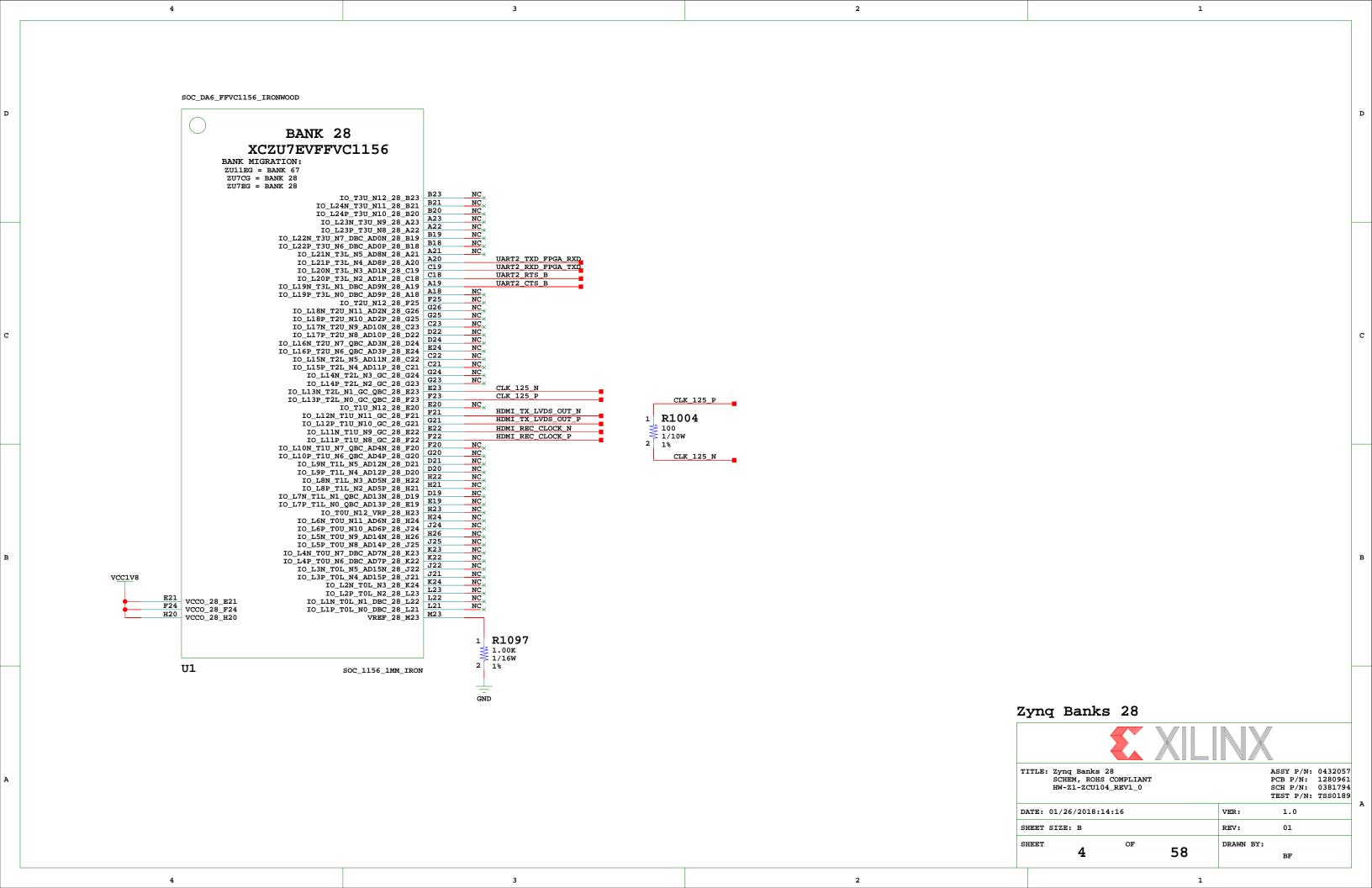
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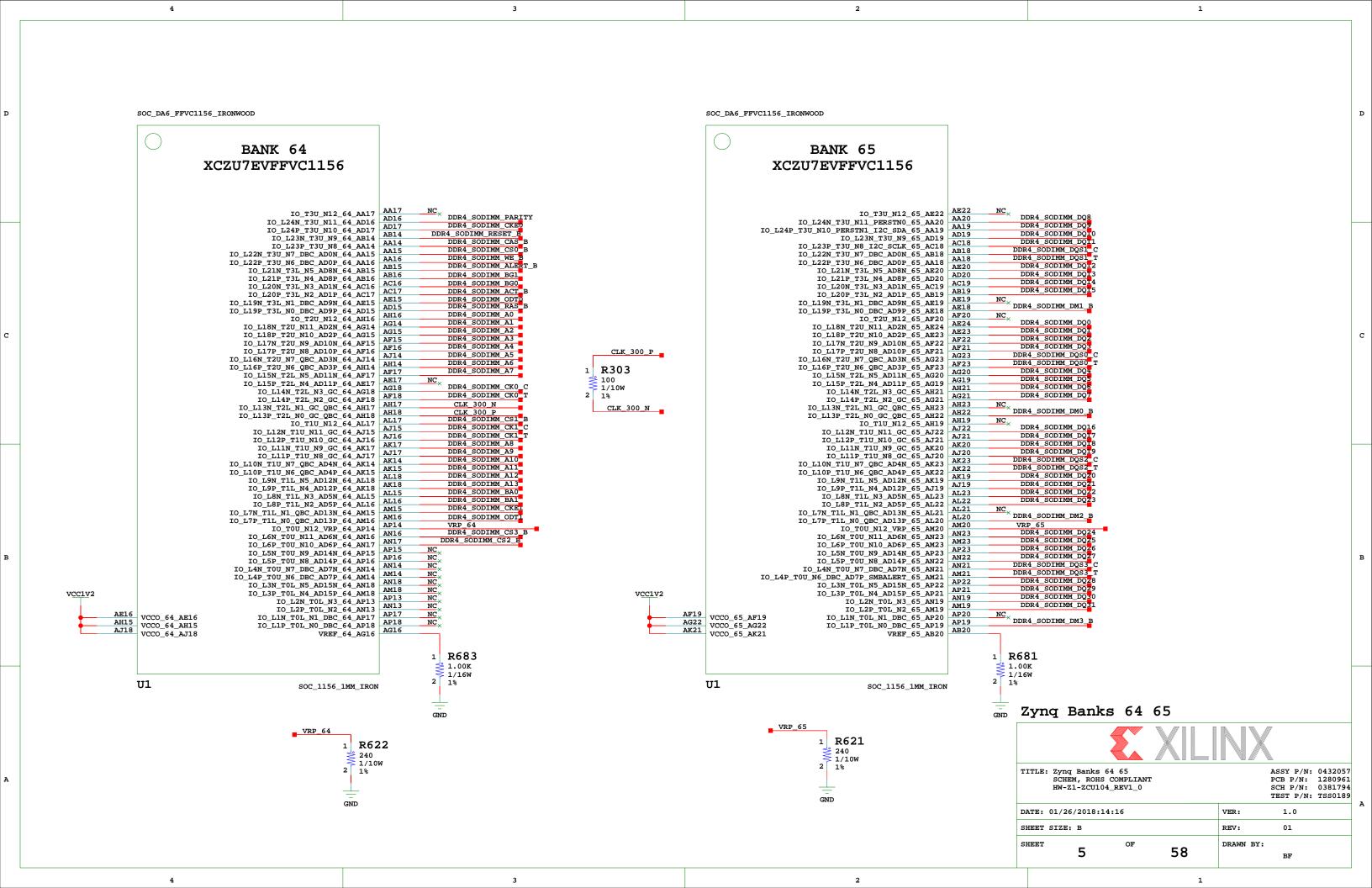
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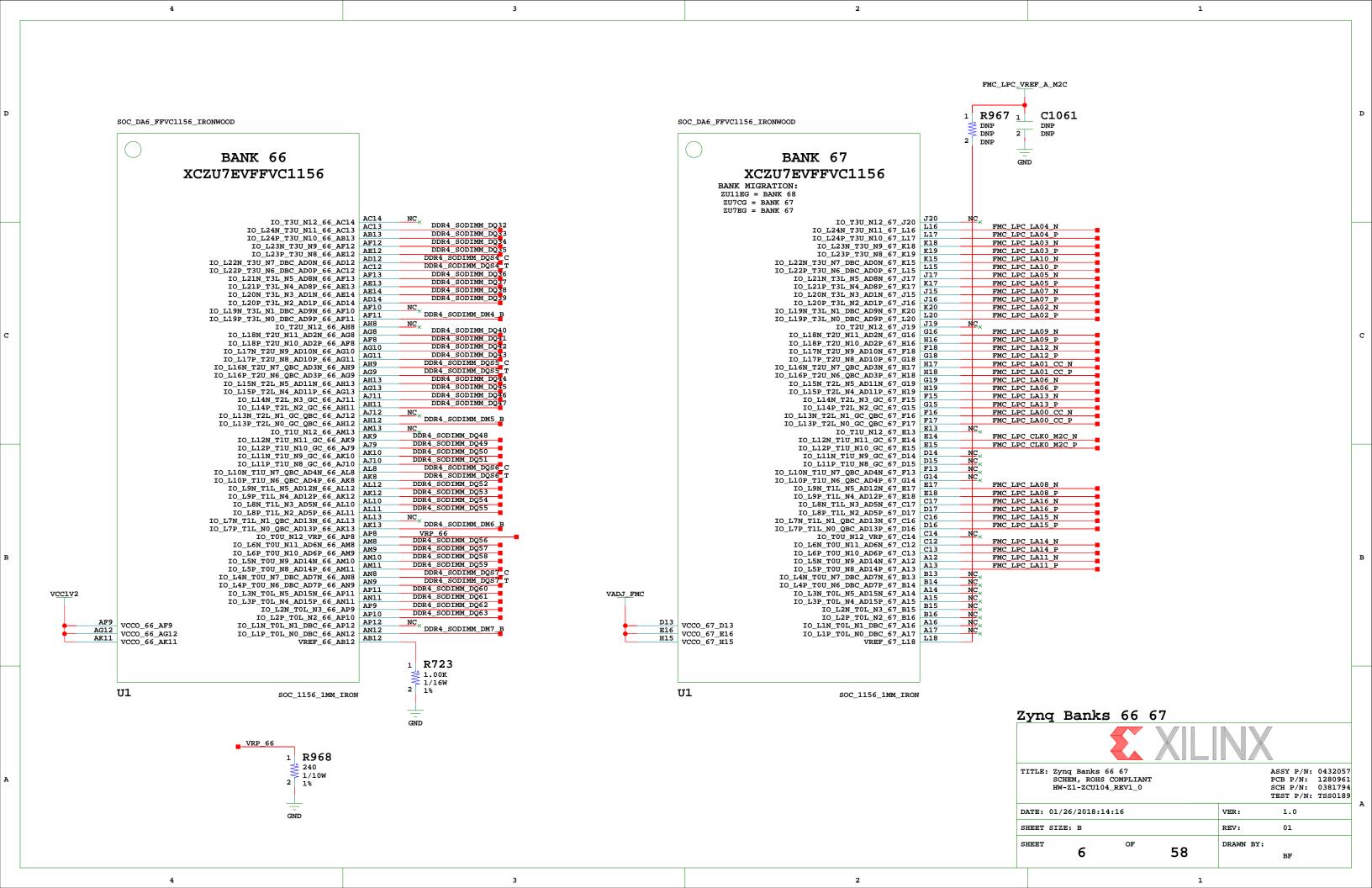


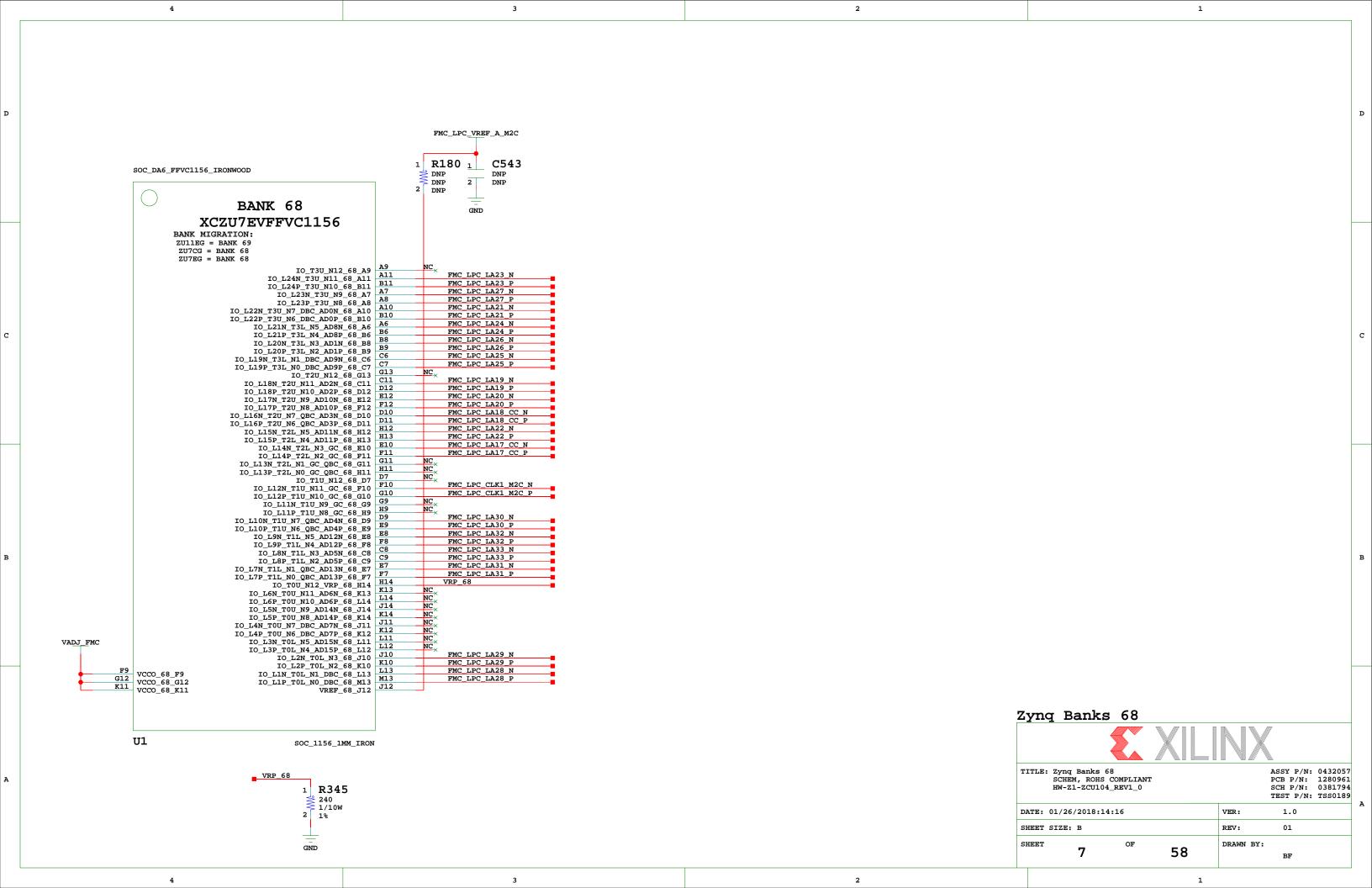


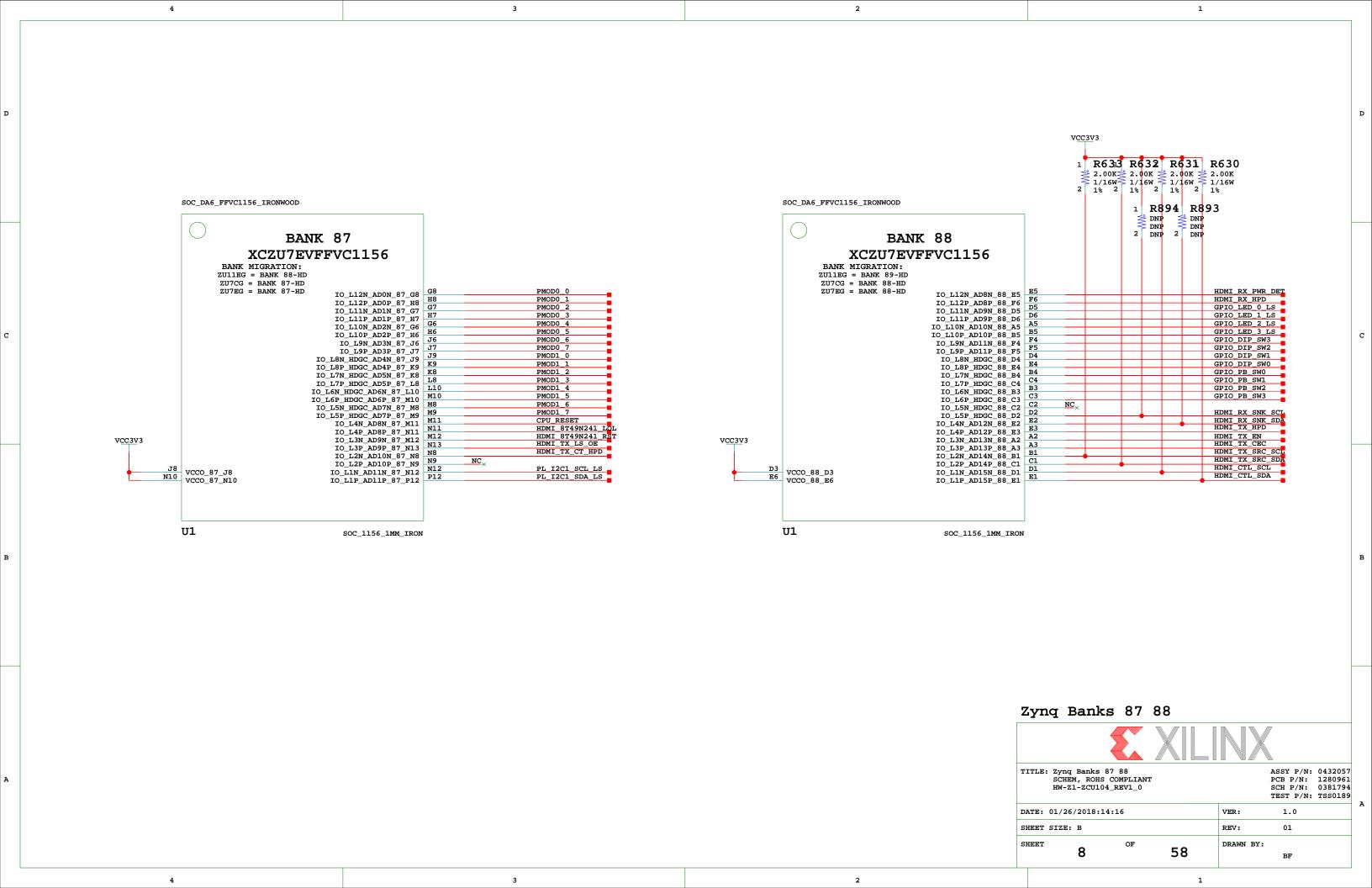


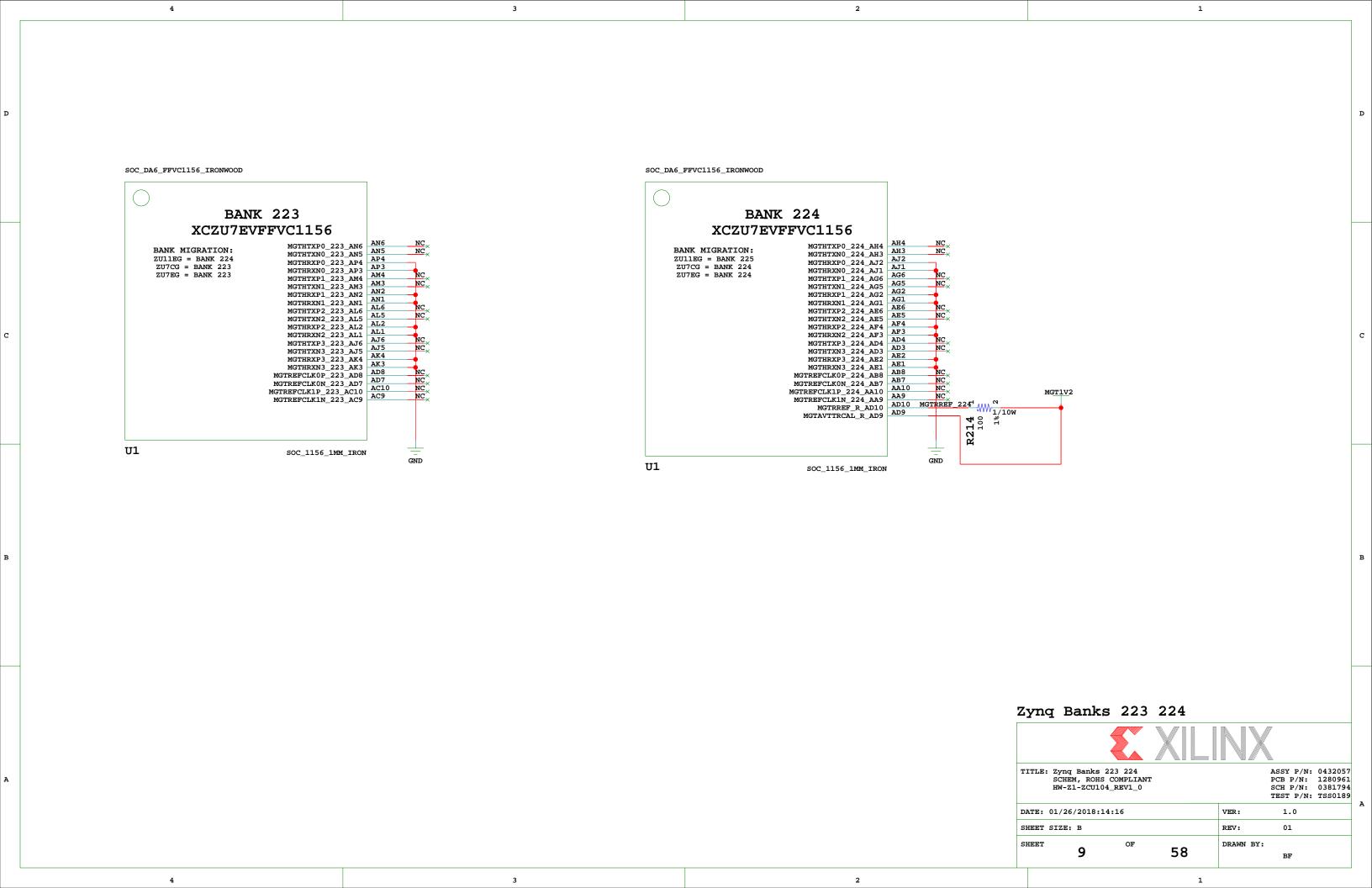


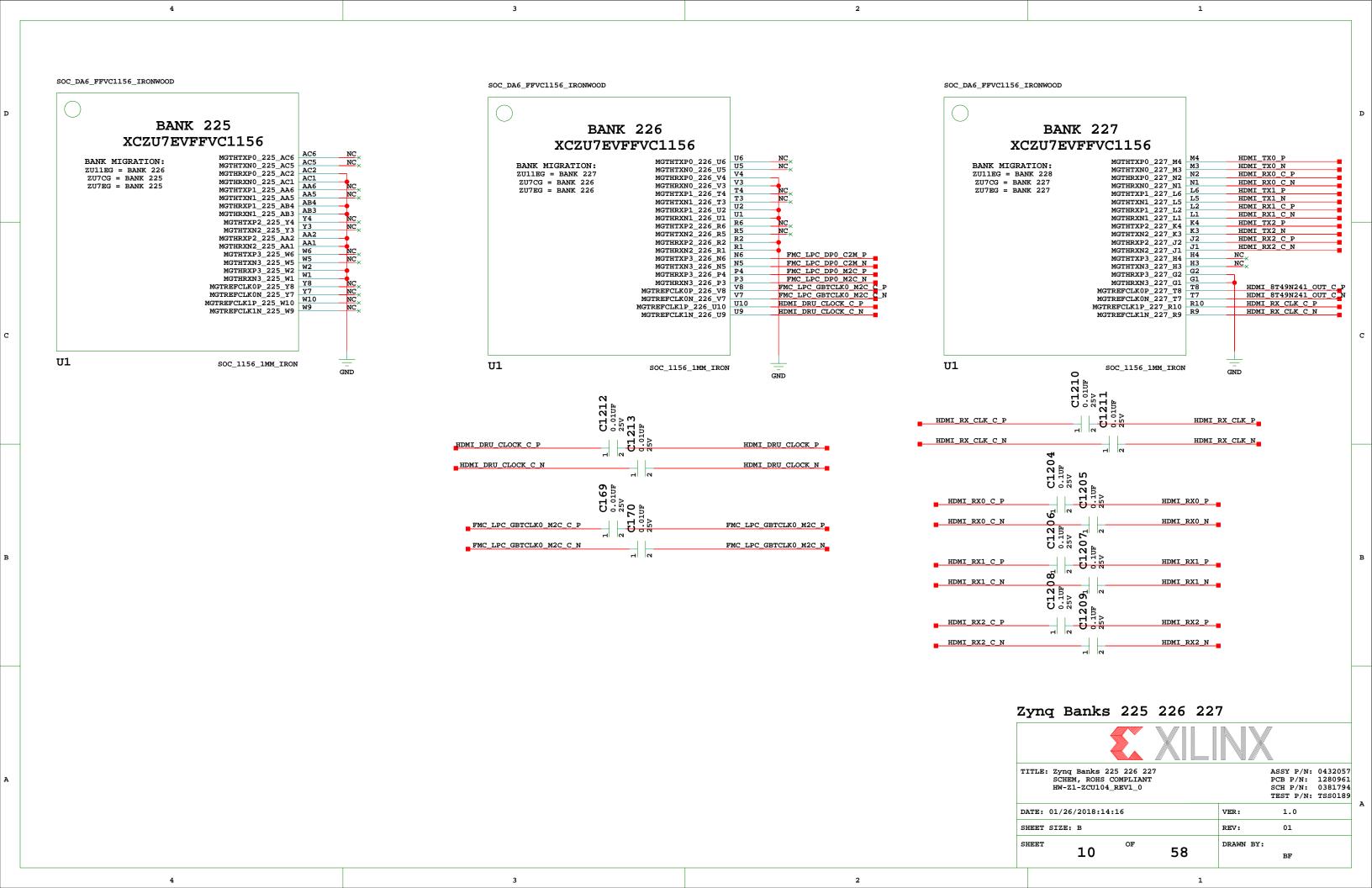


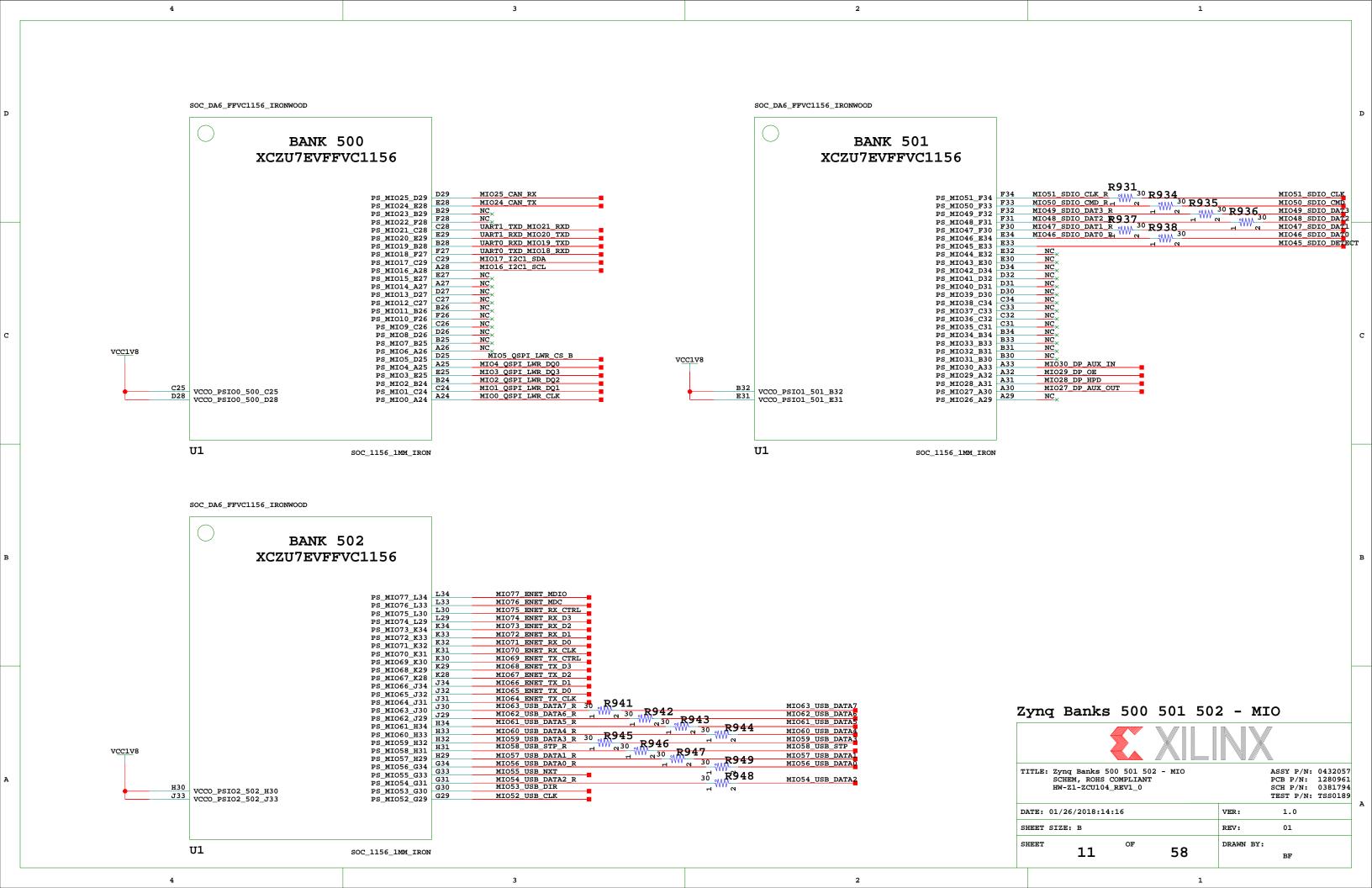


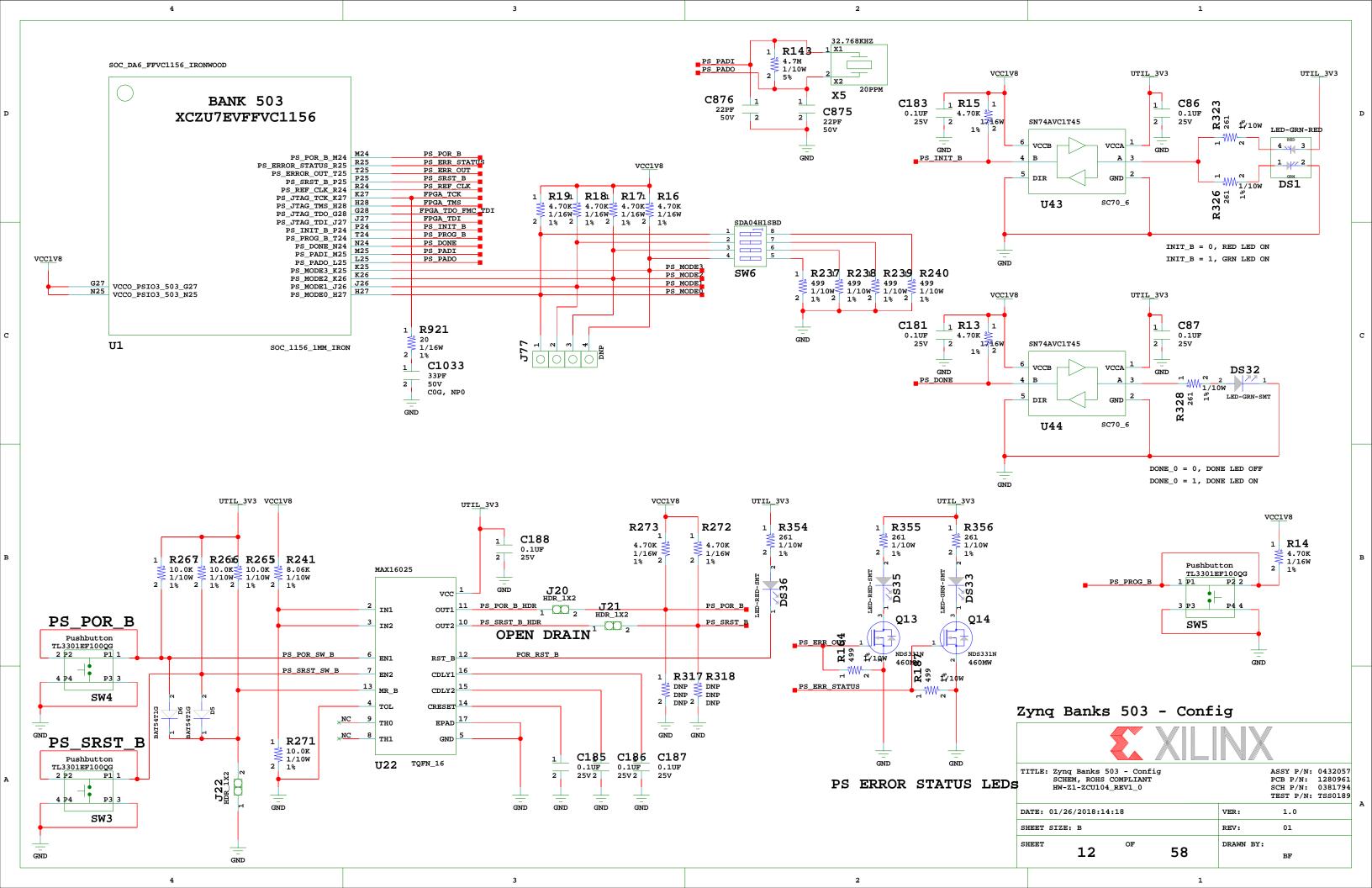


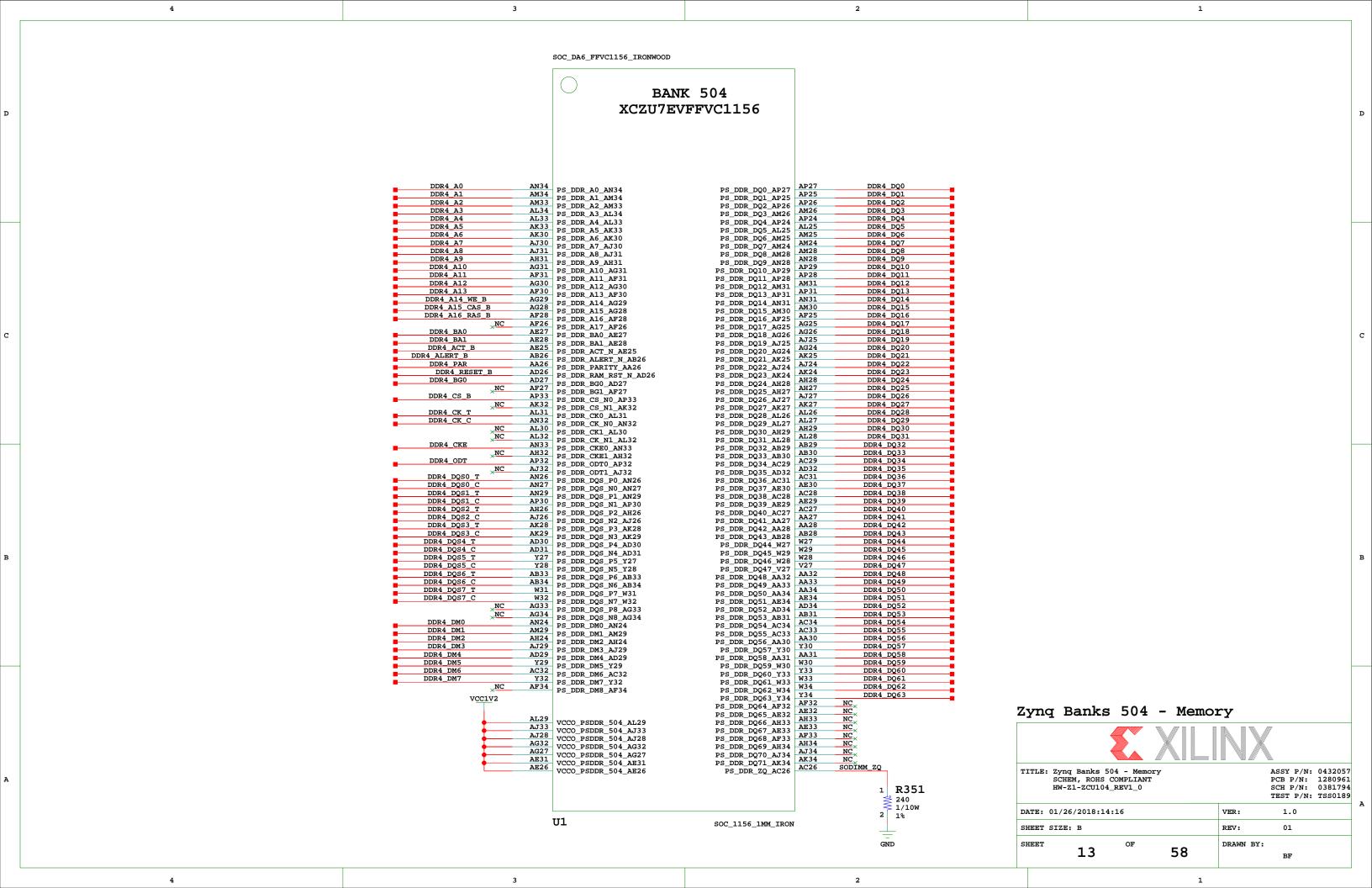


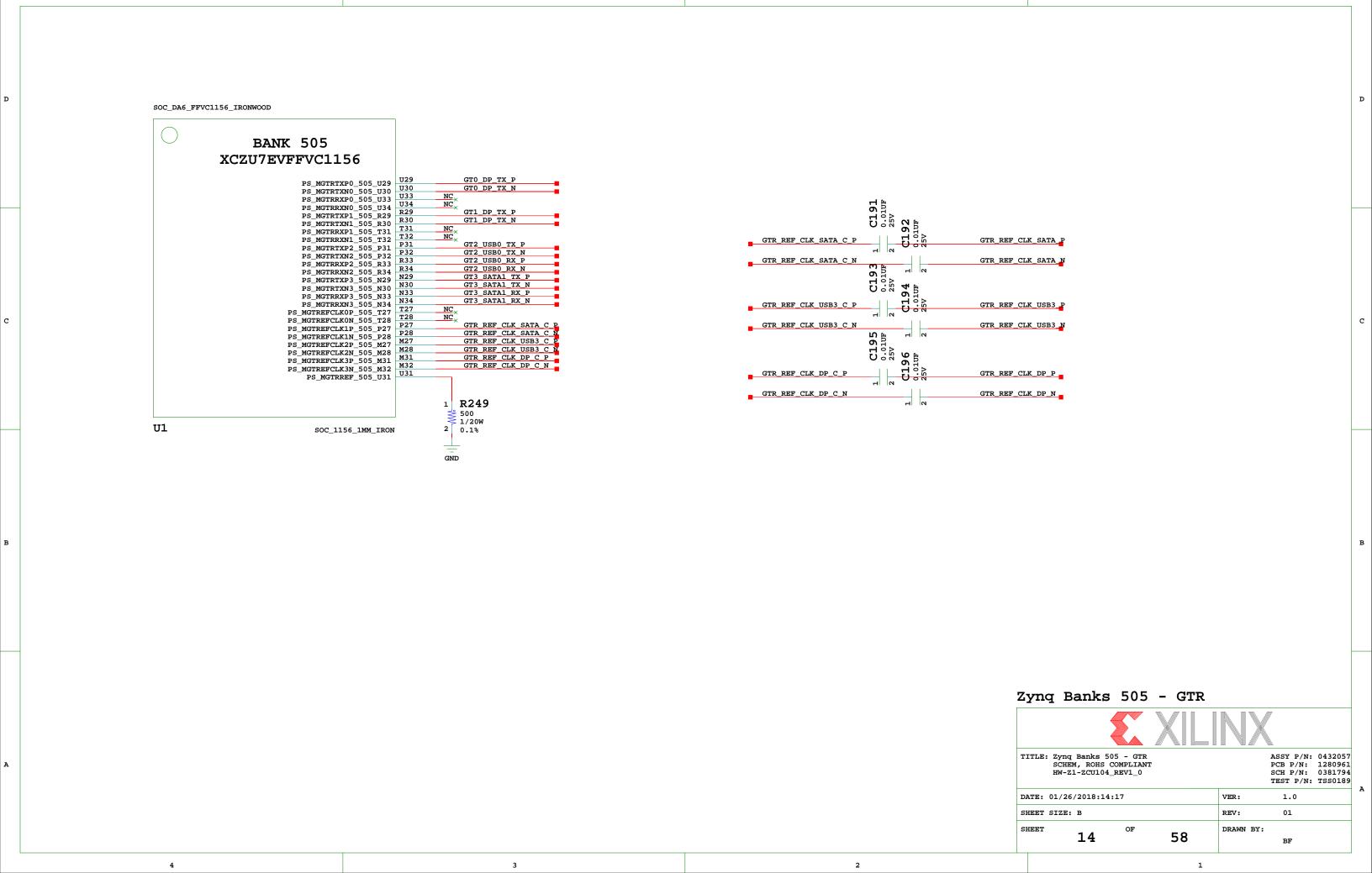




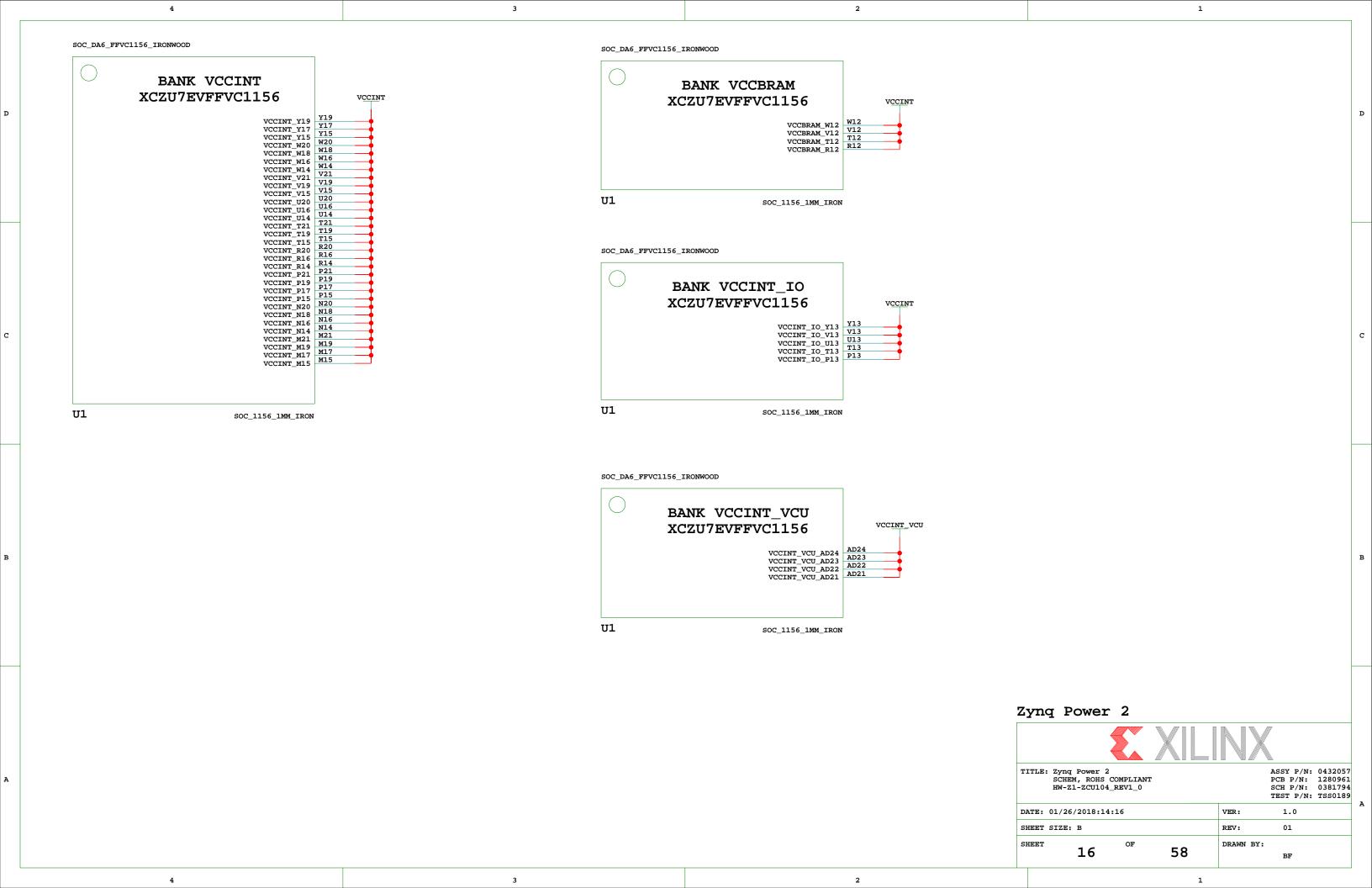


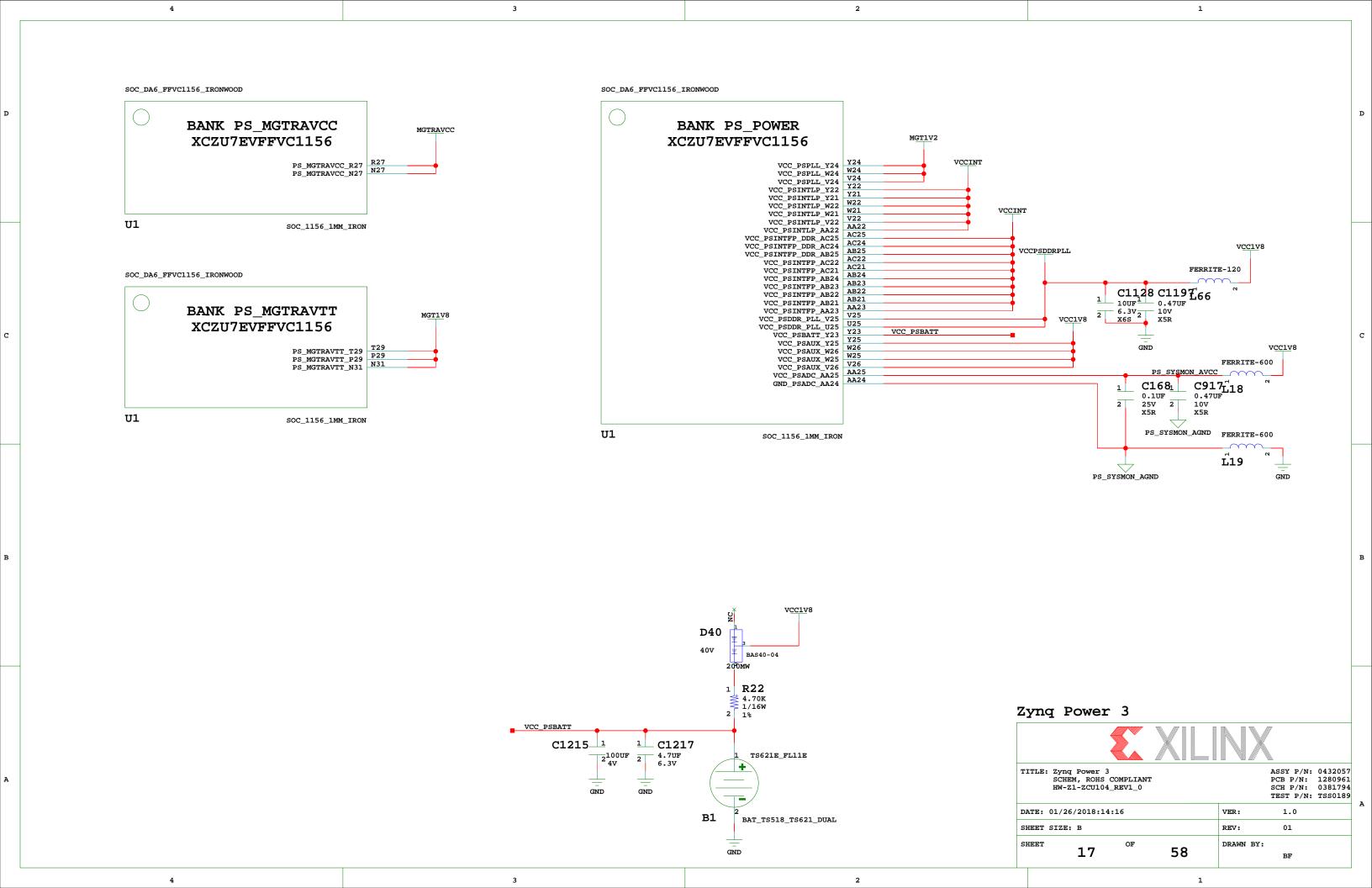


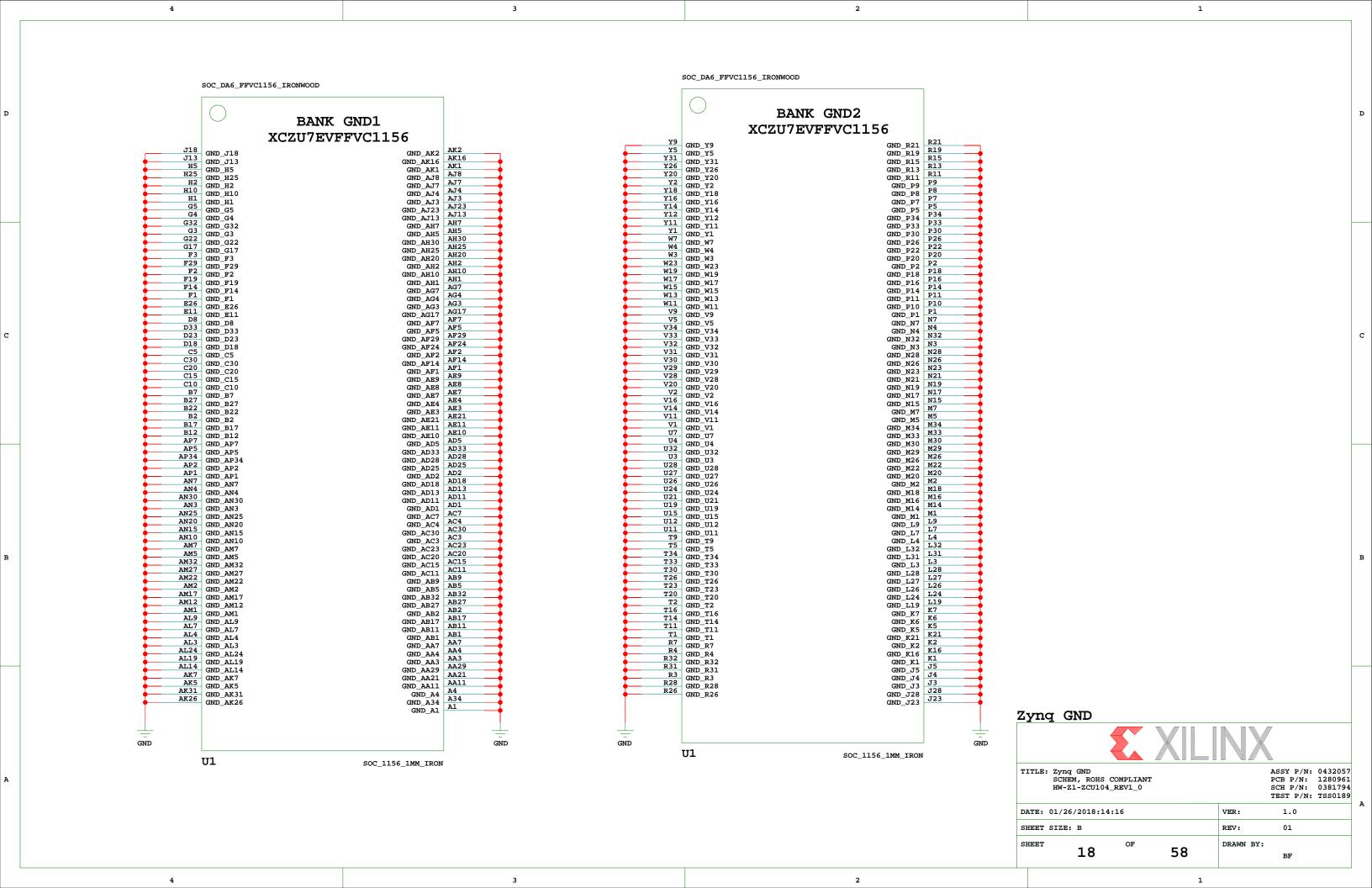


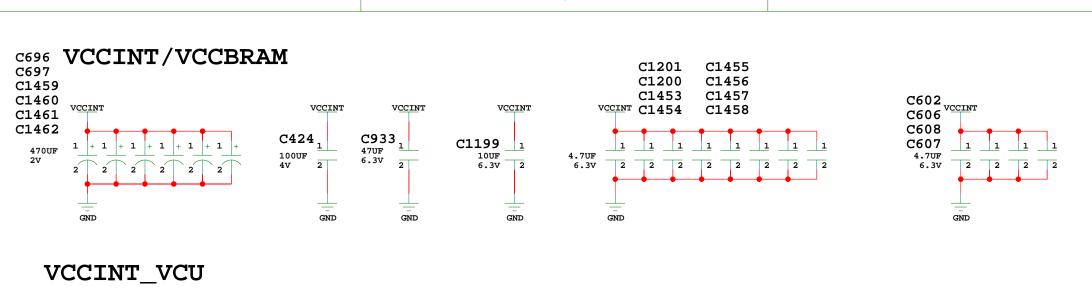


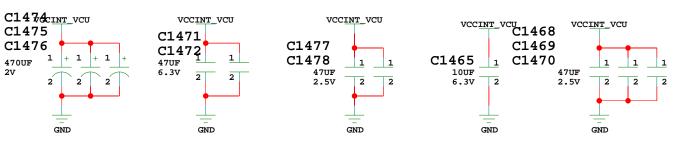


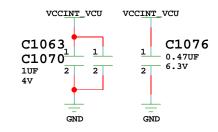




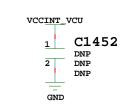






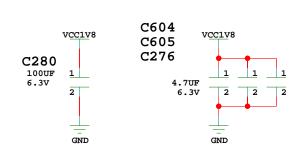


Place these 0201 capacitors directly under U1 near pins AD24/AD23/AD22/AD21 Provide a dual footprint to also support a 0402 cap

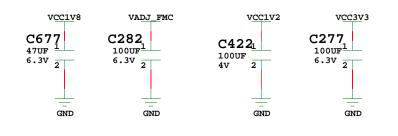


VCCINT_VCU Test Point Place just outside U1 on bottomside

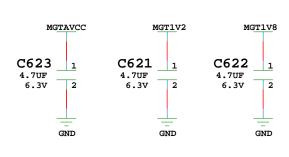
VCCAUX / VCCAUX_IO



VCCO BANKS 0 28 64 65 66 67 68 87 88



MGT Power Rails



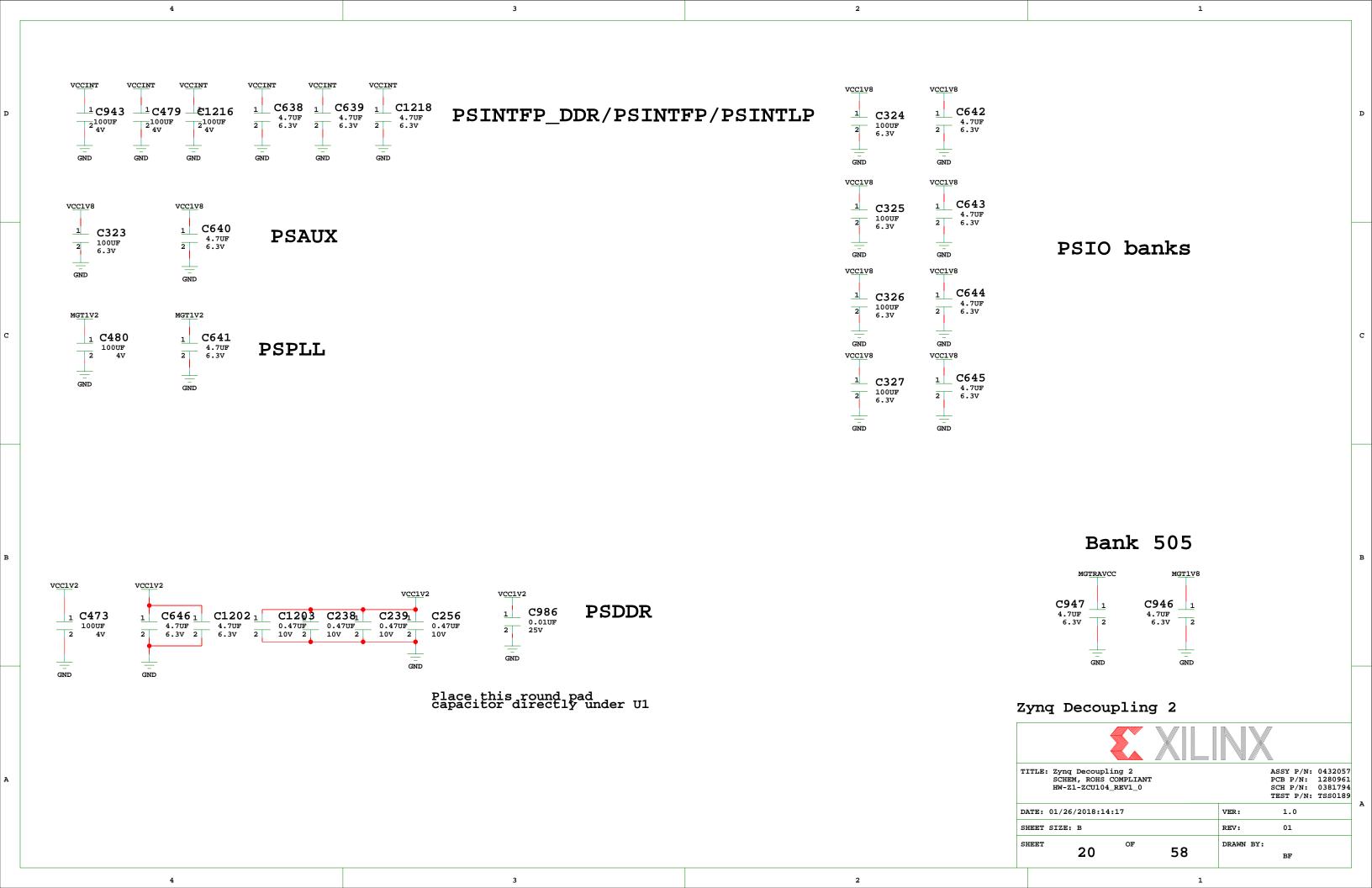
Zynq Decoupling 1

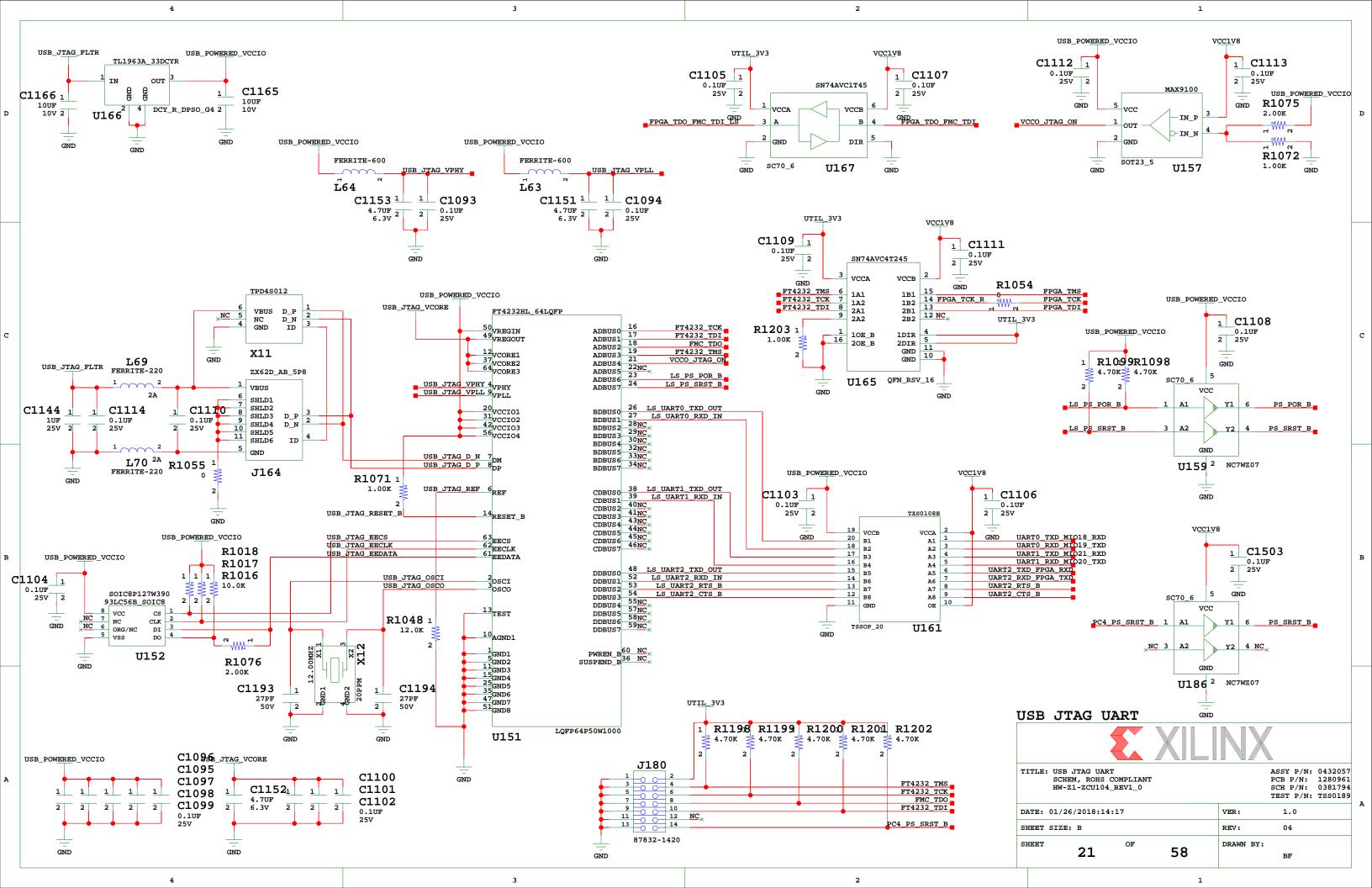


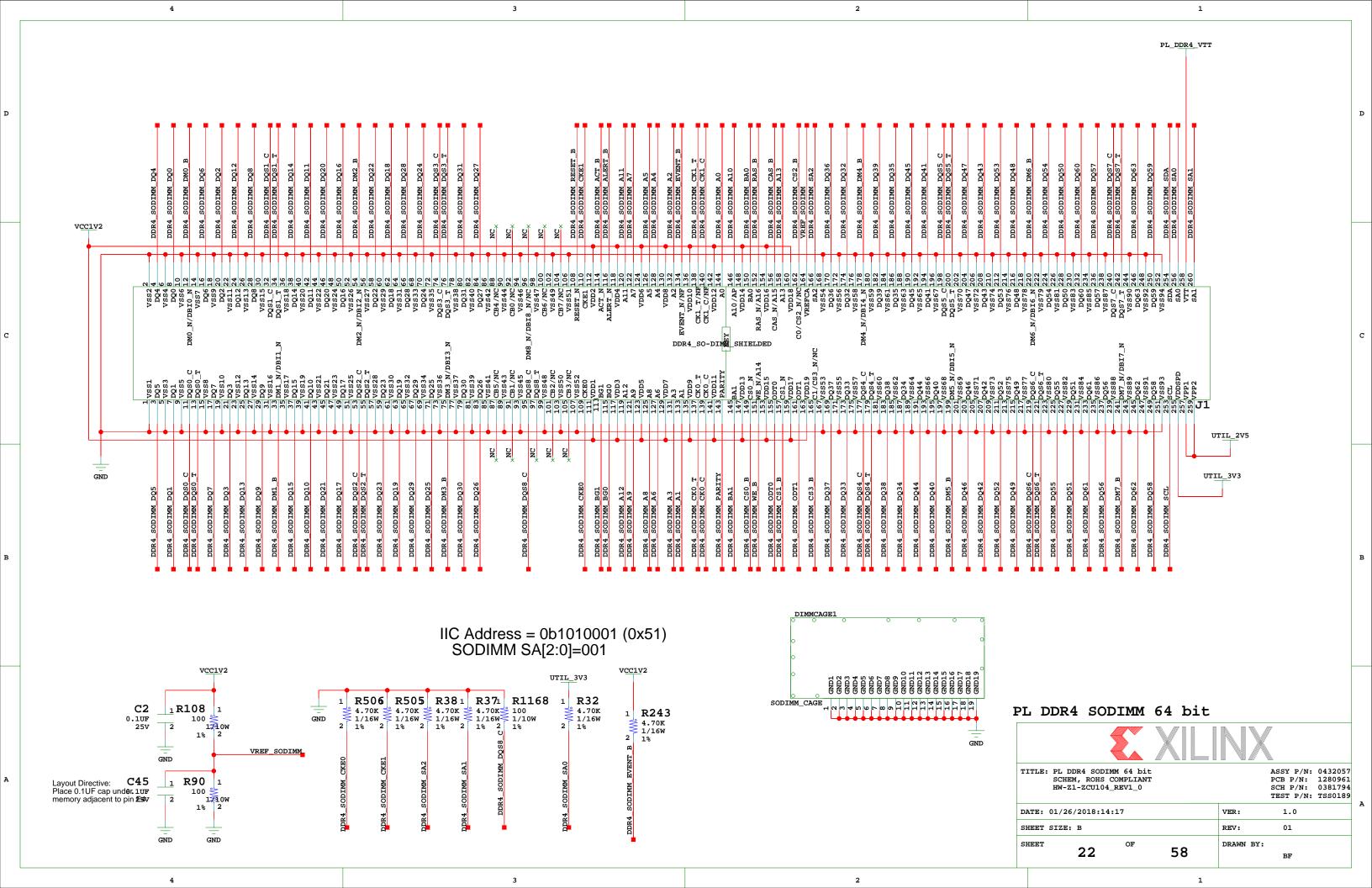
3

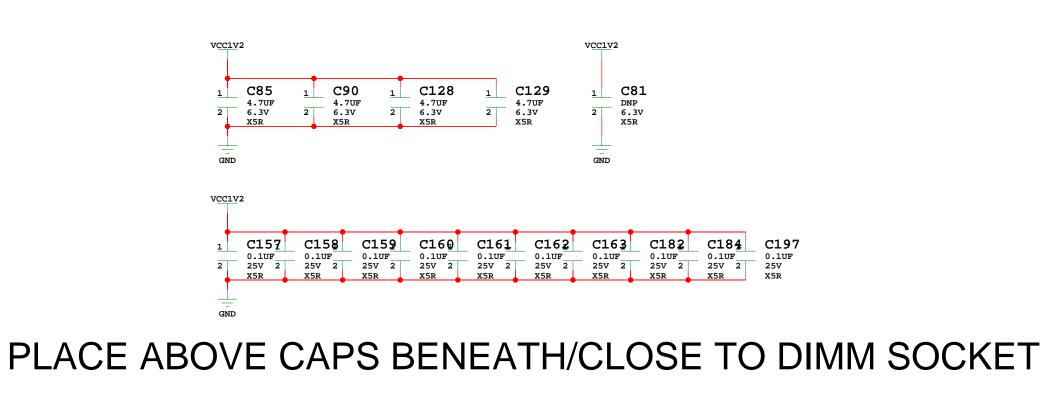
1

1

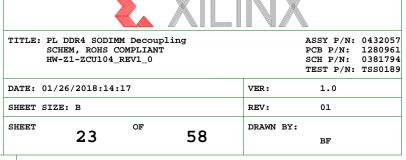




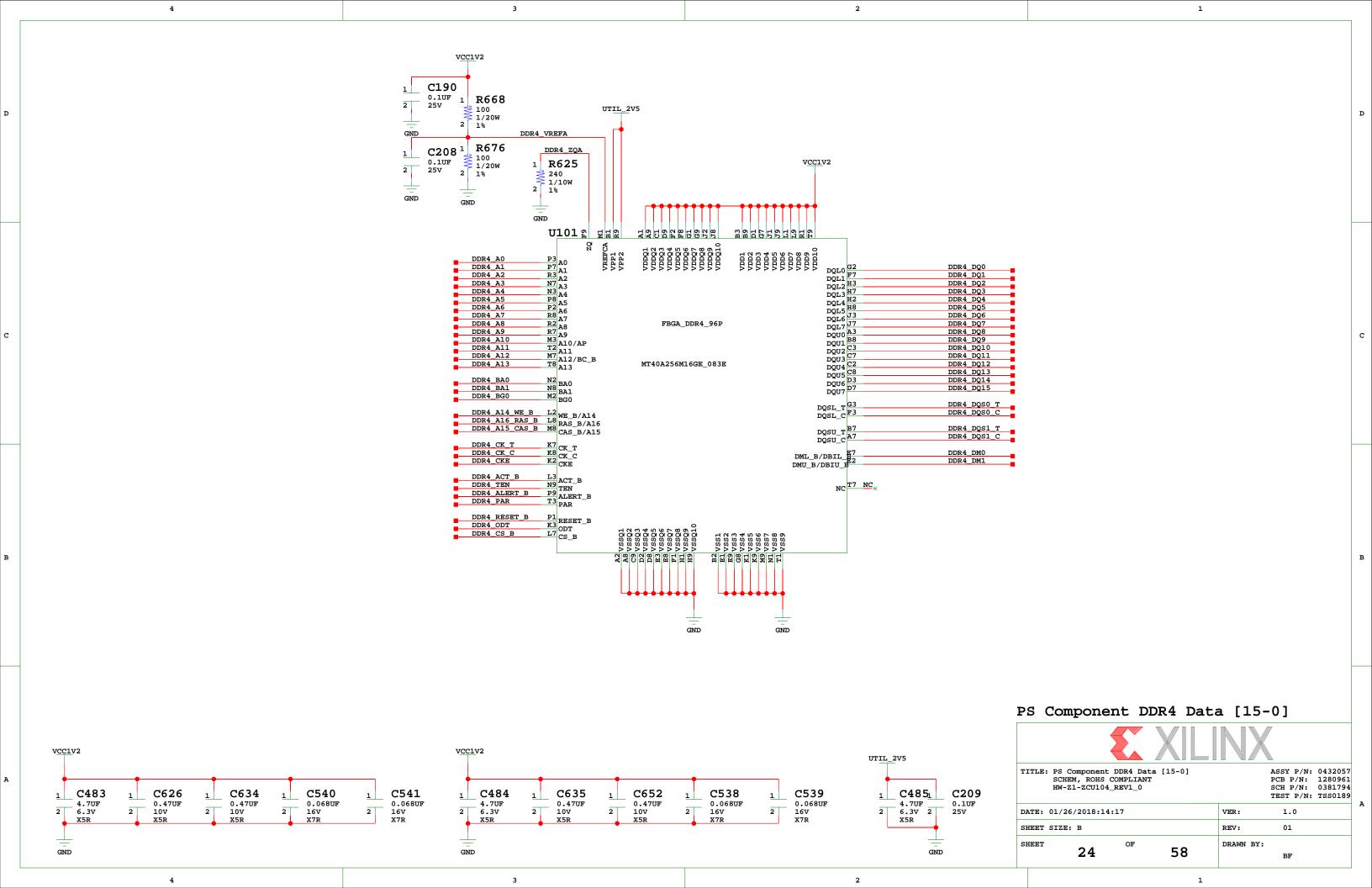


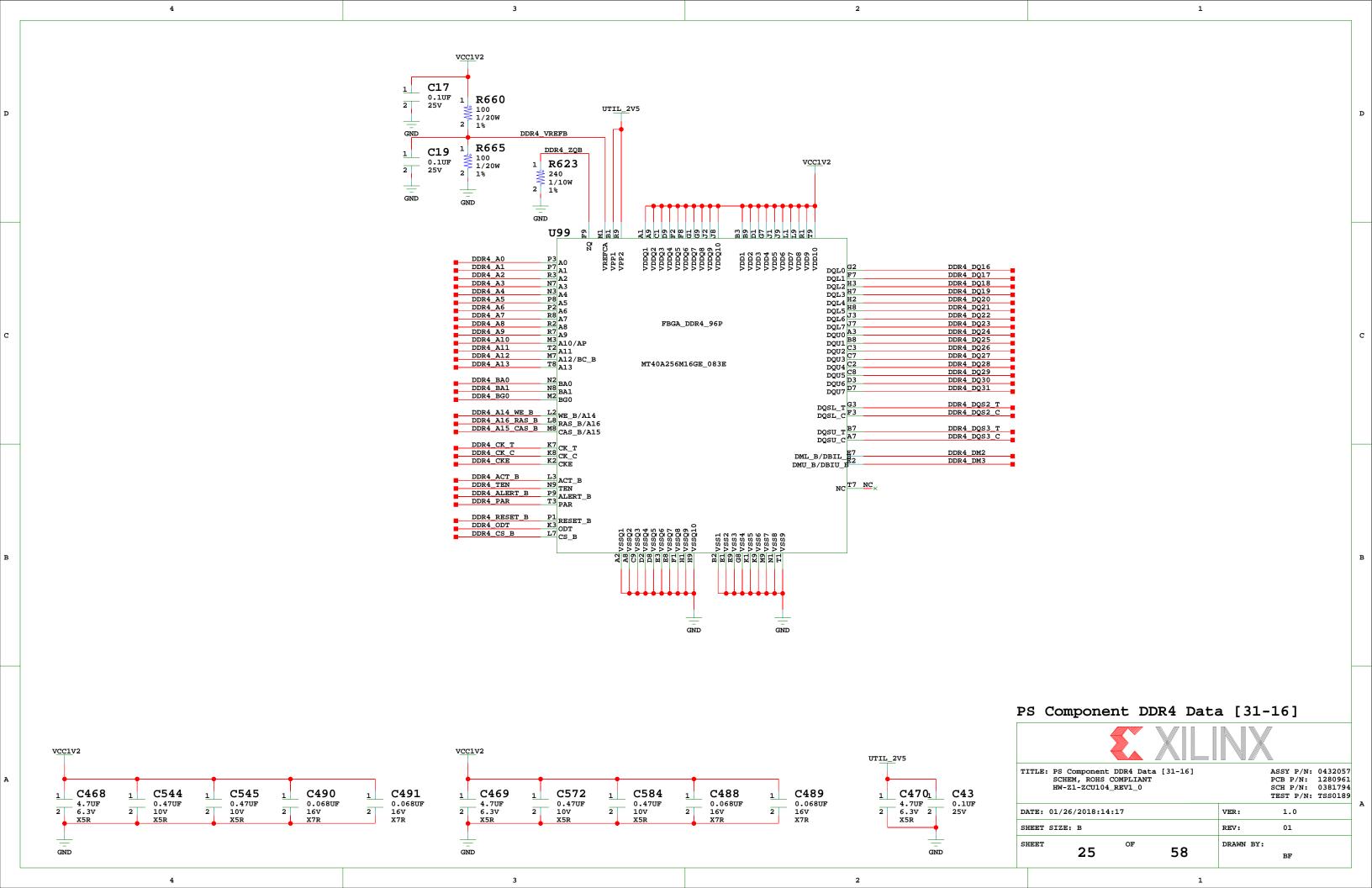


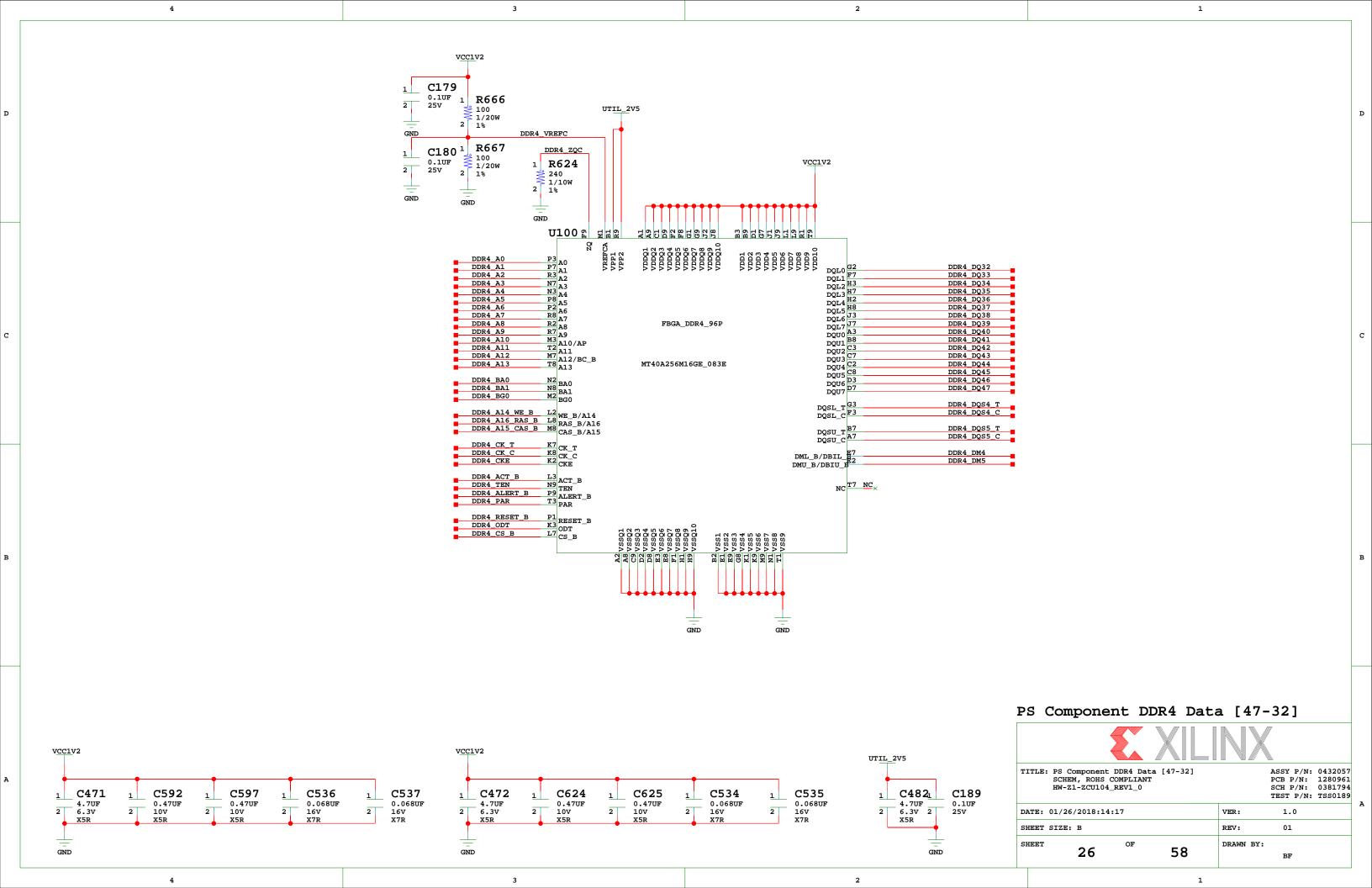
PL DDR4 SODIMM Decoupling

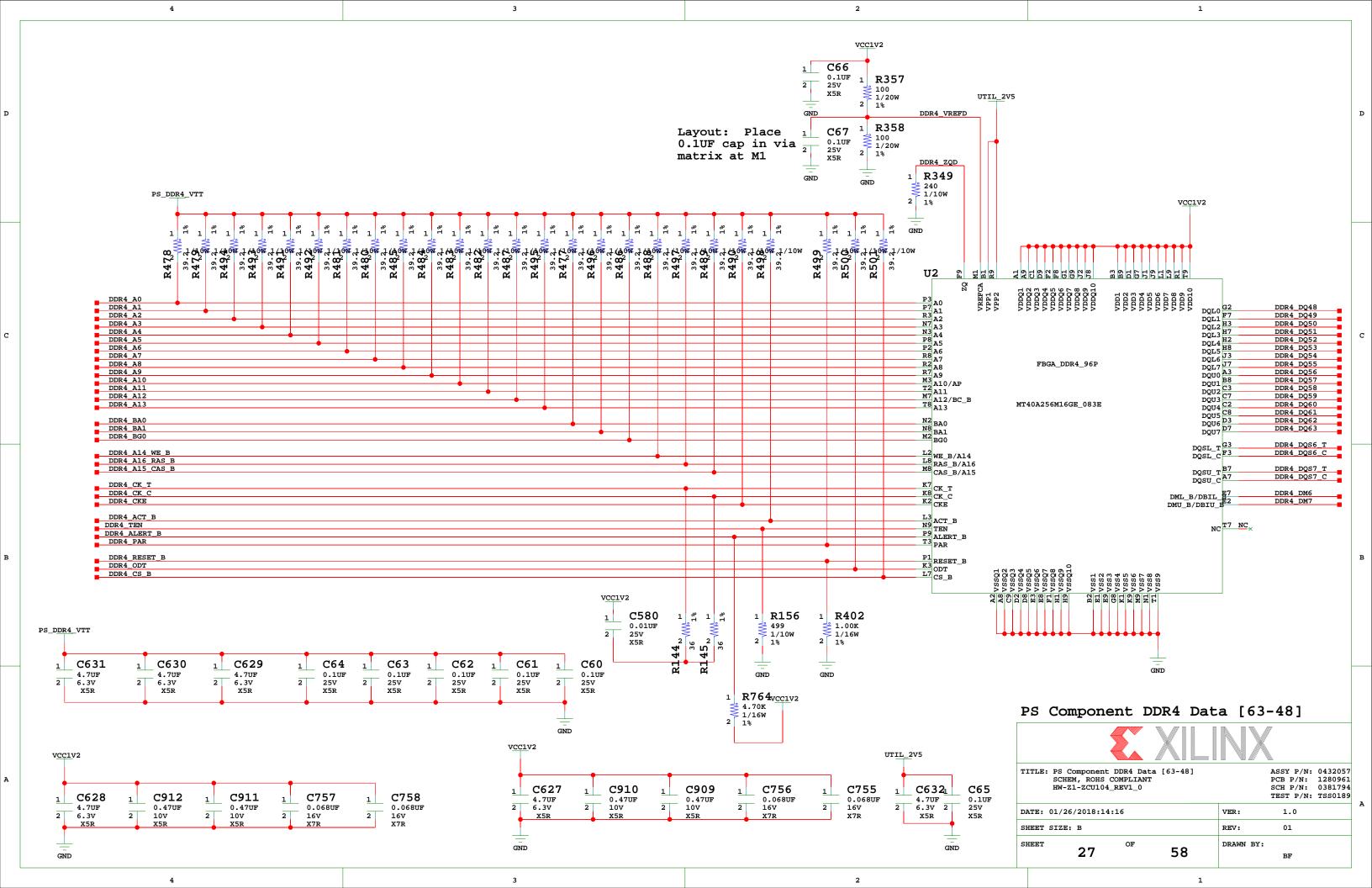


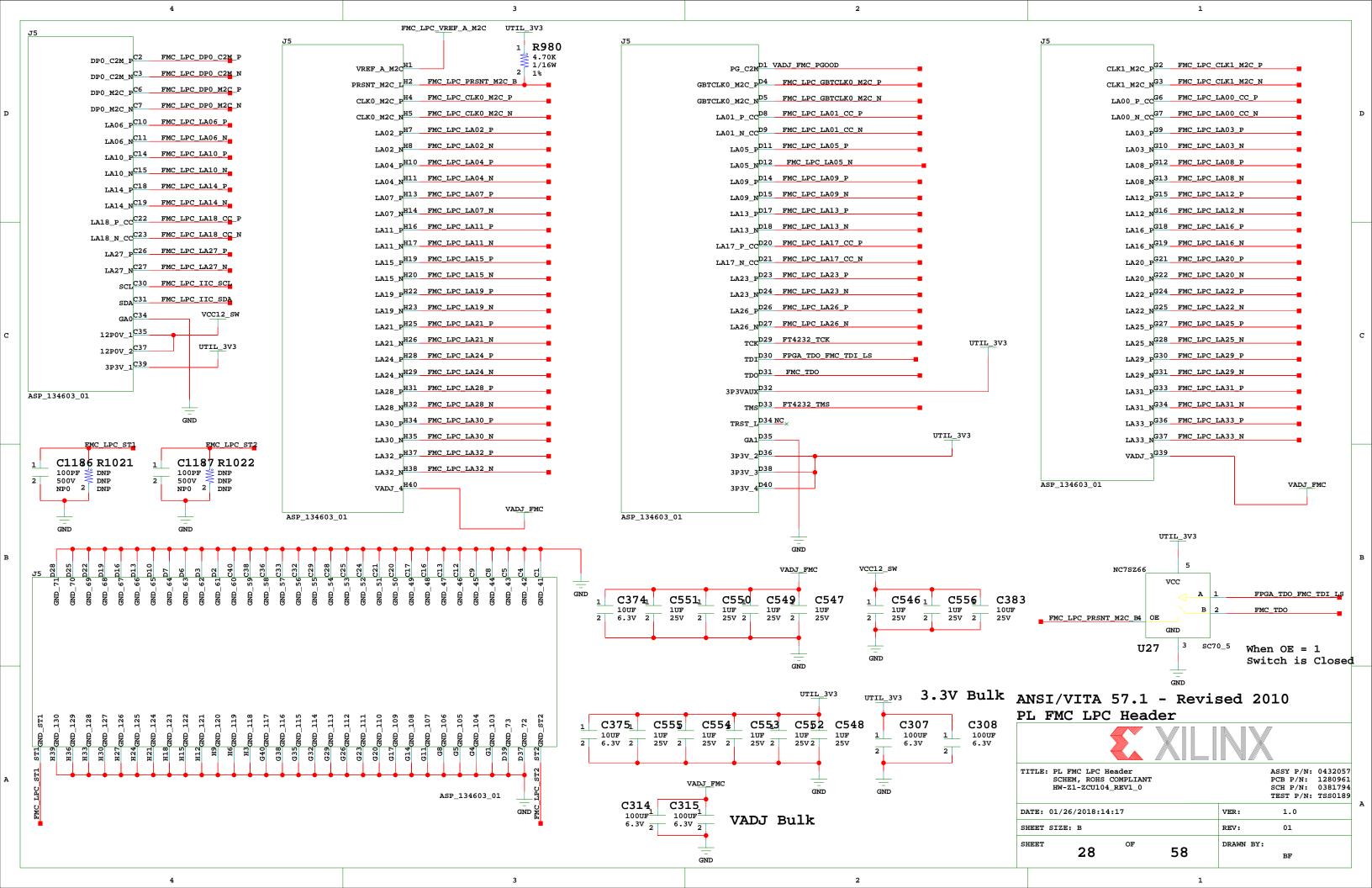
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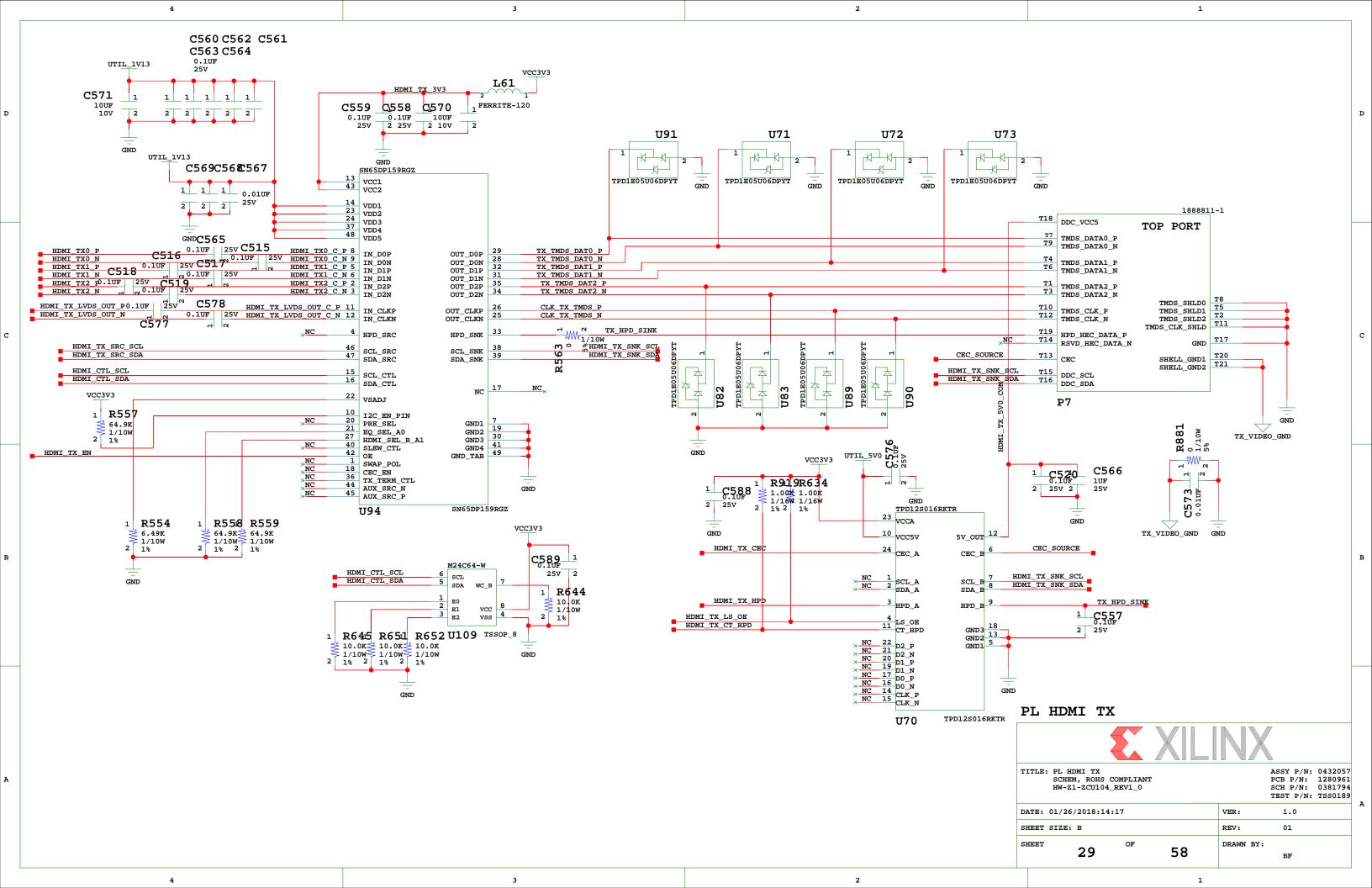


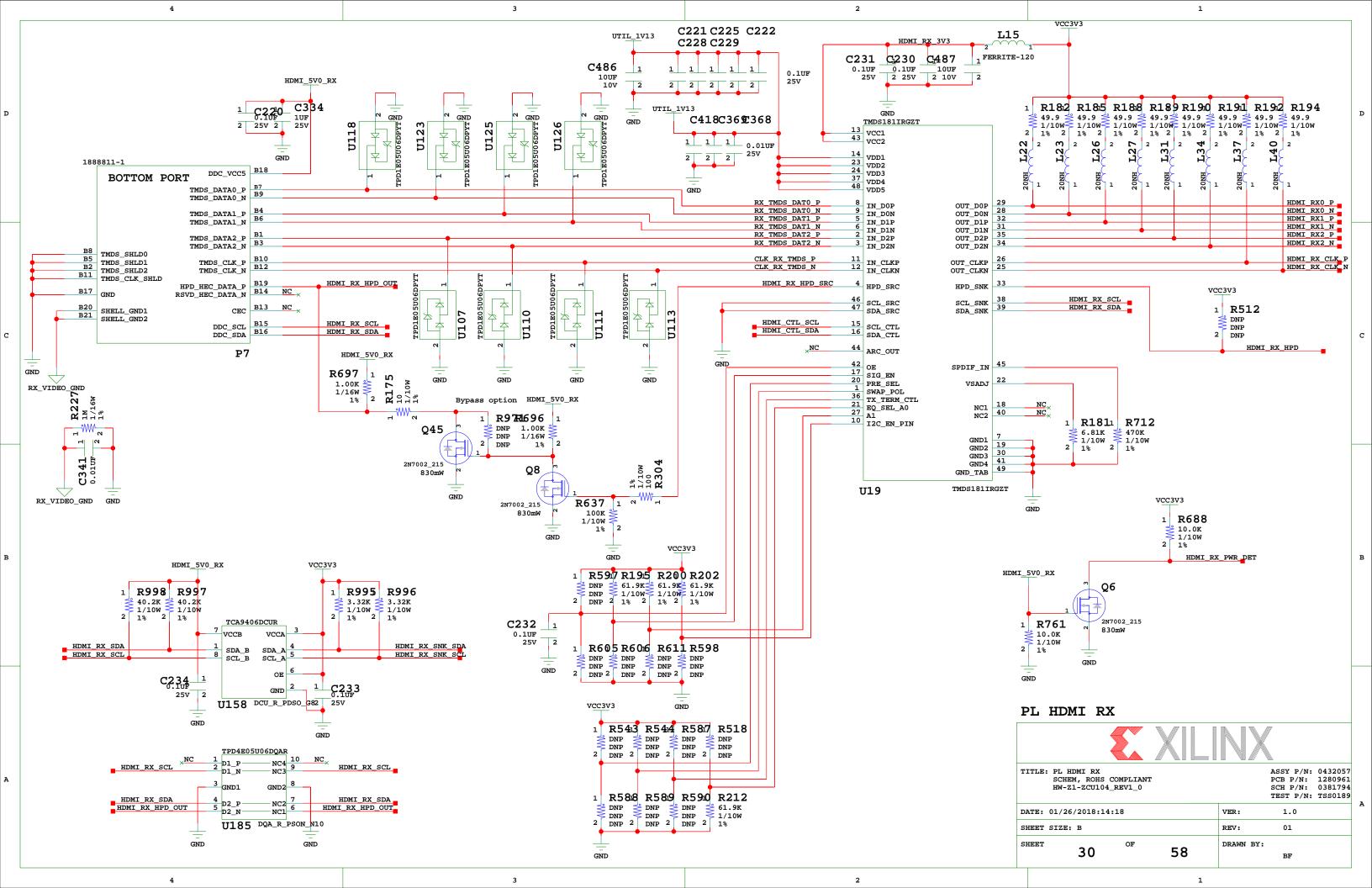


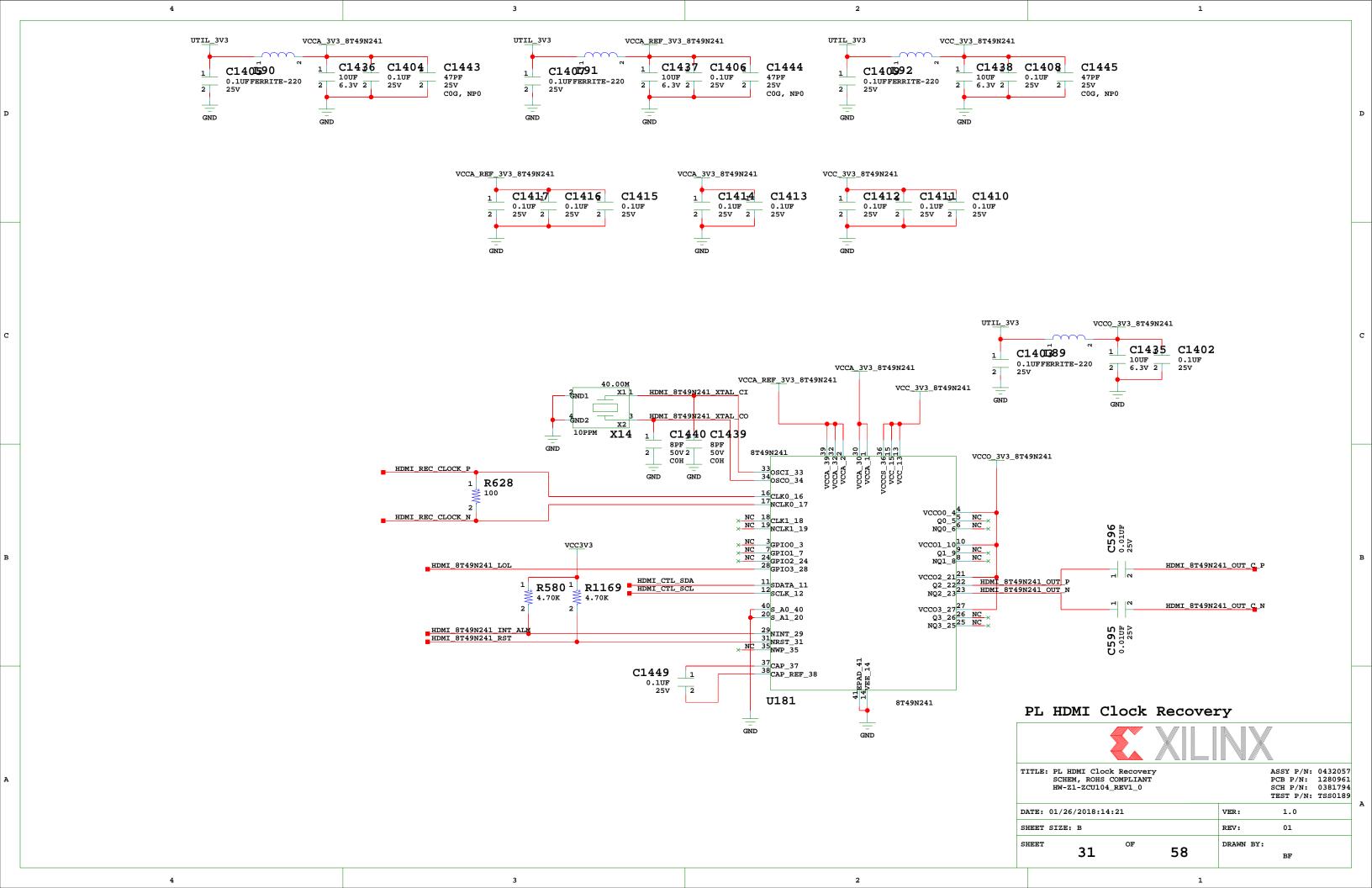


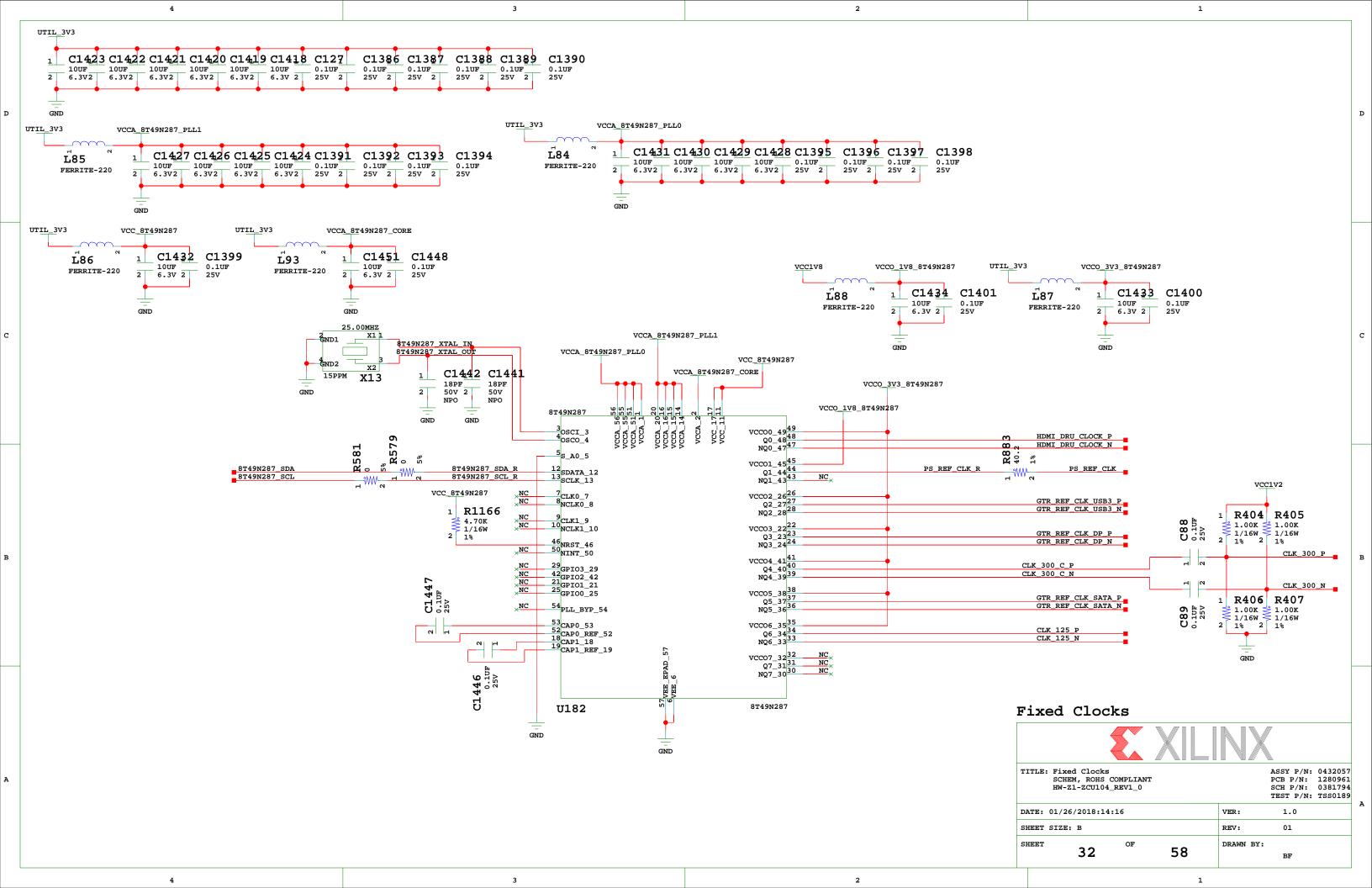


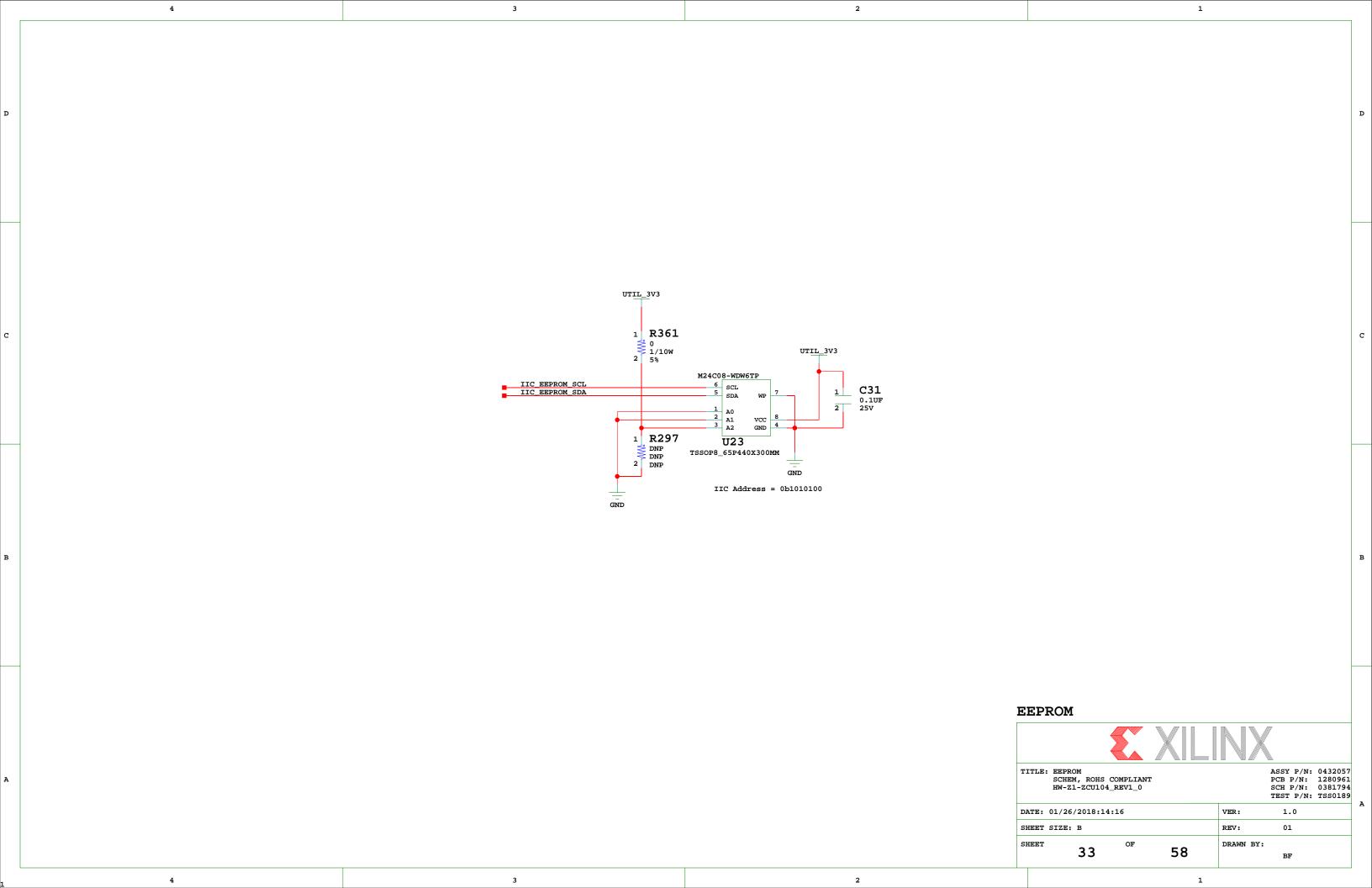


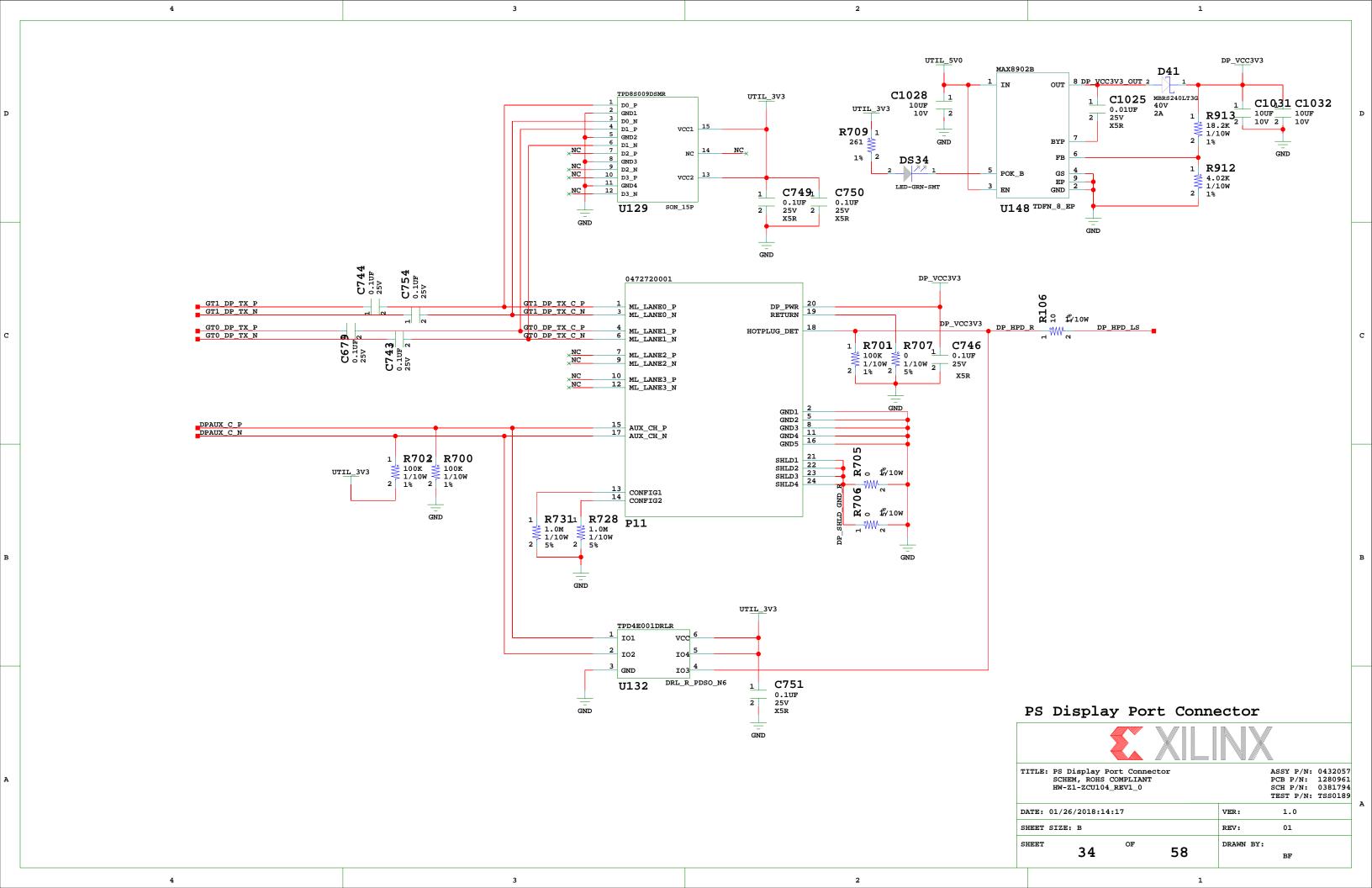


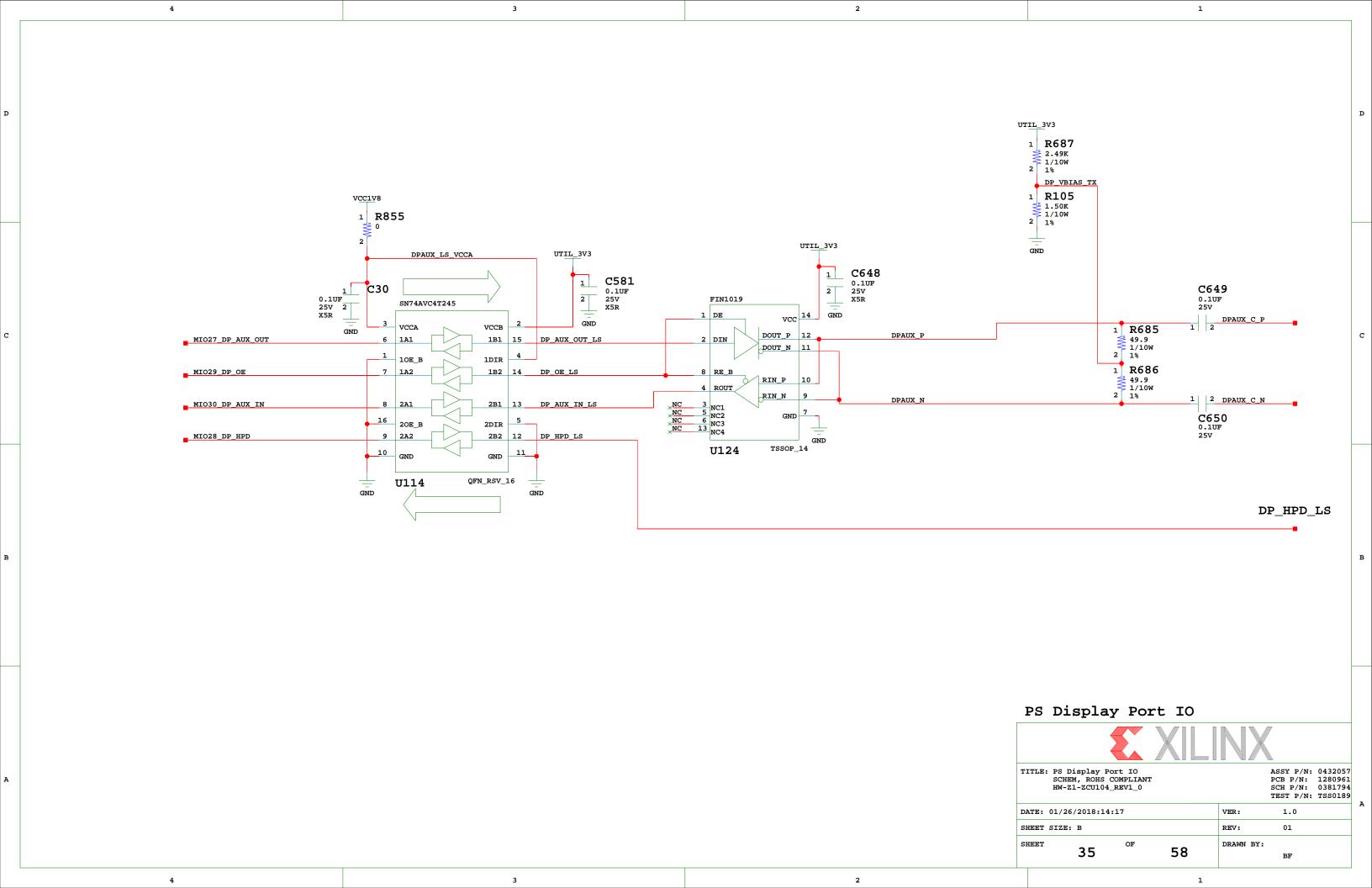


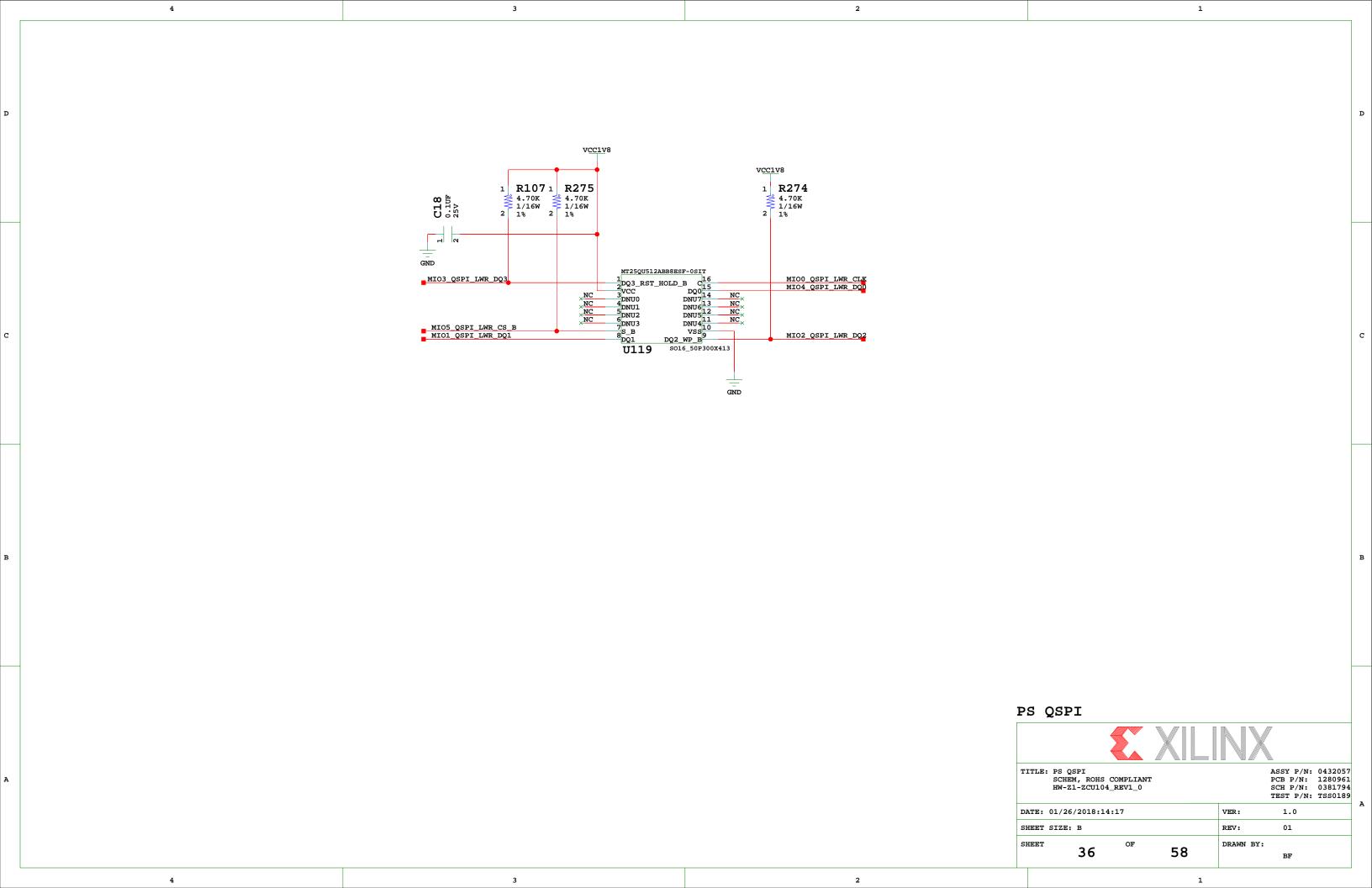


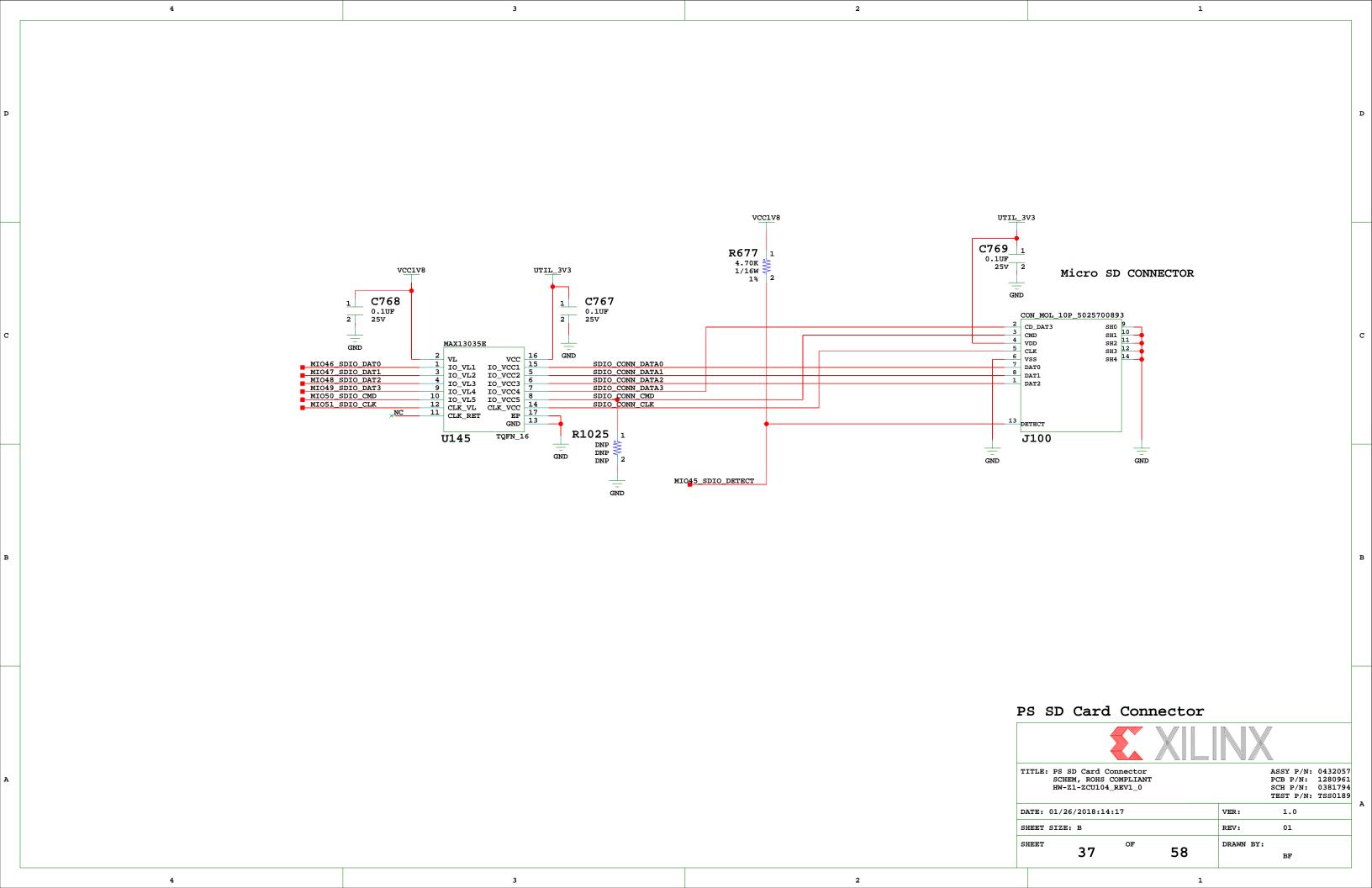


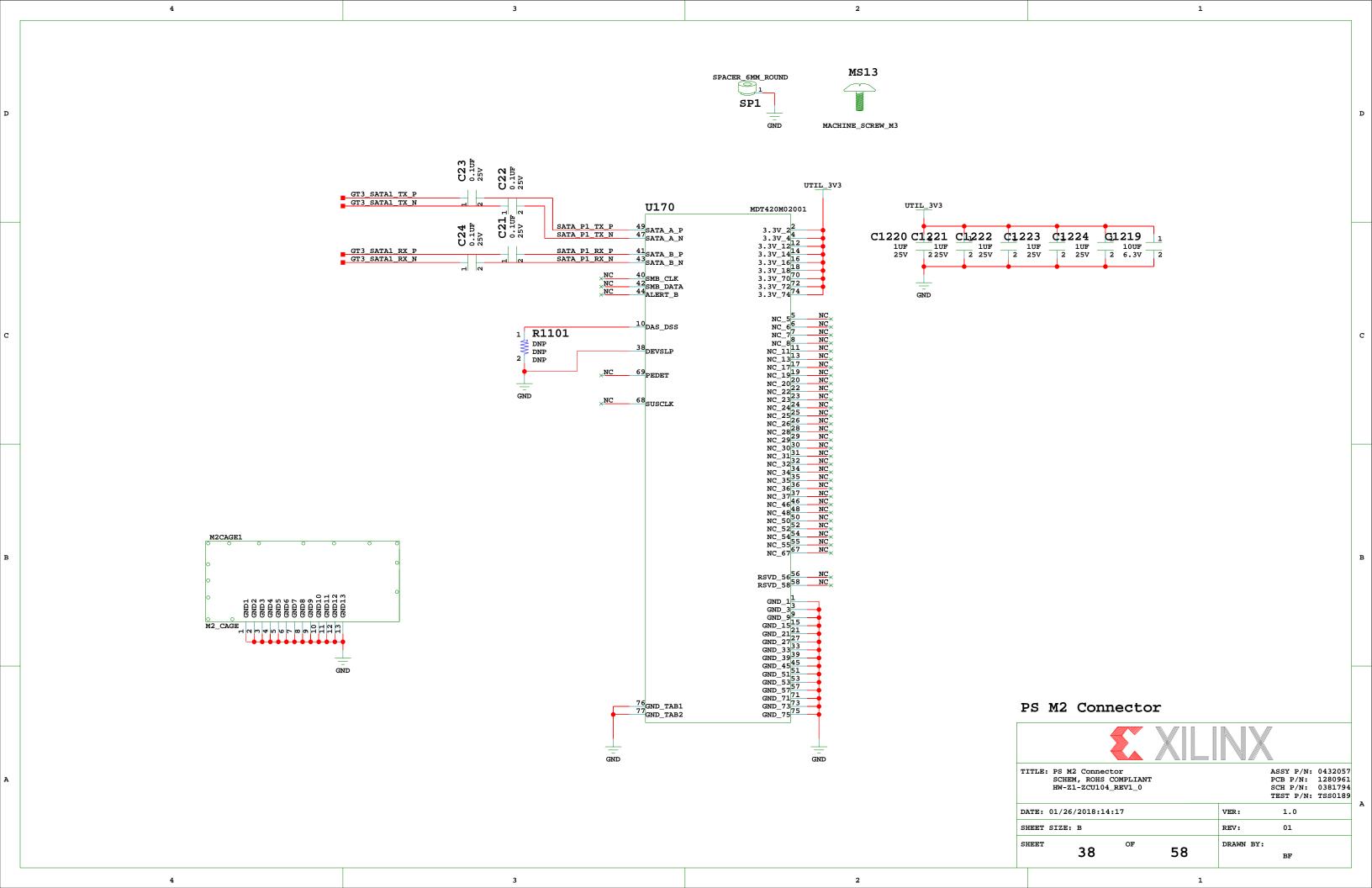


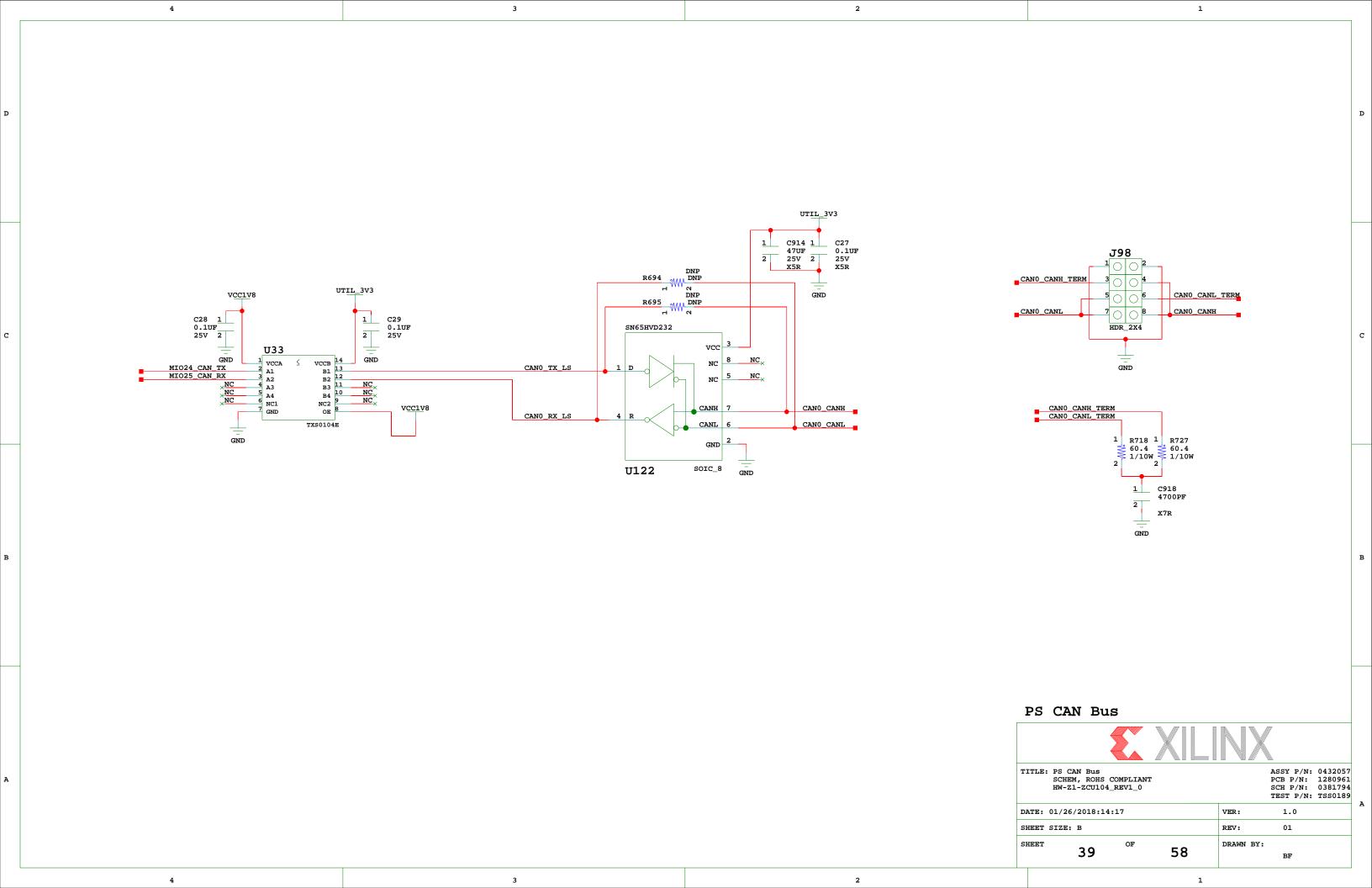


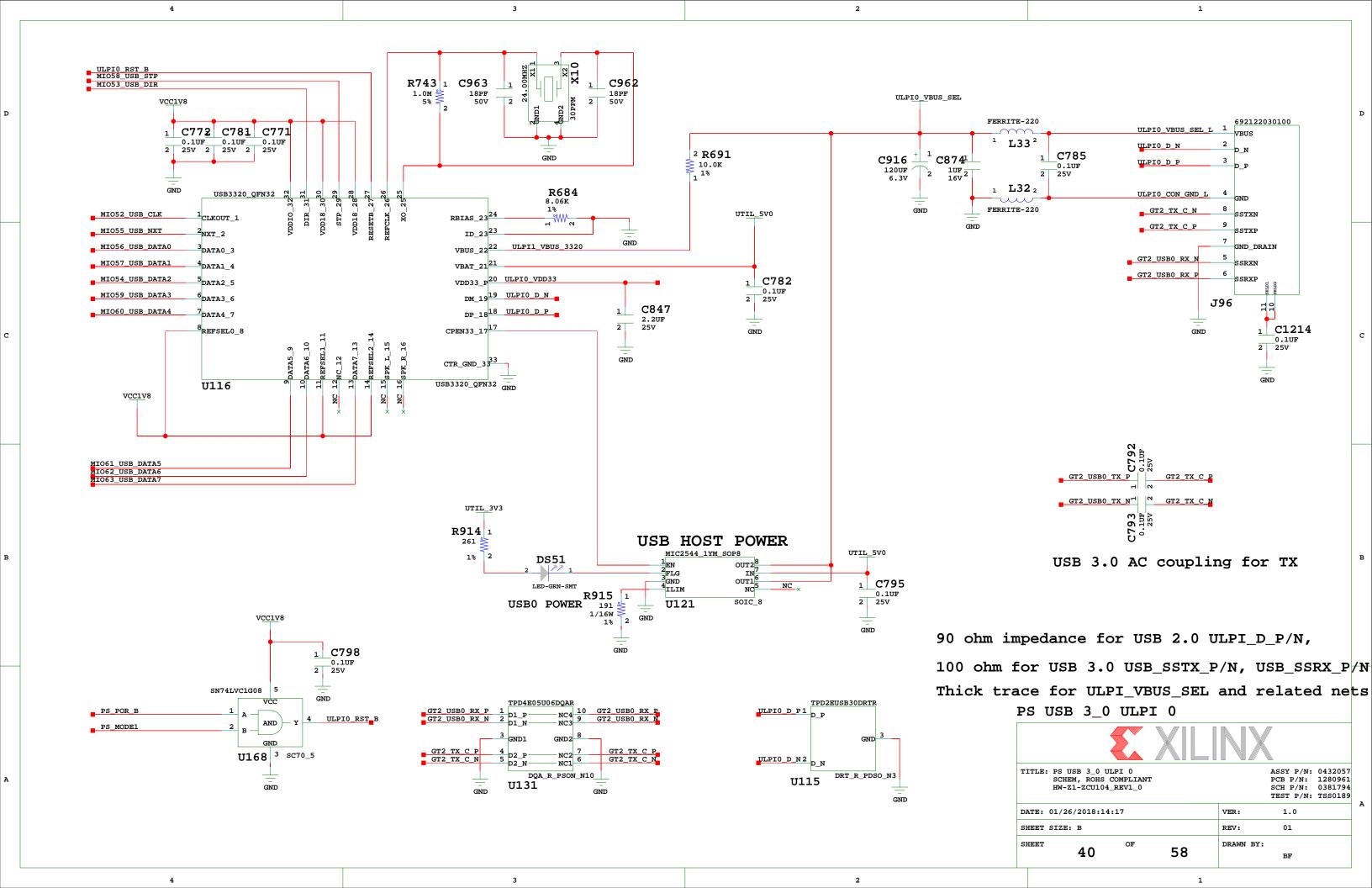


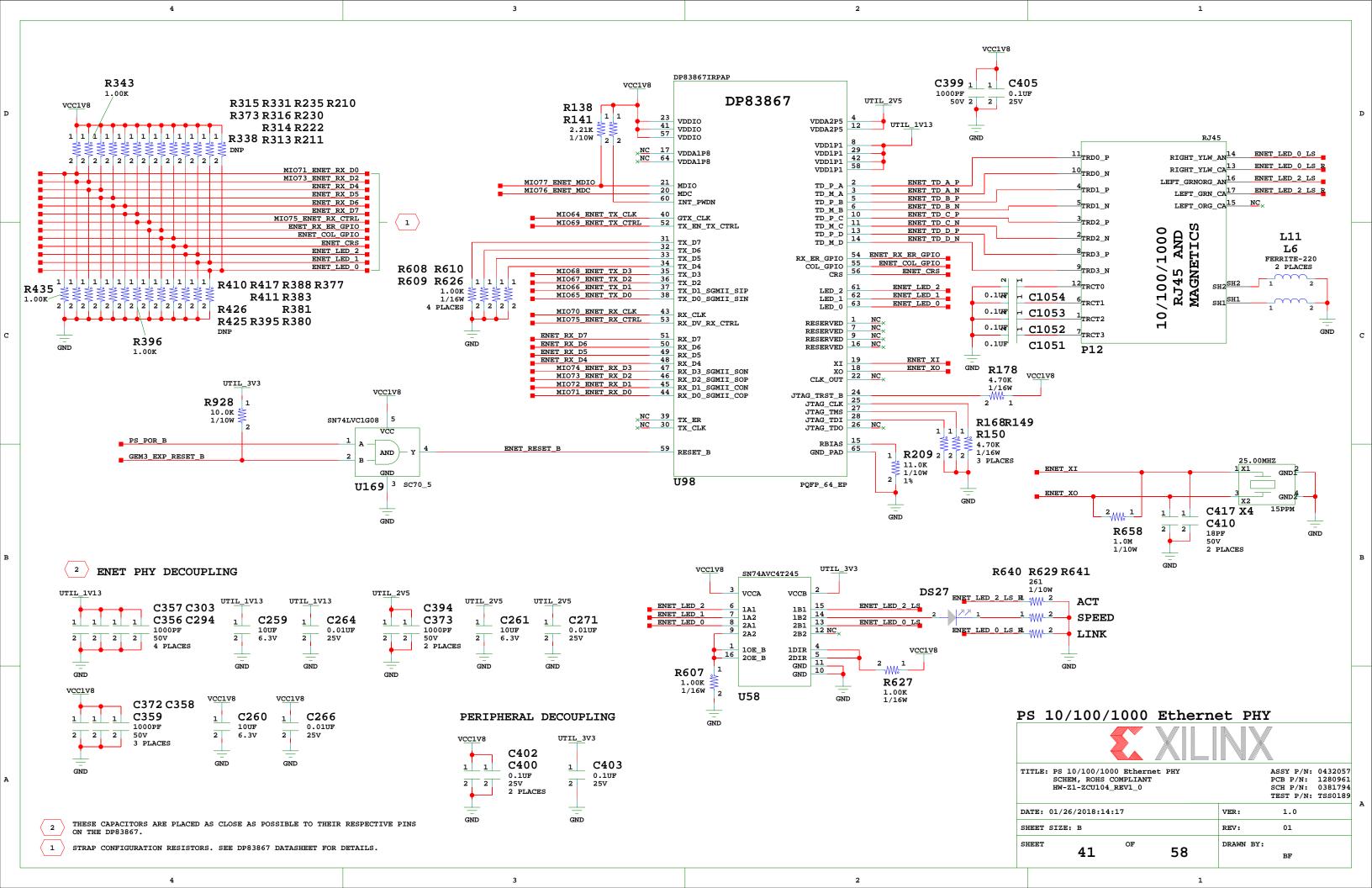


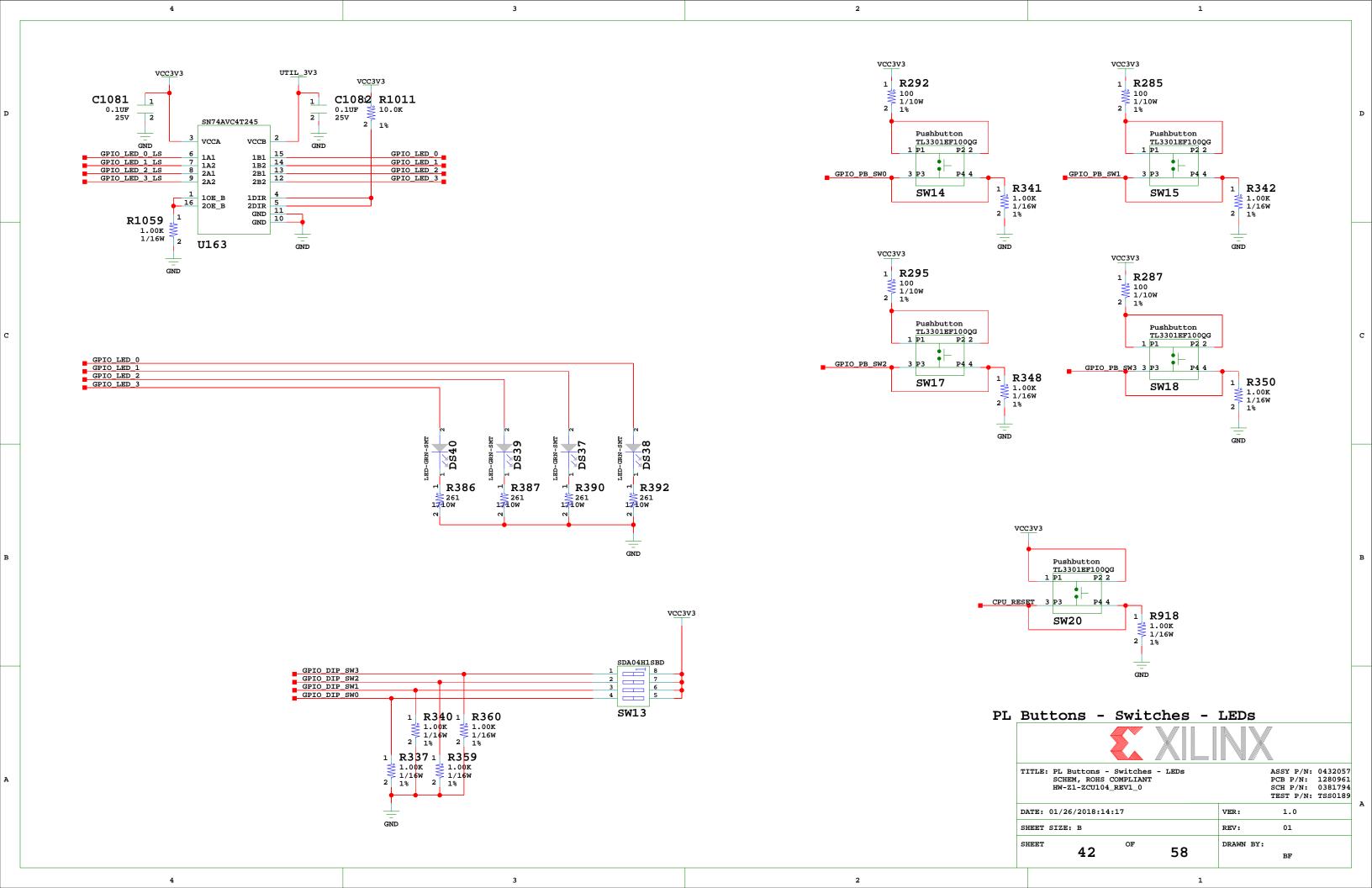


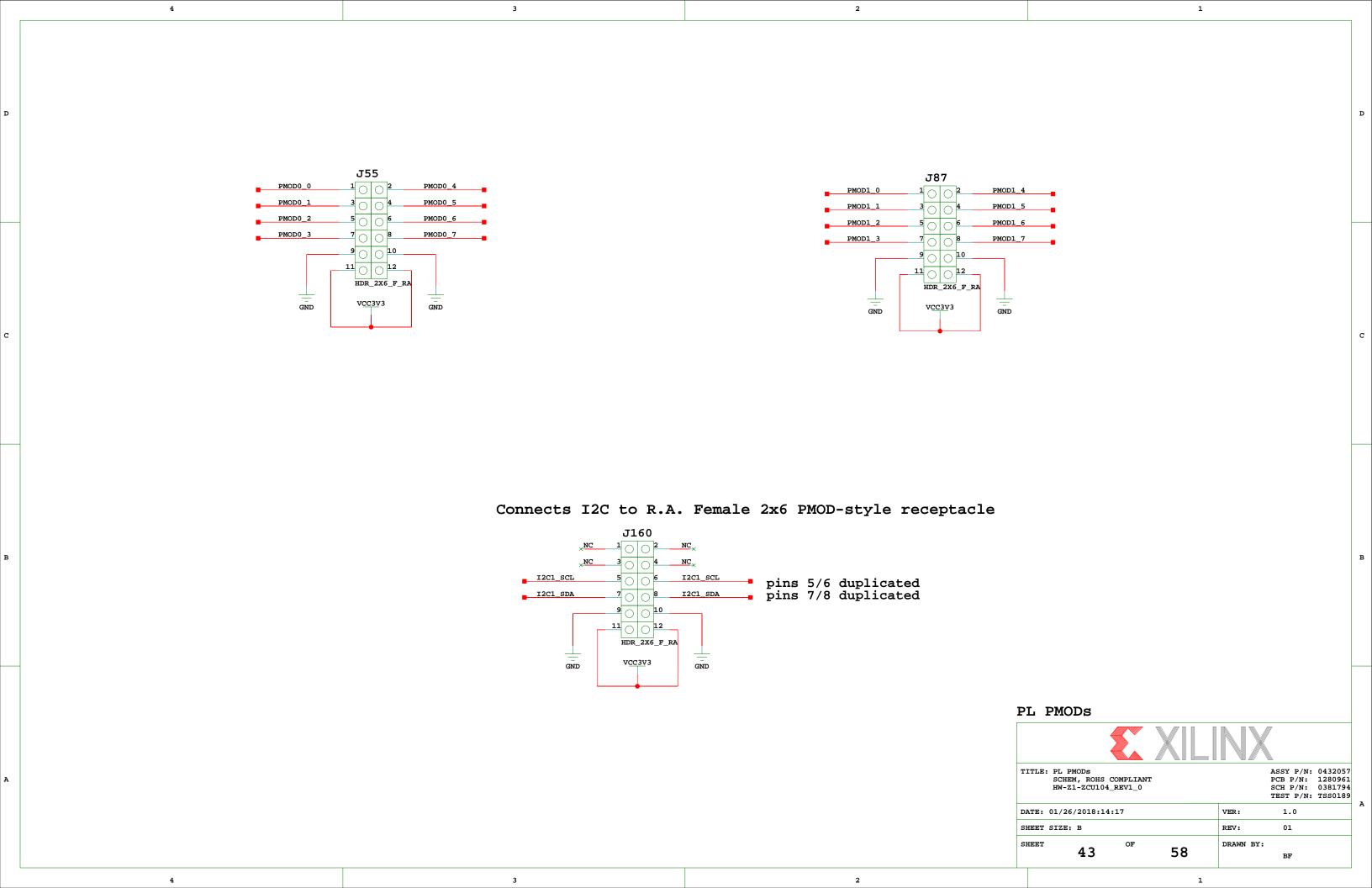


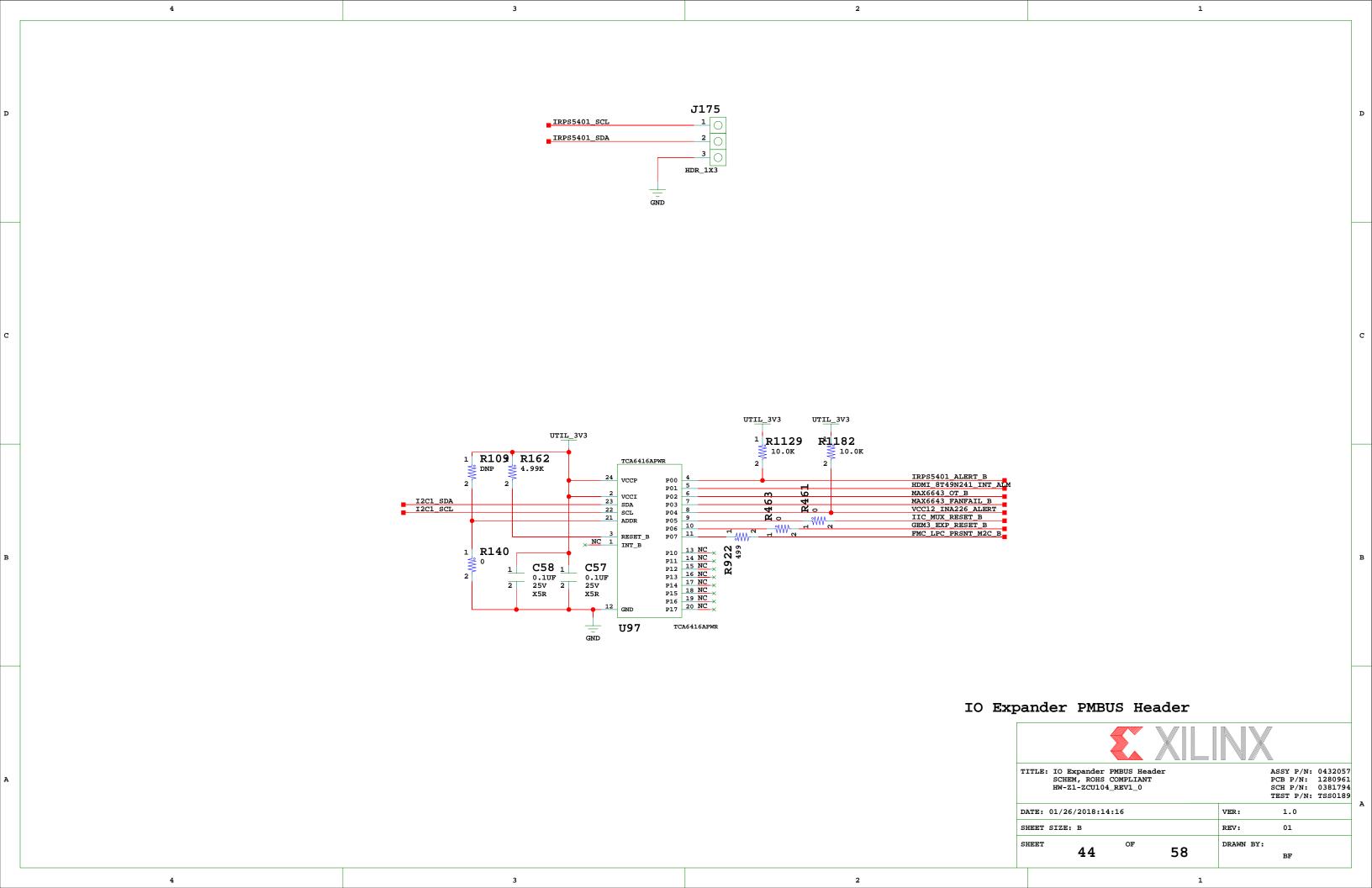


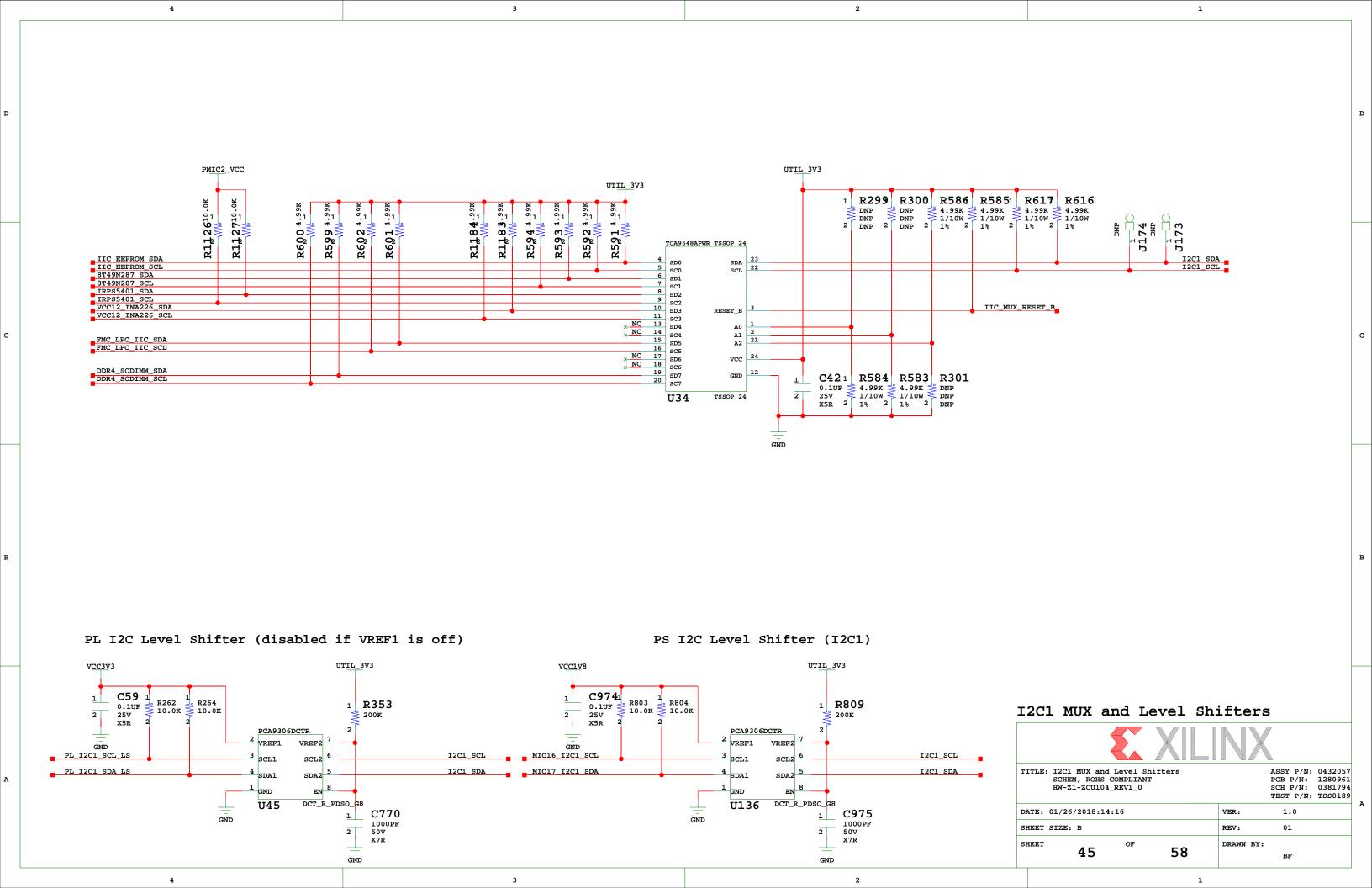


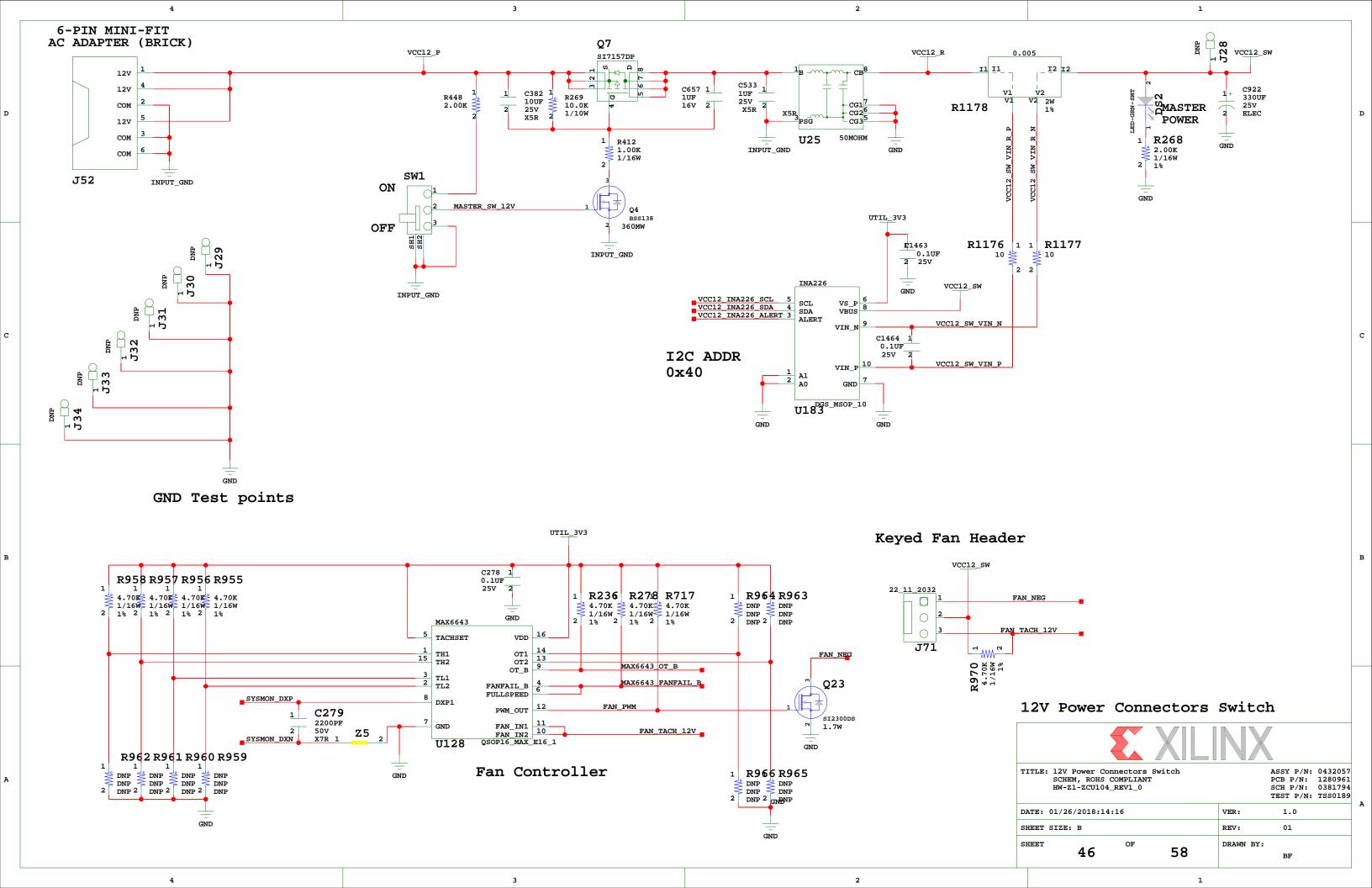


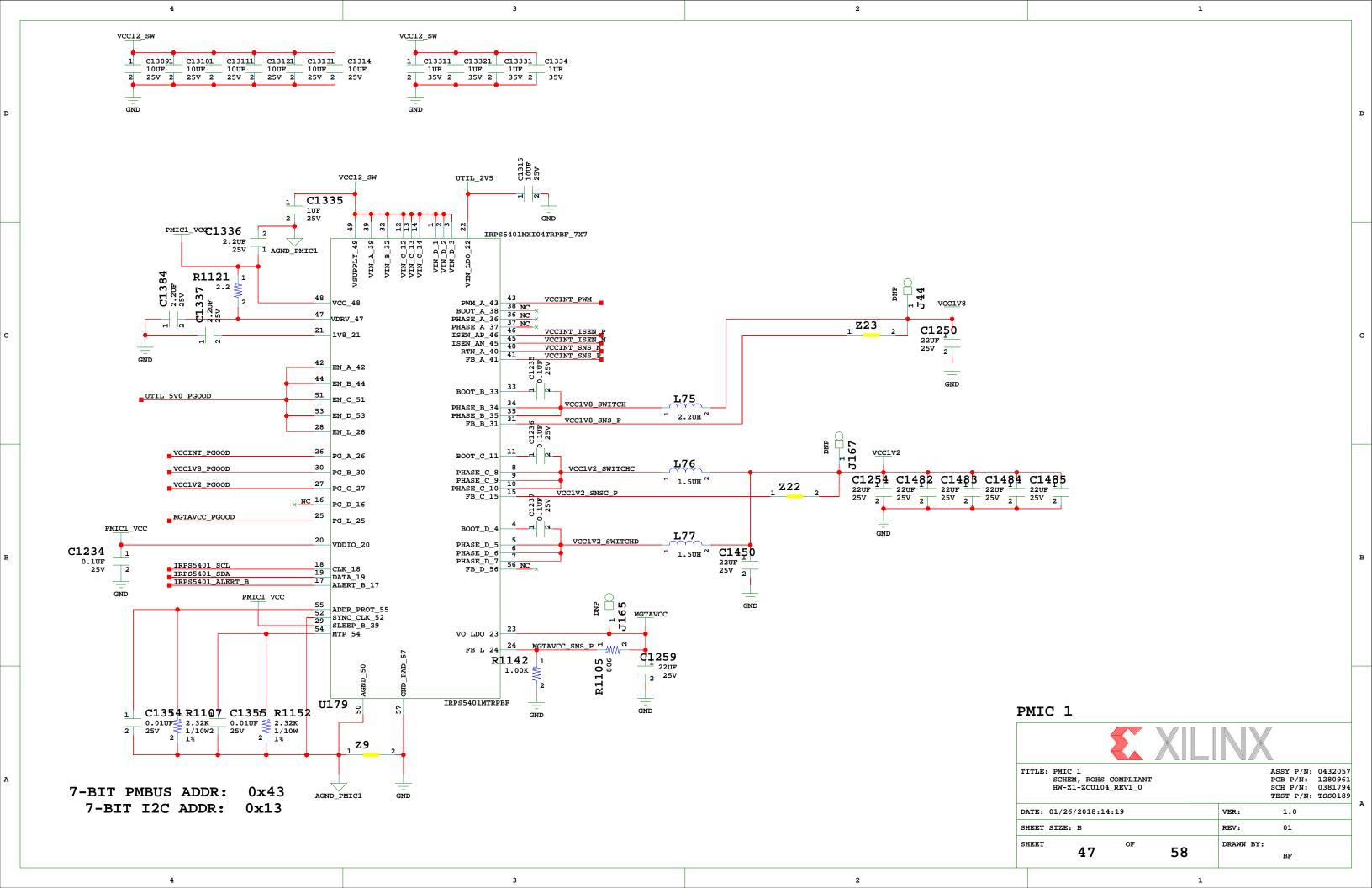


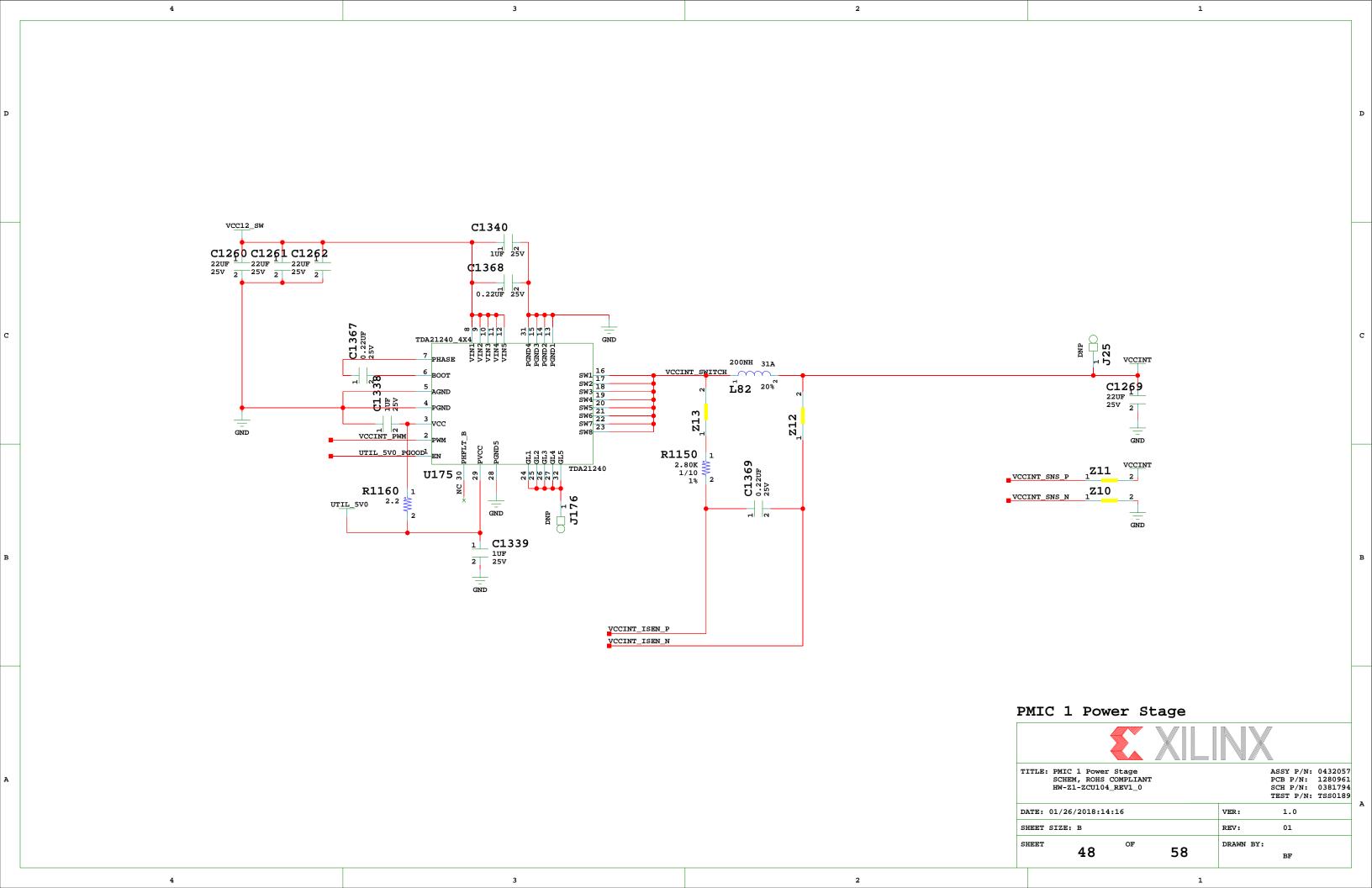


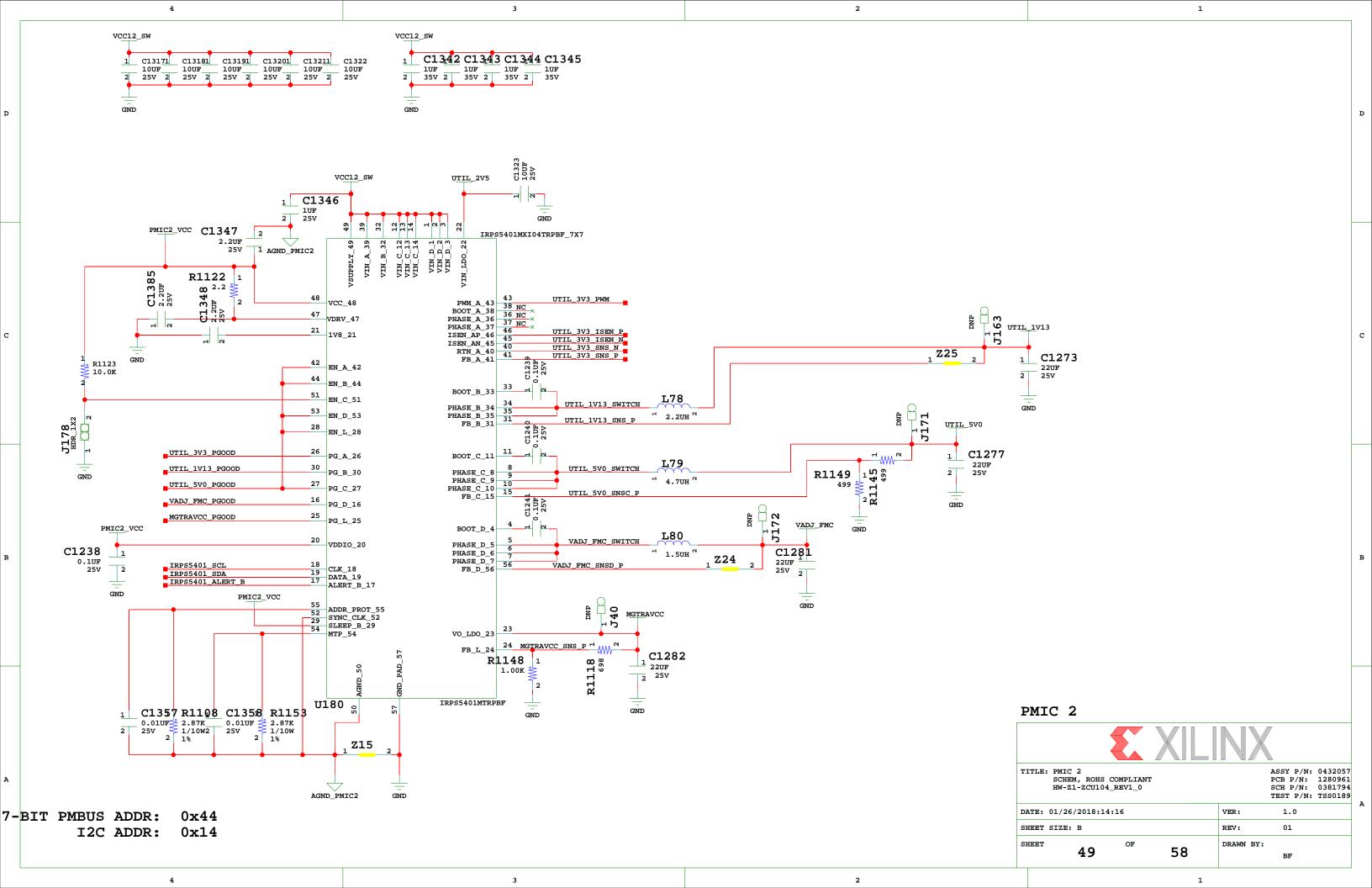


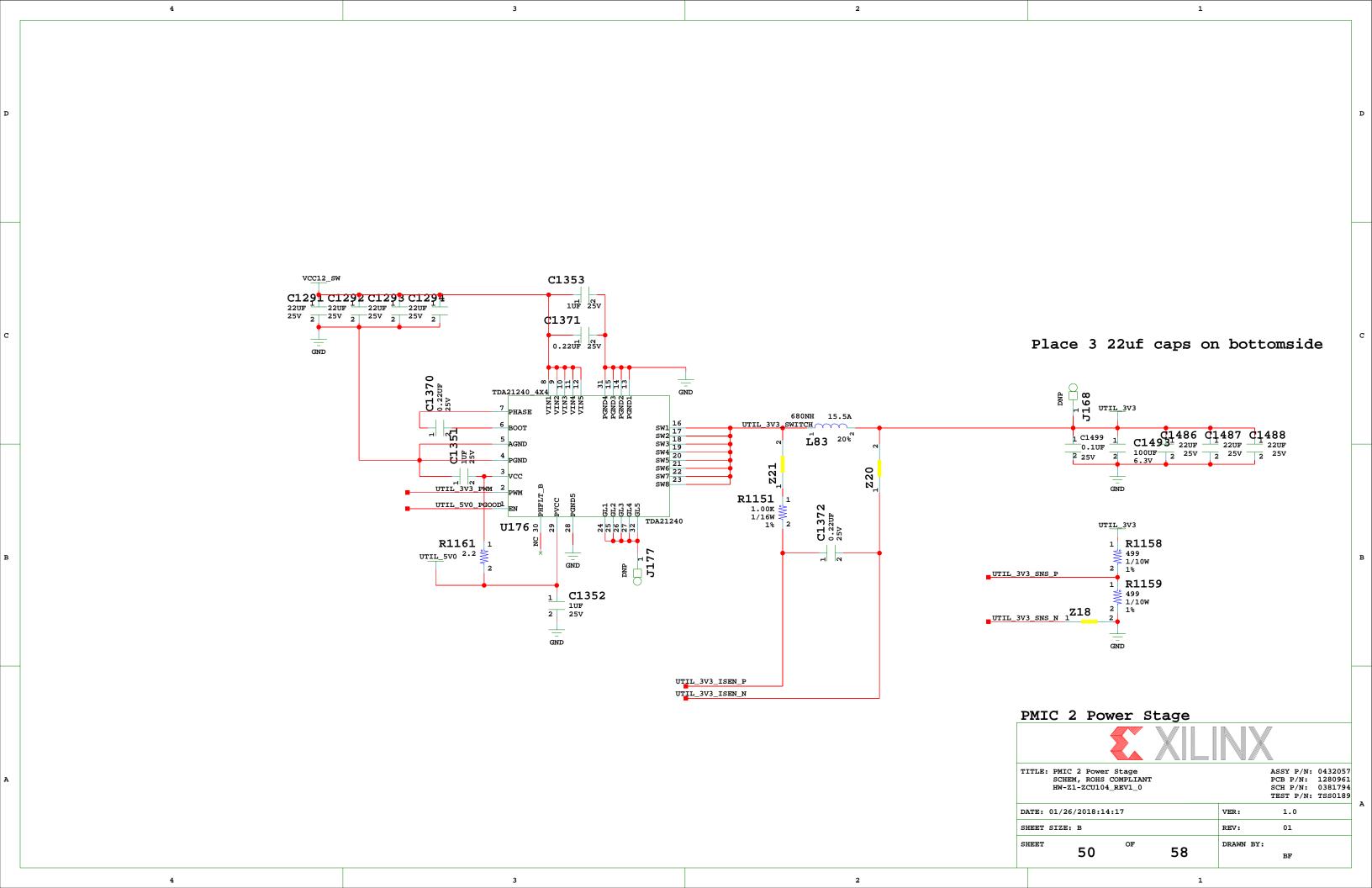


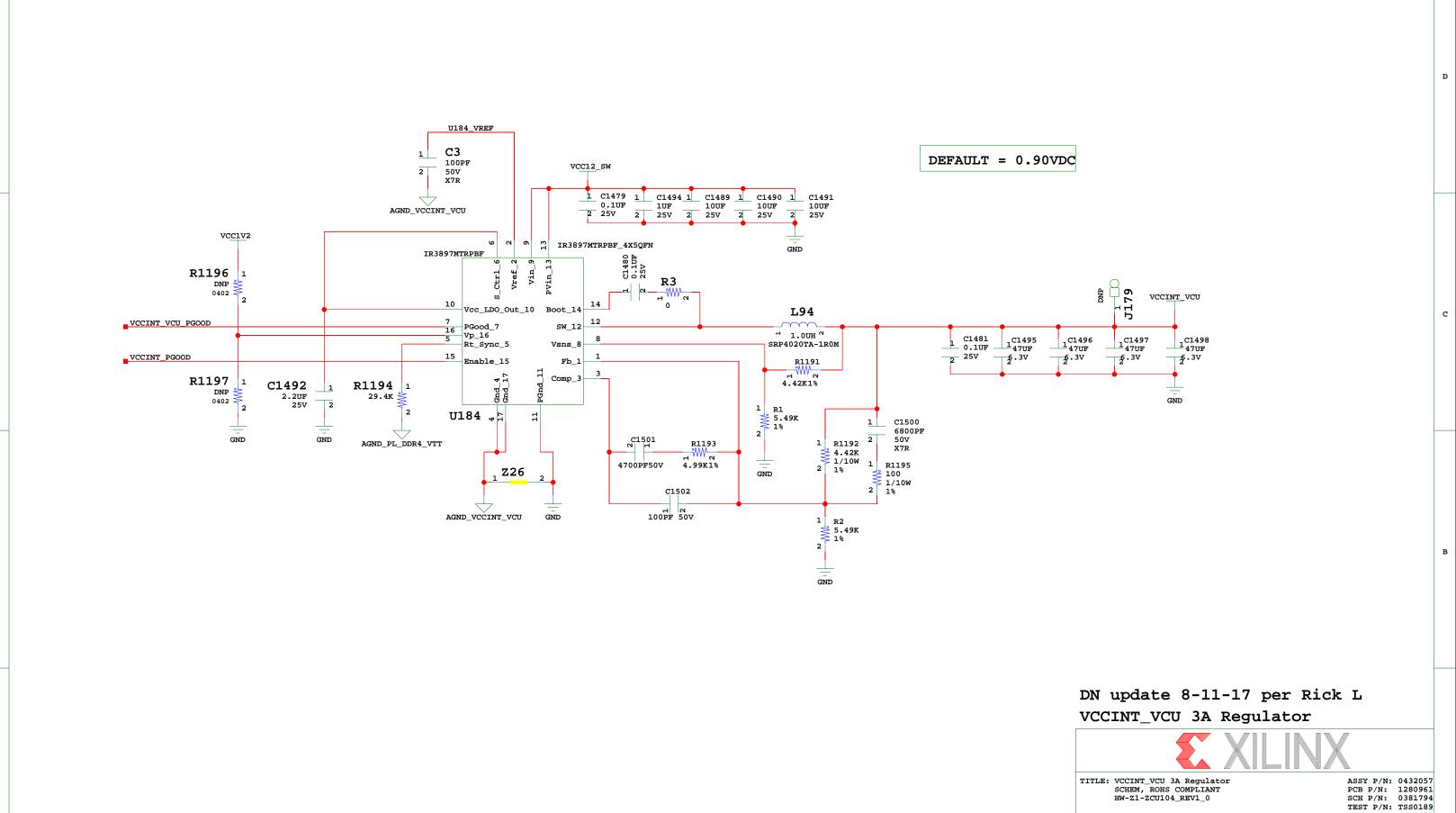












These VALUES need to change for 0.9V and voltage sense line needs to be identified

1

DATE: 01/26/2018:14:17

51

SHEET SIZE: B

VER:

REV:

58

1

DRAWN BY:

1.0

01

