

	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								

power

File: power.kicad_sch

FPGA_1

File: FPGA_1.kicad_sch

config

File: config.kicad_sch

ddr

File: ddr.kicad_sch

mgt

File: mgt.kicad_sch

FPGA_2

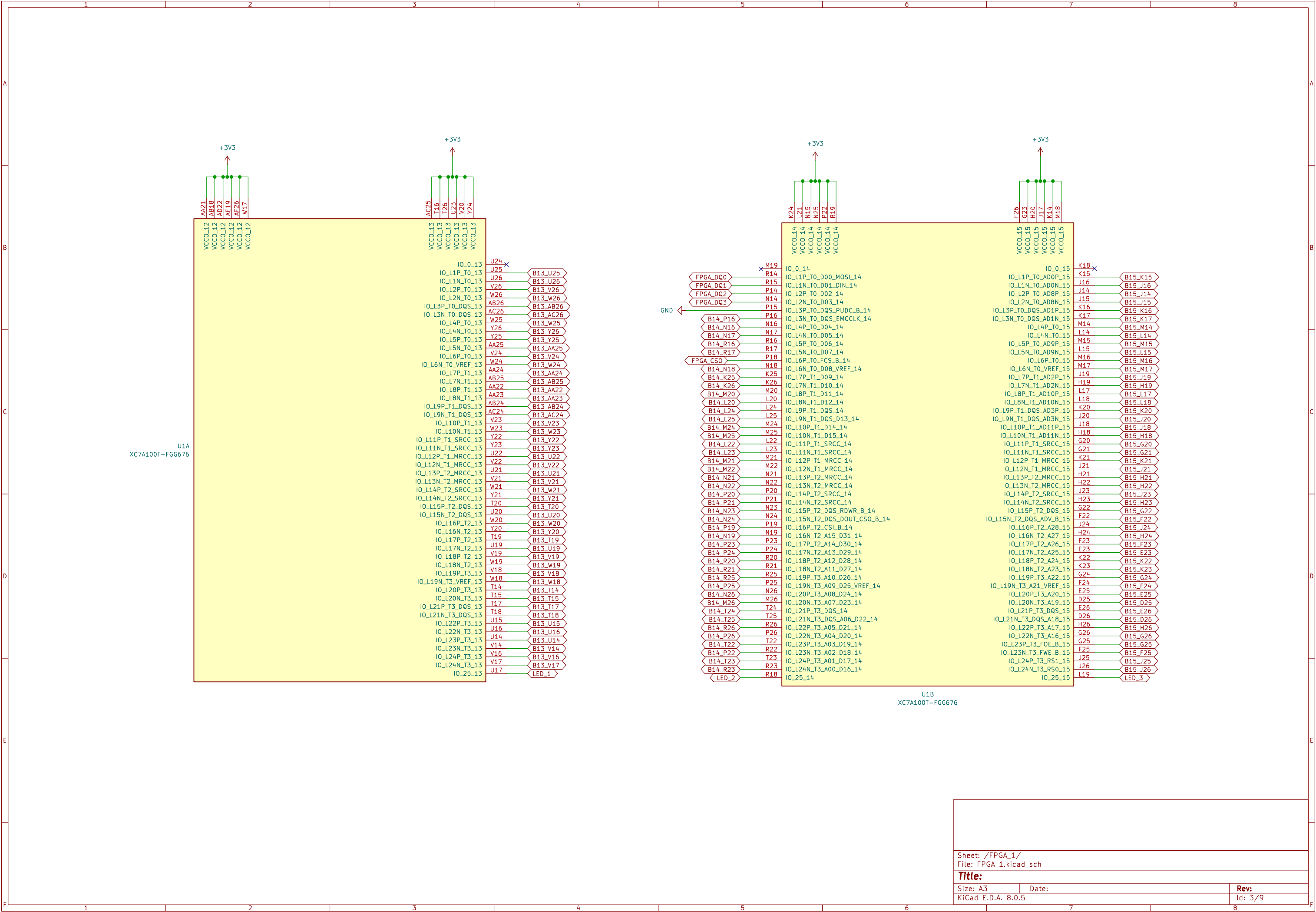
File: FPGA_2.kicad_sch

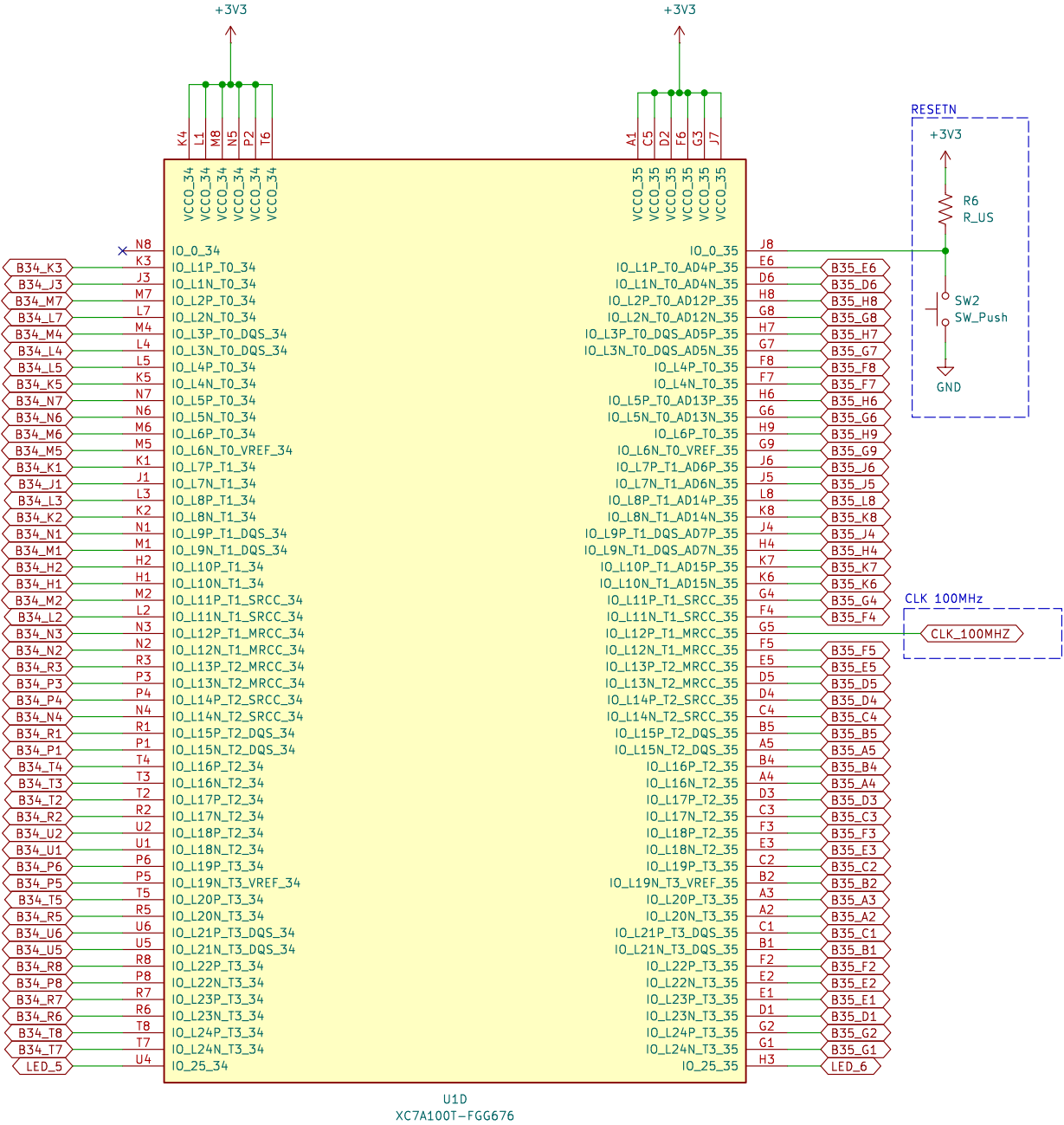
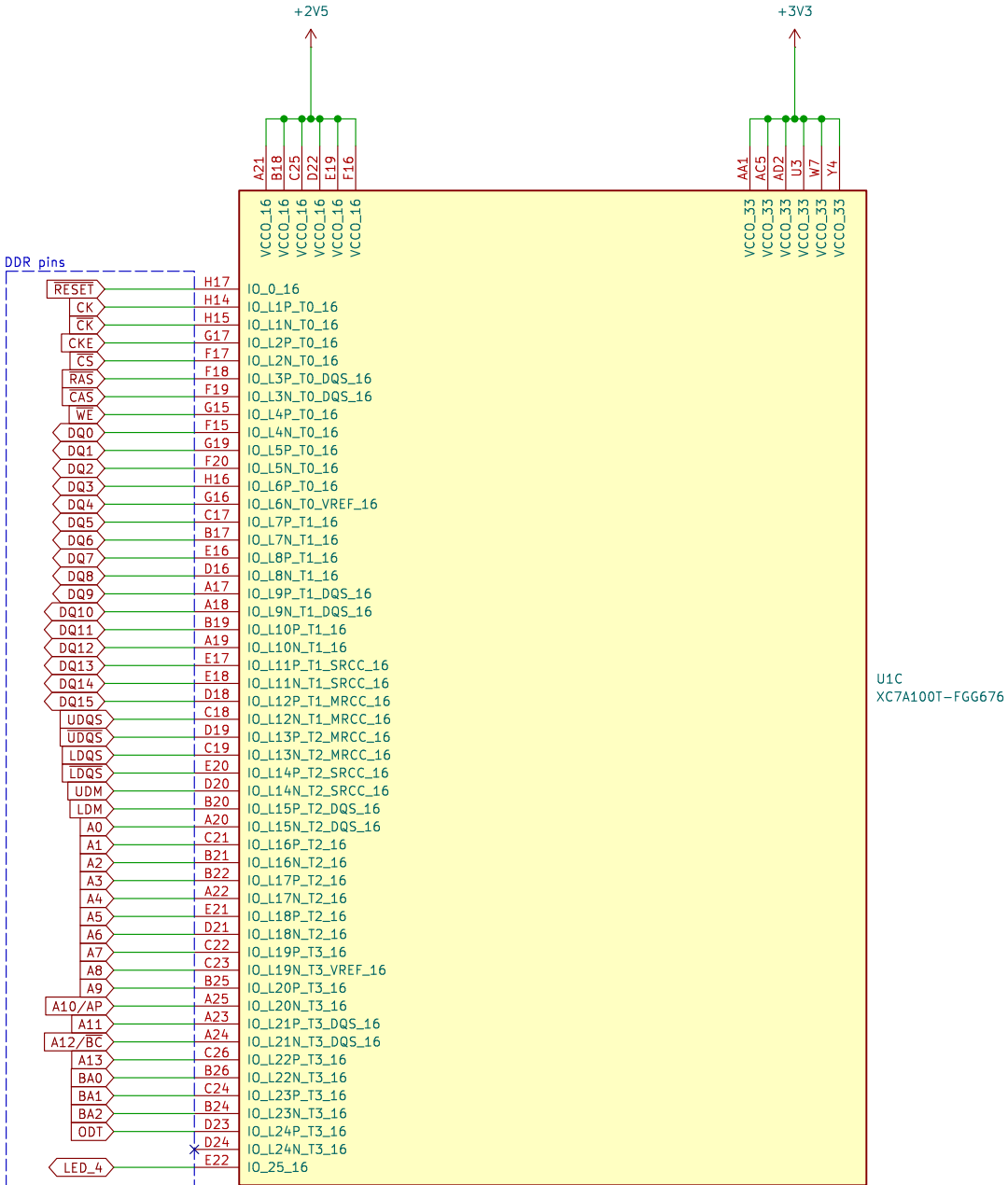
connectors

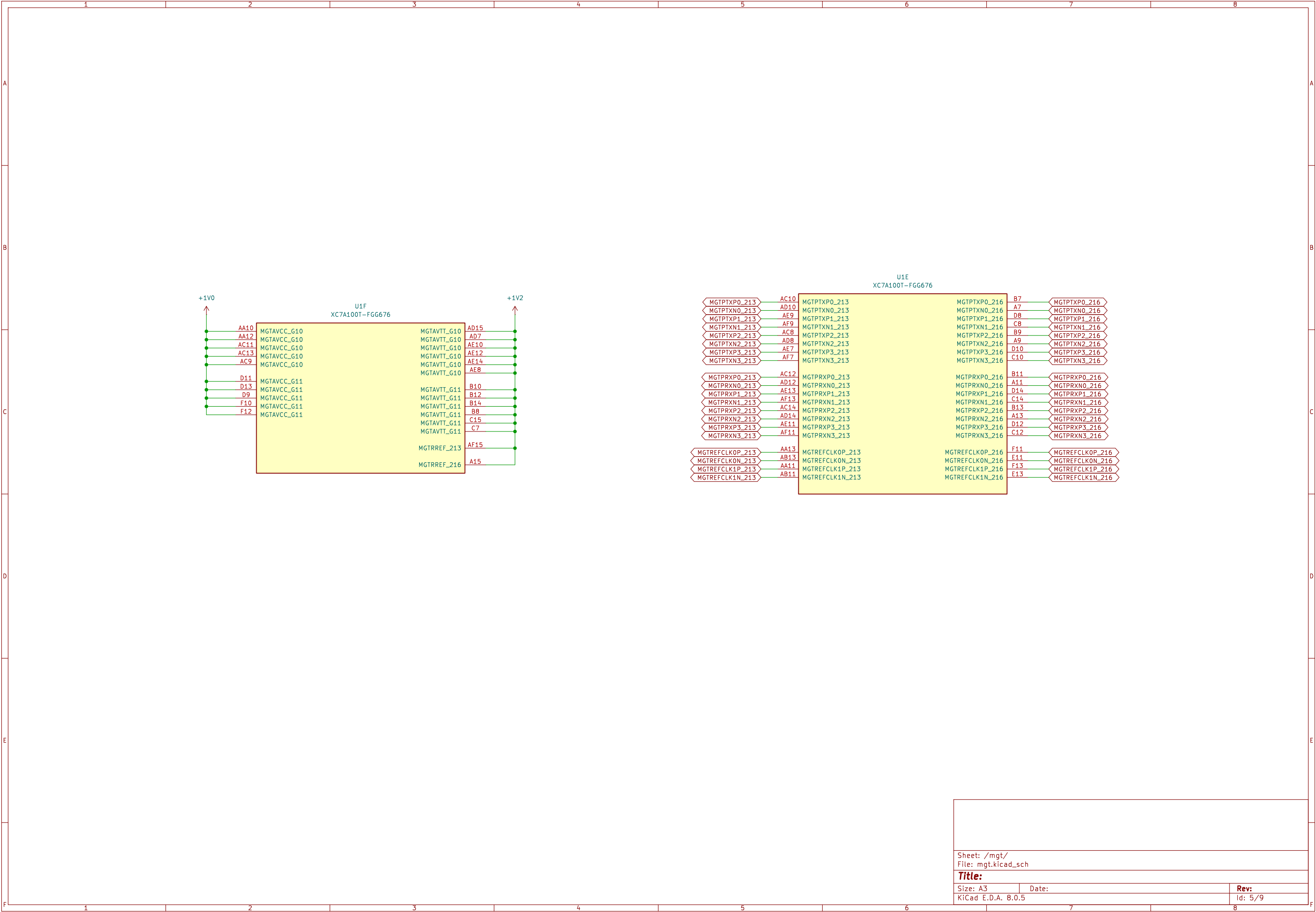
File: connectors.kicad_sch

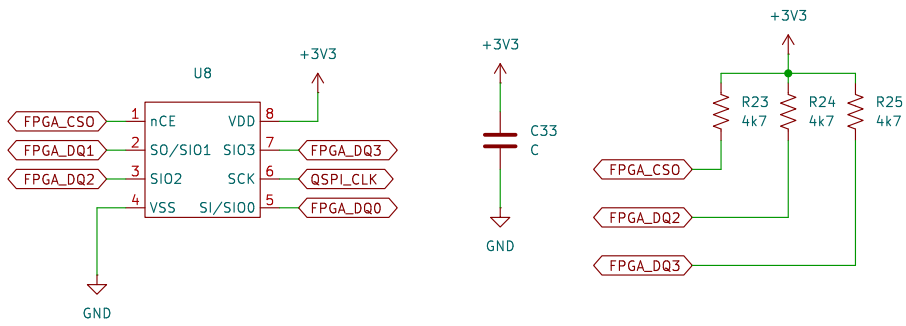
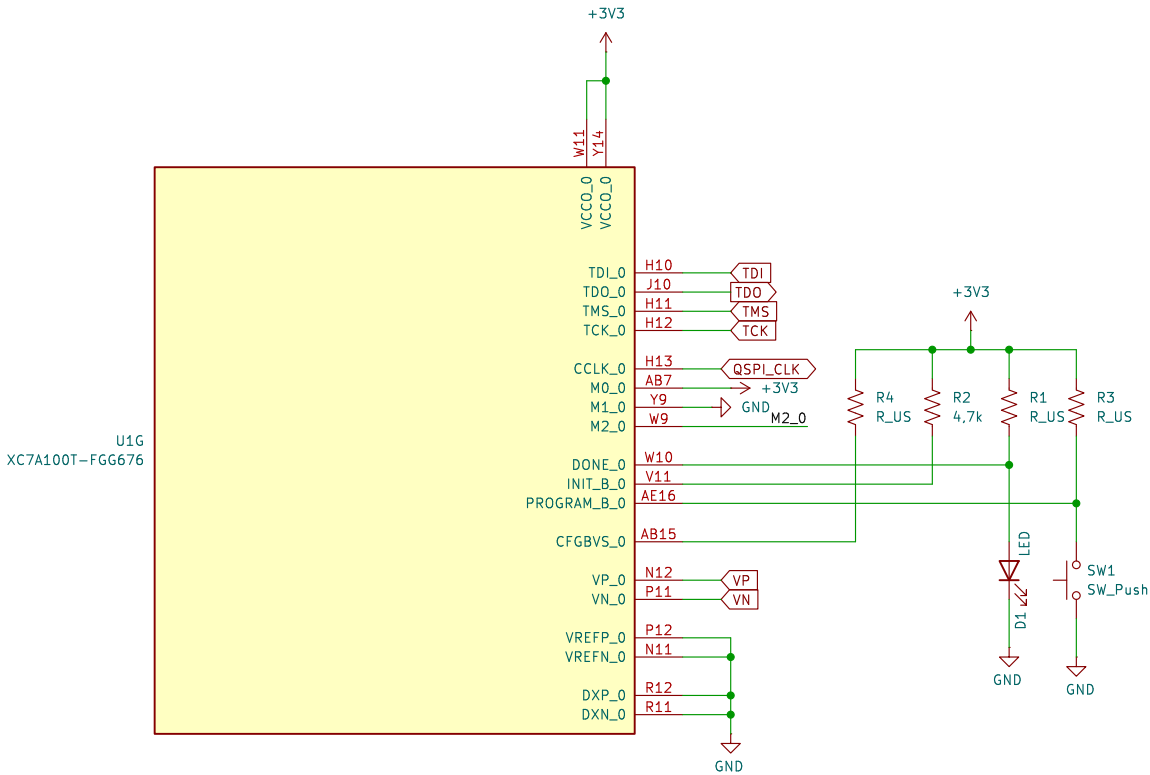
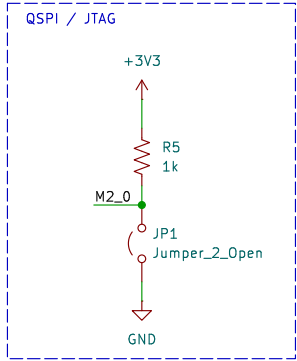
connectors2

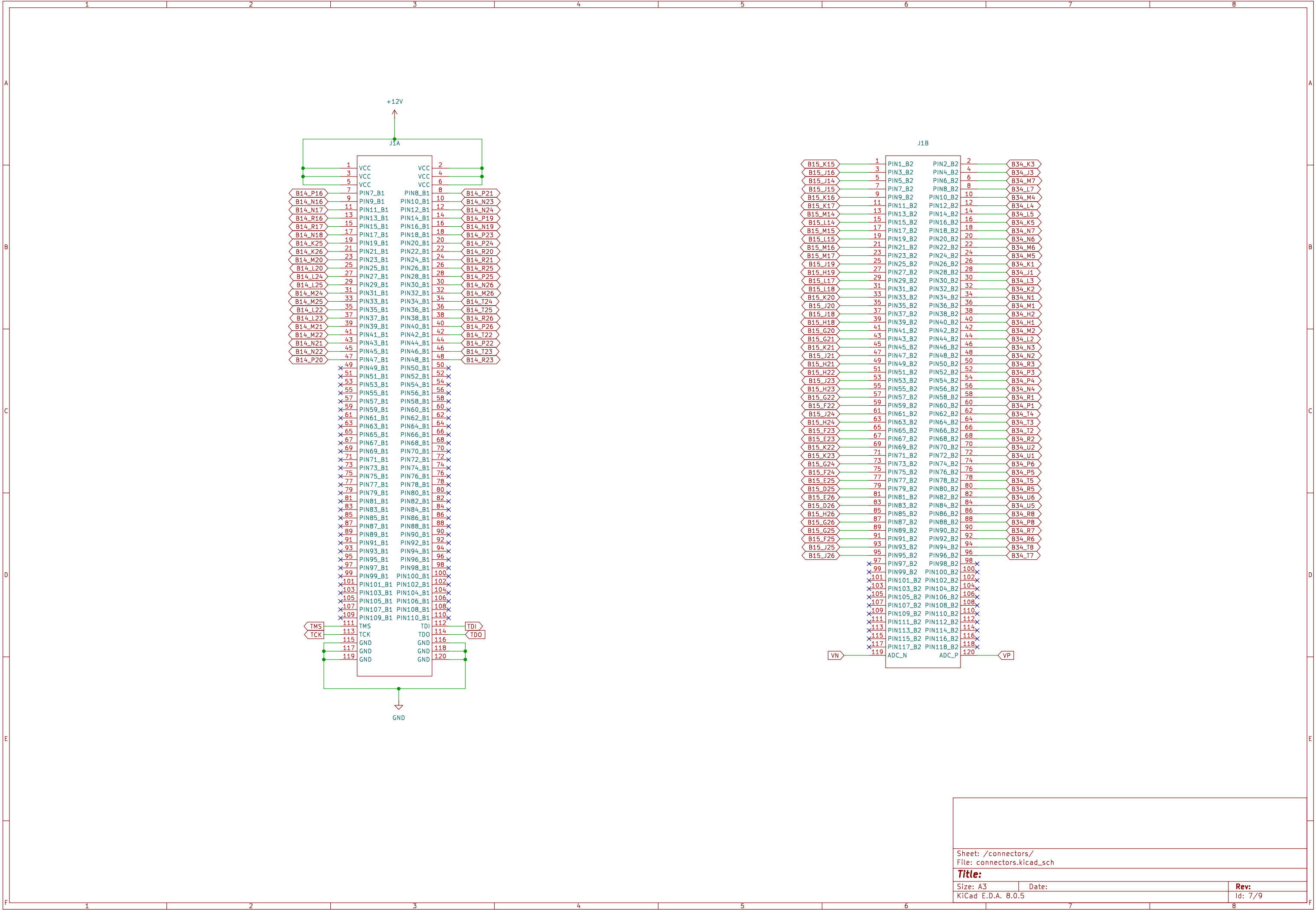
File: connectors2.kicad_sch











BANCO MGT

