

	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								

power

File: power.kicad_sch

FPGA_1

File: FPGA_1.kicad_sch

config

File: config.kicad_sch

ddr

File: ddr.kicad_sch

mgt

File: mgt.kicad_sch

FPGA_2

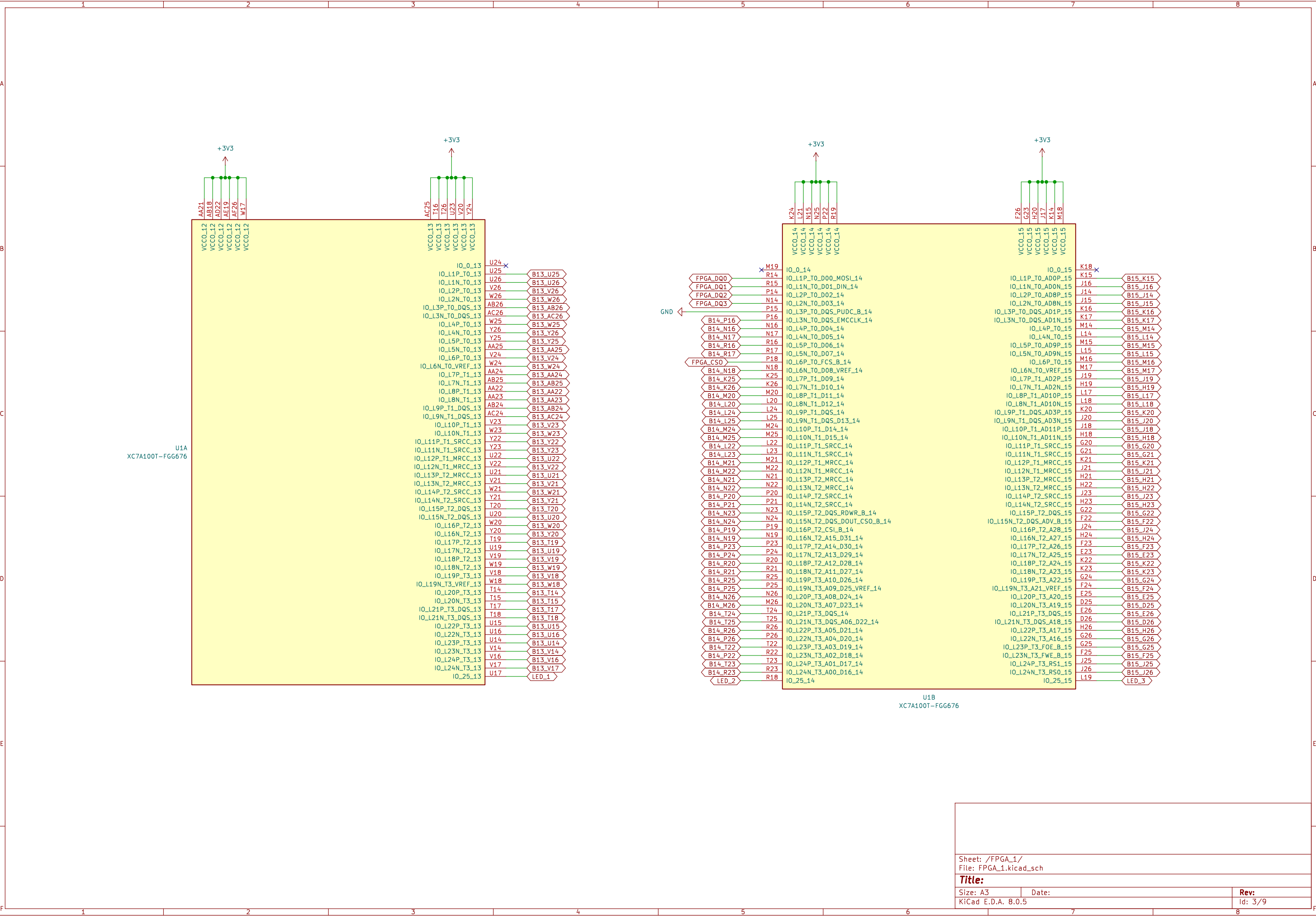
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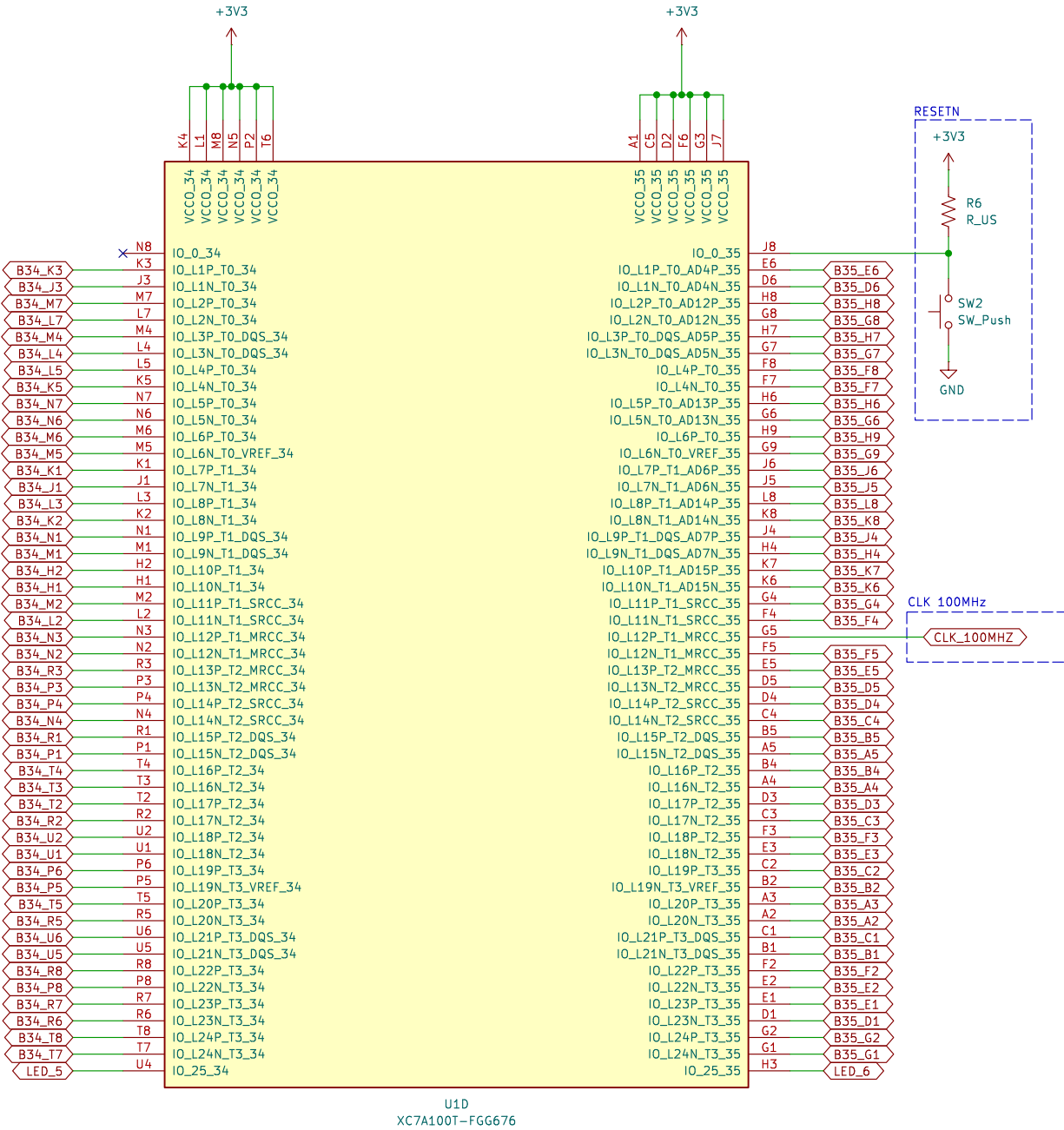
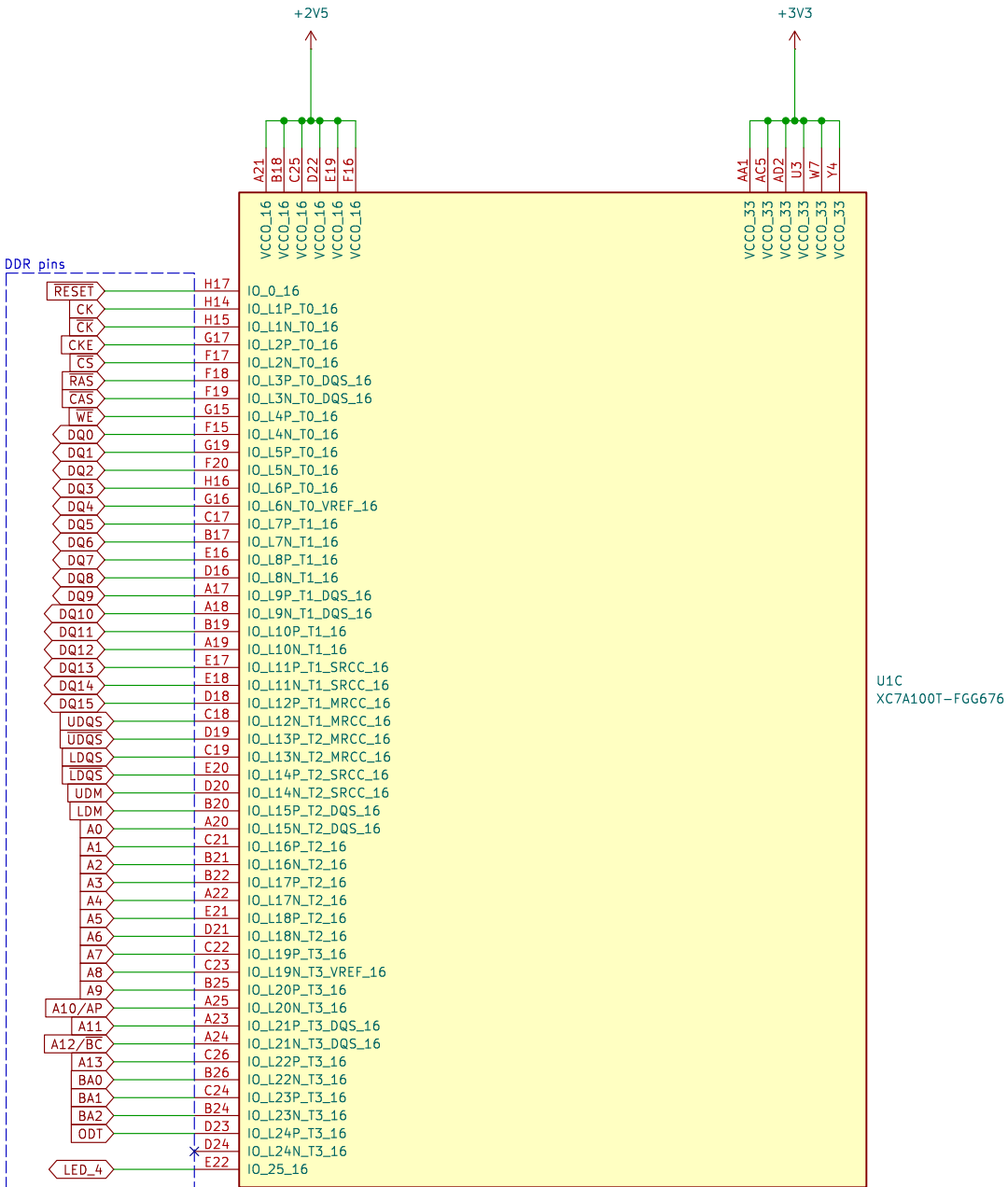
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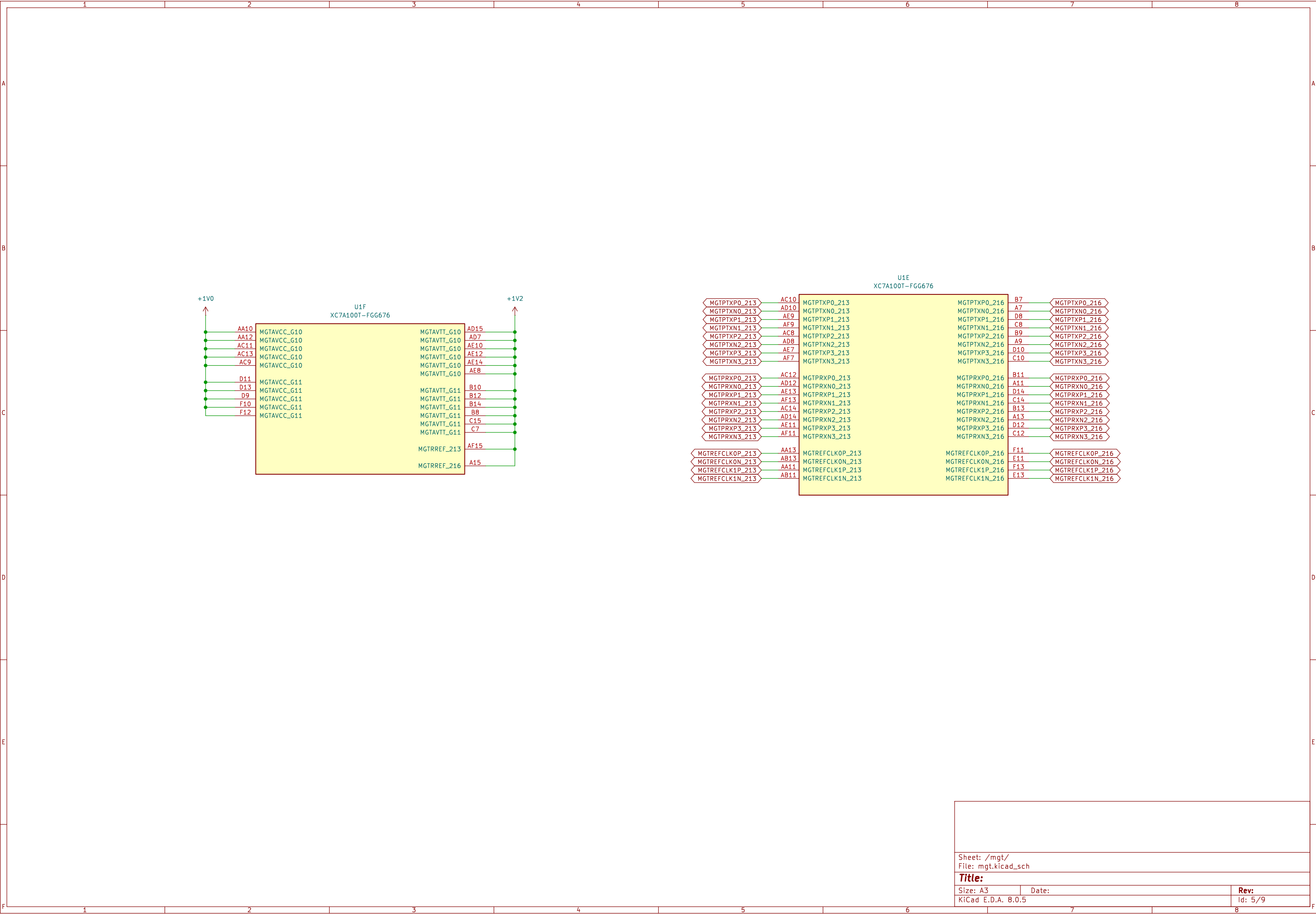
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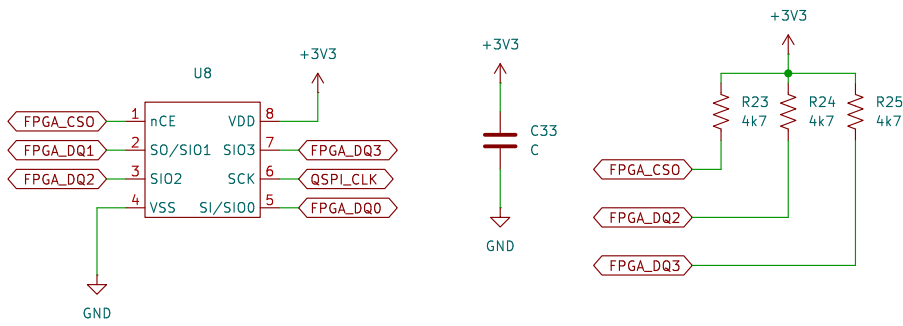
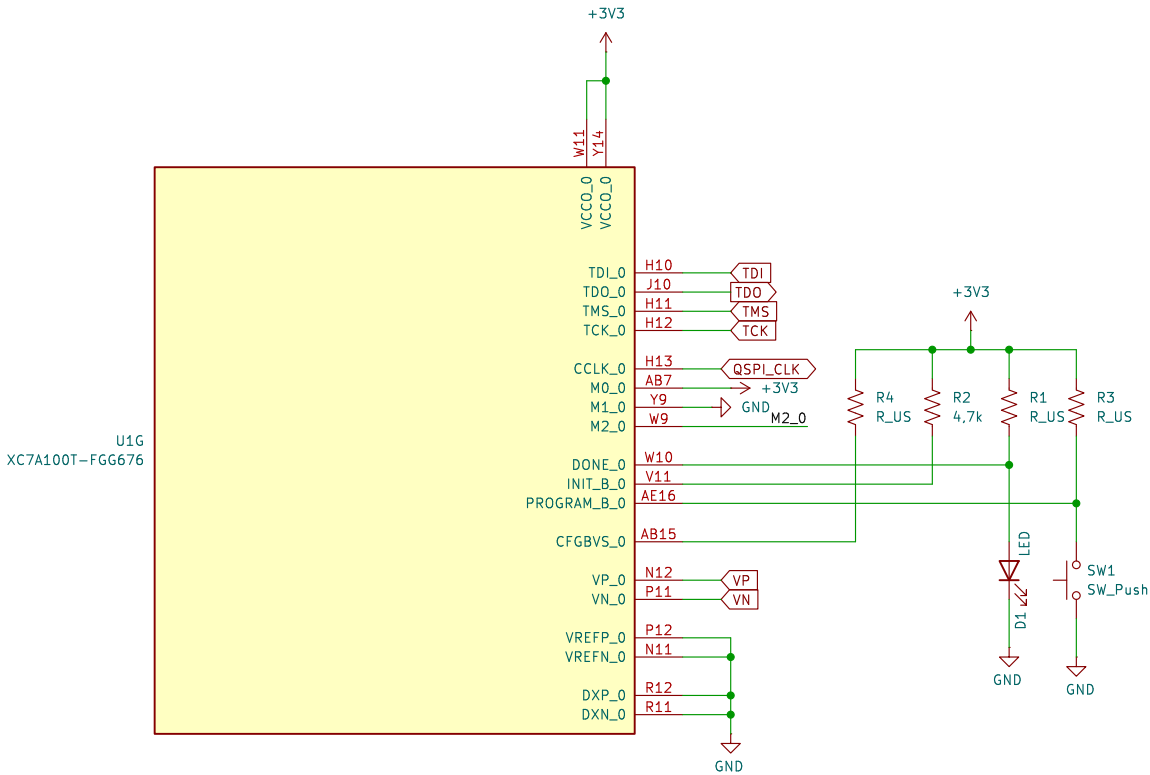
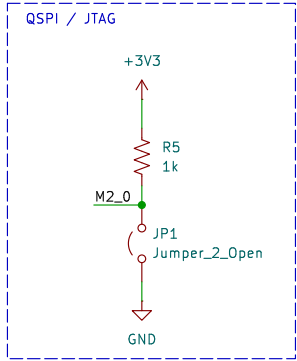
connectors2

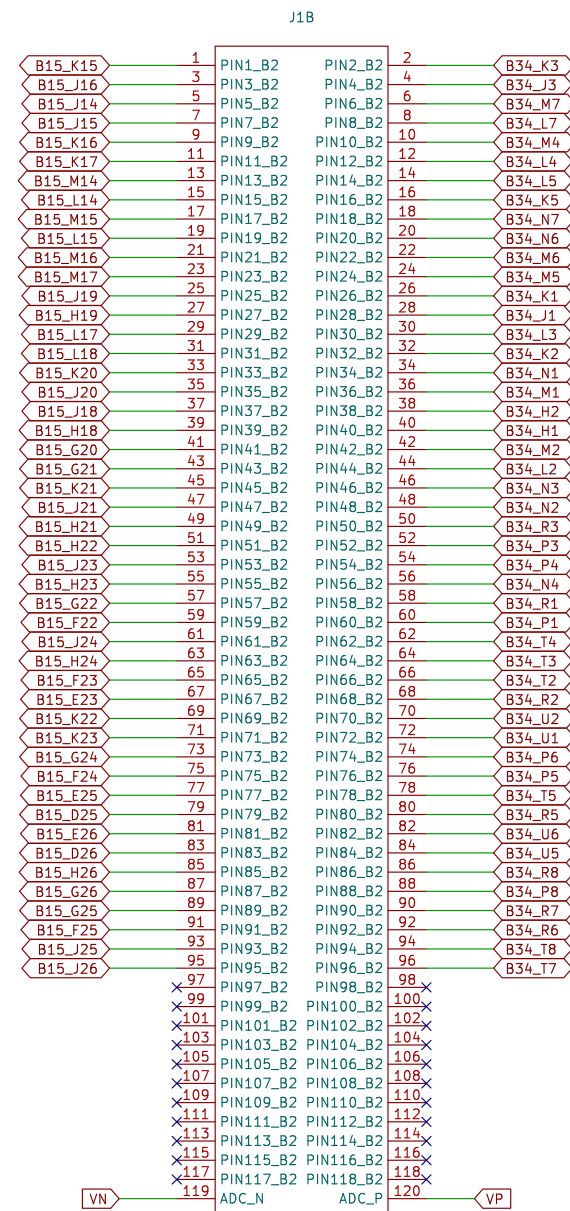
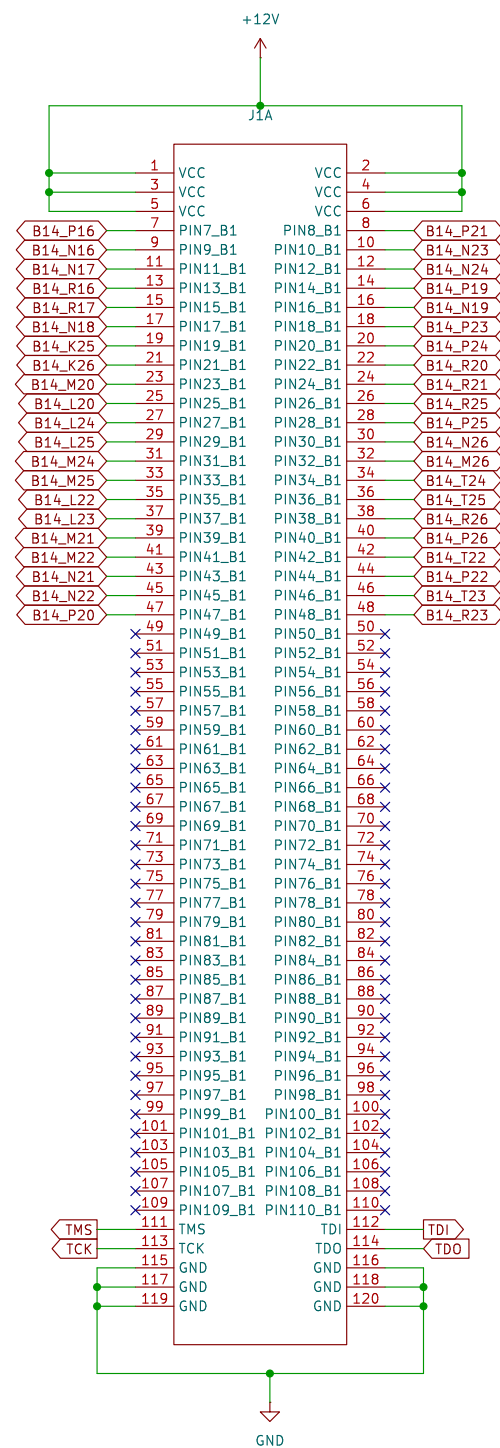
File: connectors2.kicad_sch











BANCO MGT

