TFG:GUÍA PRÁCTICA PARA EL DISEÑO DE SoCs

COMPONENTES:
- XC7Z010-CLG400 (SoC)
- TPS563201 x4
- MT41K256M16 (MEMORIA RAM) x2
- FT2232H (JTAG)
- SD CONNECTOR
- S25FL128S
- ASE - 33.333MHz
- 93C46

Sheet: alimentaciones
File: alimentaciones.sch

Sheet: memoria RAM
File: memoria_RAM.sch

Sheet: entradas y salidas
File: entradas_salidas.sch

Sheet: referencias y MIO
File: referencias_MIO.sch

https://github.com/DRubioG/Guia-practica-para-el-dise-o-de-SoCs/tree/main/esquematico_Zynq_7000

UAH

Sheet: /
File: esquematico_Zynq_7000.sch

Title: TFG: GUÍA PRÁCTICA PARA EL DISEÑO DE SoCs: Zynq-7000

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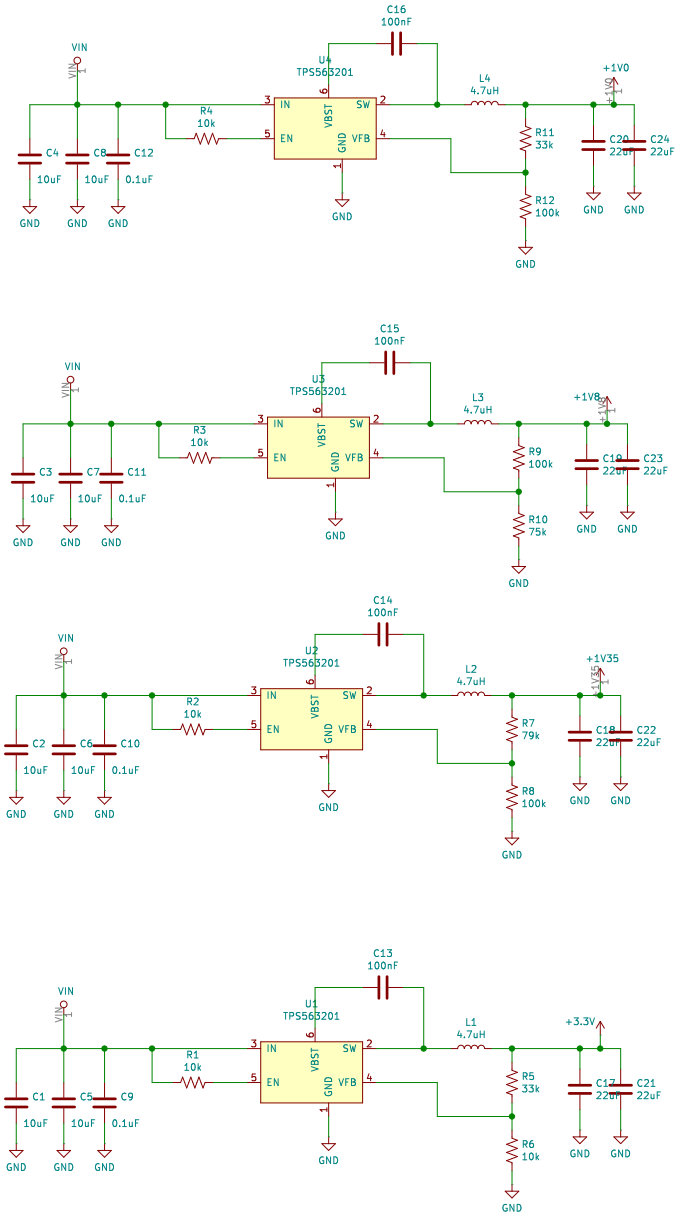
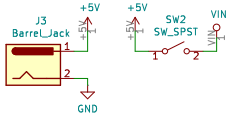
KiCad E.D.A. kicad (5.1.9)-1		Id: 1/5

TPS563201

Vout=0,768 x (1 + R1/R2)

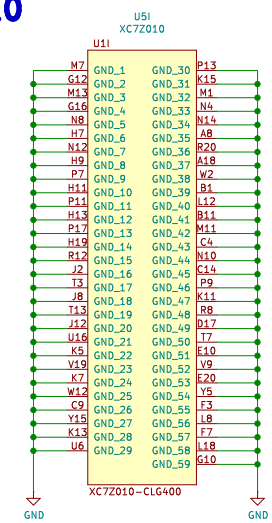


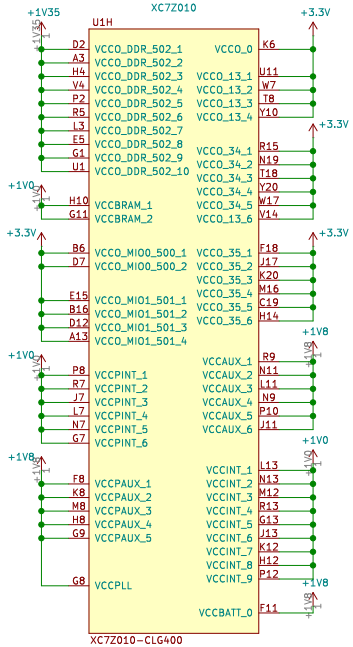
Voltage Input



led ON

XC7Z010





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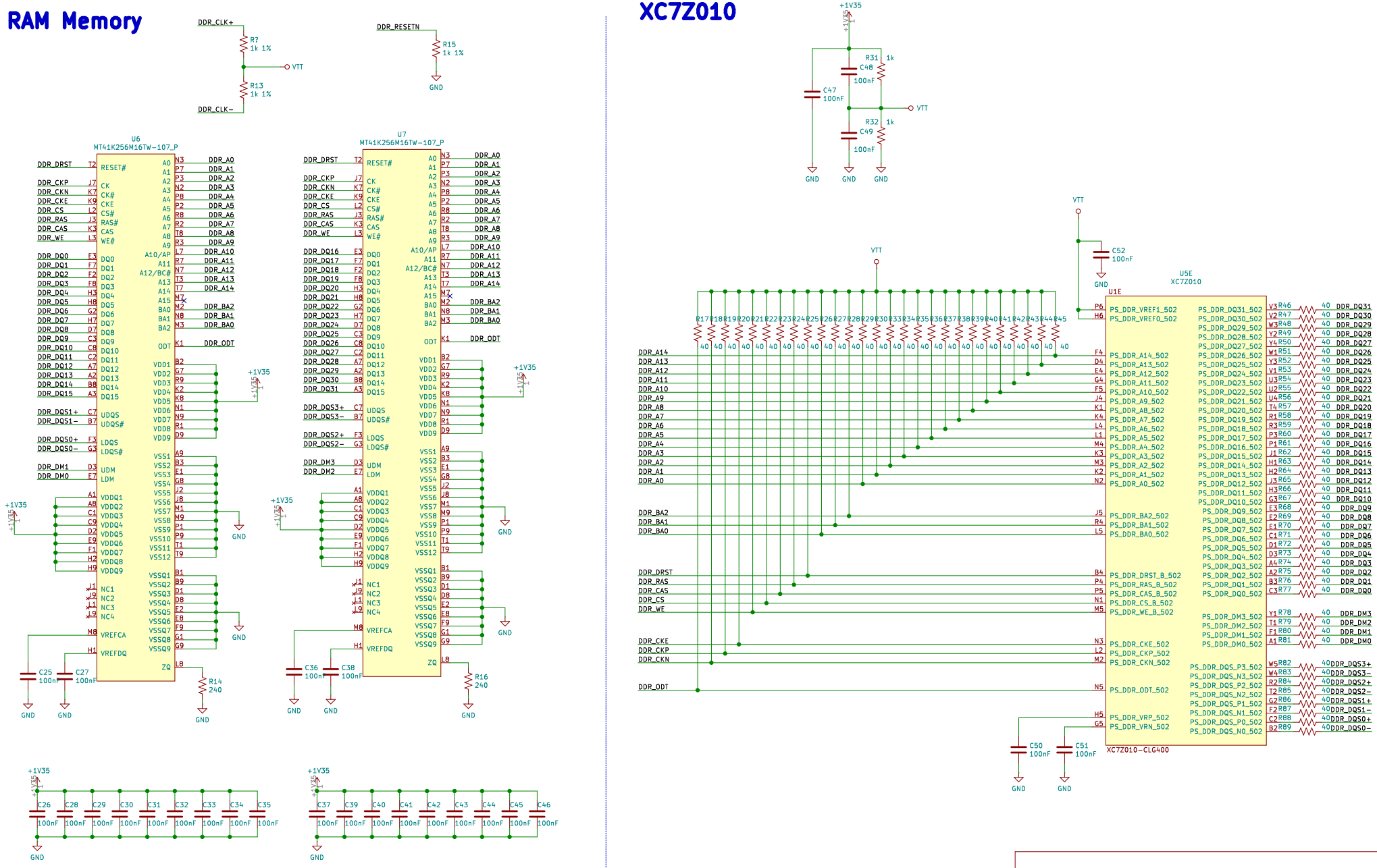
RAM Memory



XC7Z010

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Title: TFG: GUÍA PRÁCTICA PARA EL DISEÑO DE SoCs: Zynq−7000

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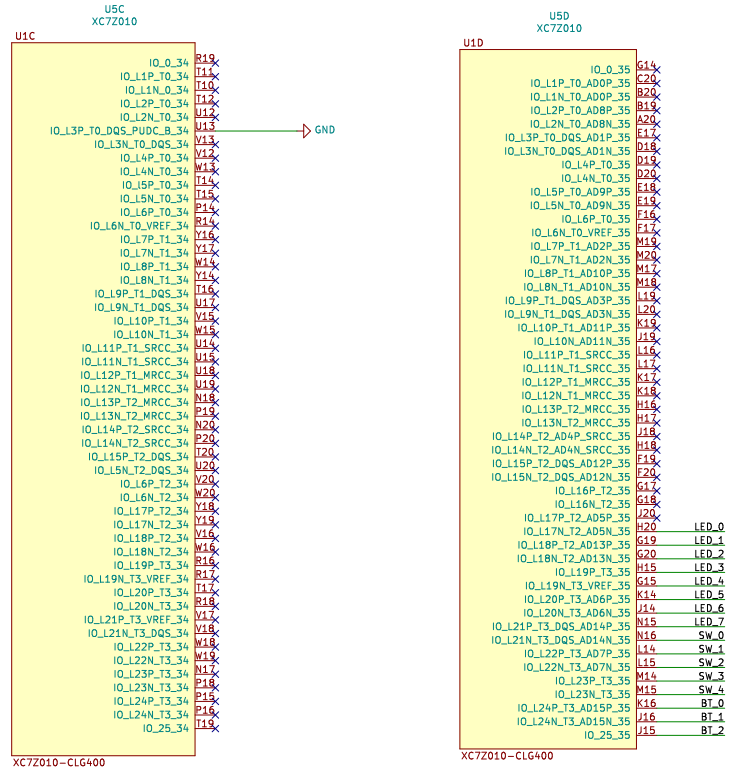
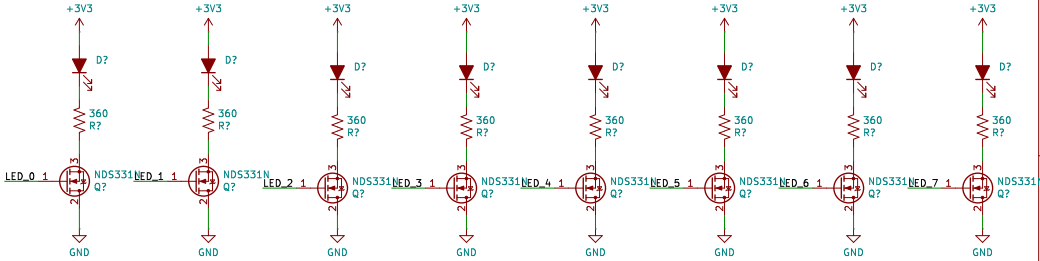
KiCad E.D.A. kicad (5.1.9)−1		Id: 3/5

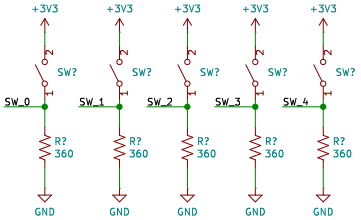
XC7Z010



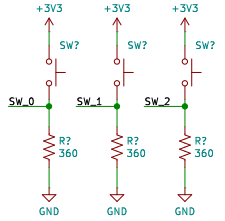
LEDS



SWITCHES



BUTTON



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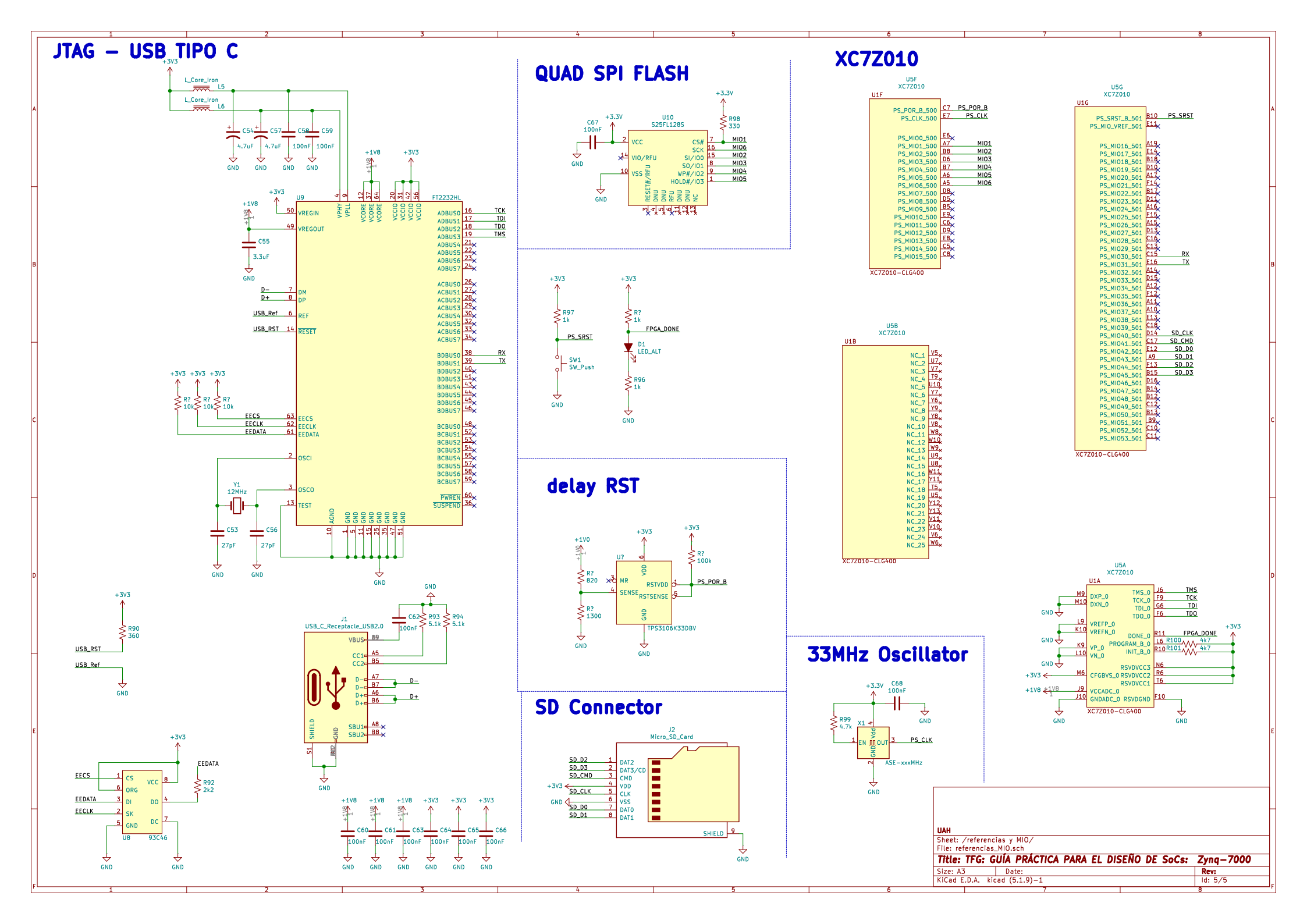
JTAG – USB TIPO C
QUAD SPI FLASH
XC7Z010
delay RST
33MHz Oscillator
SD Connector
U9
FT2232HL
U10
S25FL128S
U1F
U5F
U5G
U5B
U1B
U5A
U1A
XC7Z010-CLG400
U?
TPS3106K33DBV
J1
USB_C_Receptacle_USB2.0
J2
Micro_SD_Card
U8
93C46
Y1
12MHz
X1
ASE-xxxMHz
SW1
SW_Push
D1
LED_ALT
L_Core_Iron
L5
L6
C54
C57
C58
C59
C55
C53
C56
C60
C61
C63
C64
C65
C66
C62
C67
C68
R90
R92
R93
R94
R96
R97
R98
R99
USB_RST
USB_Ref
PS_SRST
FPGA_DONE
PS_POR_B
PS_CLK
TCK
TDI
TDO
TMS
RX
TX
SD_CLK
SD_CMD
SD_D0
SD_D1
SD_D2
SD_D3
MIO1
MIO2
MIO3
MIO4
MIO5
MIO6
EECS
EECLK
EEDATA
D-
D+
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Size: A3
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