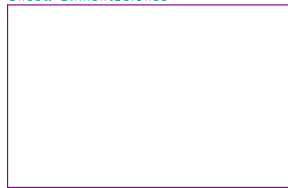


TFG:GUÍA PRÁCTICA PARA EL DISEÑO DE SoCs

COMPONENTES:

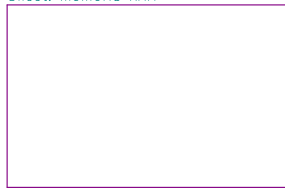
- XC7Z010-CLG400 (SoC)
- TPS563201 x4
- MT41K256M16 (MEMORIA RAM) x2
- FT2232H (JTAG)
- SD CONNECTOR
- S25FL128S
- ASE – 33.333MHz

Sheet: alimentaciones



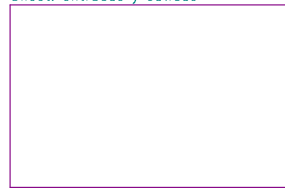
File: alimentaciones.sch

Sheet: memoria RAM



File: memoria_RAM.sch

Sheet: entradas y salidas



File: entradas_salidas.sch

Sheet: referencias y MIO



File: referencias_MIO.sch

UAH

Sheet: /

File: esquemático_Zynq_7000.sch

Title: TFG: GUÍA PRÁCTICA PARA EL DISEÑO DE SoCs: Zynq-7000

Size: A4

Date:

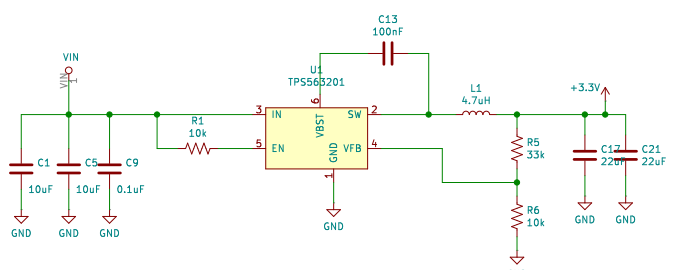
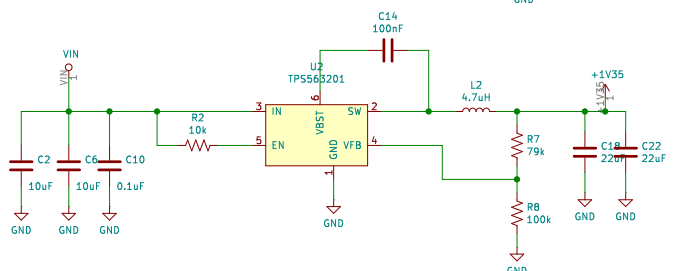
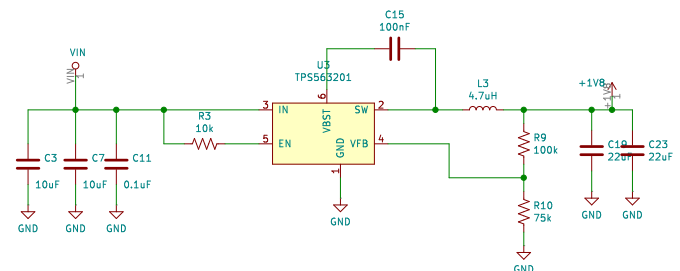
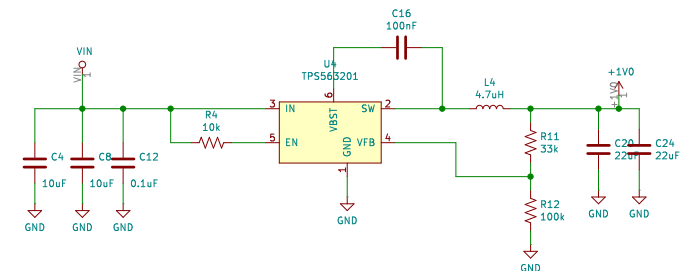
Rev:

KiCad E.D.A. kicad (5.1.9)-1

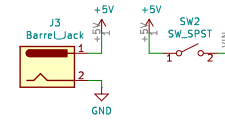
Id: 1/5

TPS563201

$$V_{out} = 0,768 \times (1 + R1/R2)$$



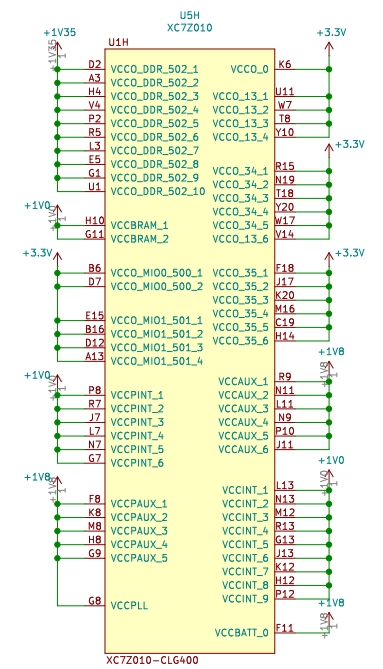
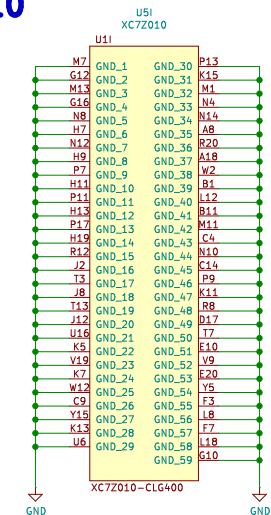
Voltage Input



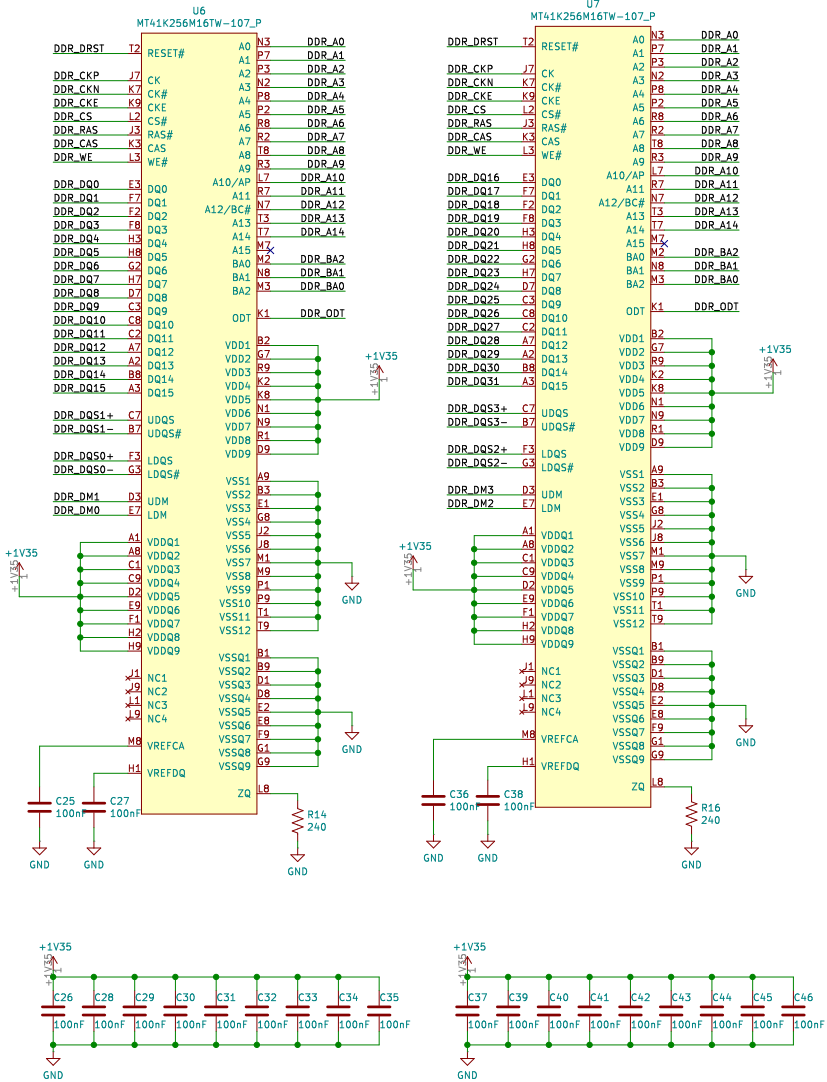
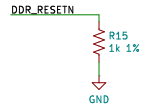
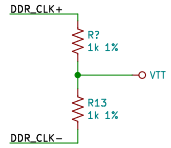
led ON



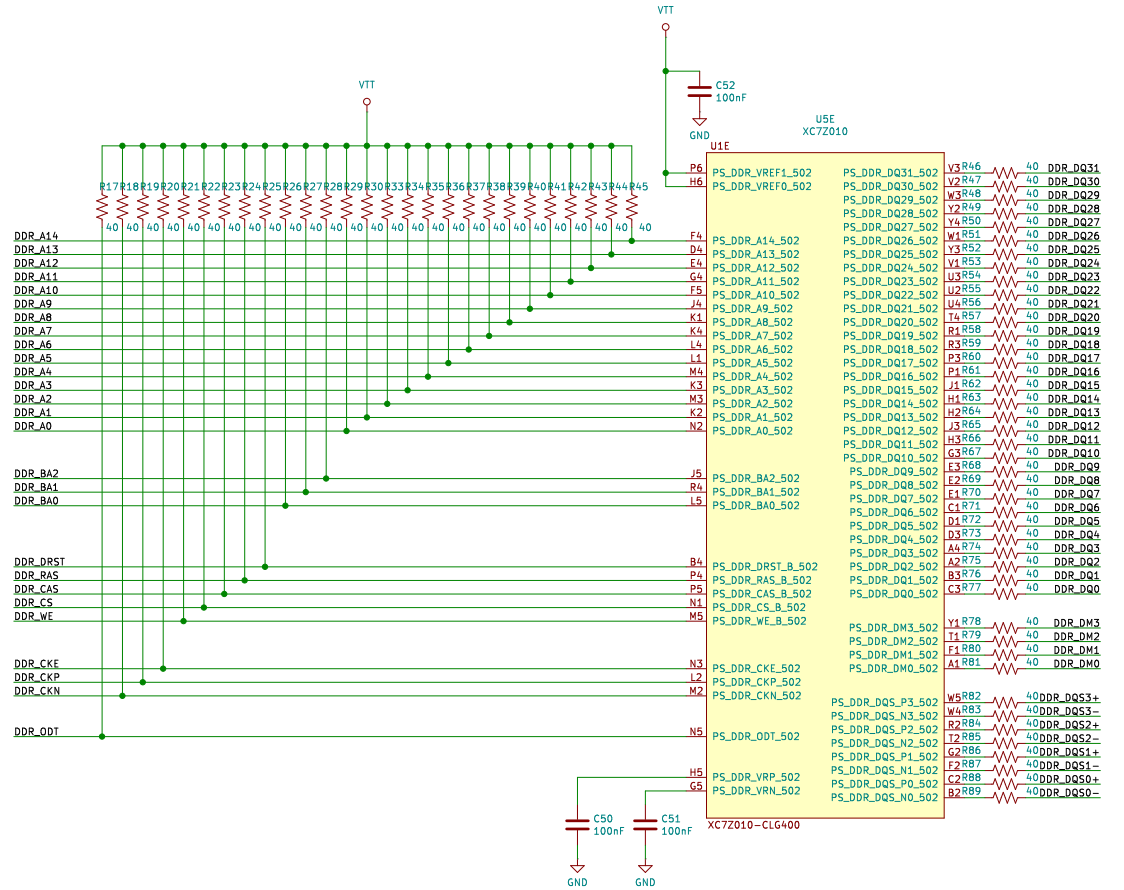
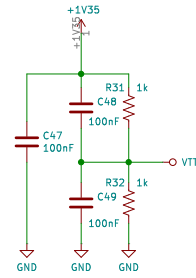
XC7Z010



RAM Memory



XC7Z010



UAH

Sheet: /memoria RAM /

File: memoria_RAM.sch

Title: TFG: GUÍA PRÁCTICA PARA EL DISEÑO DE SoCs: Zynq-7000

Size: A3

Date:

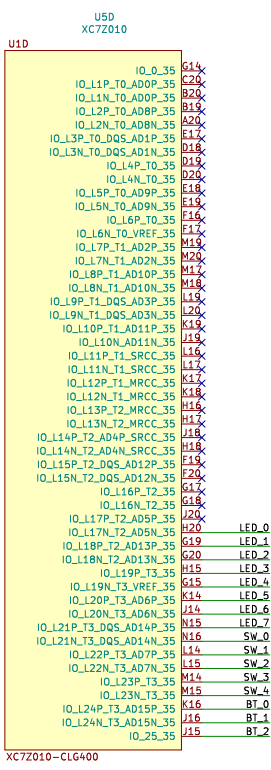
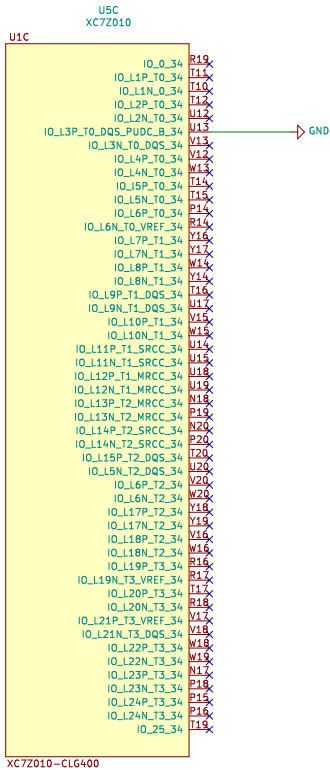
KiCad E.D.A.	kiCad (5.1.9)–1
--------------	-----------------

Id: 3/5

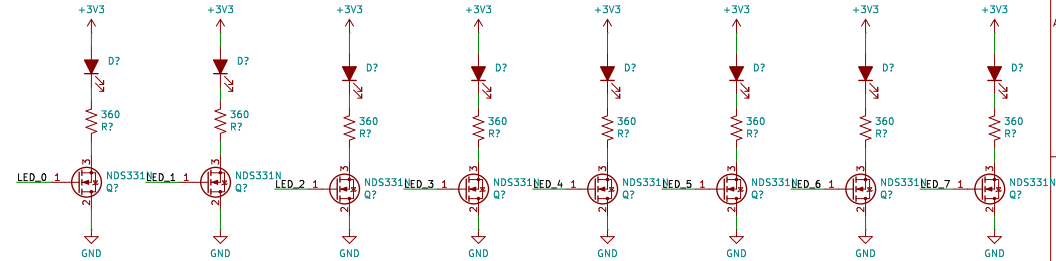
	()

3

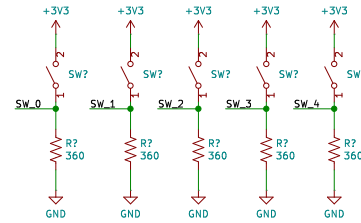
XC7Z010



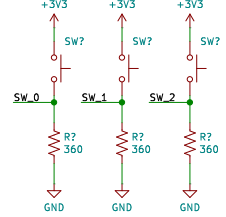
LEDS



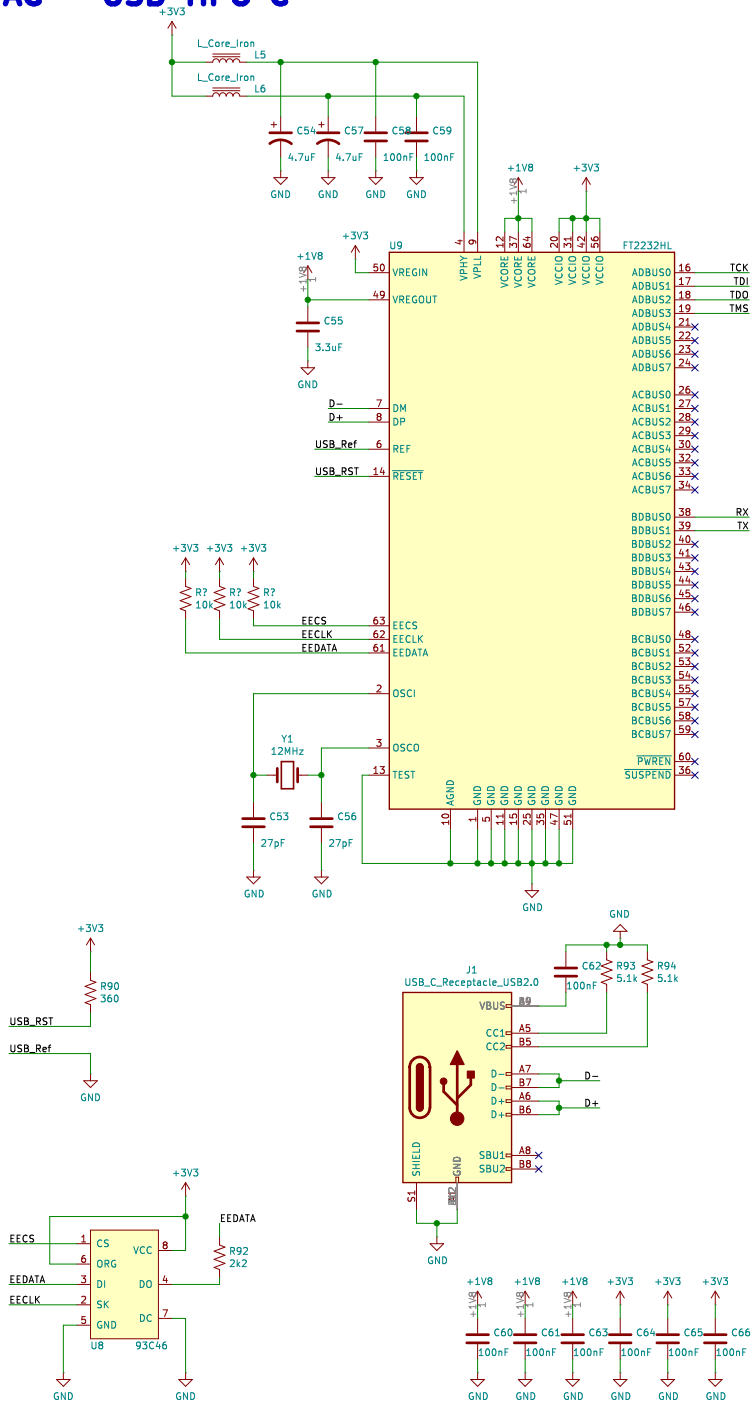
SWITCHES



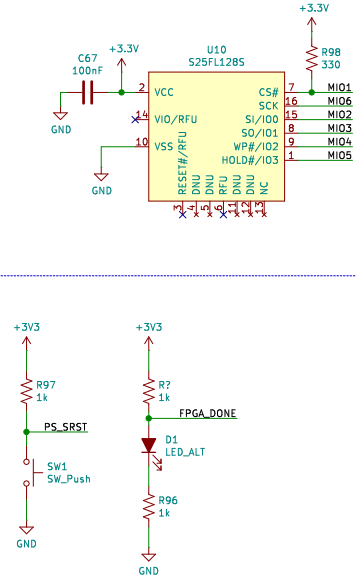
BUTTON



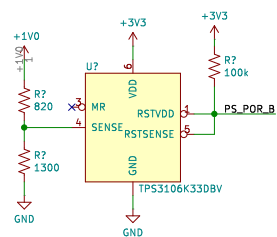
JTAG – USB TIPO C



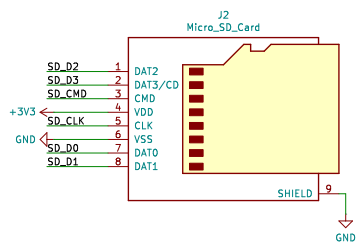
QUAD SPI FLASH



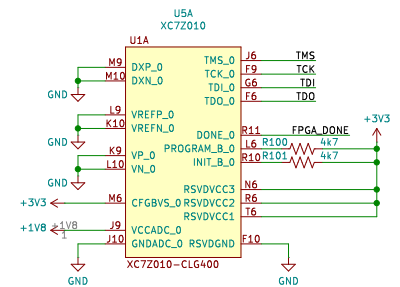
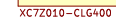
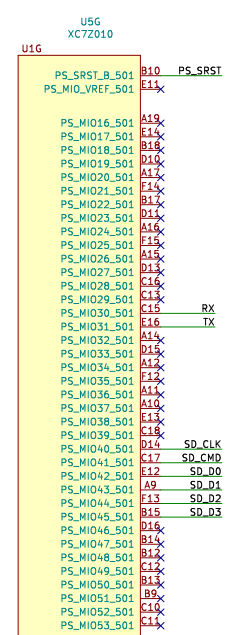
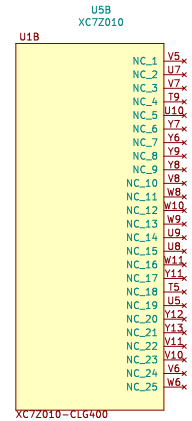
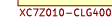
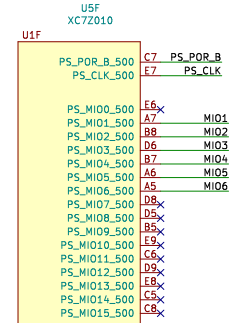
delay RST



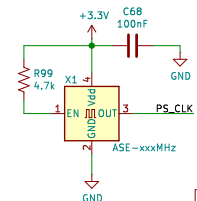
SD Connector



XC7Z010



33MHz Oscillator



UAH

Sheet: /referencias y MIO/

File: referencias_MIO.sch

Title: TFG: GUÍA PRÁCTICA PARA EL DISEÑO DE SoCs: Zynq-7000

Size: A3

KiCad E.D.A. kicad (5.1.9)-1

Rev:

Id: 5/5