

# Decoder

## Generics

- yfg6yugiukj
- U
- N
- T
- Gad

## Ports

- |                       |                            |  |
|-----------------------|----------------------------|--|
| - clk                 | <type> input<br>: alfa     | <value> std_logic                                  |
| - rst                 | <type> input<br>: alfa     | <value> std_logic                                  |
| - yhmm                | <type> input<br>: alfa     | <value> std_logic                                  |
| - Rowerererrerererere | <type> input<br>: dfhdfg   | <value> std_logic_vector <bits> (N-1) downto (N-2) |
| - Col                 | <type> output<br>: dfhdfg1 | <value> std_logic_vector <bits> 3 downto 0         |
| - DecodeOut           | <type> output<br>: marcafa | <value> std_logic_vector <bits> 3 downto 0         |