Version 0: SoC: XC7Z010-CLG400 ALIMENTACIONES - SoC: XC7Z010-CLG400 (GND & VCC) - TPS563201 x4 - TPS3106K33DBV (DELAY RST) MEMORIA RAM - SoC: XC7Z010-CLG400 (DDR) - RAM: MTK41K256M16TW-107:P x2 ENTRADAS Y SALIDAS - SoC: XC7Z010-CLG400 (BANK 34 & 35) - TP0125016 - HDMI CONNECTOR - LEDS - BUTTONS - SWITCHES REFERENCIAS Y MIOs - SoC: XC7Z010-CLG400 (MIO, NC & REFERENCE) - JTAG (FT2232H &93C46)` - CP2102N (UART) - SD CONNECTOR - S25FL128S (QUAD-SPI FLASH MEMORY) - OSCILLATOR 33.333MHz

DESIGNED BY: SoC-eame DESIGNED IN SPAIN

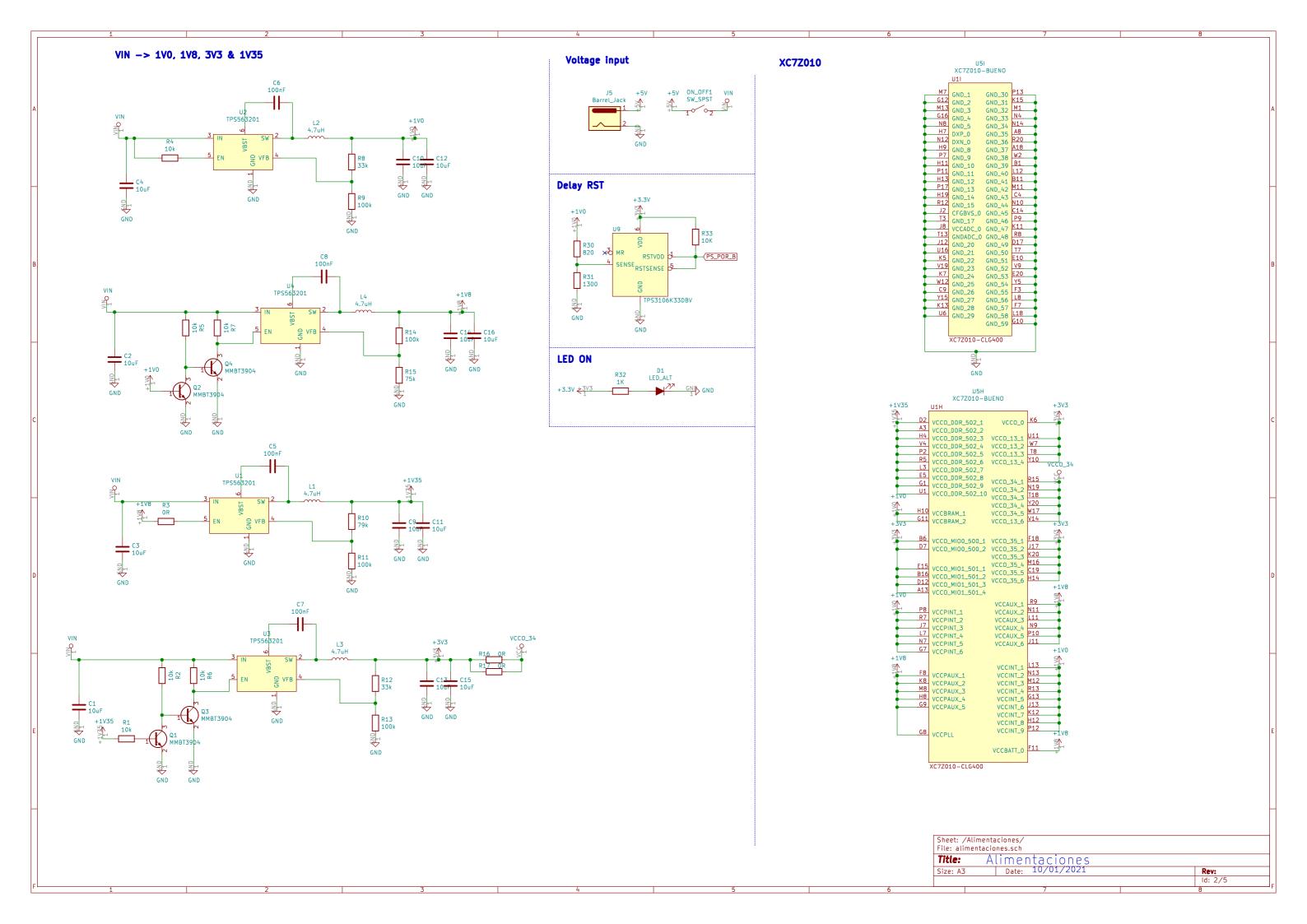


soceame.wordpress.com

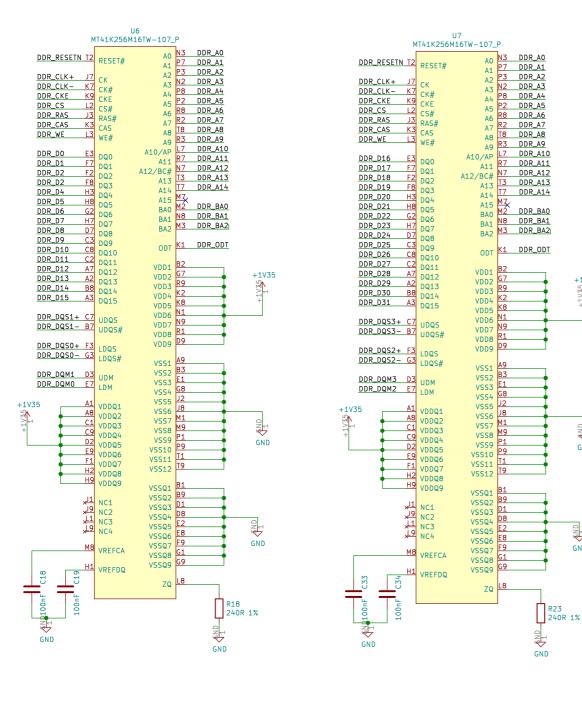
neet: Alimentaciones	Sheet: Memoria RAM	Sheet: Entradas y Salidas	Sheet: Referencias y MIOs
le: alimentaciones.sch	File: DDR.sch	File: ent sal.sch	File: referencia.sch

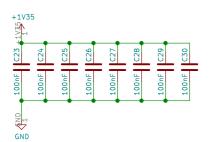
Zynq-eame 0

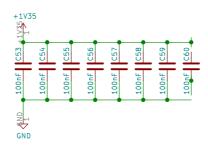
Sheet: / File: Diseno_1	.sch	
Title: in	dex	
Size: A3	Date:	Rev:
	•	ld: 1/5

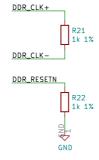


MT41K256M16TW-107:P x2







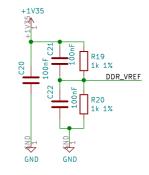


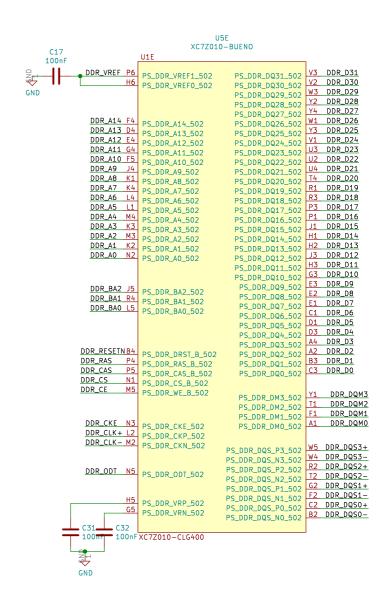
1/35

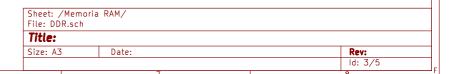
GND

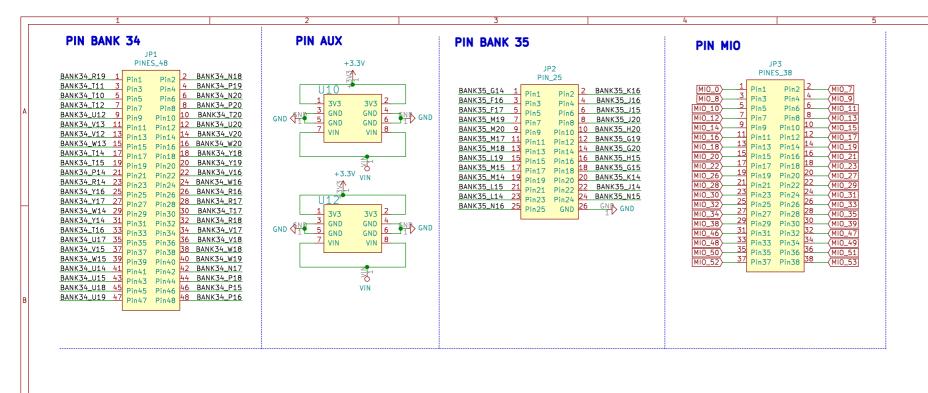
GND

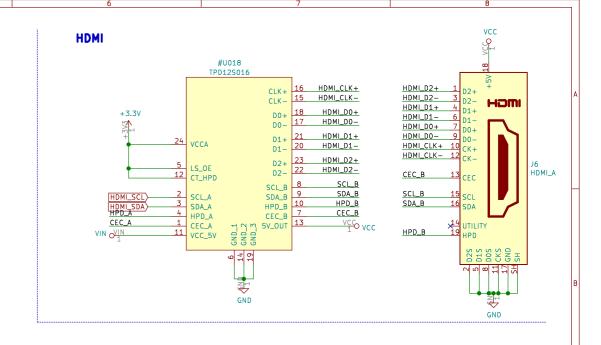
XC7Z010

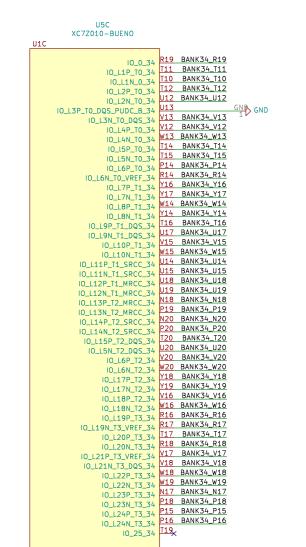












XC7Z010-CLG400

