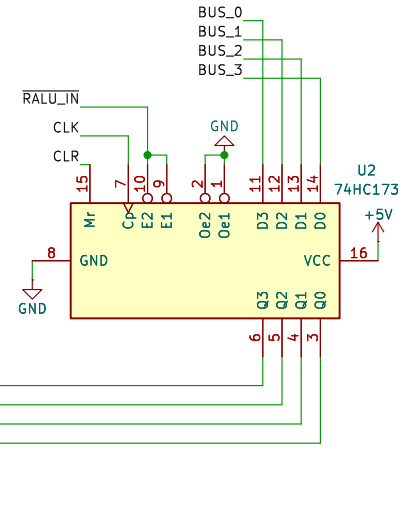
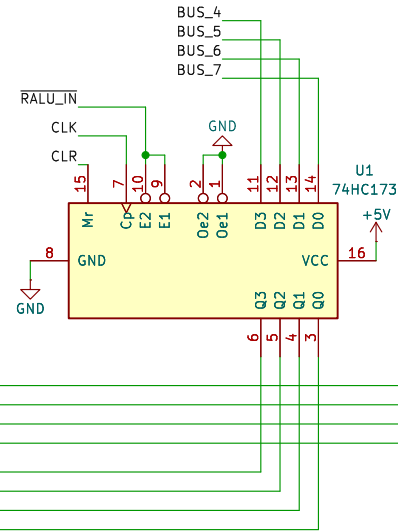
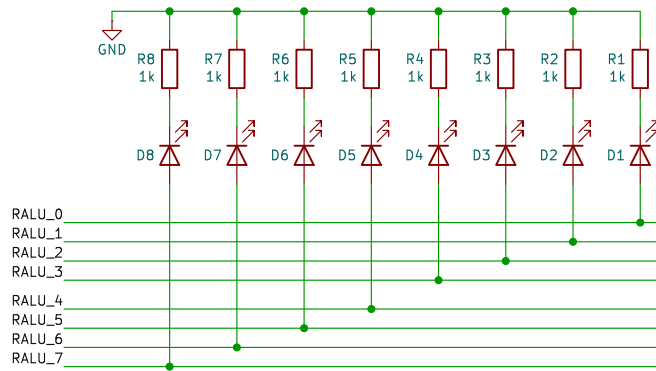
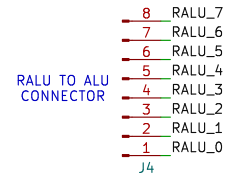
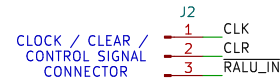
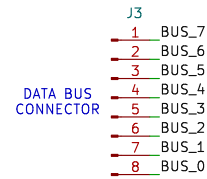
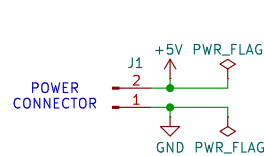


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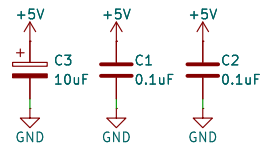


# PCB CONNECTORS



## POWER AND DECOUPLING CAPACITORS

TOOLING HOLES



Sheet: /  
File: RALU.kicad\_sch

**Title:**

Size: A4 Date:

KiCad E.D.A. 8.0.6

**Rev:**

Id: 1/1