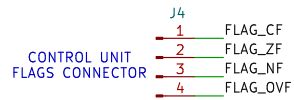
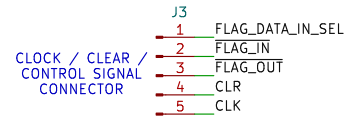
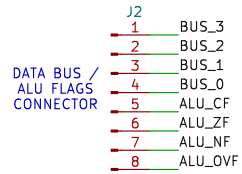
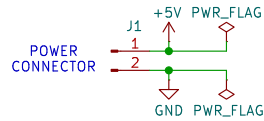
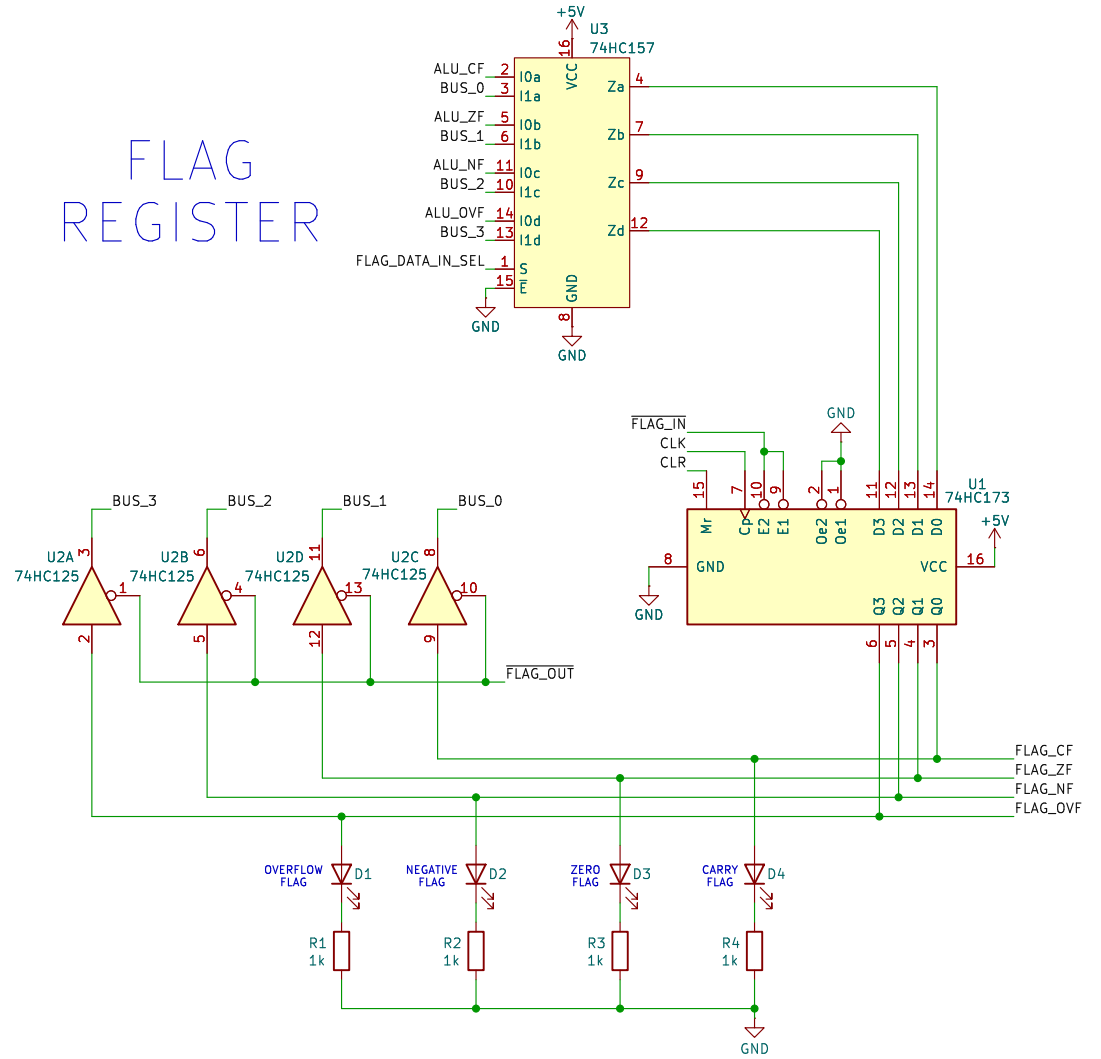


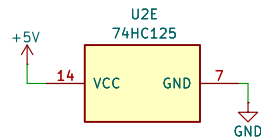
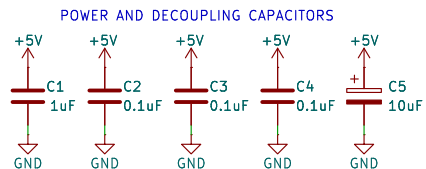
# PCB CONNECTORS



# FLAG REGISTER



TOOLING HOLES



Sheet: /

File: FLAG.kicad\_sch

**Title:**

Size: A4

Date:

KiCad E.D.A. 8.0.6

**Rev:**

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