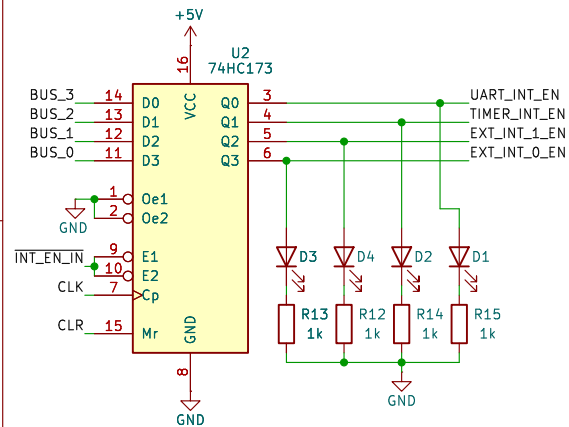
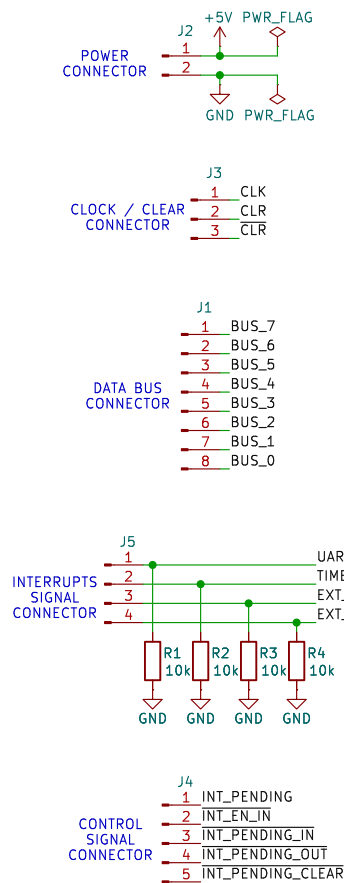


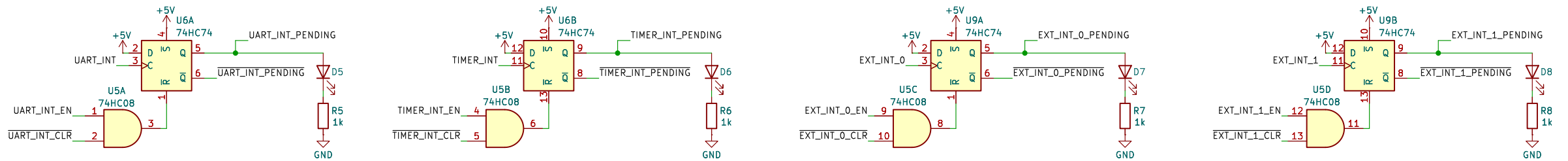
INTERRUPT ENABLE REGISTER



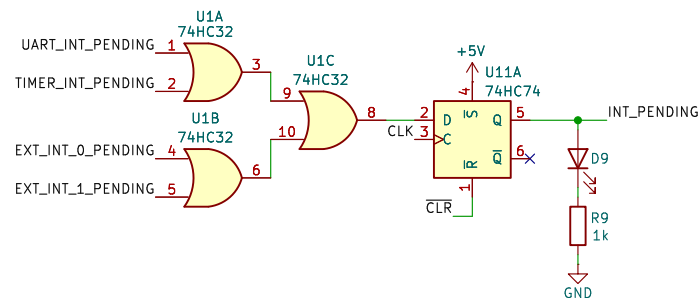
PCB CONNECTORS



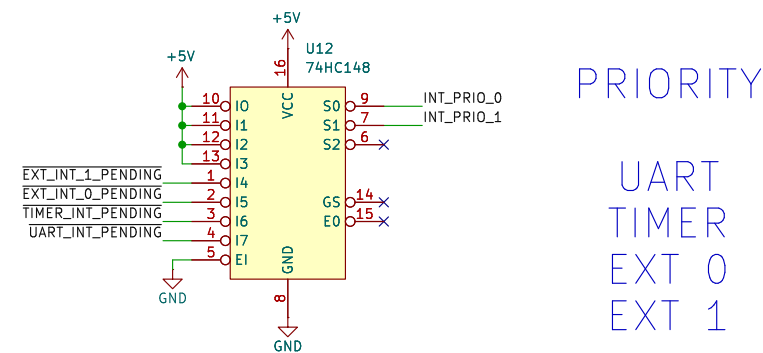
INTERRUPT LATCHES



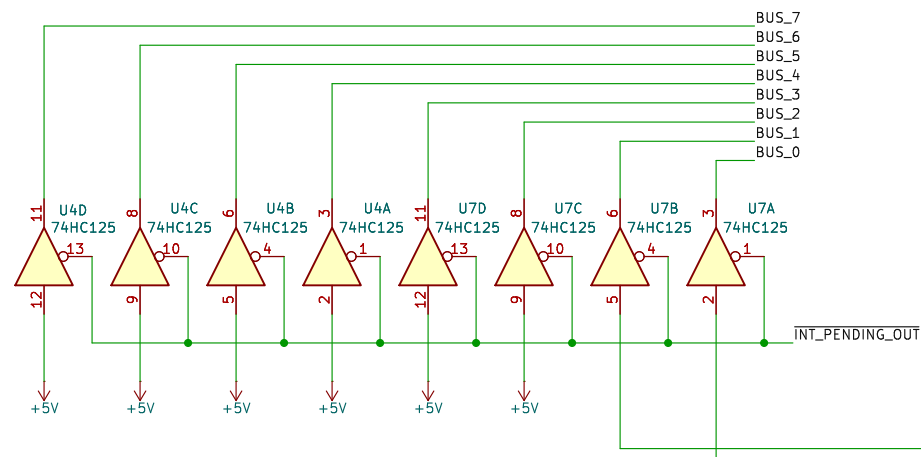
INTERRUPT PENDING TO CPU



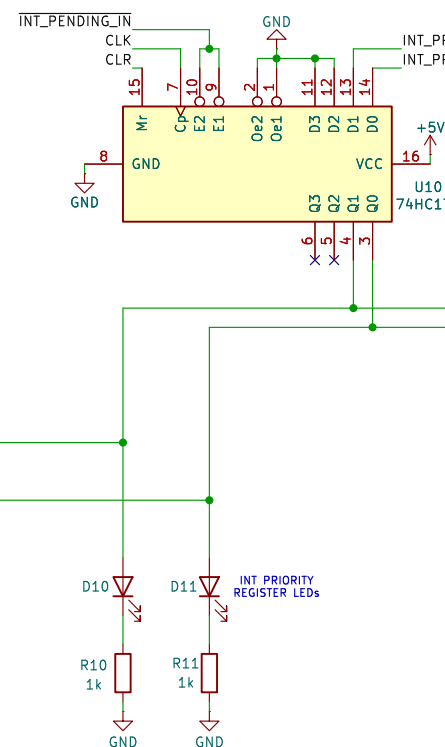
INTERRUPT PRIORITY ENCODER



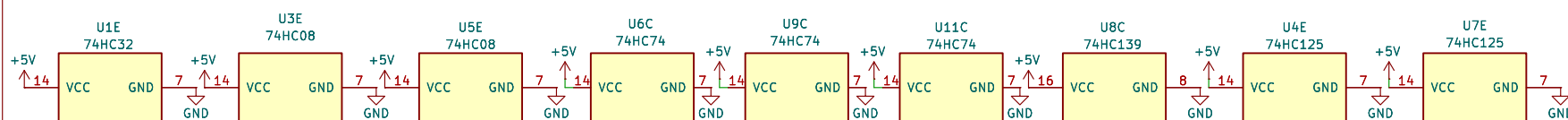
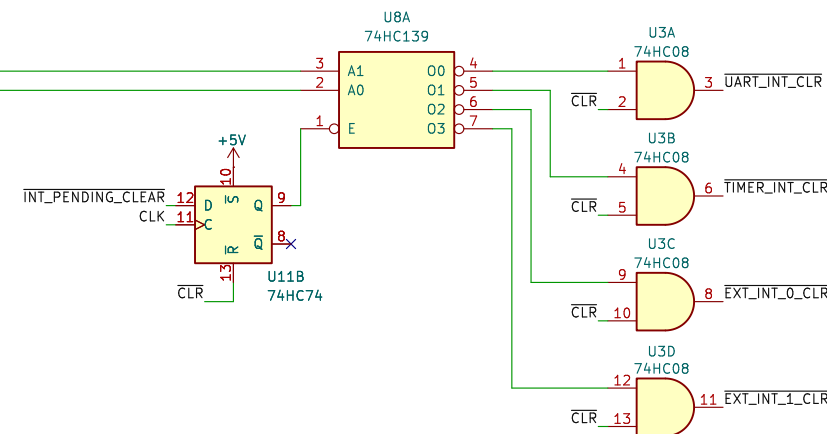
INTERRUPT PENDING ADDRESS



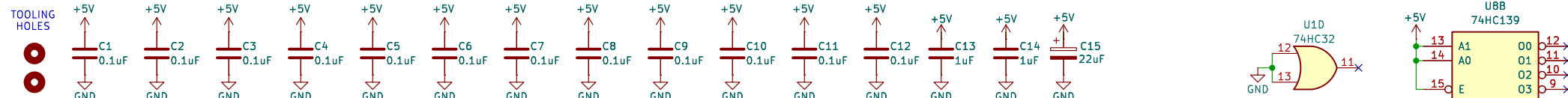
INTERRUPT PRIORITY REGISTER



INTERRUPT PENDING CLEAR



POWER AND DECOUPLING CAPACITORS



Sheet: /		
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