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Government College of Engineering, Amravati

Date : 16th Sept., 2017 Session Class Test - I Time : 1Hr

Marks: 8

Course code & Title: ETU101 - Basic Electronics Engineering

Solve any two (2). All questions carry equal marks.

(02x 04Mks)

Qu.1. Define rectification and distinguish between Half Wave, Center Tap and Bridge Rectifiers.

Qu.2. Discuss how voltage is regulated using Zener diode?

Qu.3. With neat diagram explain the construction, working and V-I characteristics of a P-N junction Diode.

Compulsory question.

(01 x 04Mks)

Qu.1. Comment in detail on what will happen if a shunt capacitor filter is connected at the output of a Full Wave Rectifier? Draw the waveforms for I/P, rectifier O/P, filter O/P.

Qu.2. Solve any two (2). All questions carry equal marks.

(02 x 02Mks)

- With neat diagram explain the construction and working of Light Emitting Diode.
- Define the terms: i) Cut-in voltage, ii) Ripple factor, iii) PIV, iv) Rectifier Efficiency.
- Explain the working of zener voltage regulator with suitable diagram.