Government College of Engineering, Amravati

(An Autonomous Institute of Government of Maharashtra)

Sixth Semester B. Tech. (Electronics and Telecommunication)

Summer - 2016

Course Code: ETU604

Course Name: Digital Signal Processing

Гіте: 2 hr. 30min.

Max. Marks: 60

Instructions to Candidate

1) All questions are compulsory. Solve any two sub-questions from Q1 and Q2.

2) Assume suitable data wherever necessary and clearly state the assumptions made.

3) Diagrams/sketches should be given wherever necessary.

4) Use of logarithmic table, drawing instruments and non-programmable calculators is permitted.

5) Figures to the right indicate full marks.

What is region of convergence (ROC) in case of 6 Z-transform? Explain the nature of ROC for right sided, left sided and both sided sequences with finite and infinite durations.

b Compute 4-point and 8-point discrete Fourier 6 transform (DFT) of following signal

$$X[n] = \frac{1}{8}; 0 \le n \le 3$$
$$= 0; else$$

c Compare decimation in time (DIT) and 6 decimation in Frequency (DIF) fast Fourier transform (FFT)

- Q2 a Enlist the windows with their window sequence 6 available for finite impulse response (FIR) filter design; explain any one with its frequency response.
 - b Determine the coefficient of linear phase FIR 6 filter of length N=15 with symmetric unit sample response and frequency response that satisfies the condition.

$$H\left(\frac{2\pi k}{15}\right) = 1;$$
 for $k = 0,1,2,3$
= 0.4; for $k = 4$
= 0; for $k = 5,6,7$

c Draw the direct form-I and direct form -II 6 representation of following IIR (Infinite Impulse Response) low-pass filter.

$$H(Z) = \frac{0.1 \, Z^{-1} + 0.5 \, z^{-2}}{1 - 0.75 Z^{-1} + 0.5 Z^{-2} - Z^{-3}}$$

Q3 Design a Butterworth digital IIR (Infinite Impulse Response) high-pass filter using bilinear transformation by taking T = 0.1 second, to satisfy following specifications

 $0.6 \le |H(e^{jw})| \le 1.0$; for $0.7\pi \le w \le \pi$ $|H(e^{jw})| \le 0.1$; for $0 \le w \le 0.35 \pi$

Draw the equivalent Direct Form-I and Direct Form-II structures.

Q4 a What is interpolator? Show that it is time invariant and linear system.

- b What are the applications of multi-rate DSP? 6 Explain sub band coding in details.
- Q5 a Explain the instruction set of TMS320 family 6 processors.
 - b Write short note on following related to DSP 6 processors
 - a. Pipelining
 - b. MAC (Multiply/Accumulate)