

Time : 1Hr
Date: 09/03/2016

Department of Electronics and Telecommunication Engineering
Class Test - II
ETU403 Microprocessor and it's Interfacing

Marks: 15

Solve any five (5)

- ✓ Qu.1. "It becomes extremely important to preserve the lower order address byte every time". Justify the statement and discuss the method to achieve this. 3
- ✓ Qu.2. Enlist the various data transfer schemes in 8085 μ P and discuss the Interrupt driven data transfer with flowchart. 3
- ✓ Qu.3. In the given delay subroutine, calculate the amount of delay inserted, if $f_{osci} = 3\text{MHz}$; 3

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LXI B,01EBh; 10
again: MVI D,FFh; 7
up: DCR D; 4
JNZ Up; 10/7
DCX B; 6
MOV A,C; 7
ORA B; 7
JNZ Again; 10/7
RET ; 10
```

- ✓ Qu.4. Discuss how an instruction cycle progresses along each T-state for 'ADD B' instruction 3
- ✓ Qu.5. With an available memory chip of 4kB, expand the memory capacity upto 16kB, draw a neat interfacing diagram. 3
- ⑥ Qu.6. For a 8085 μ P system WAP to read the status of a switch connected to the SID line and accordingly turn ON/OFF the LED connected to the SOD line. Draw the diagram. 3