

# Government College of Engineering, Amravati

## Department of Electronics and Telecommunication Engineering Class Test- I

Course Code: ETU304

Course Name: Digital Electronics

Date: 23/09/15

Duration: 01 Hr

Max. Marks: 15

Time: 10:30 - 11:30

- 1] Design a four bit binary to BCD (binary coded decimal) convertor circuit using AOI logic. 05
- 2] Design a full subtractor using minimum numbers of 1:4 Demultiplexer. 05
- OR
- 2] Design a logic circuit that will resolve the priority of a resource within five processes. 05
- 3] Describe 1. Fan-in, 2. Fan-out, 3. Noise Margin, 4. Propagation Delay, 5. Power Consumption. 05
- OR
- 3] Out of the existing logic families; which is preferred most? Why? 05

7  
3  
12  
18