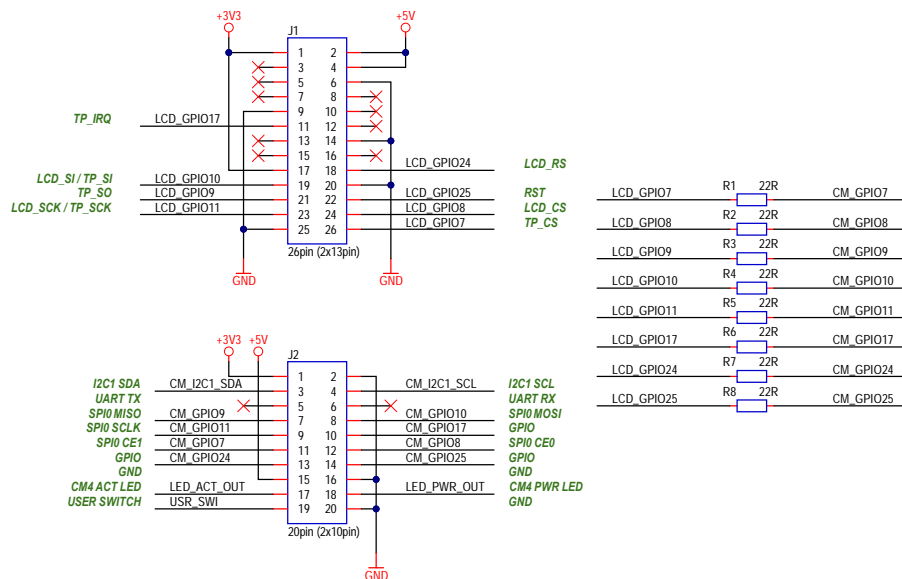


3.5" 480x320 RPi LCD Display



3v3 Power	1	2	5v Power
GPIO 2 (I2C1 SDA)	3	4	5v Power
GPIO 3 (I2C1 SCL)	5	6	Ground
GPIO 4 (GPCLK0)	7	8	GPIO 14 (UART TX)
Ground	9	10	GPIO 15 (UART RX)
GPIO 17	11	12	GPIO 18 (PCM CLK)
GPIO 27	13	14	Ground
GPIO 22	15	16	GPIO 23
3v3 Power	17	18	GPIO 24
GPIO 10 (SPI0 MOSI)	19	20	Ground
GPIO 9 (SPI0 MISO)	21	22	GPIO 25
GPIO 11 (SPI0 SCLK)	23	24	GPIO 8 (SPI0 CE0)
Ground	25	26	GPIO 7 (SPI0 CE1)
GPIO 0 (EEPROM SDA)	27	28	GPIO 1 (EEPROM SCL)
GPIO 5	29	30	Ground
GPIO 6	31	32	GPIO 12 (PWM0)
GPIO 13 (PWM1)	33	34	Ground
GPIO 19 (PCM FS)	35	36	GPIO 16
GPIO 26	37	38	GPIO 20 (PCM DIN)
Ground	39	40	GPIO 21 (PCM DOUT)

SCH:

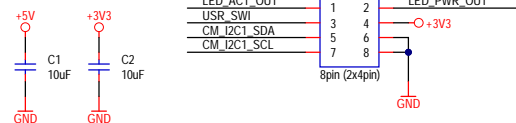
PCIE v4 reference board: PINE64 -ROCKPro64
https://files.pine64.org/doc/rockpro64/rockpro64_v21-SCH.pdf

BOM:

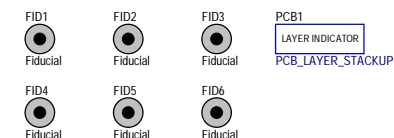
Description: NGFF connector (NGFF), M-Key:
 Lotes, MPN = APC10107-P001A (H4.2)
 Lotes, MPN = APC10113-P001A (H4.8)
 TE Connectivity, MPN = 1-2199230-5 (H4.2)

LAYOUT:

Place decoupling capacitors as close as possible to M.2 connector.



PCB MARKING



	Mirko Electronics Smoka Wawelskiego 1 30-535 Kraków, Poland	Size B
Title LCD Front Panel for BitPiRat board		Version VER1
Project: LCD Panel	RefDes: 1_99	Revision REV1
Variant: [No Variations]	Sheet: 1 / 1	
Designer: Smok Wawelski	Printed: 17.05.2022	
File Name: [01] BP_LCD_Panel_v1.SchDoc		