FEMB#3 Checkout Test Report

FEMB ID = 3
Date&Time: 2025-09-24 15:46:58
WIB_TCP_Version: 0x100
Temperature: RT
Note: for WIB QC

Tester: lke
WIB_UDP_Version: 0x1a6
Input Capacitor(Cd): 0pF

FEMB Configuration

FE_CFG: 14mV/fC, 900mV BL, 2.0us, SE_OFF, 500pA, ASIC_CAL, ASICDAC=0x10
ADC_CFG: CMOS reference set to default, Auto Calibration,
ADC_CFG: SE, SDC off, offset_binary_format, Auto Calibration,
CD_FE_pulse: 500 samples/pulse, CD Addr0x06:0x30,0x07:0x00, 0x08:0x38, 0x09:0x80

FE SE OFF Interface Power Consumption (including cable dissipation) = 7.630W

Power rail	V_set /V	V_meas /V	I_meas /A	P_meas /W
LArASIC	3.000	2.950	0.440	1.299
ColdADC	3.500	3.426	1.617	5.541
COLDATA	3.000	2.989	0.217	0.649
BIAS	5.000	5.000	0.028	0.141

Monitoring path for FE-ADC#0 (unit: mV)

ASIC#	FE Vref	FE T	ADC VCMI	ADC VCMO	ADC VREFP	ADC VREFN
0	620.000	616.000	909.000	1196.000	1912.000	474.000
4	618.000	615.000	910.000	1195.000	1913.000	474.000