

# FEMB#2 Checkout Test Report

FEMB ID = 2

Date&Time: 2025-09-24 15:44:54

WIB\_TCP\_Version: 0x100

Temperature: RT

Note: for WIB QC

Tester: lke

WIB\_UDP\_Version: 0x1a6

Input Capacitor(Cd): 0pF

## FEMB Configuration

FE\_CFG: 14mV/fC, 900mV BL, 2.0us, SE\_OFF, 500pA, ASIC\_CAL, ASICDAC=0x10

ADC\_CFG: CMOS reference set to default, Auto Calibration,

ADC\_CFG: SE, SDC off, offset\_binary\_format, Auto Calibration,

CD\_FE\_pulse: 500 samples/pulse, CD Addr0x06:0x30,0x07:0x00, 0x08:0x38, 0x09:0x80

FE SE OFF Interface Power Consumption (including cable dissipation) = 7.551W

Power rail	V_set /V	V_meas /V	I_meas /A	P_meas /W
LArASIC	3.000	2.961	0.443	1.313
ColdADC	3.500	3.424	1.587	5.436
COLDATA	3.000	2.974	0.222	0.662
BIAS	5.000	5.000	0.028	0.141

Monitoring path for FE-ADC#0 (unit: mV)

ASIC#	FE Vref	FE T	ADC VCM1	ADC VCM0	ADC VREFP	ADC VREFN
0	562.000	572.000	912.000	1199.000	1914.000	475.000
4	569.000	574.000	911.000	1198.000	1914.000	476.000

