FEMB#1 Checkout Test Report

FEMB ID = 1
Date&Time: 2025-09-24 15:42:52
WIB_TCP_Version: 0x100
Temperature: RT
Note: for WIB QC

Tester: lke
WIB_UDP_Version: 0x1a6
Input Capacitor(Cd): 0pF

FEMB Configuration

FE_CFG: 14mV/fC, 900mV BL, 2.0us, SE_OFF, 500pA, ASIC_CAL, ASICDAC=0x10
ADC_CFG: CMOS reference set to default, Auto Calibration,
ADC_CFG: SE, SDC off, offset_binary_format, Auto Calibration,
CD_FE_pulse: 500 samples/pulse, CD Addr0x06:0x30,0x07:0x00, 0x08:0x38, 0x09:0x80

FE SE OFF Interface Power Consumption (including cable dissipation) = 7.575W

Power rail	V_set /V	V_meas /V	I_meas /A	P_meas /W
LArASIC	3.000	2.947	0.439	1.294
ColdADC	3.500	3.449	1.588	5.477
COLDATA	3.000	2.991	0.222	0.664
BIAS	5.000	5.000	0.028	0.141

Monitoring path for FE-ADC#0 (unit: mV)

ASIC#	FE Vref	FE T	ADC VCMI	ADC VCMO	ADC VREFP	ADC VREFN
0	673.000	672.000	905.000	1194.000	1908.000	472.000
4	673.000	671.000	906.000	1193.000	1908.000	471.000