

FEMB#0 Checkout Test Report

FEMB ID = 0

Date&Time: 2025-09-24 15:40:49

WIB_TCP_Version: 0x100

Temperature: RT

Note: for WIB QC

Tester: lke

WIB_UDP_Version: 0x1a6

Input Capacitor(Cd): 0pF

FEMB Configuration

FE_CFG: 14mV/fC, 900mV BL, 2.0us, SE_OFF, 500pA, ASIC_CAL, ASICDAC=0x10

ADC_CFG: CMOS reference set to default, Auto Calibration,

ADC_CFG: SE, SDC off, offset_binary_format, Auto Calibration,

CD_FE_pulse: 500 samples/pulse, CD Addr0x06:0x30,0x07:0x00, 0x08:0x38, 0x09:0x80

FE SE OFF Interface Power Consumption (including cable dissipation) = 7.656W

Power rail	V_set /V	V_meas /V	I_meas /A	P_meas /W
LArASIC	3.000	2.944	0.433	1.276
ColdADC	3.500	3.448	1.619	5.584
COLDATA	3.000	2.969	0.221	0.656
BIAS	5.000	4.999	0.028	0.140

Monitoring path for FE-ADC#0 (unit: mV)

ASIC#	FE Vref	FE T	ADC VCM1	ADC VCM0	ADC VREFP	ADC VREFN
0	700.000	705.000	912.000	1198.000	1914.000	475.000
4	698.000	694.000	910.000	1196.000	1912.000	474.000

