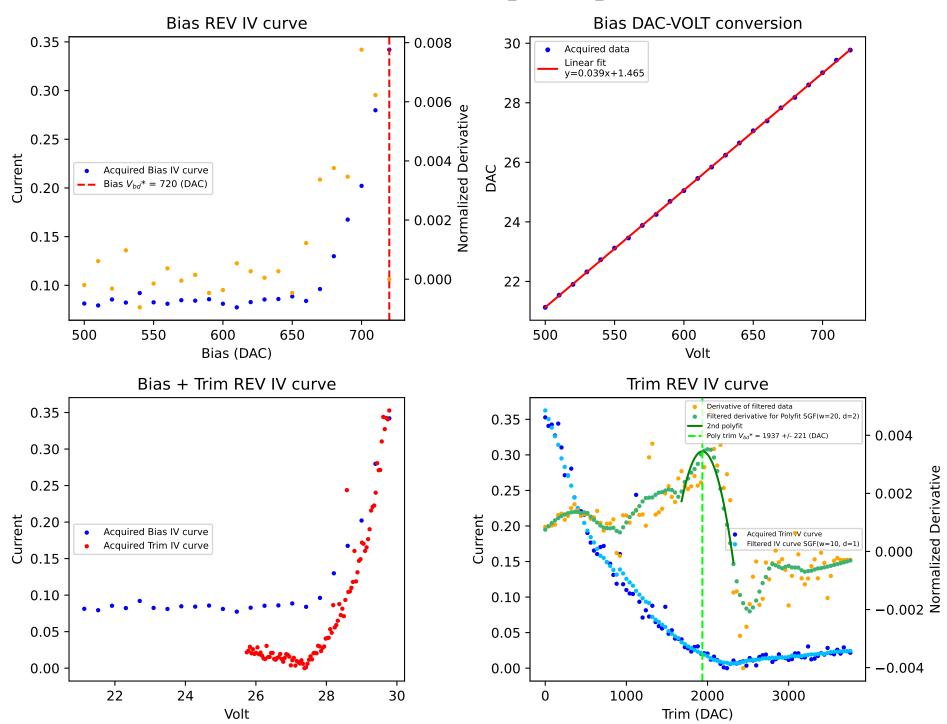
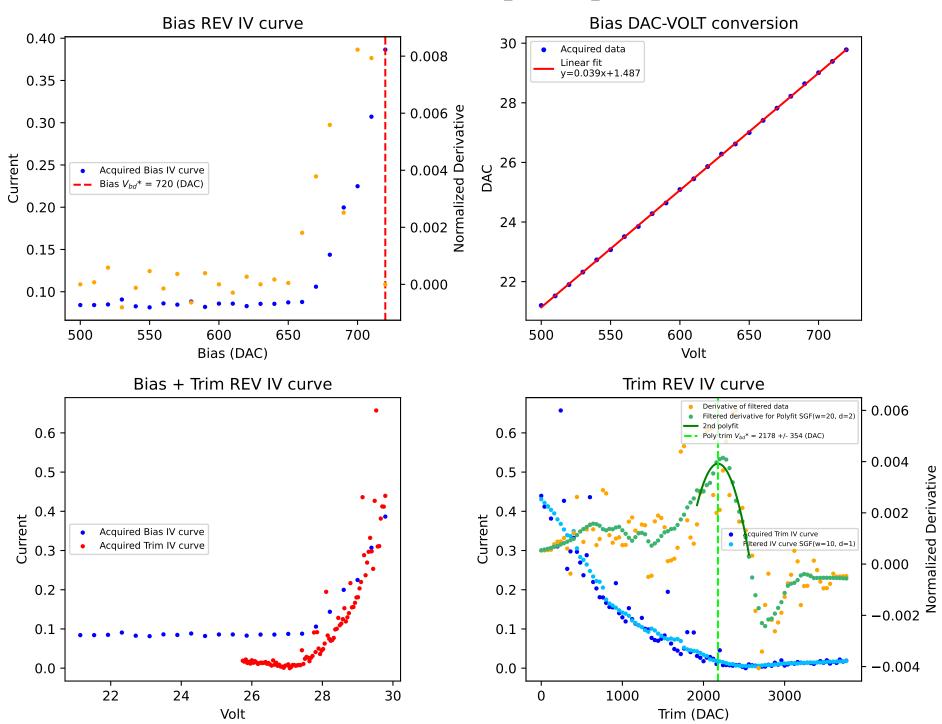


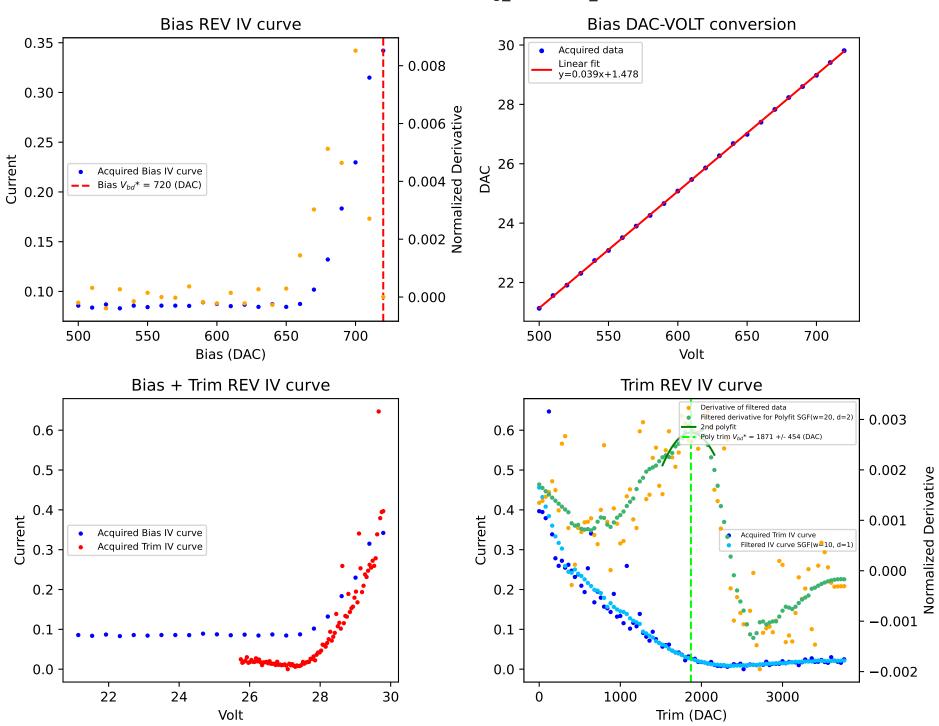
# ENDPOINT:104 APA:1 AFE:0 Config\_CH:1 DAQ\_CH:1 SiPM:FBK



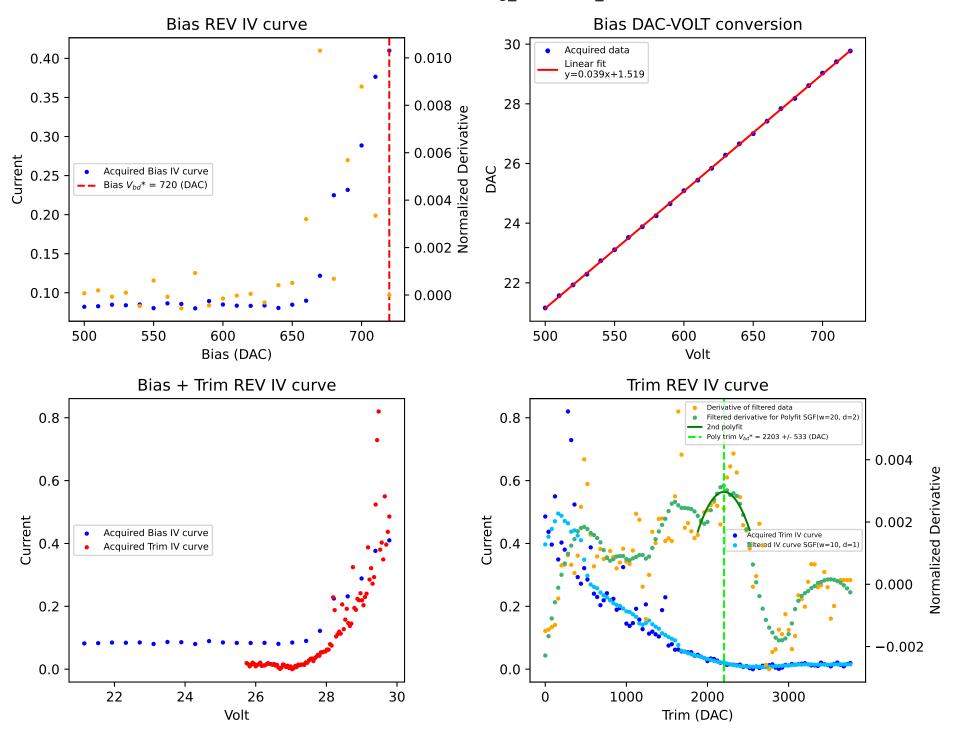
# ENDPOINT:104 APA:1 AFE:0 Config\_CH:2 DAQ\_CH:2 SiPM:FBK



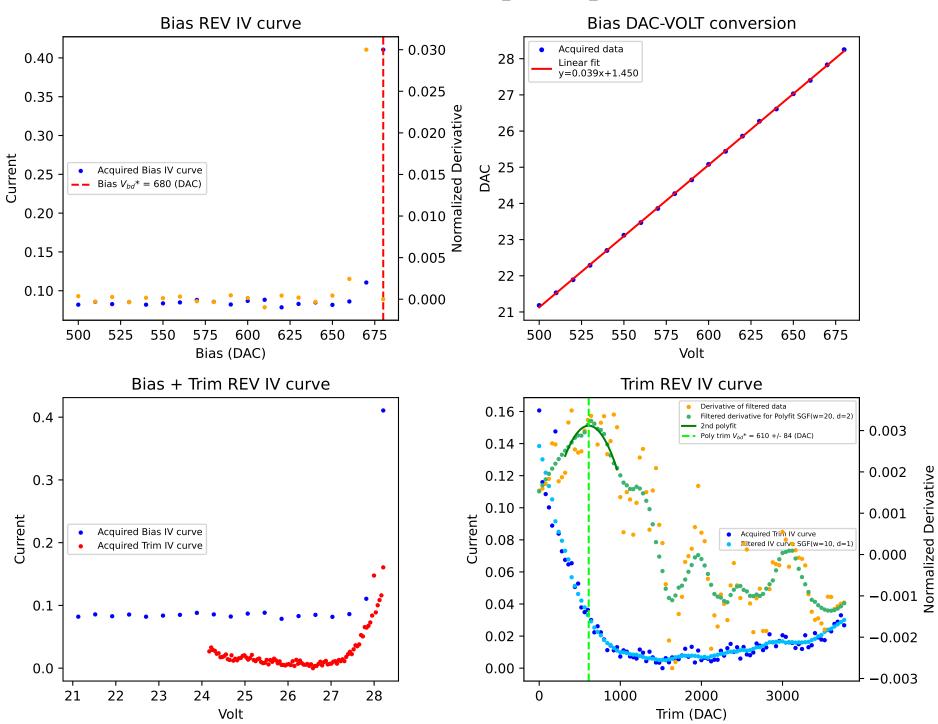
## ENDPOINT:104 APA:1 AFE:0 Config CH:3 DAQ CH:3 SiPM:FBK

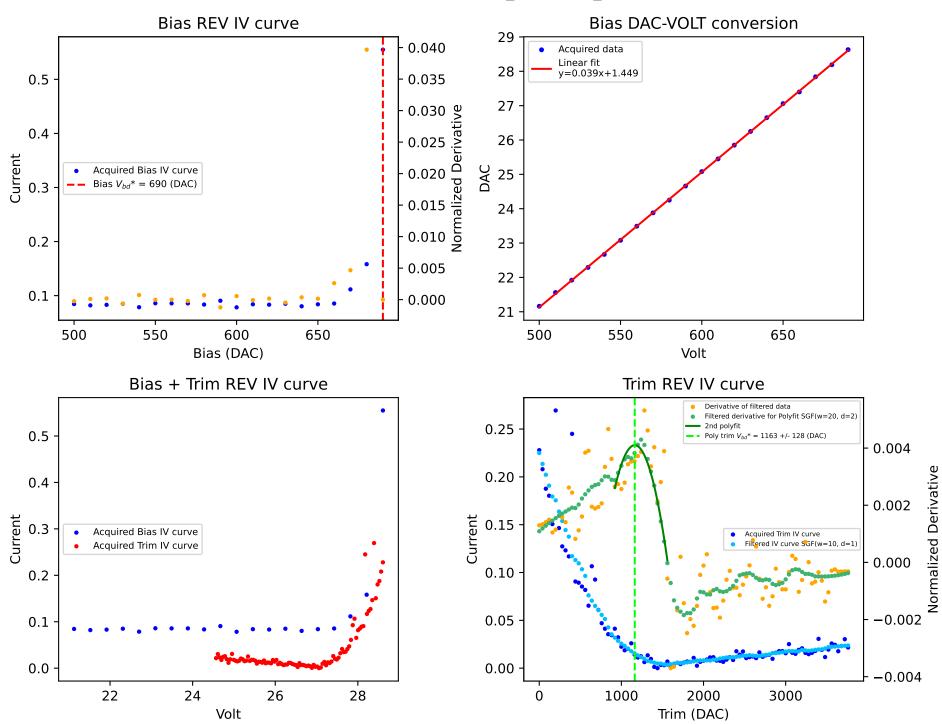


## ENDPOINT:104 APA:1 AFE:0 Config CH:4 DAQ CH:4 SiPM:FBK

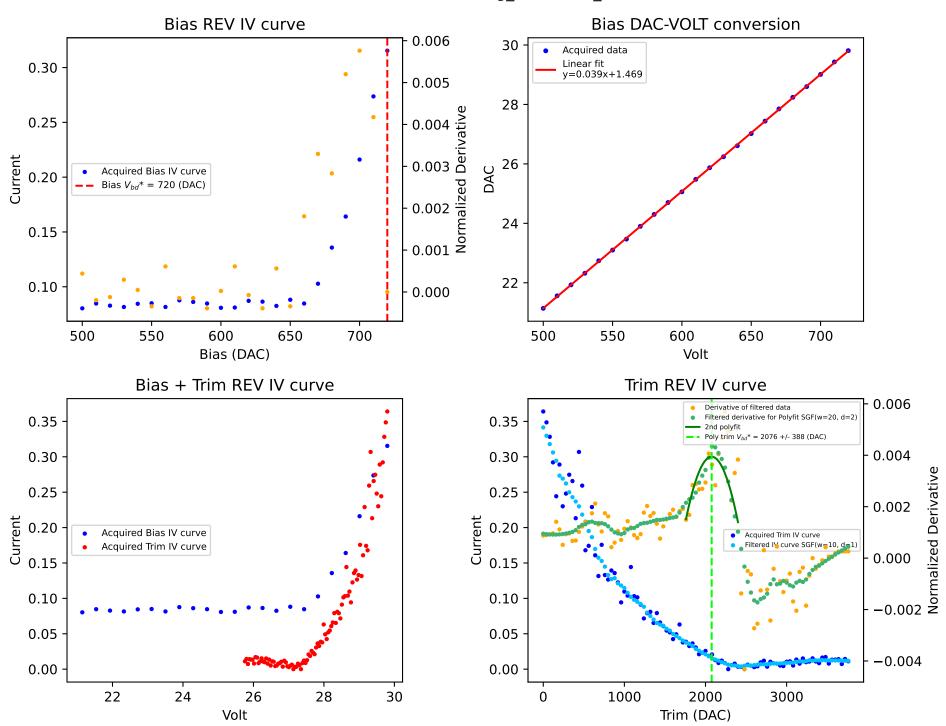


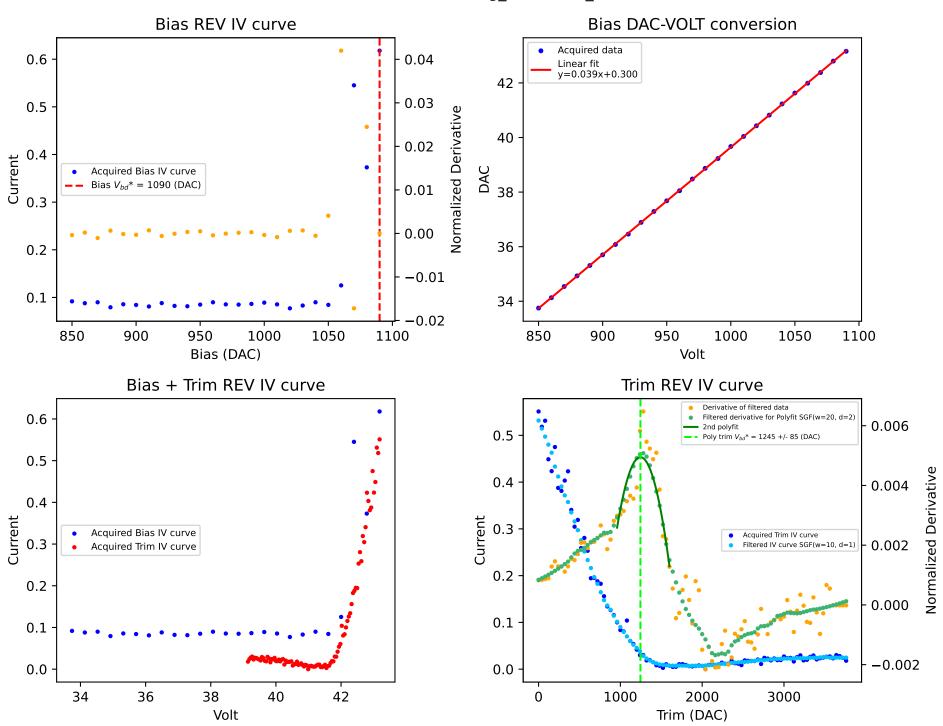
# ENDPOINT:104 APA:1 AFE:0 Config\_CH:5 DAQ\_CH:5 SiPM:FBK



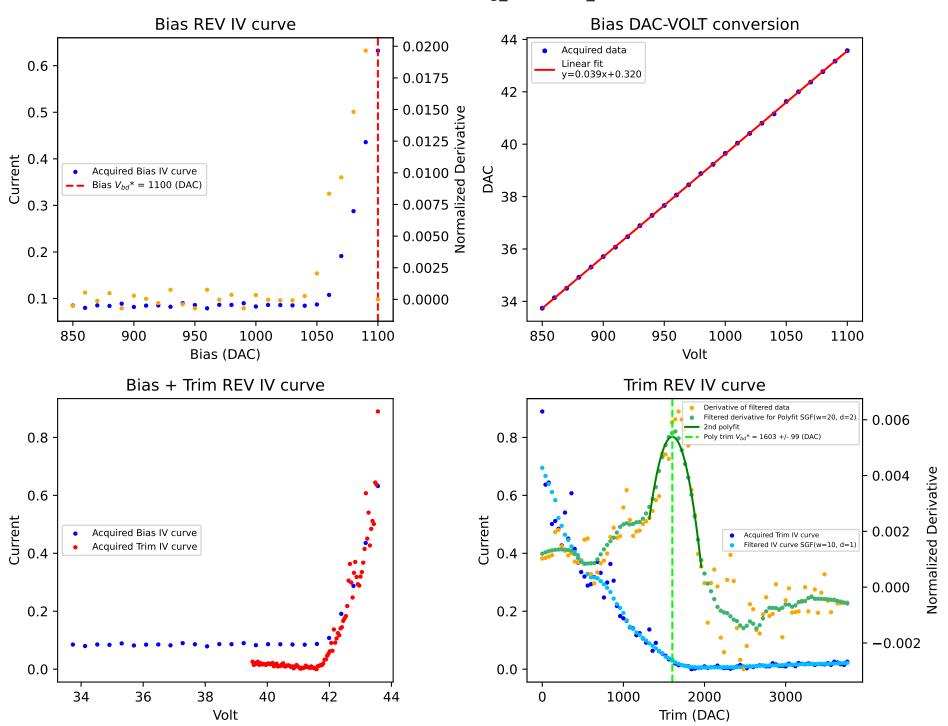


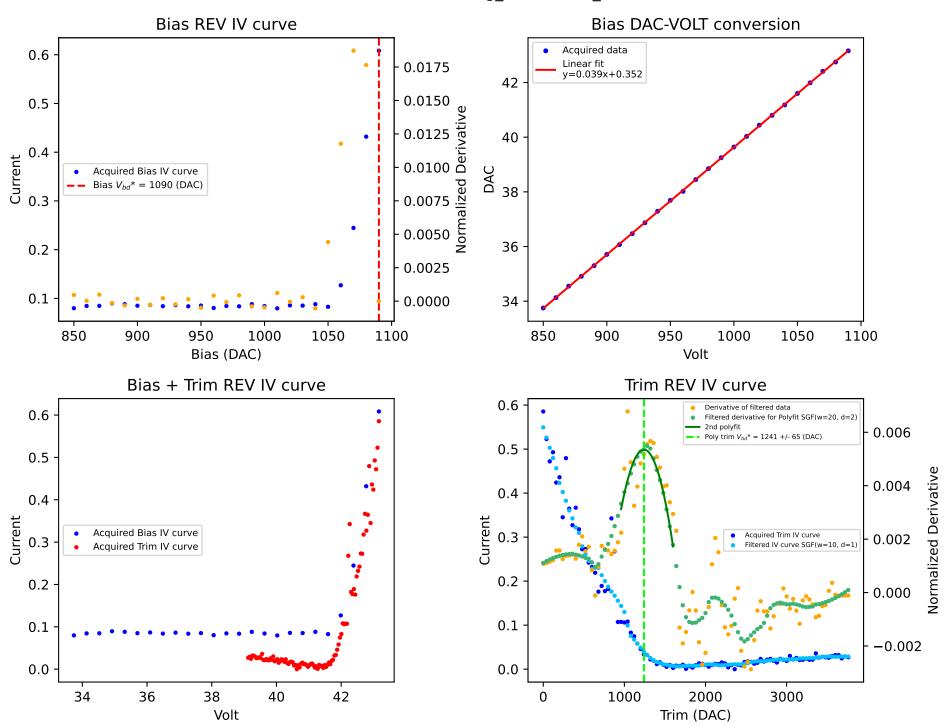
## ENDPOINT:104 APA:1 AFE:0 Config CH:7 DAQ CH:7 SiPM:FBK



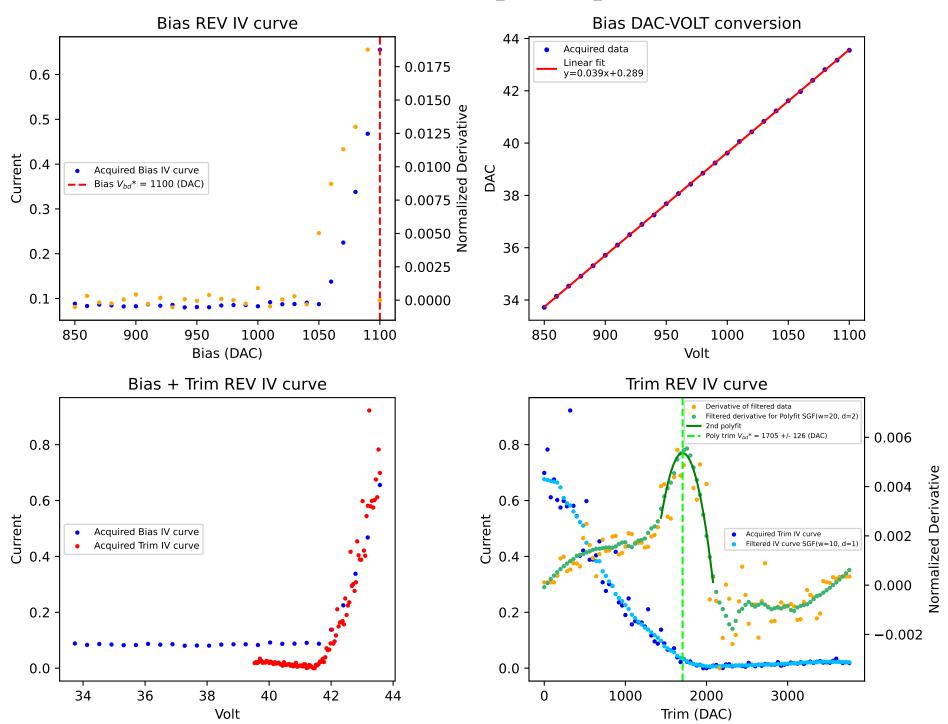


## ENDPOINT:104 APA:1 AFE:1 Config CH:9 DAQ CH:11 SiPM:HPK





# ENDPOINT:104 APA:1 AFE:1 Config\_CH:11 DAQ\_CH:13 SiPM:HPK



# ENDPOINT:104 APA:1 AFE:1 Config\_CH:12 DAQ\_CH:14 SiPM:HPK

