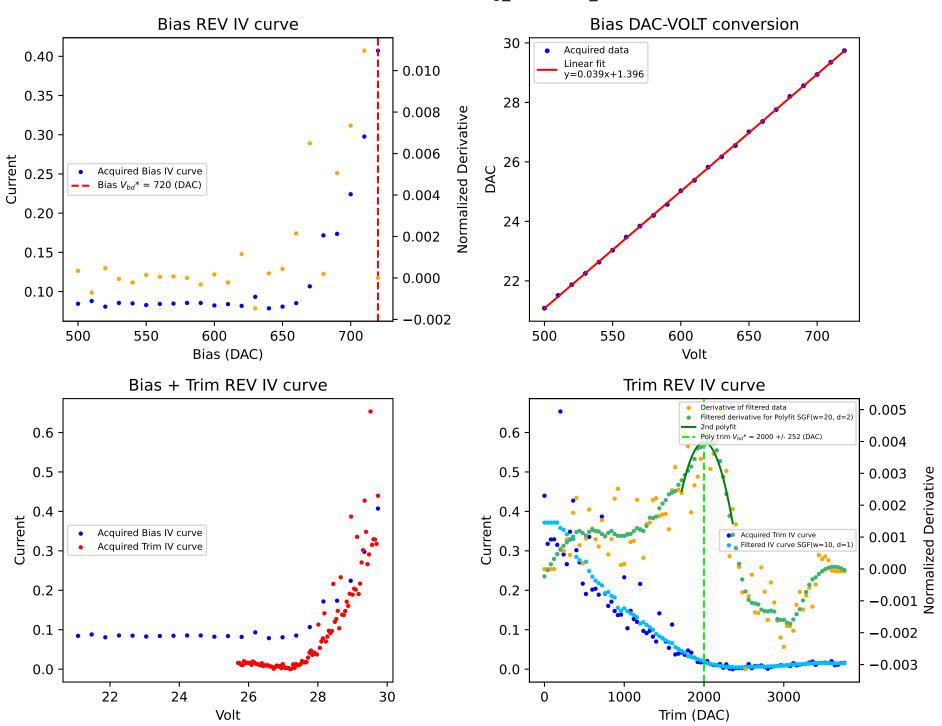
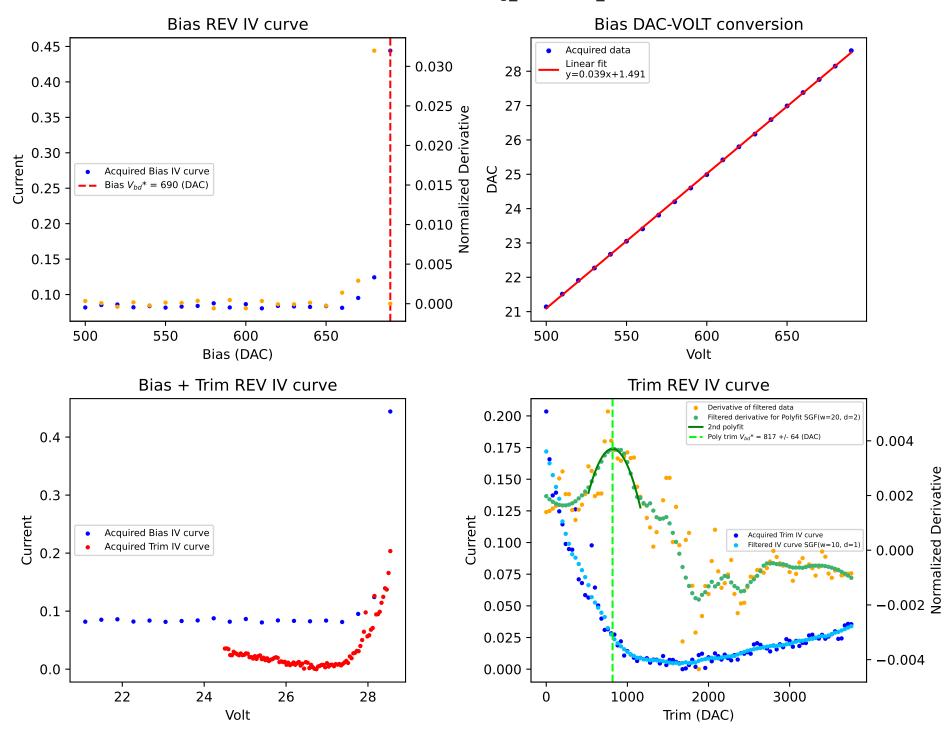
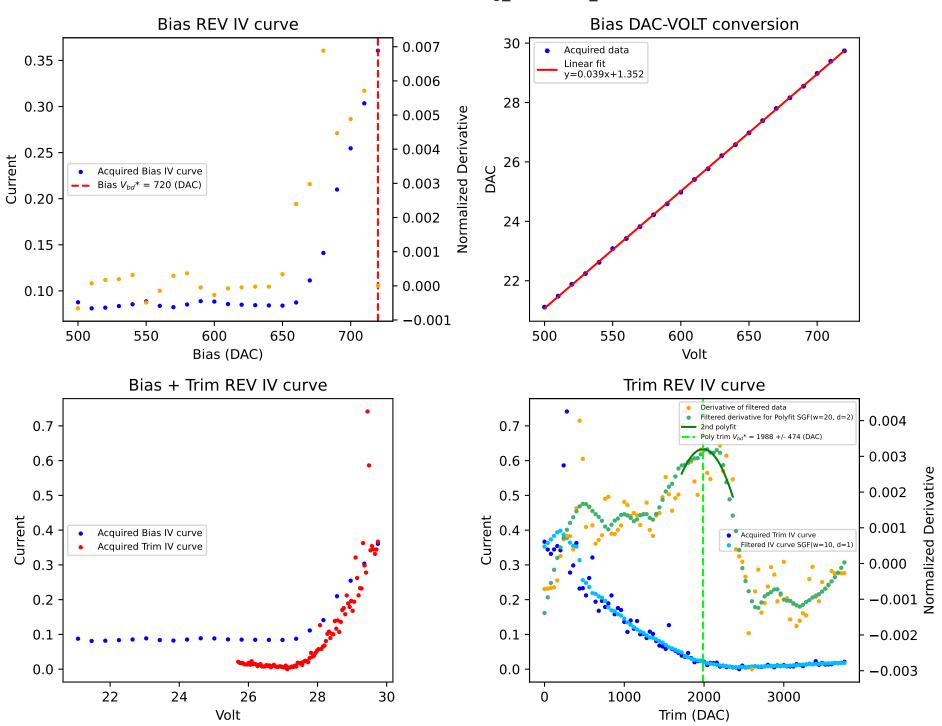
ENDPOINT:104 APA:1 AFE:0 Config CH:0 DAQ CH:0 SiPM:FBK

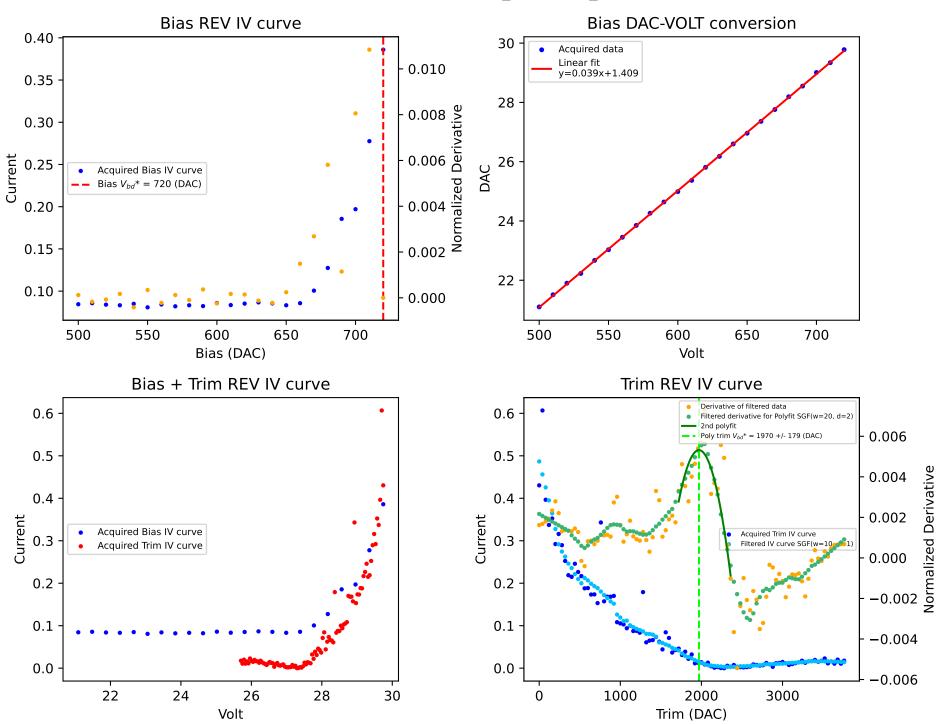




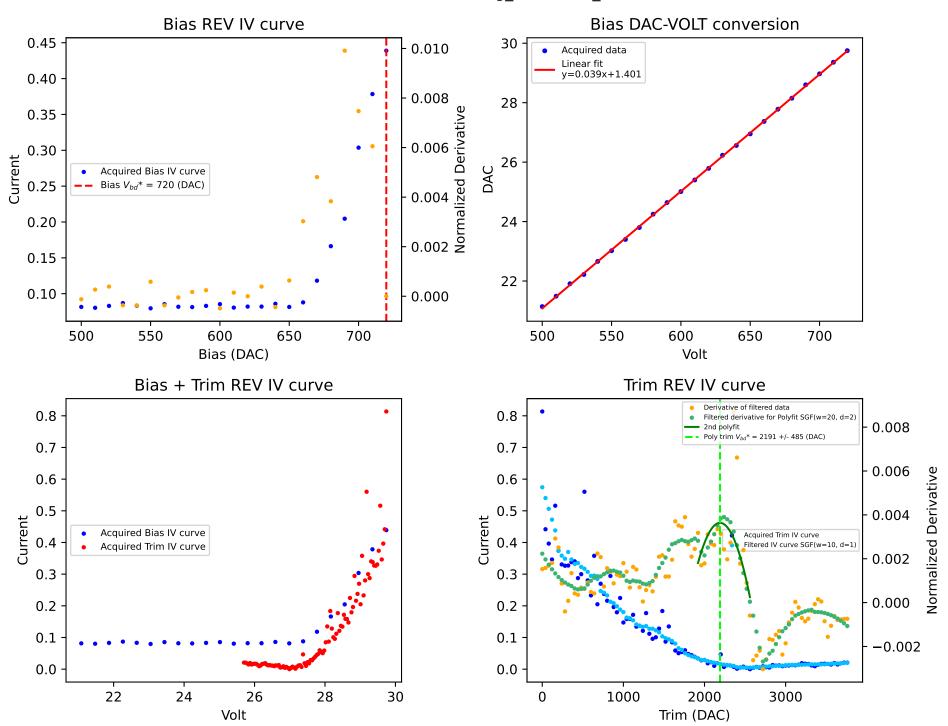
ENDPOINT:104 APA:1 AFE:0 Config CH:2 DAQ CH:2 SiPM:FBK



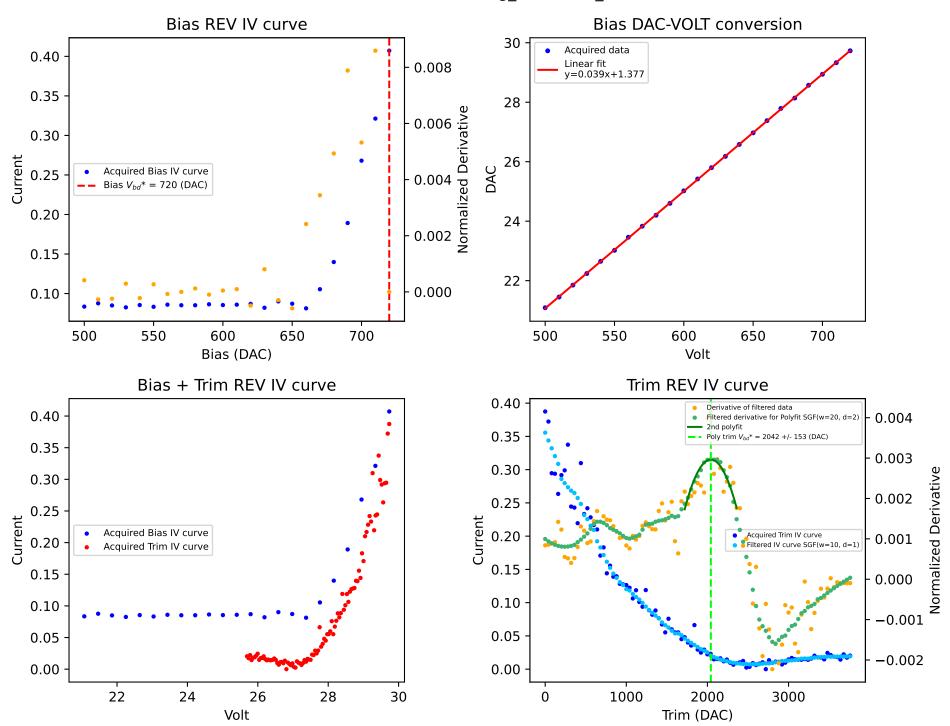
ENDPOINT:104 APA:1 AFE:0 Config_CH:3 DAQ_CH:3 SiPM:FBK



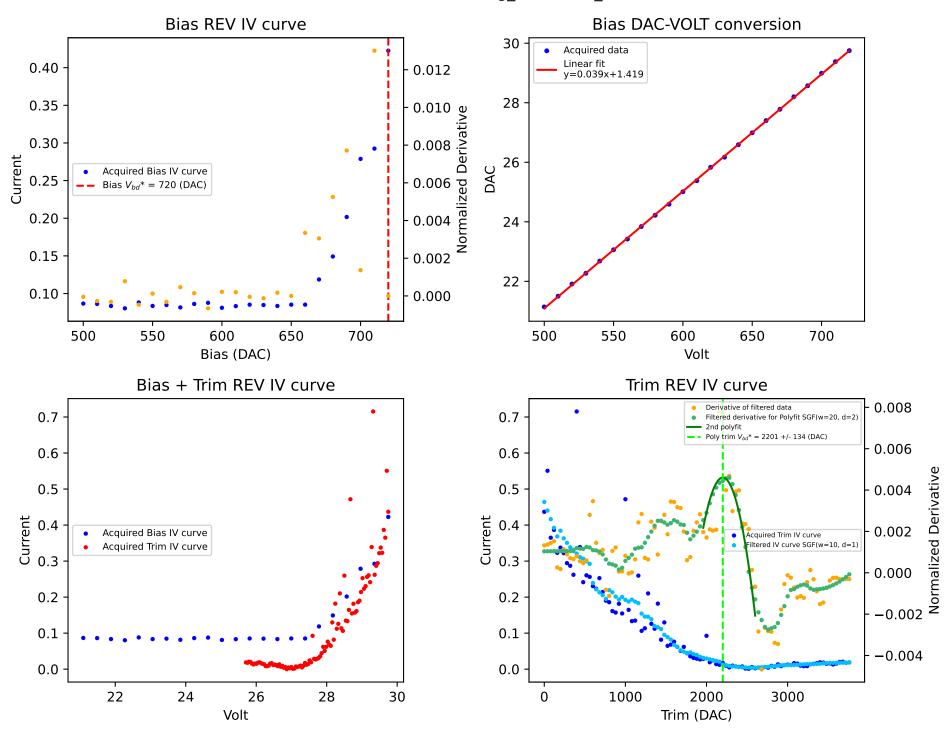
ENDPOINT:104 APA:1 AFE:0 Config CH:4 DAQ CH:4 SiPM:FBK



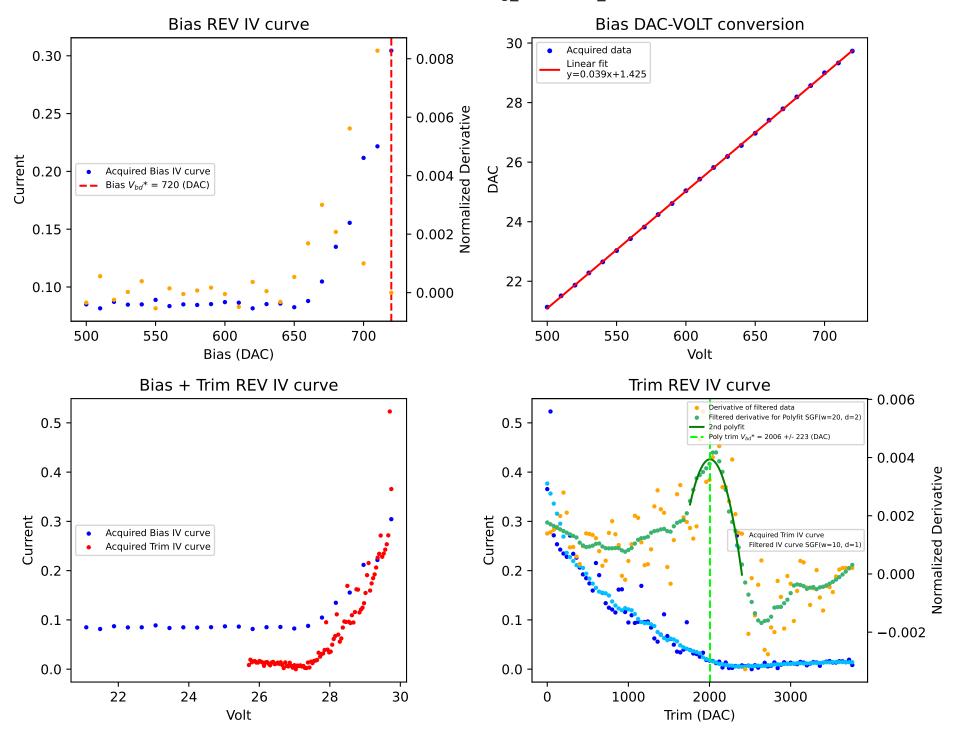
ENDPOINT:104 APA:1 AFE:0 Config CH:5 DAQ CH:5 SiPM:FBK

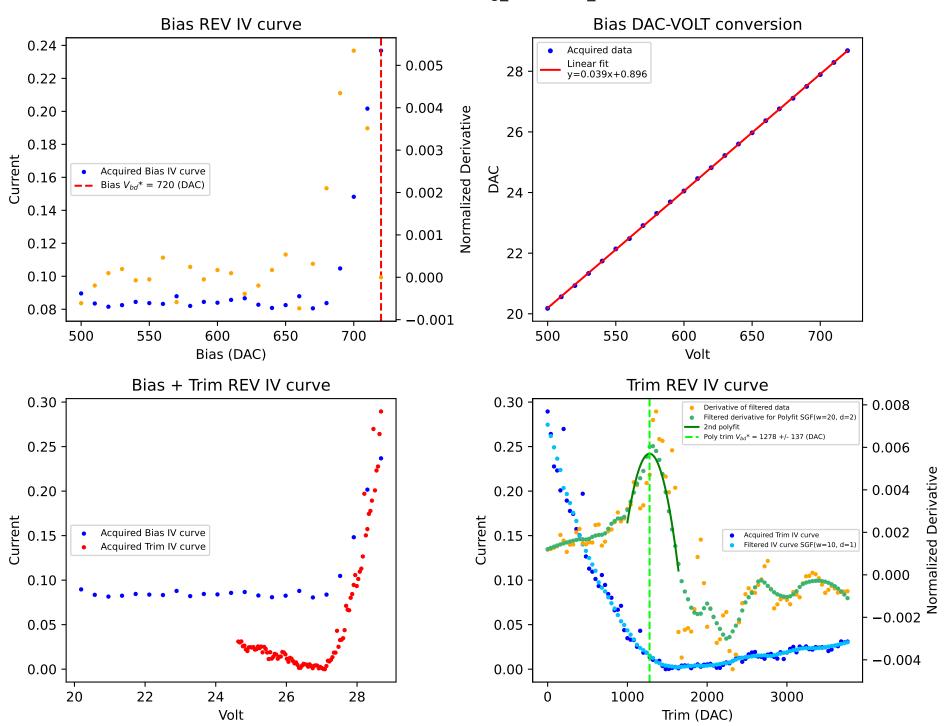


ENDPOINT:104 APA:1 AFE:0 Config CH:6 DAQ CH:6 SiPM:FBK

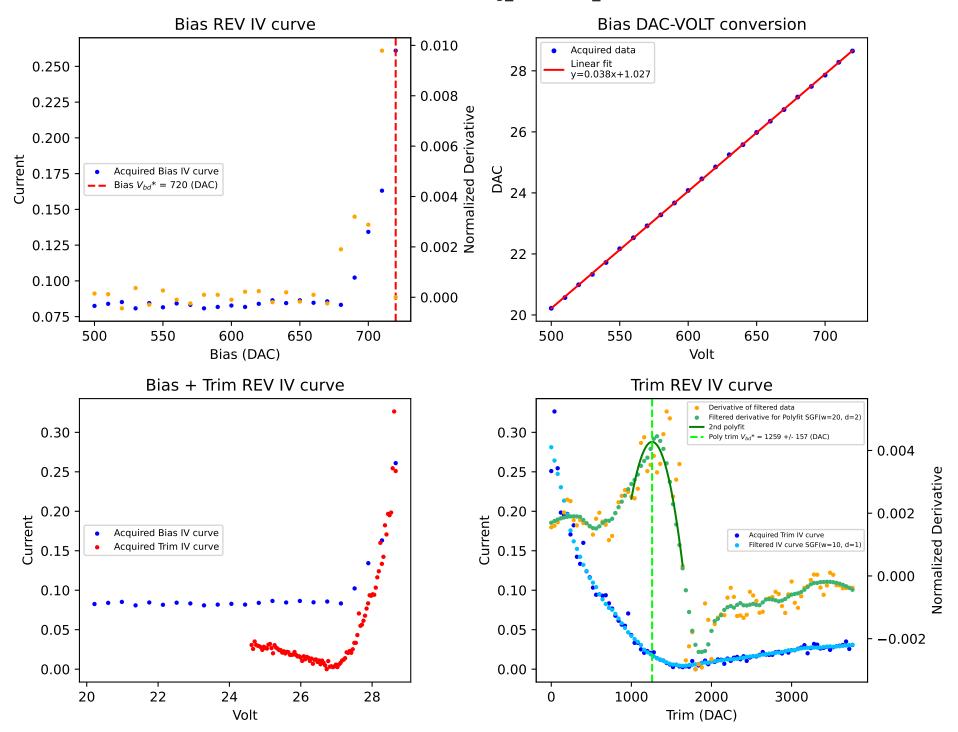


ENDPOINT:104 APA:1 AFE:0 Config CH:7 DAQ CH:7 SiPM:FBK

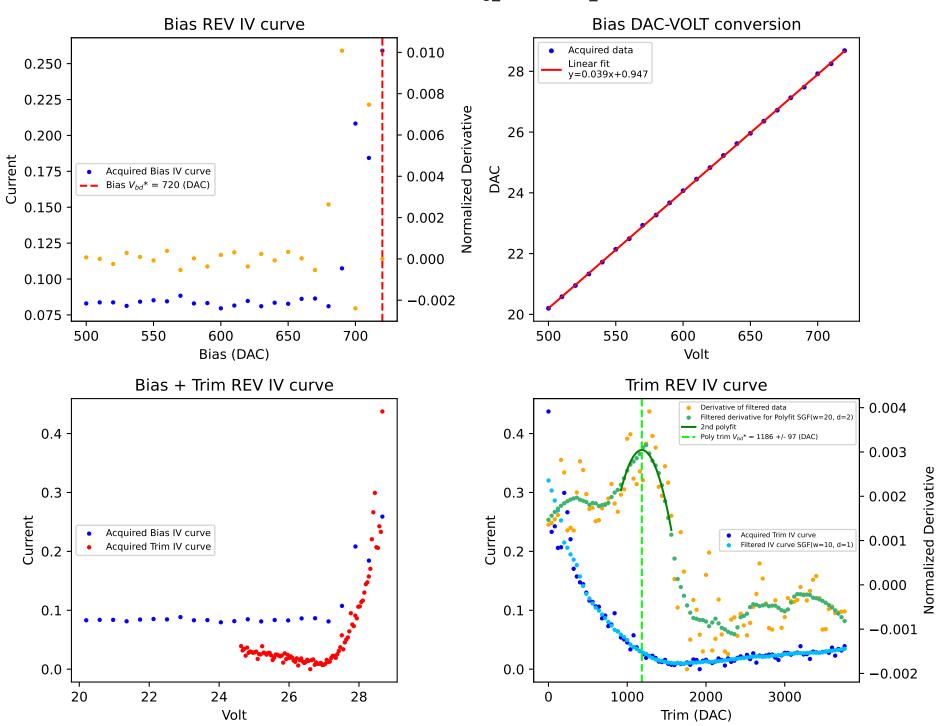




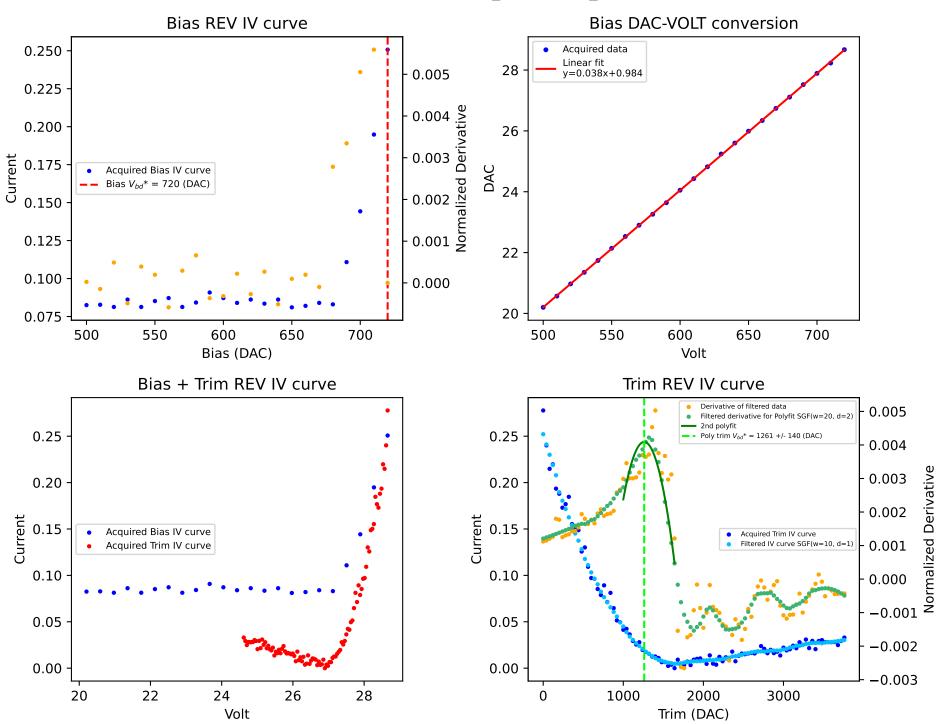
ENDPOINT:104 APA:1 AFE:1 Config CH:9 DAQ CH:11 SiPM:FBK



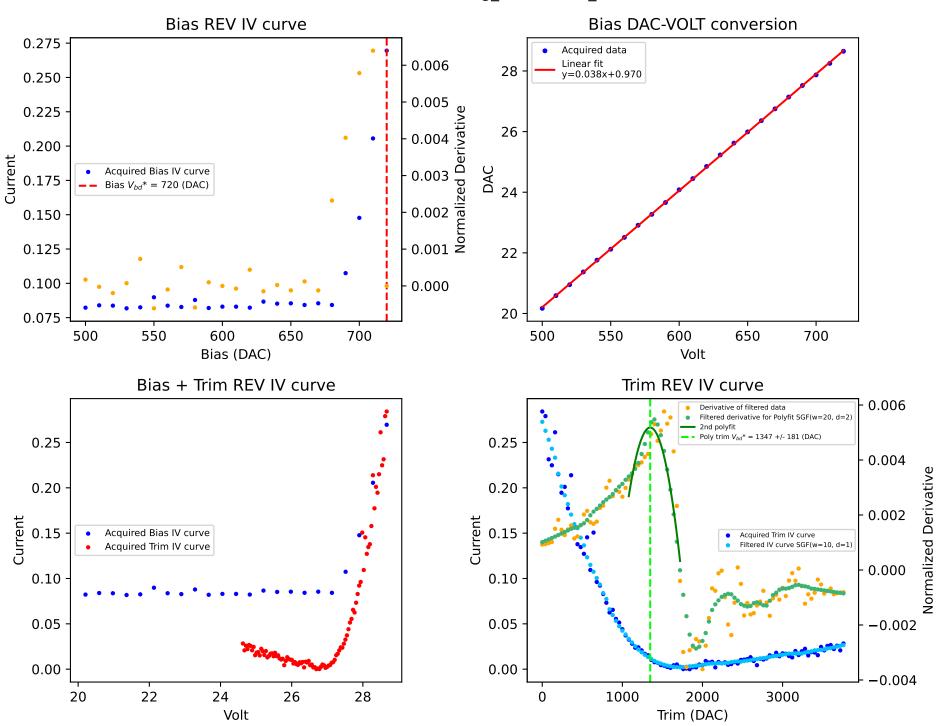
ENDPOINT:104 APA:1 AFE:1 Config CH:10 DAQ CH:12 SiPM:FBK



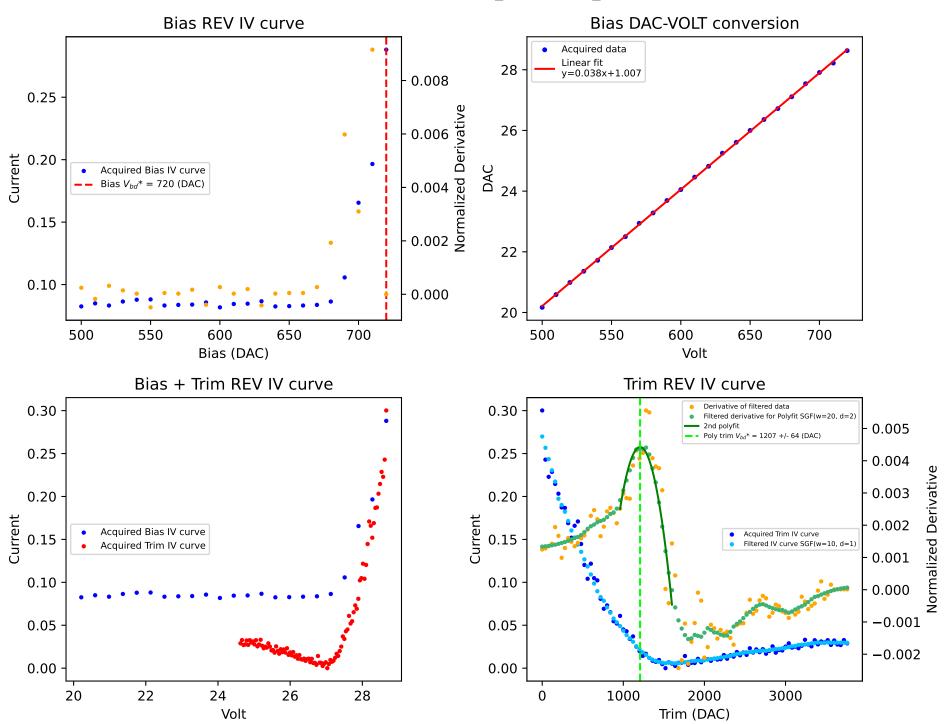
ENDPOINT:104 APA:1 AFE:1 Config_CH:11 DAQ_CH:13 SiPM:FBK



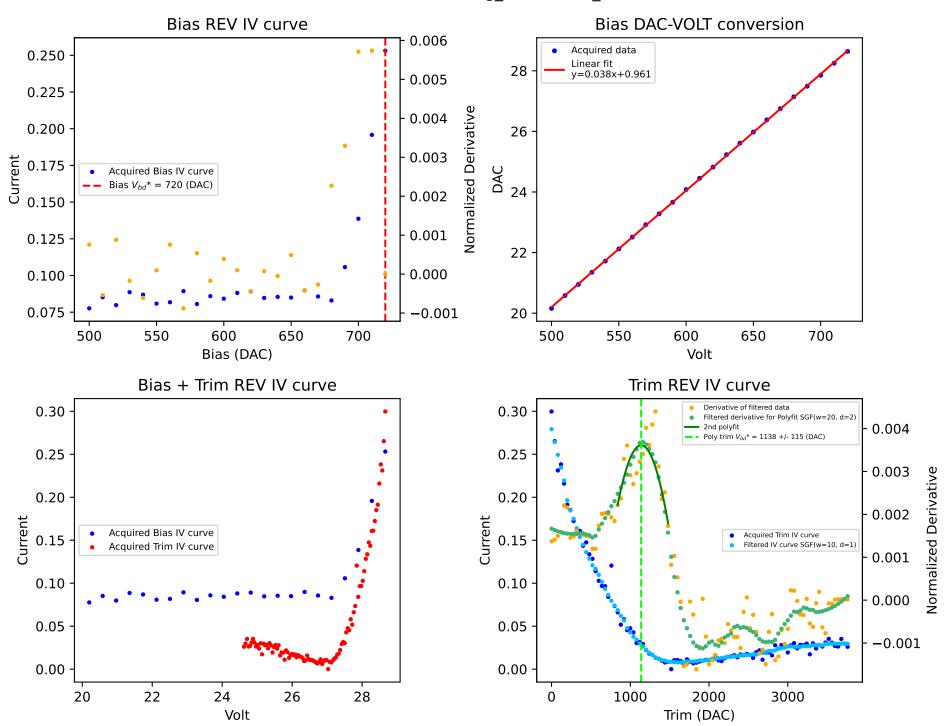
ENDPOINT:104 APA:1 AFE:1 Config CH:12 DAQ CH:14 SiPM:FBK



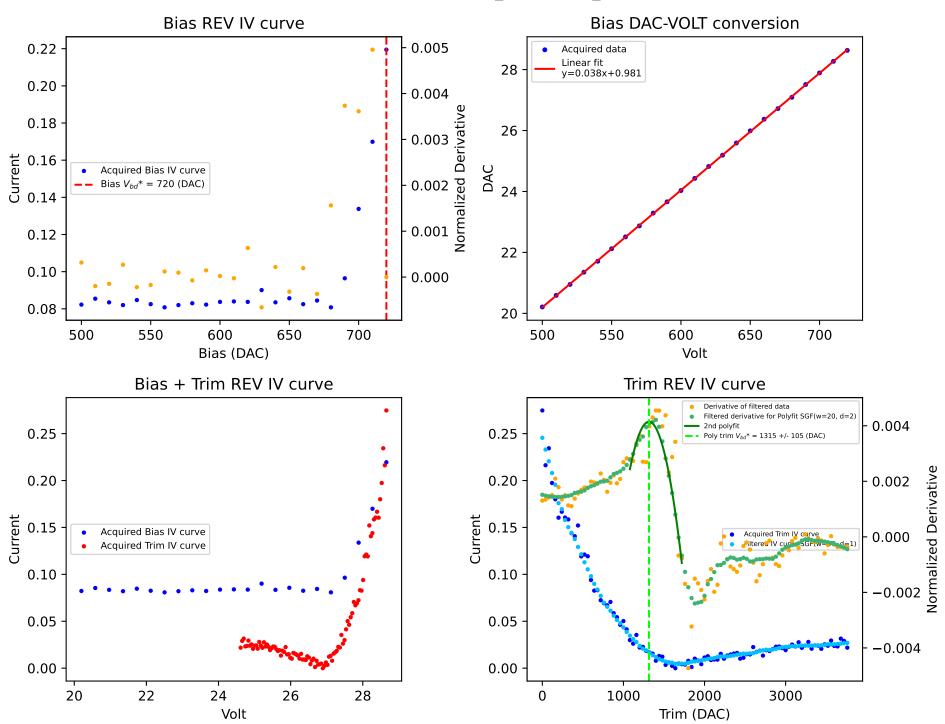
ENDPOINT:104 APA:1 AFE:1 Config_CH:13 DAQ_CH:15 SiPM:FBK

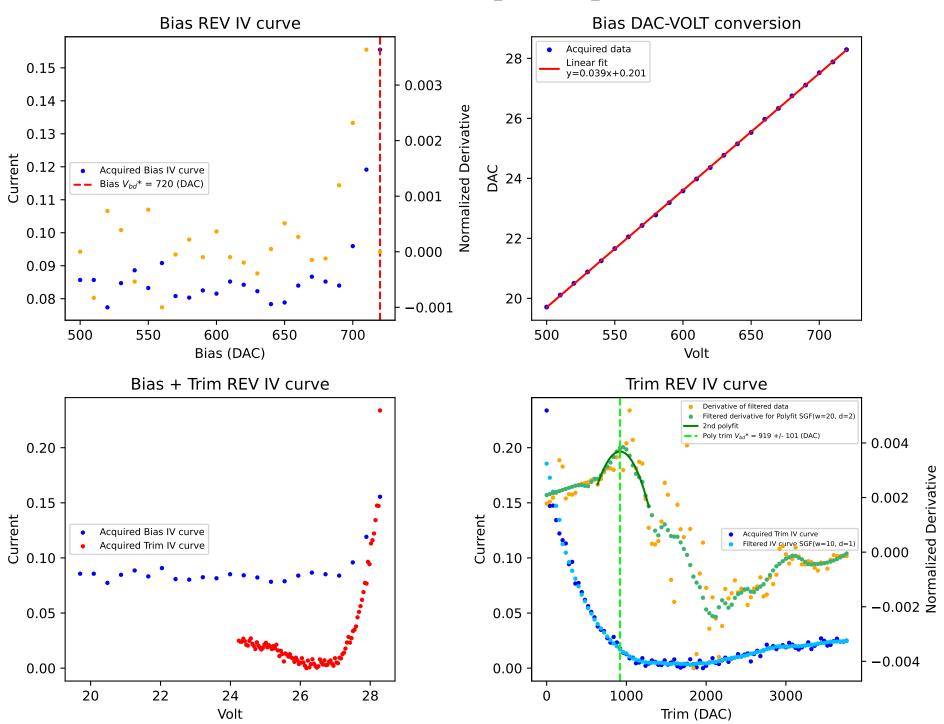


ENDPOINT:104 APA:1 AFE:1 Config CH:14 DAQ CH:16 SiPM:FBK

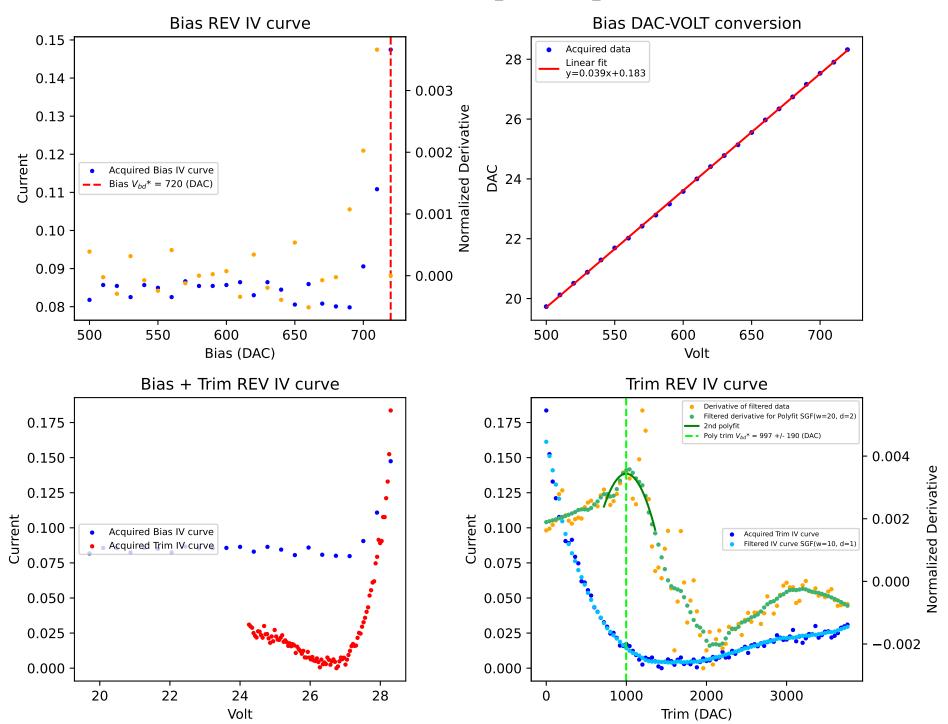


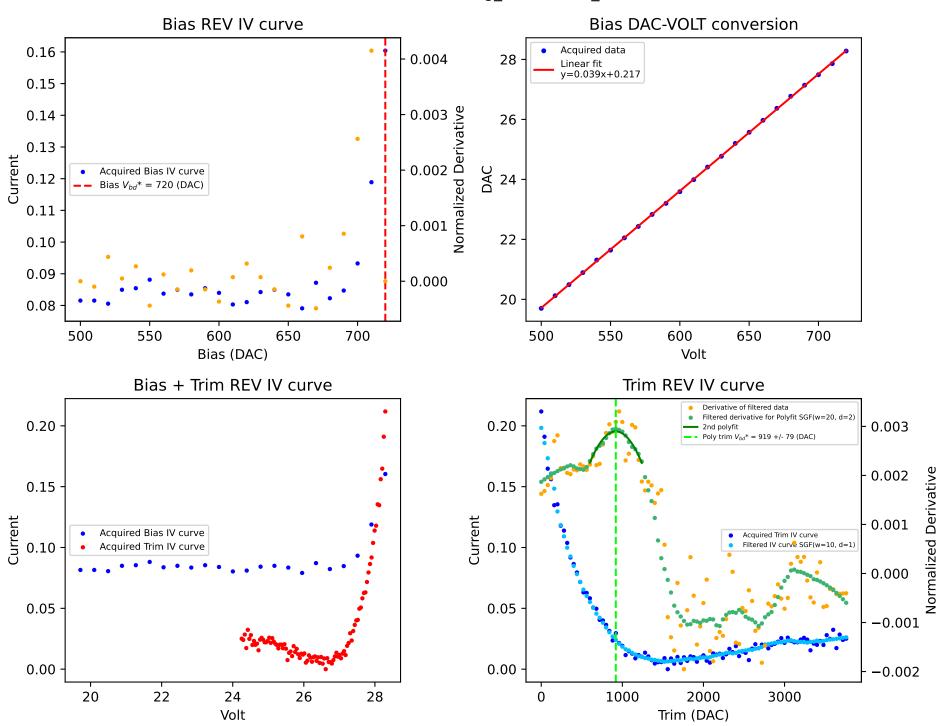
ENDPOINT:104 APA:1 AFE:1 Config_CH:15 DAQ_CH:17 SiPM:FBK



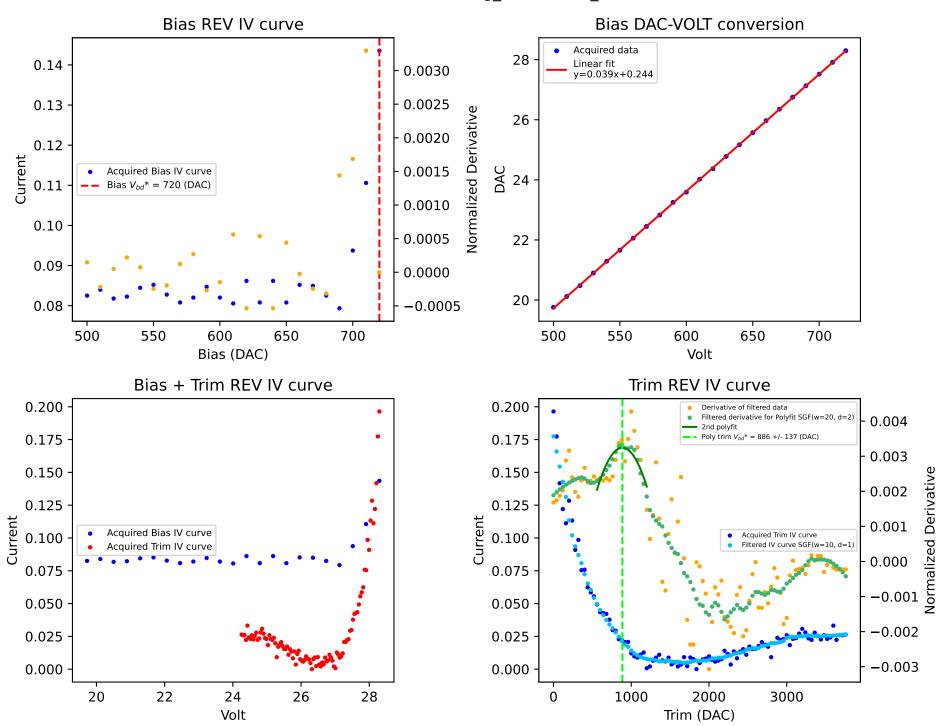


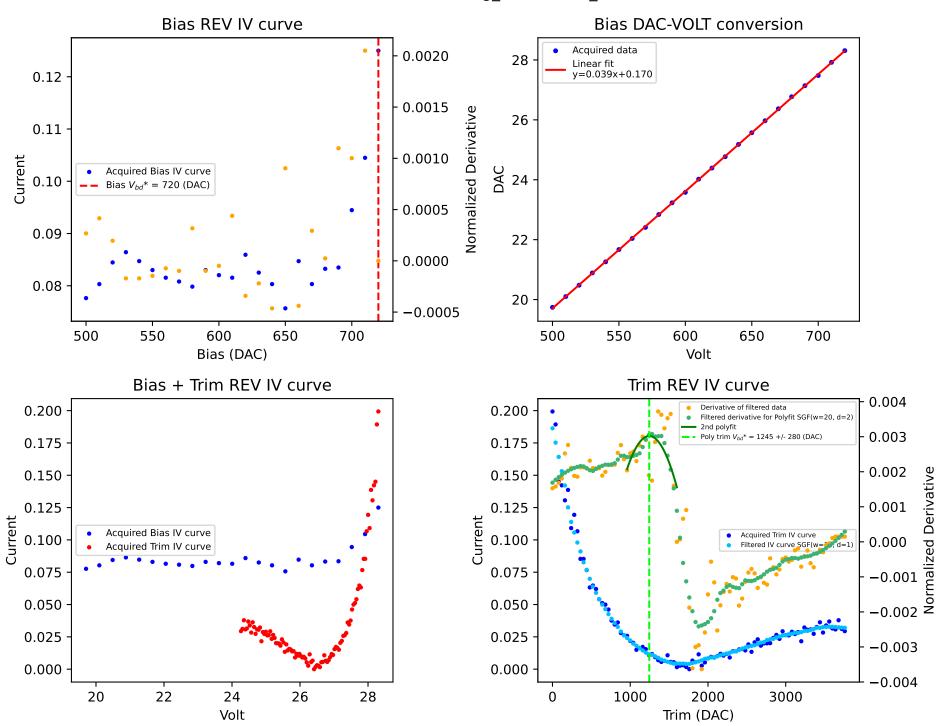
ENDPOINT:104 APA:1 AFE:2 Config_CH:17 DAQ_CH:21 SiPM:FBK

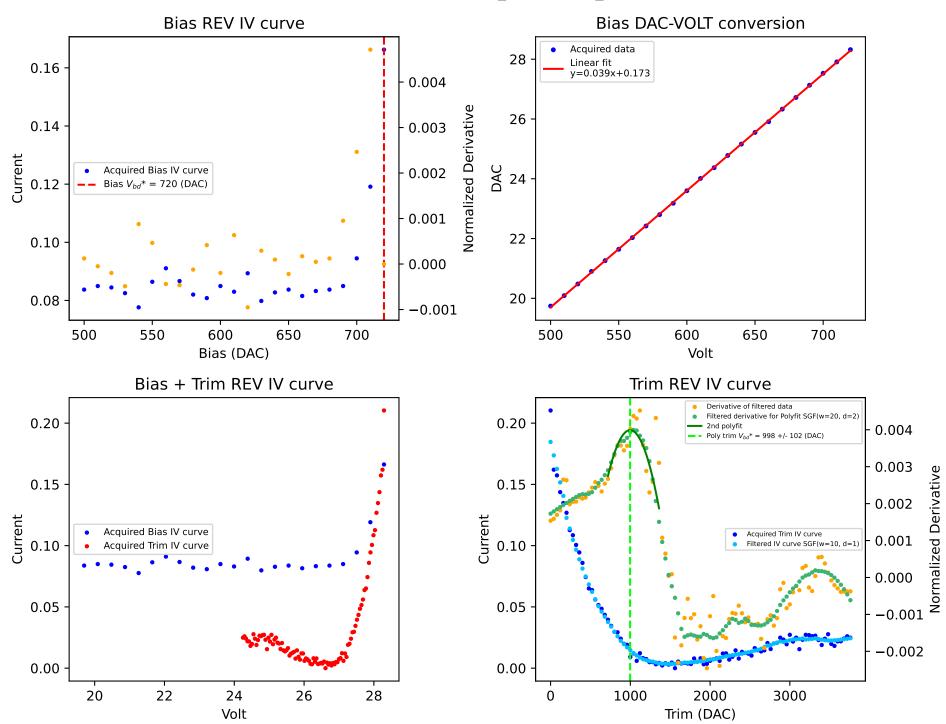


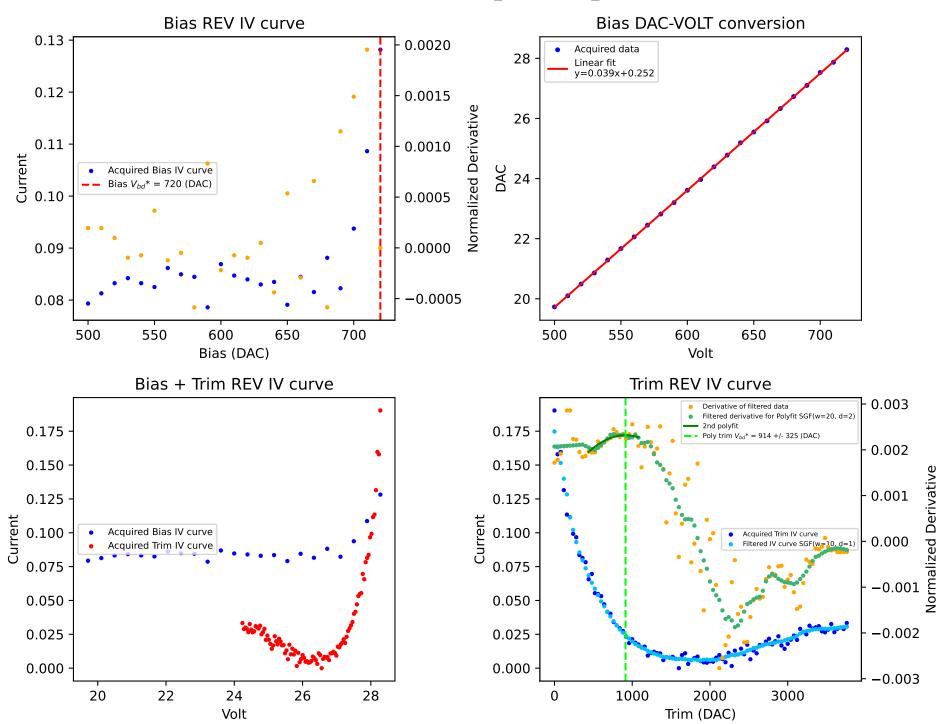


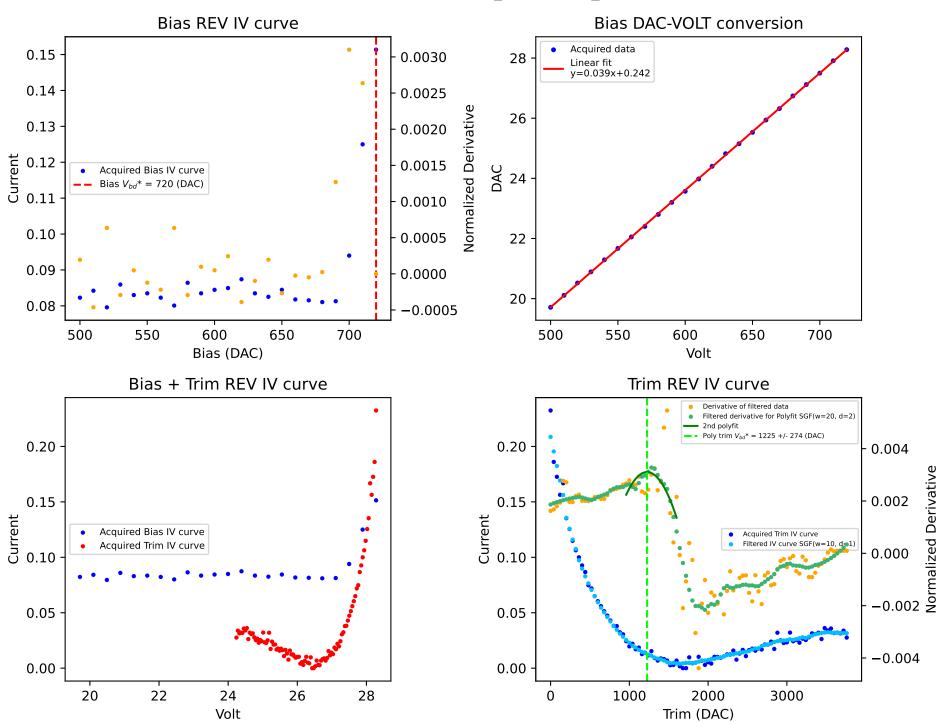
ENDPOINT:104 APA:1 AFE:2 Config CH:19 DAQ CH:23 SiPM:FBK

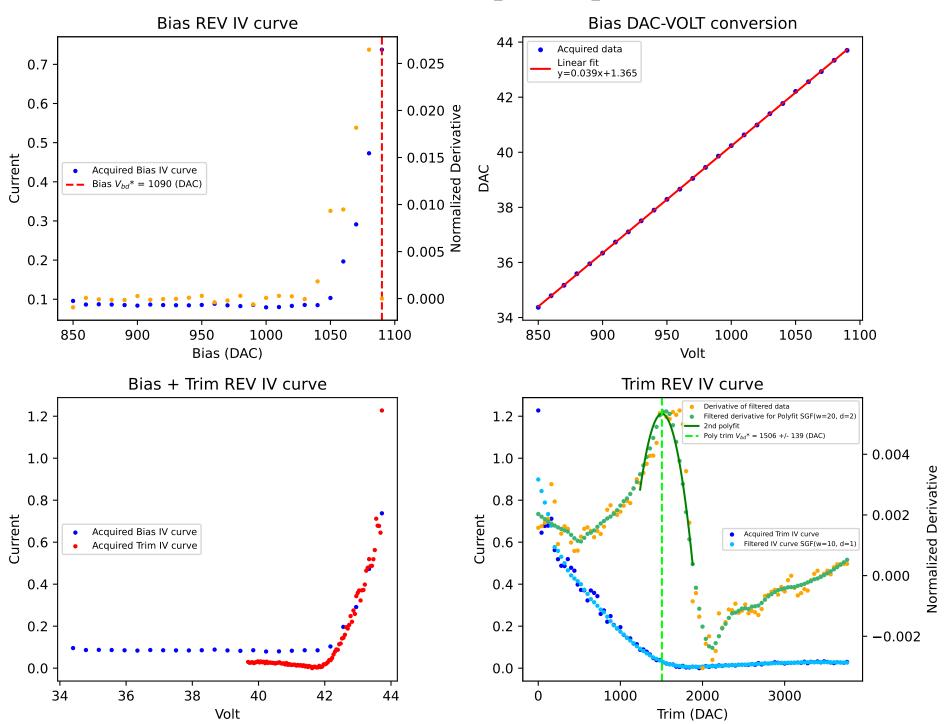




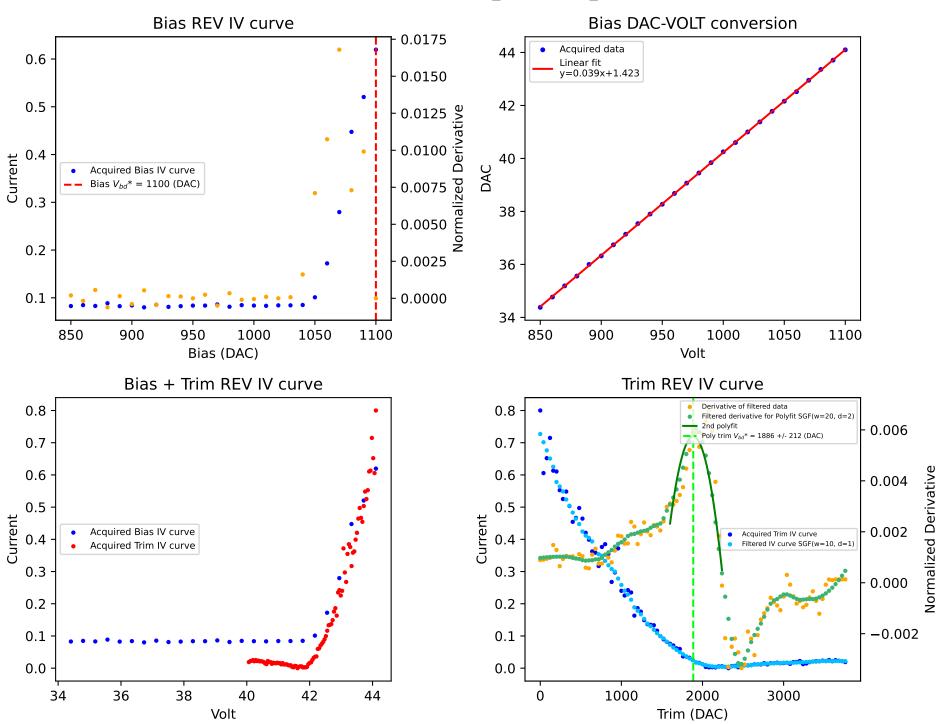


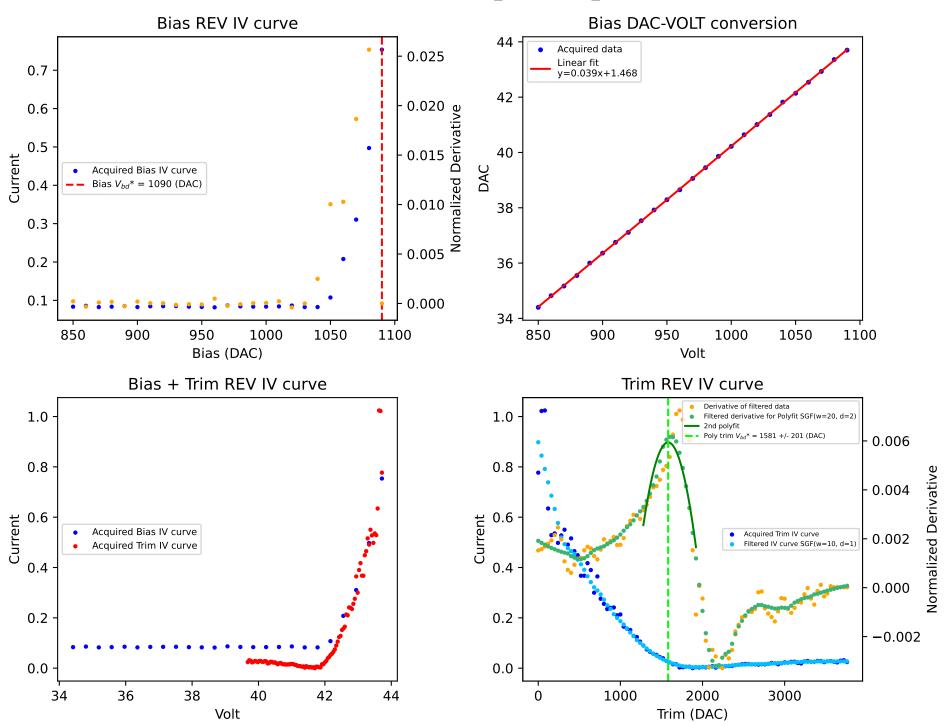


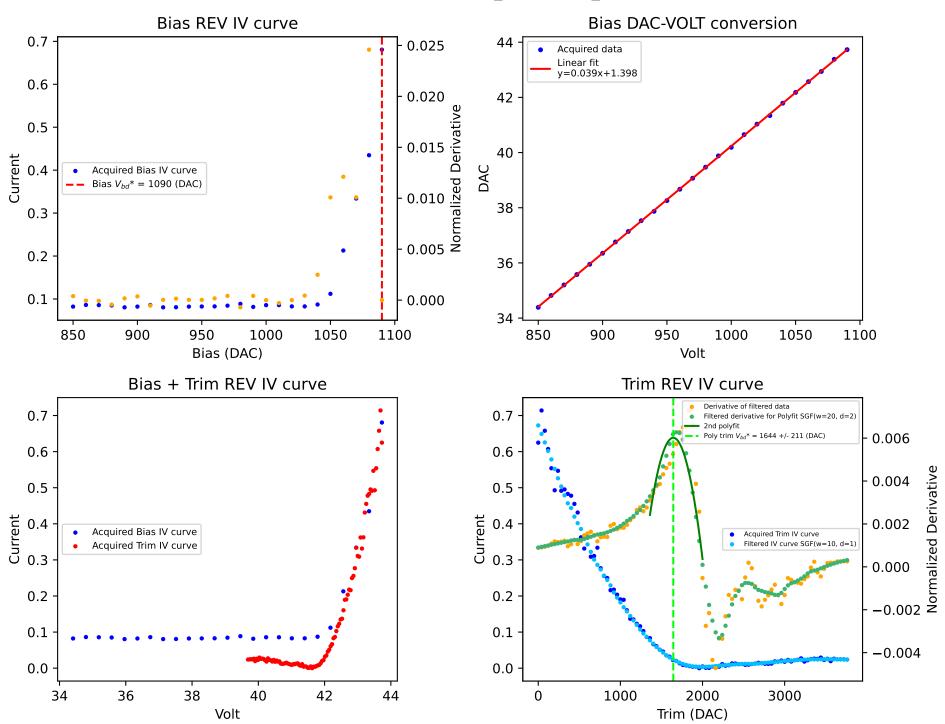


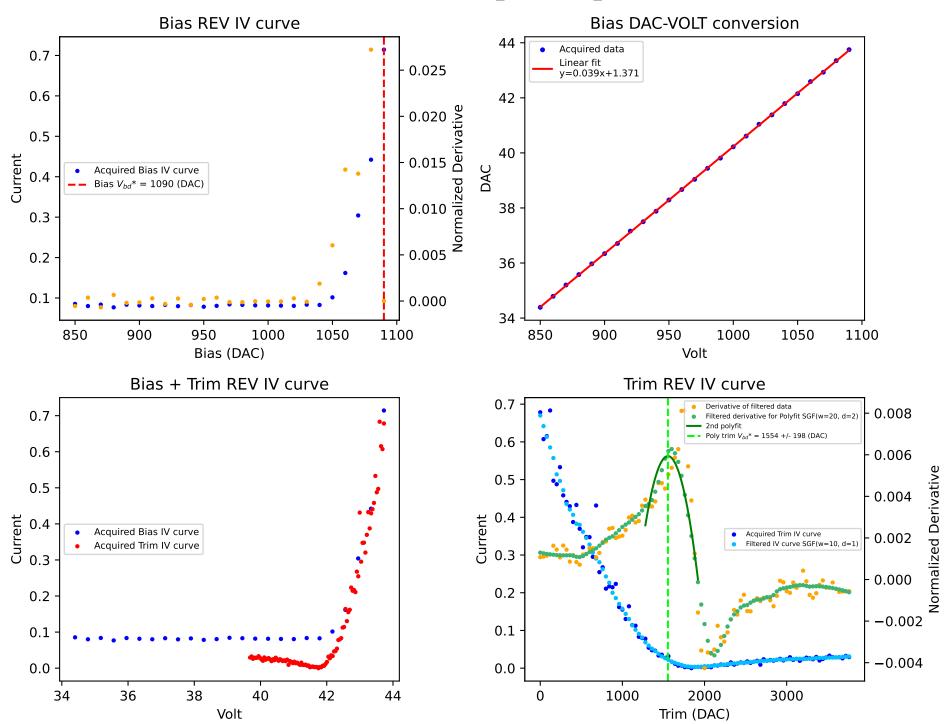


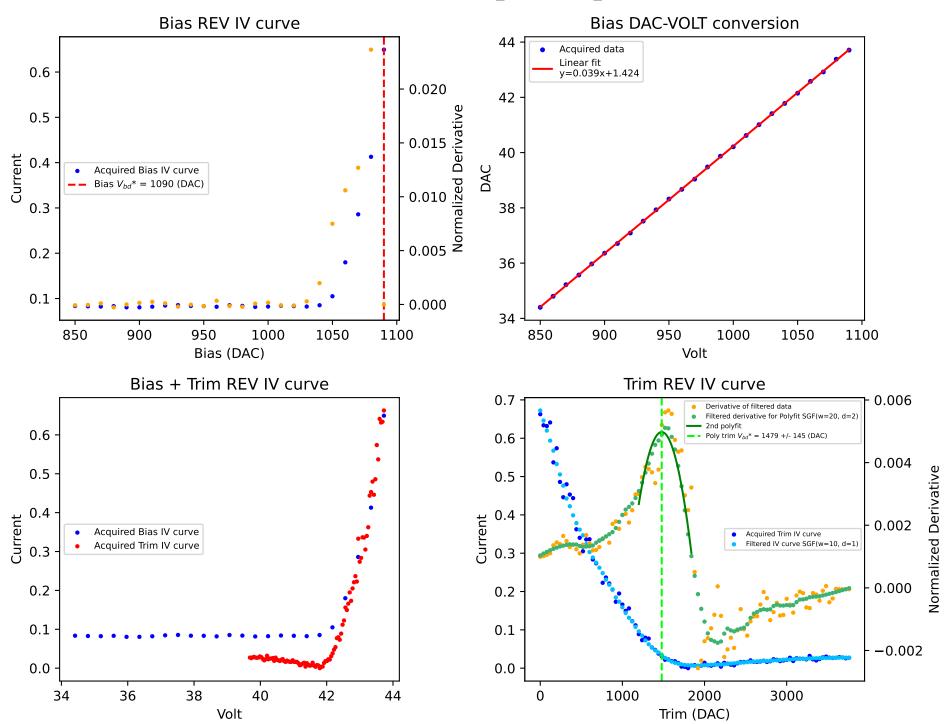
ENDPOINT:104 APA:1 AFE:3 Config_CH:25 DAQ_CH:31 SiPM:HPK

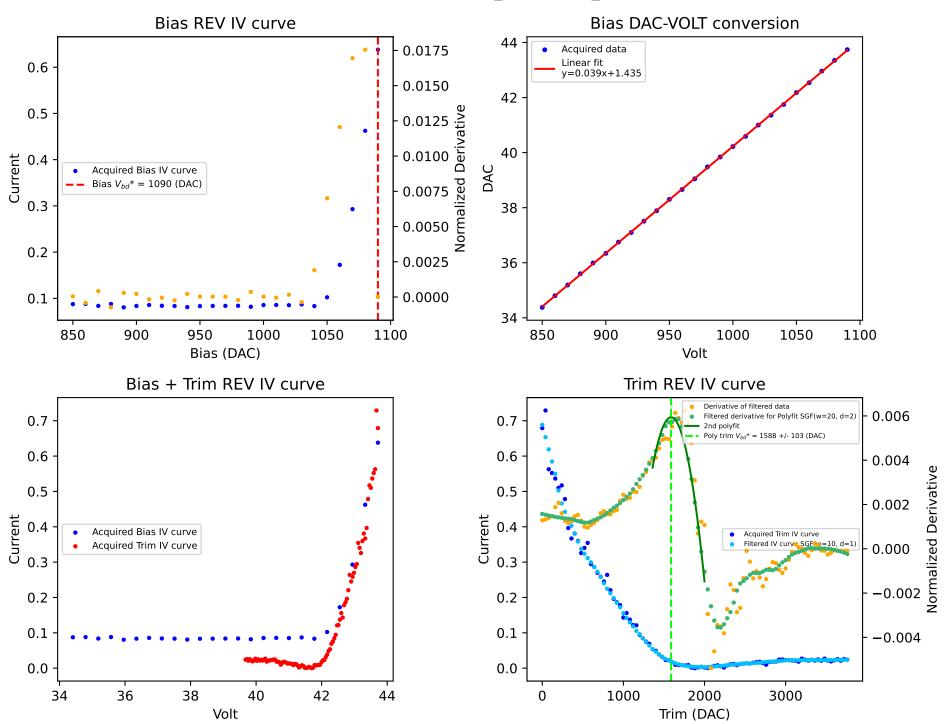


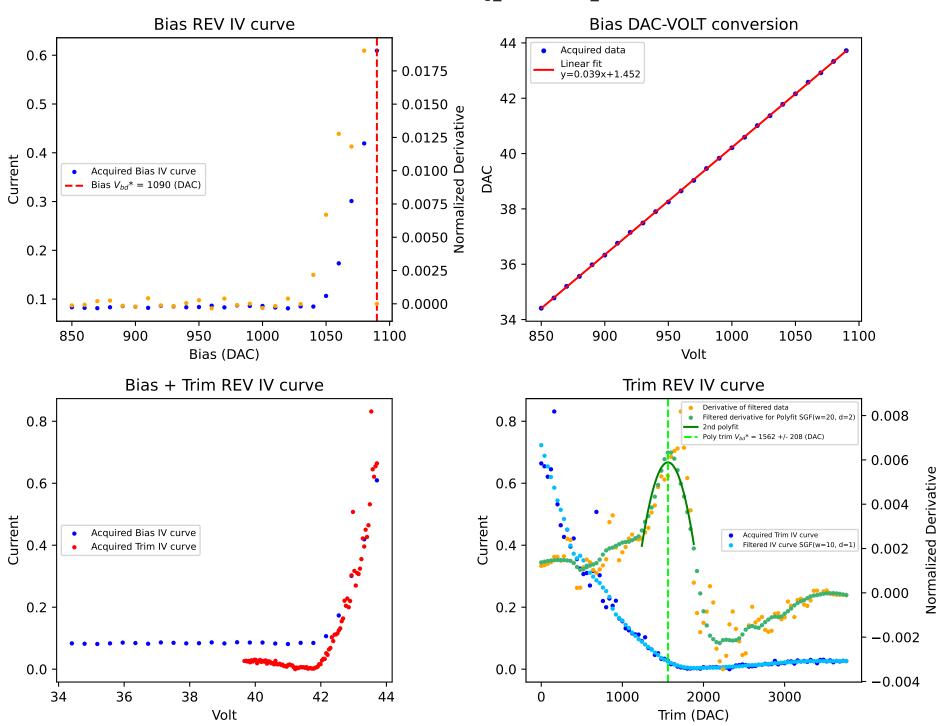


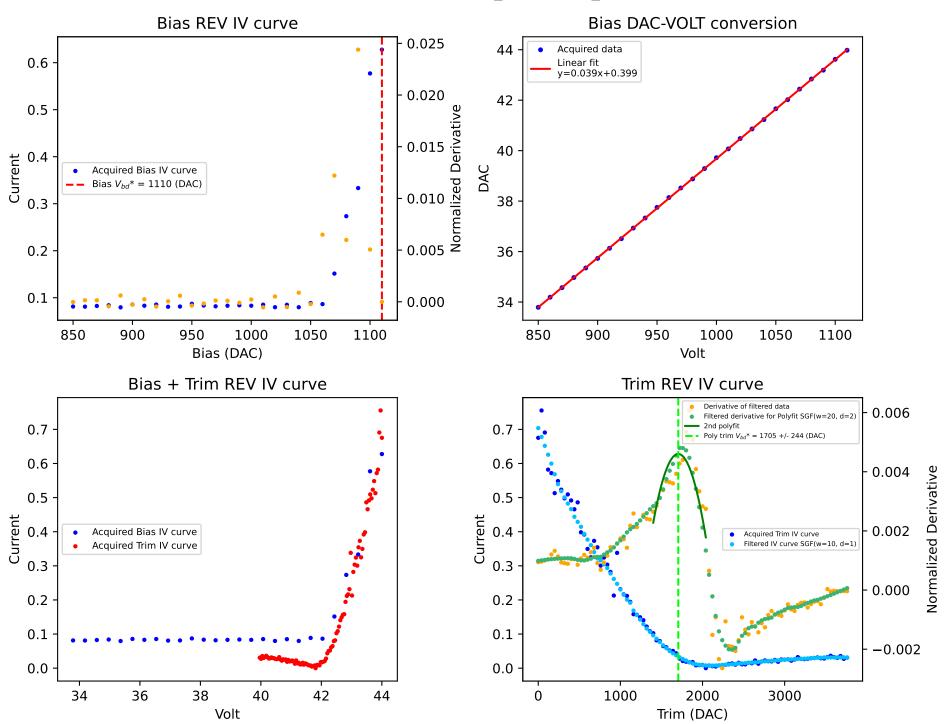




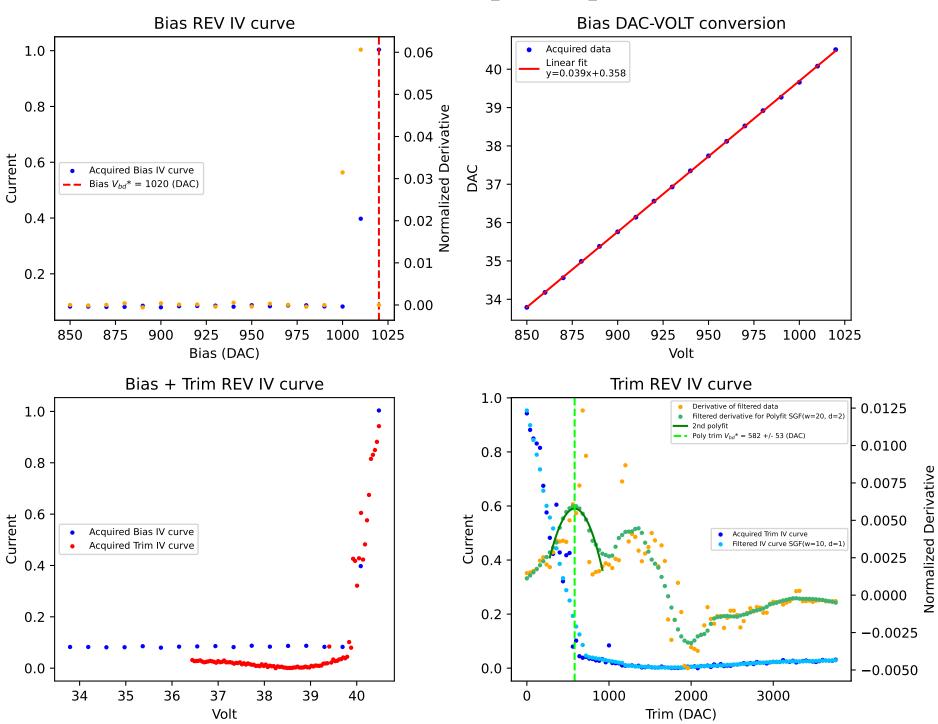


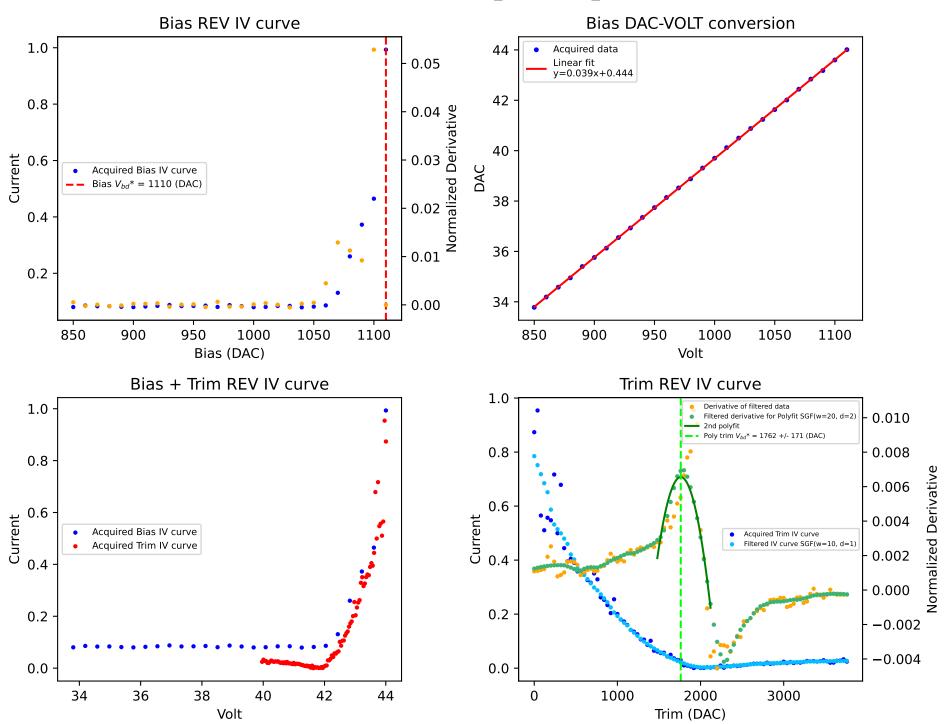


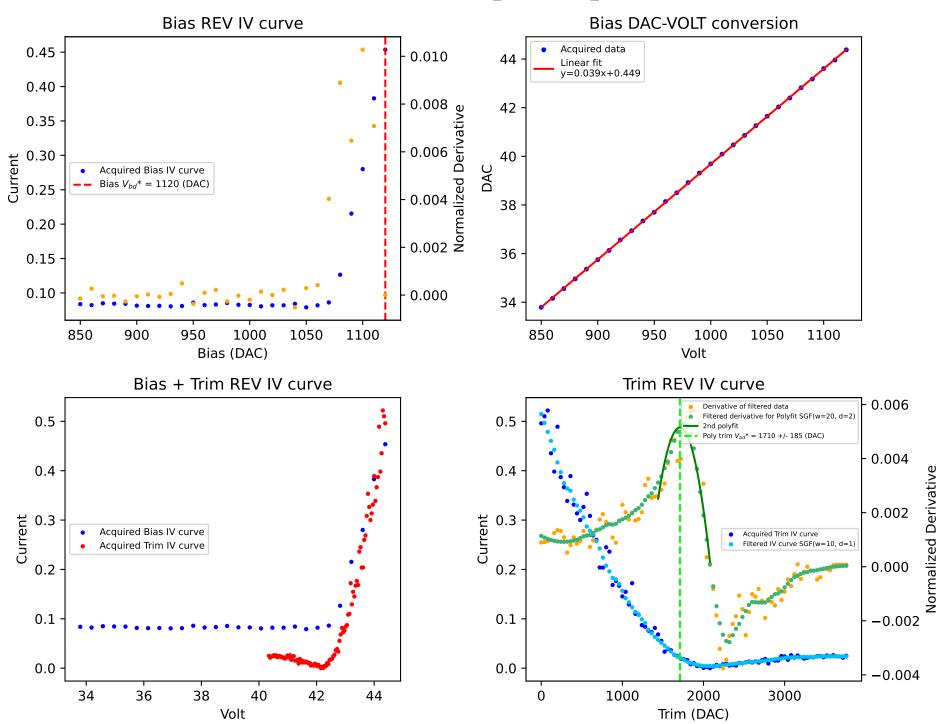




ENDPOINT:104 APA:1 AFE:4 Config_CH:33 DAQ_CH:41 SiPM:HPK







ENDPOINT:104 APA:1 AFE:4 Config CH:36 DAQ CH:44 SiPM:HPK

