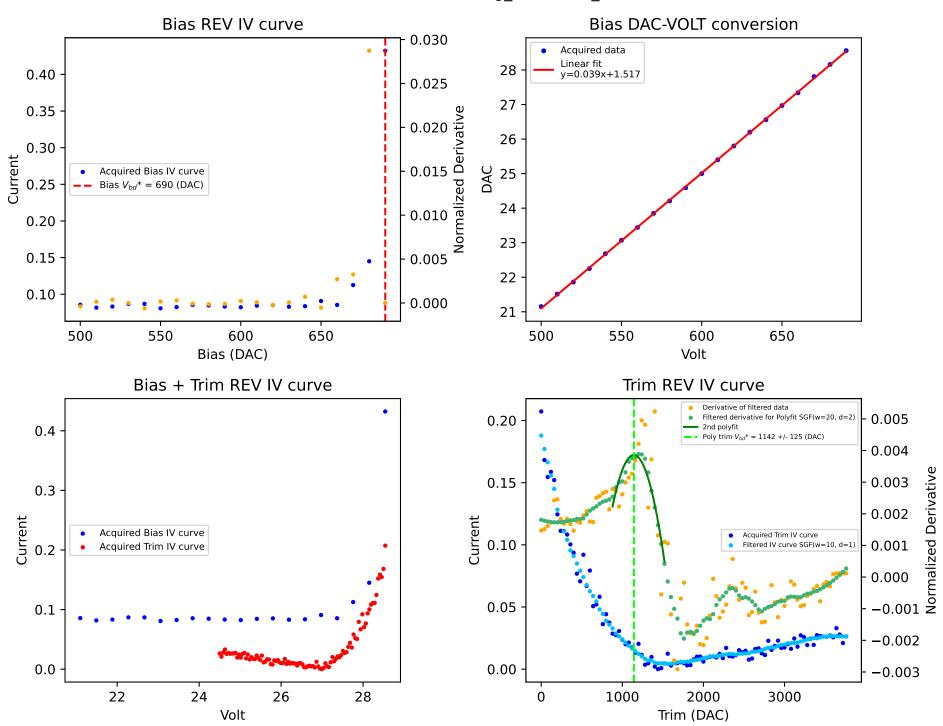
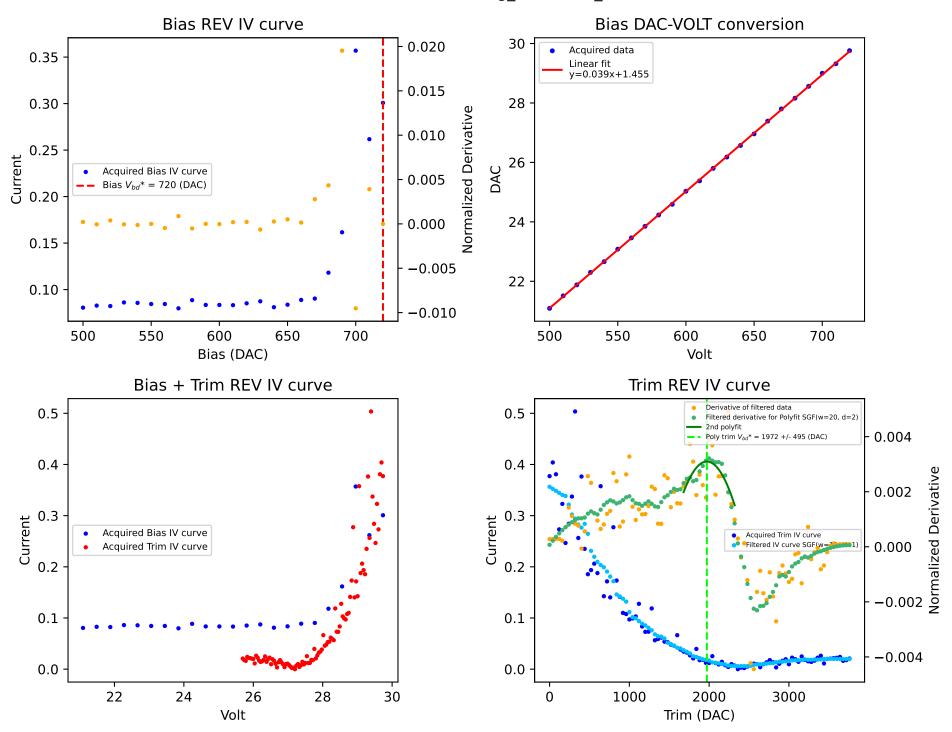
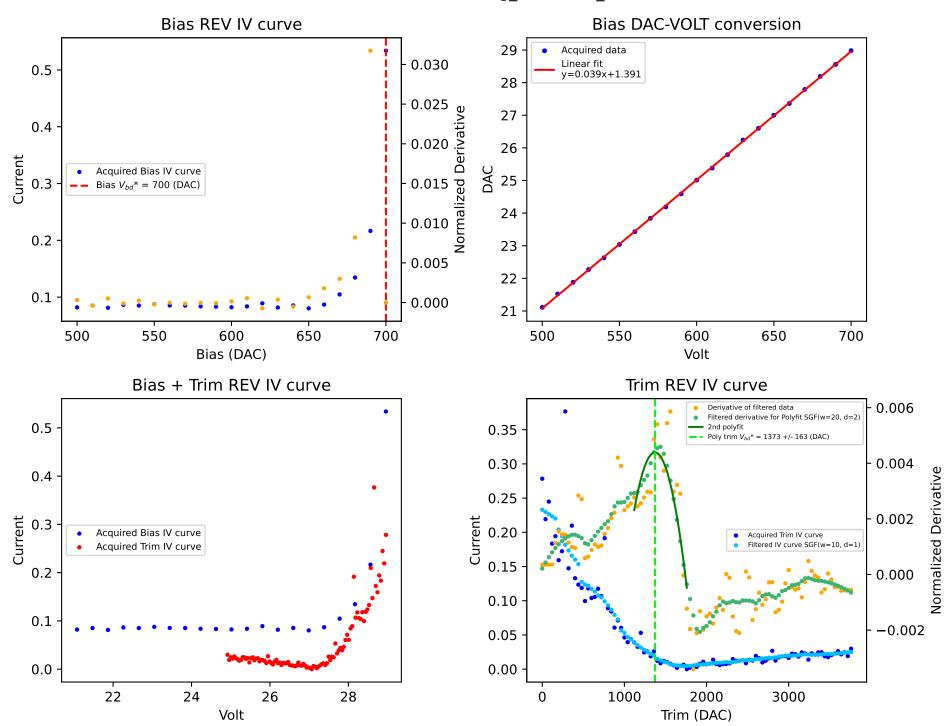
## ENDPOINT:104 APA:1 AFE:0 Config CH:0 DAQ CH:0 SiPM:FBK

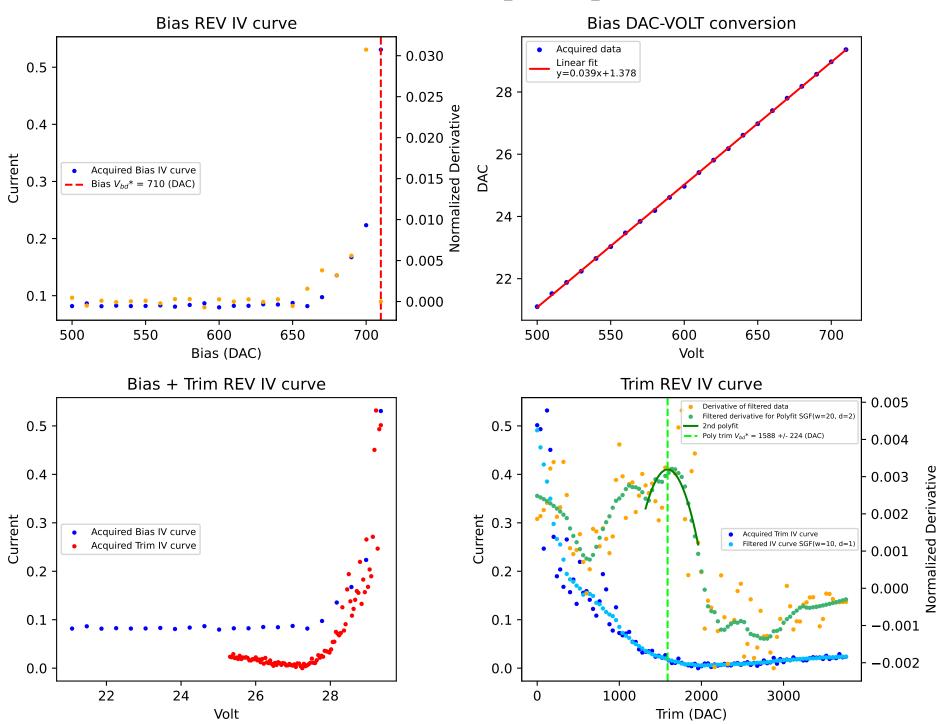


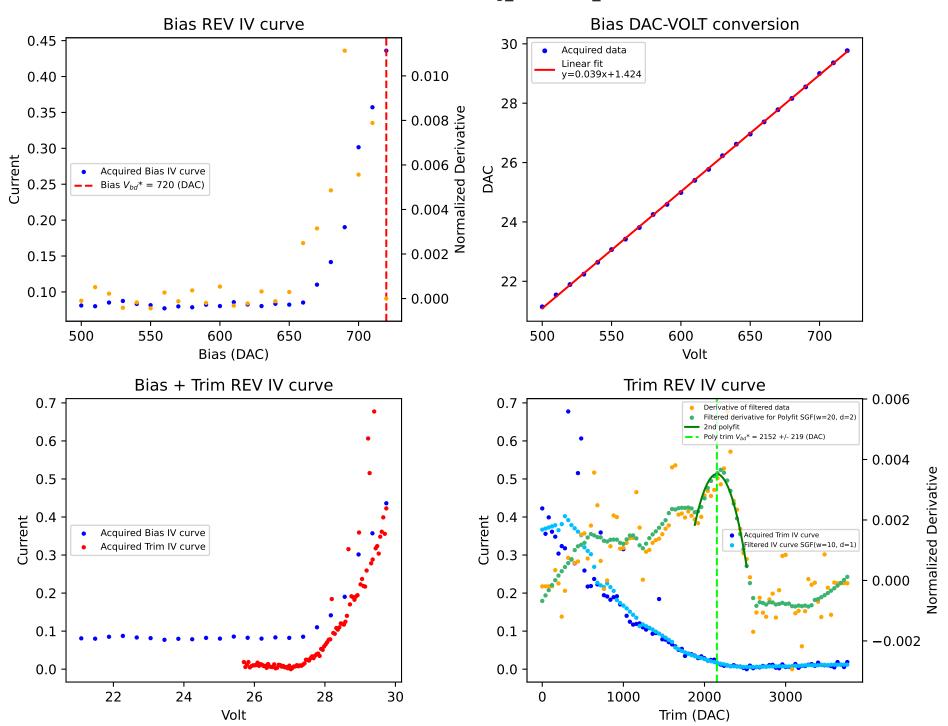
## ENDPOINT:104 APA:1 AFE:0 Config CH:1 DAQ CH:1 SiPM:FBK



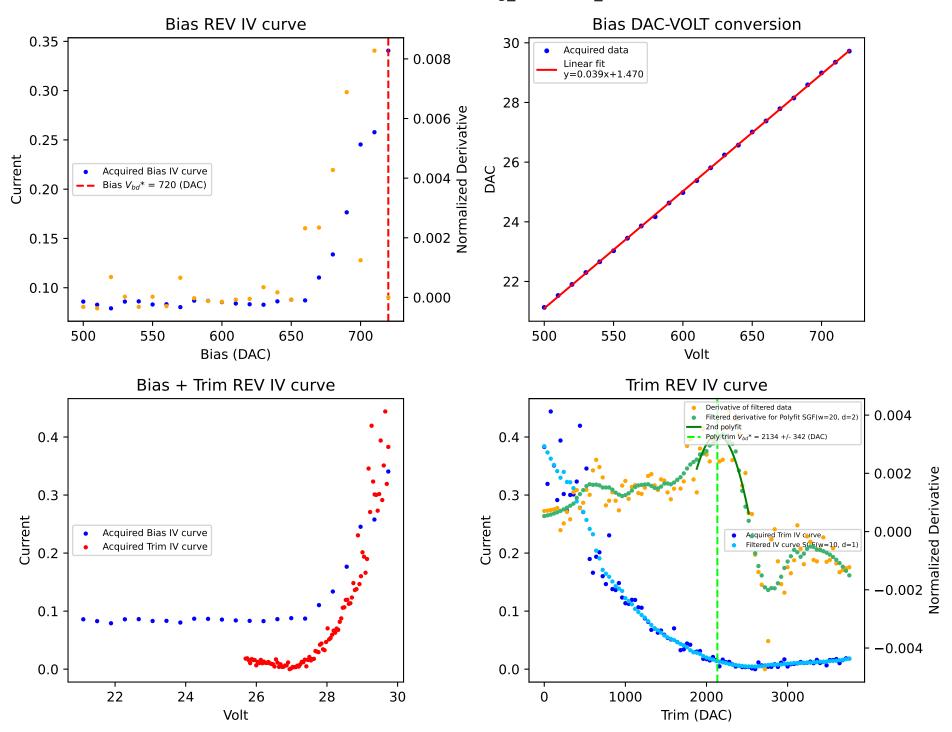


# ENDPOINT:104 APA:1 AFE:0 Config\_CH:3 DAQ\_CH:3 SiPM:FBK

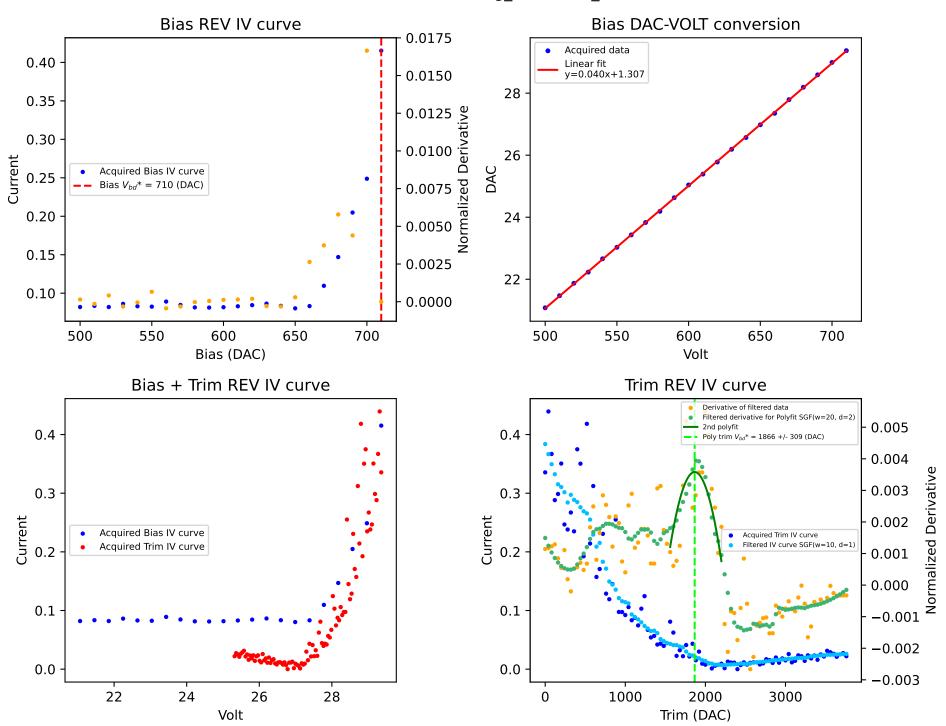




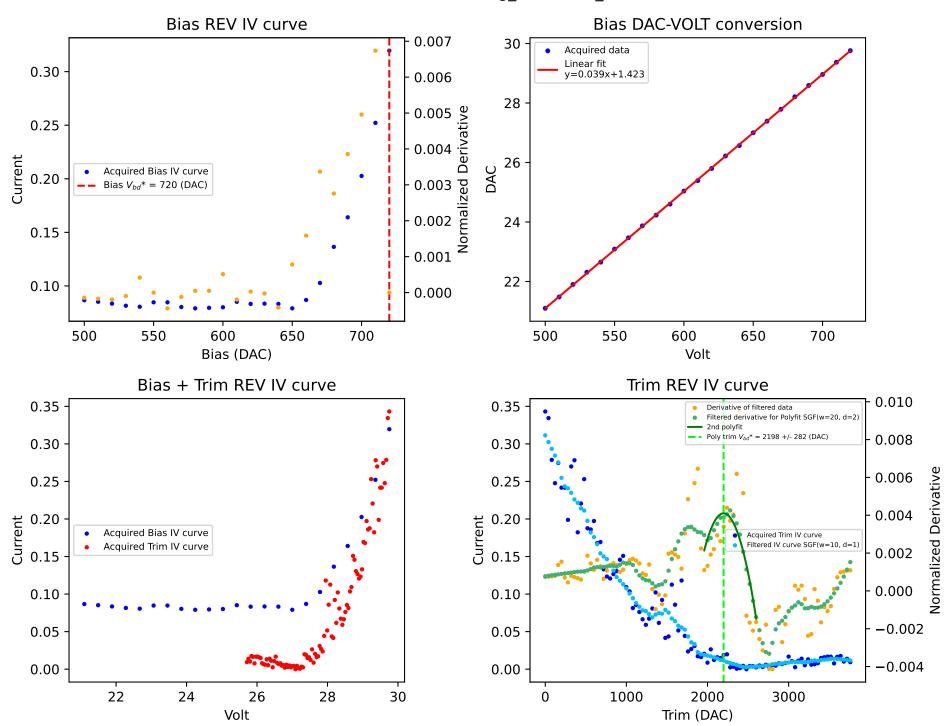
## ENDPOINT:104 APA:1 AFE:0 Config CH:5 DAQ CH:5 SiPM:FBK

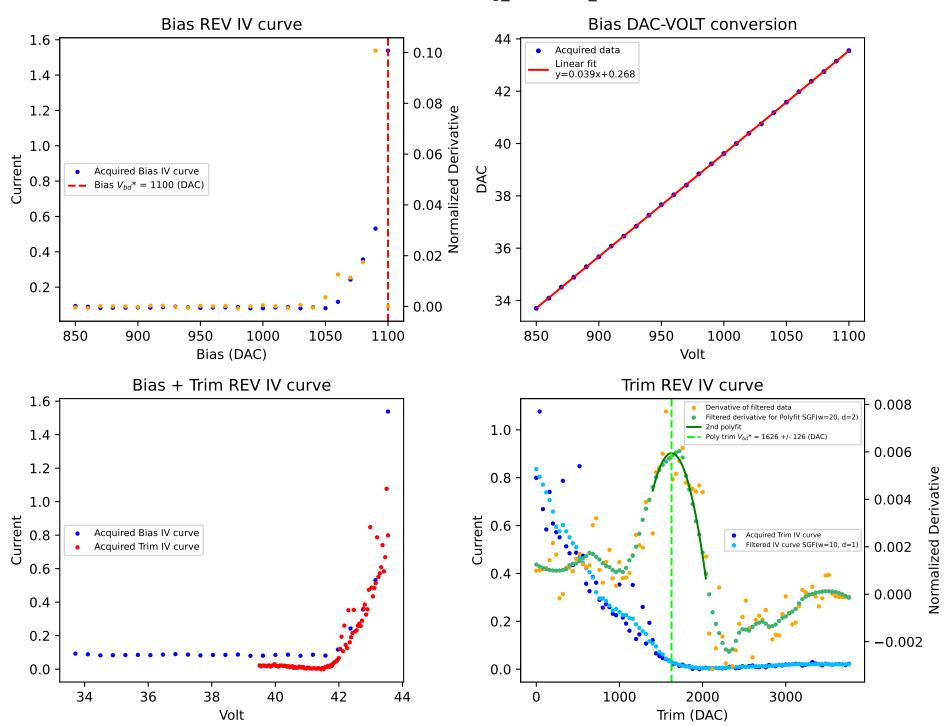


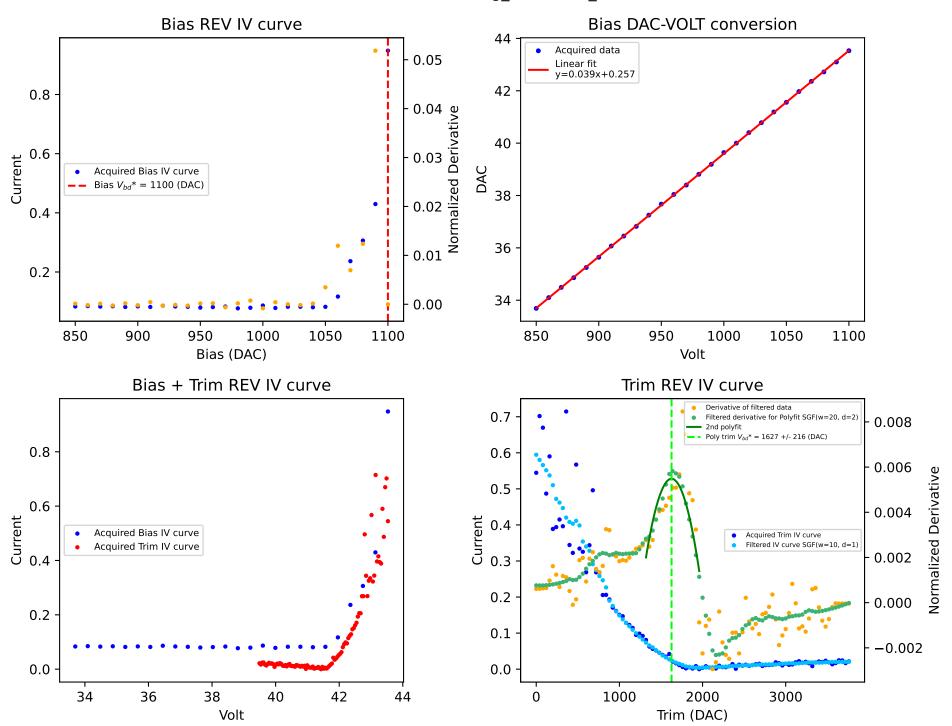
## ENDPOINT:104 APA:1 AFE:0 Config CH:6 DAQ CH:6 SiPM:FBK



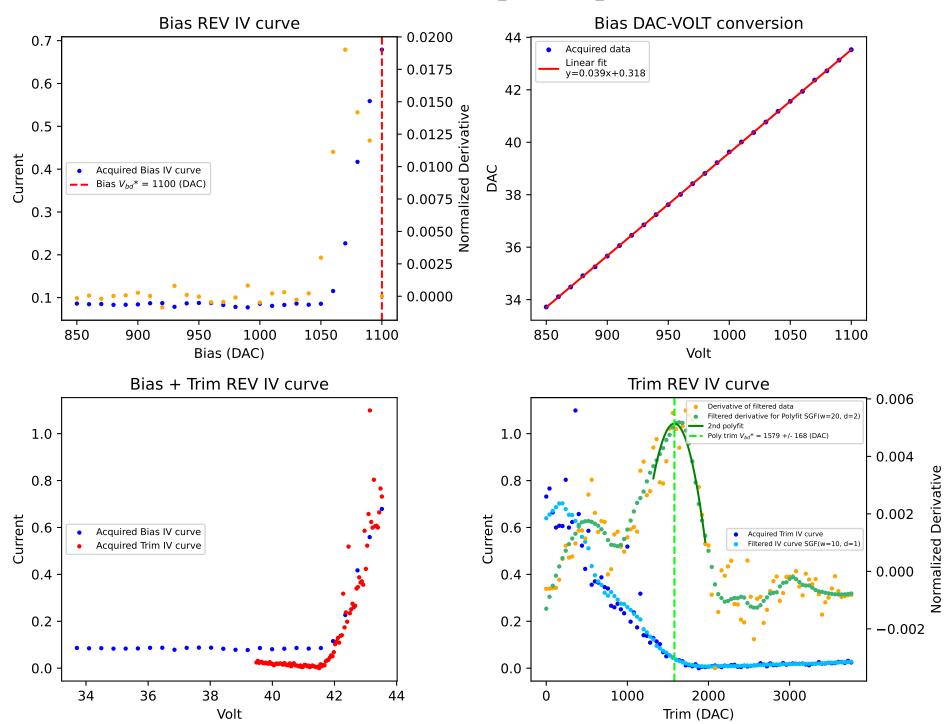
## ENDPOINT:104 APA:1 AFE:0 Config CH:7 DAQ CH:7 SiPM:FBK

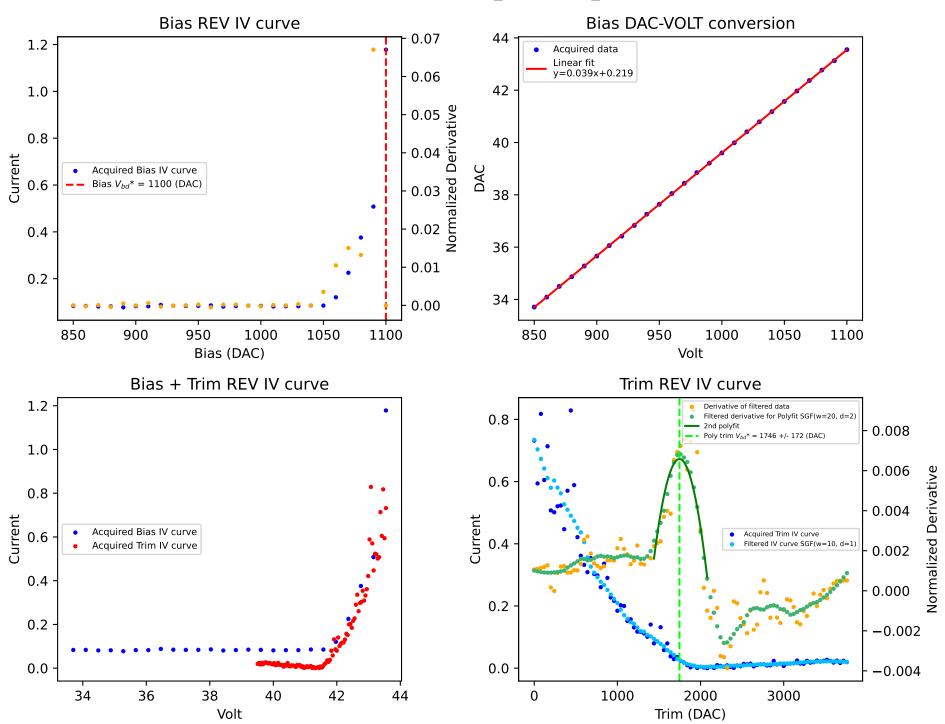


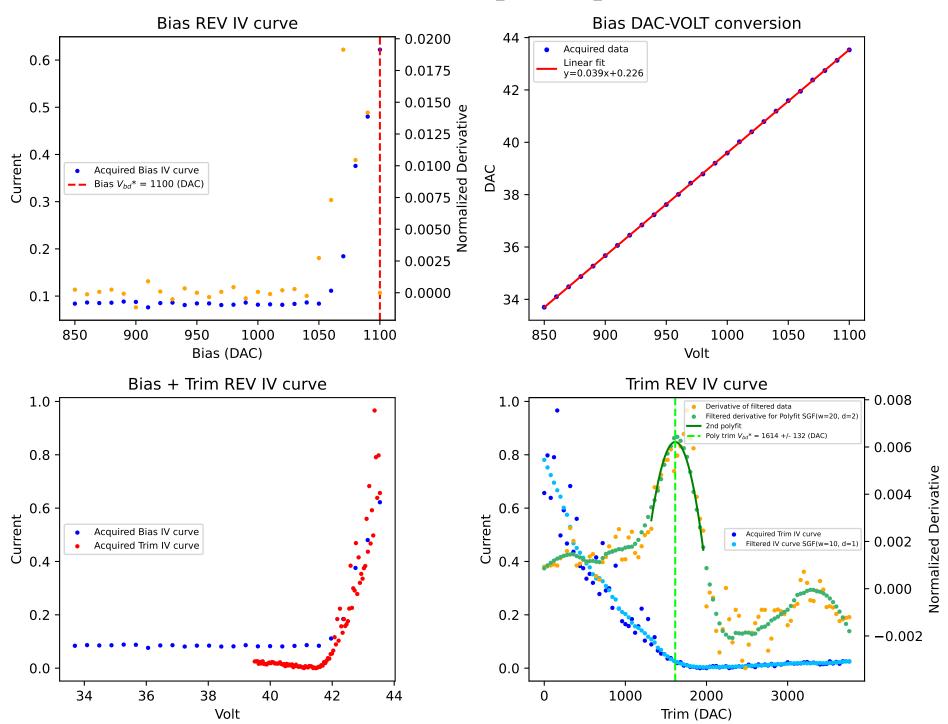


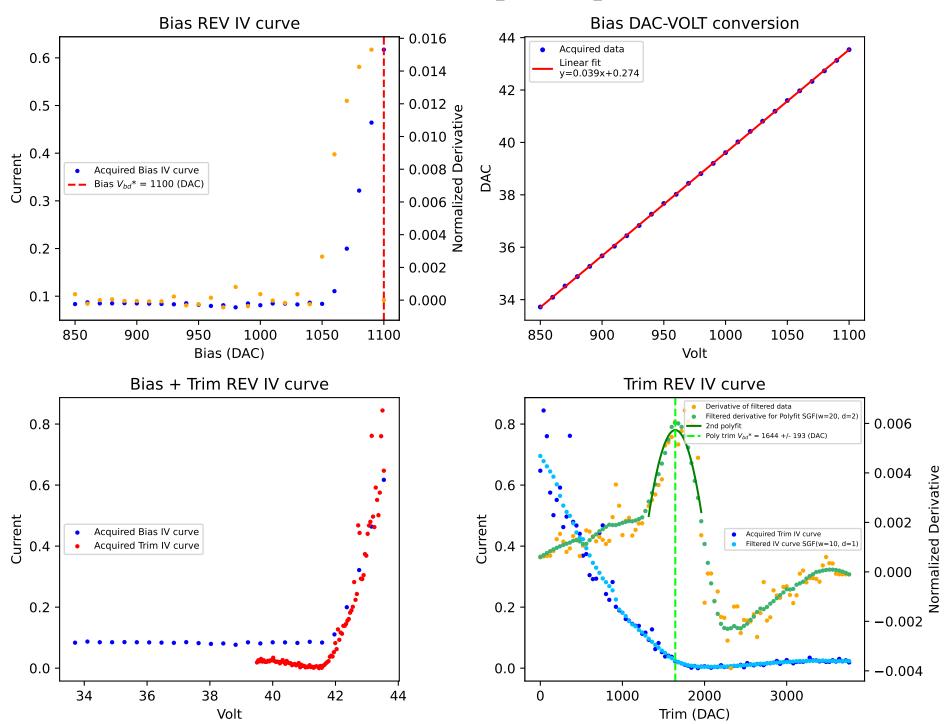


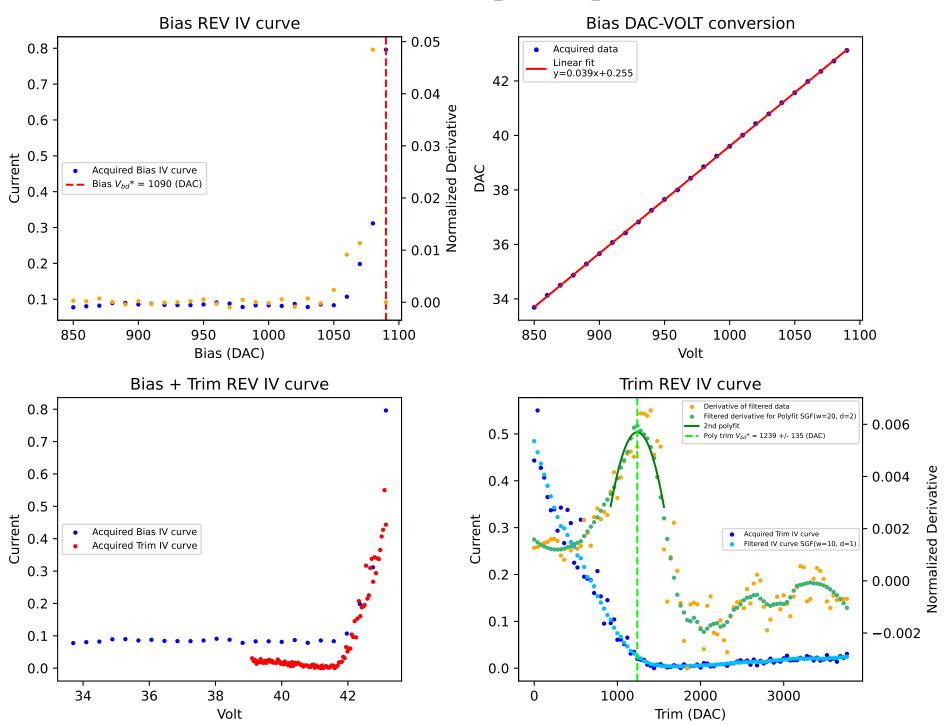
# ENDPOINT:104 APA:1 AFE:1 Config\_CH:10 DAQ\_CH:12 SiPM:HPK











## ENDPOINT:104 APA:1 AFE:1 Config CH:15 DAQ CH:17 SiPM:HPK

