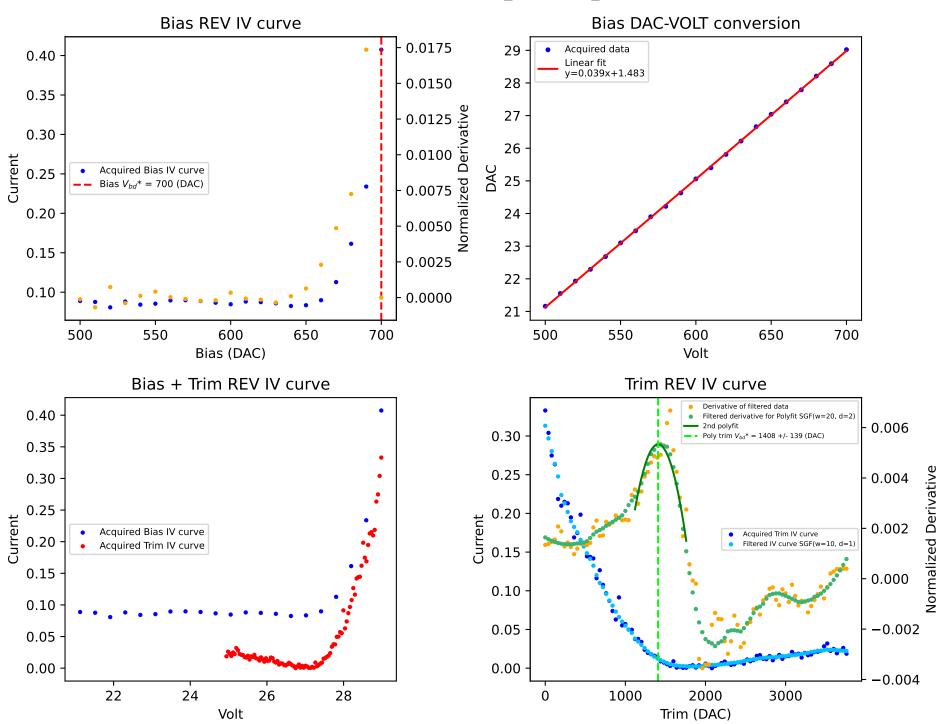
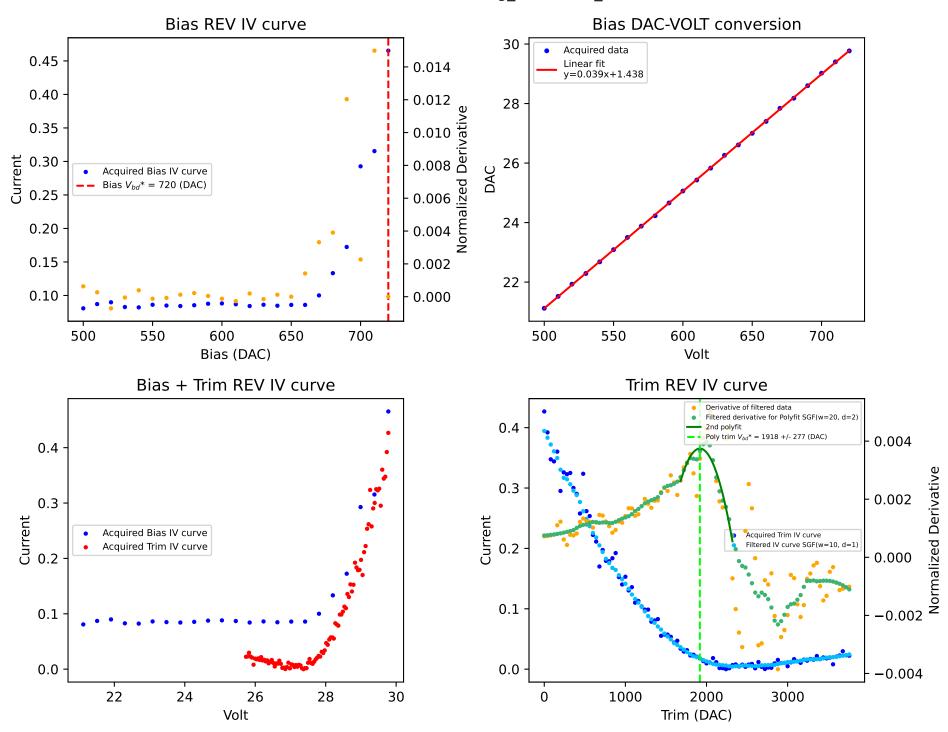
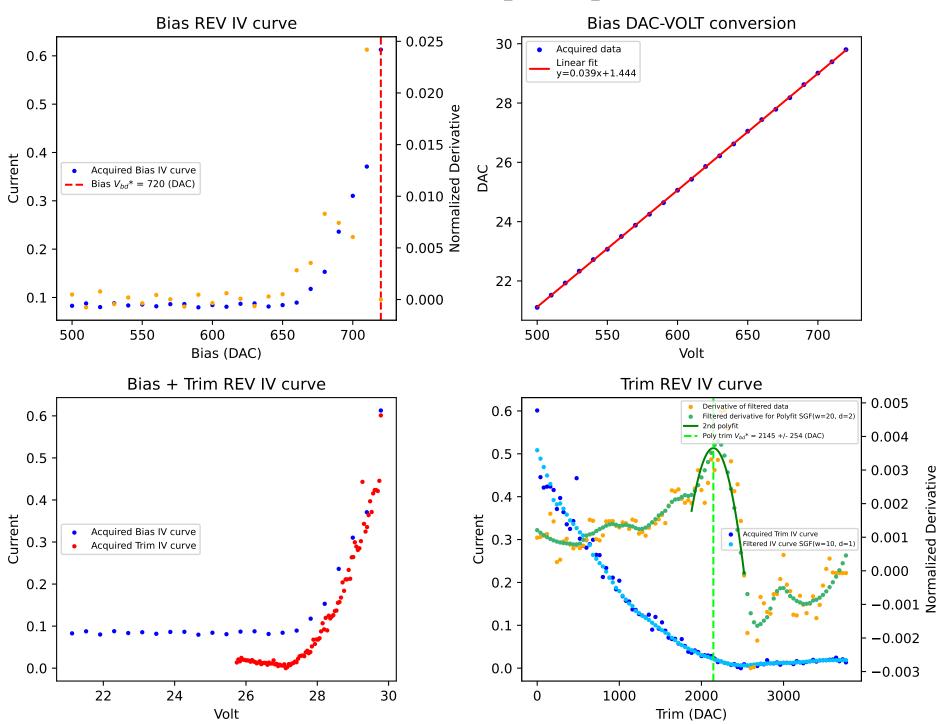
# ENDPOINT:104 APA:1 AFE:0 Config\_CH:0 DAQ\_CH:0 SiPM:FBK



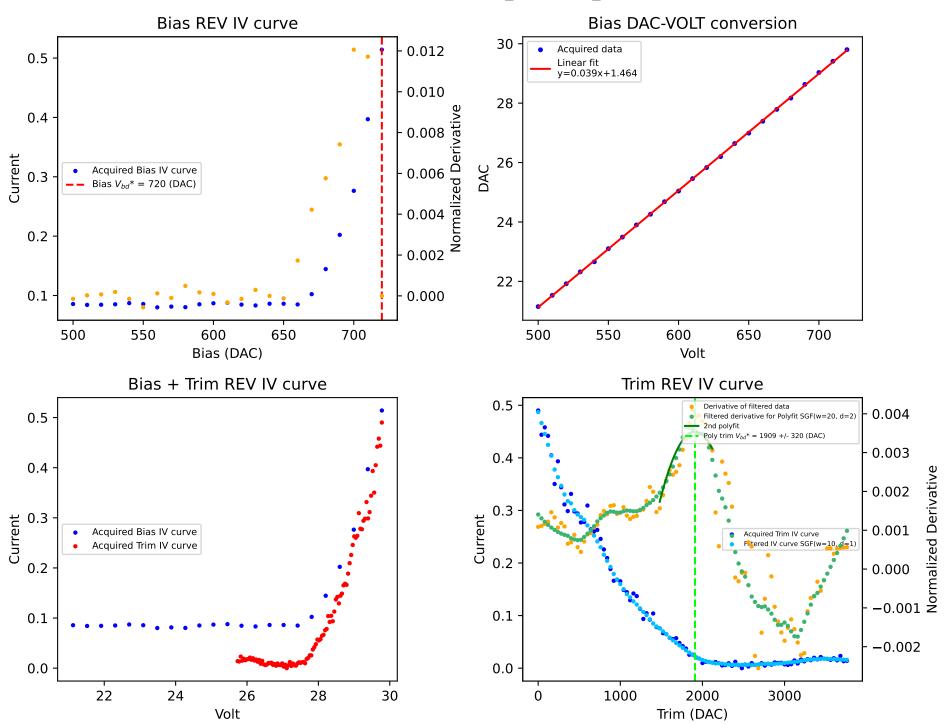
## ENDPOINT:104 APA:1 AFE:0 Config CH:1 DAQ CH:1 SiPM:FBK



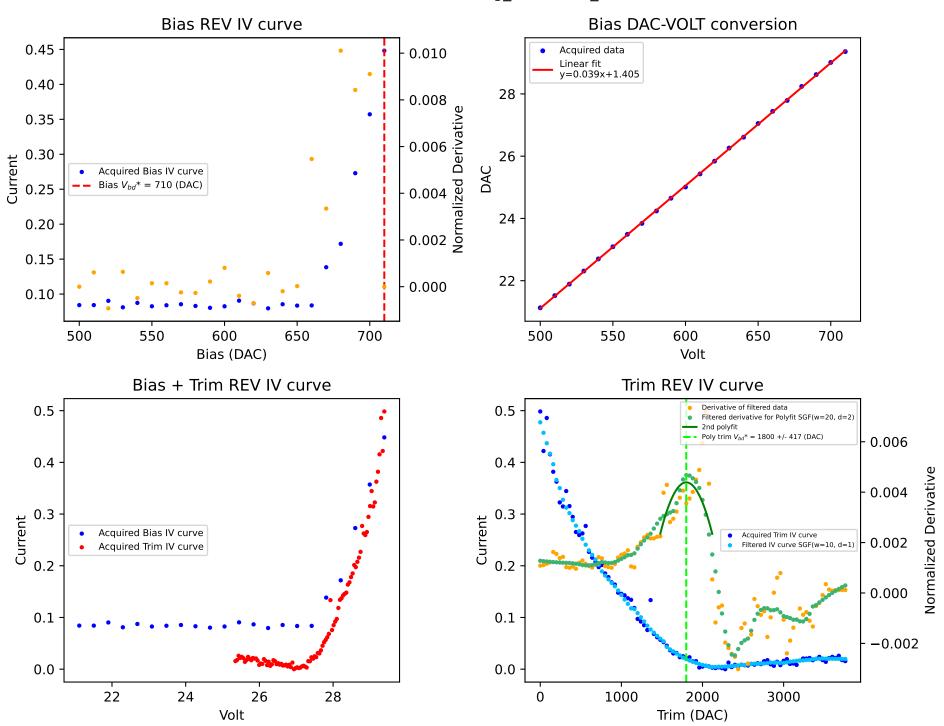
# ENDPOINT:104 APA:1 AFE:0 Config\_CH:2 DAQ\_CH:2 SiPM:FBK



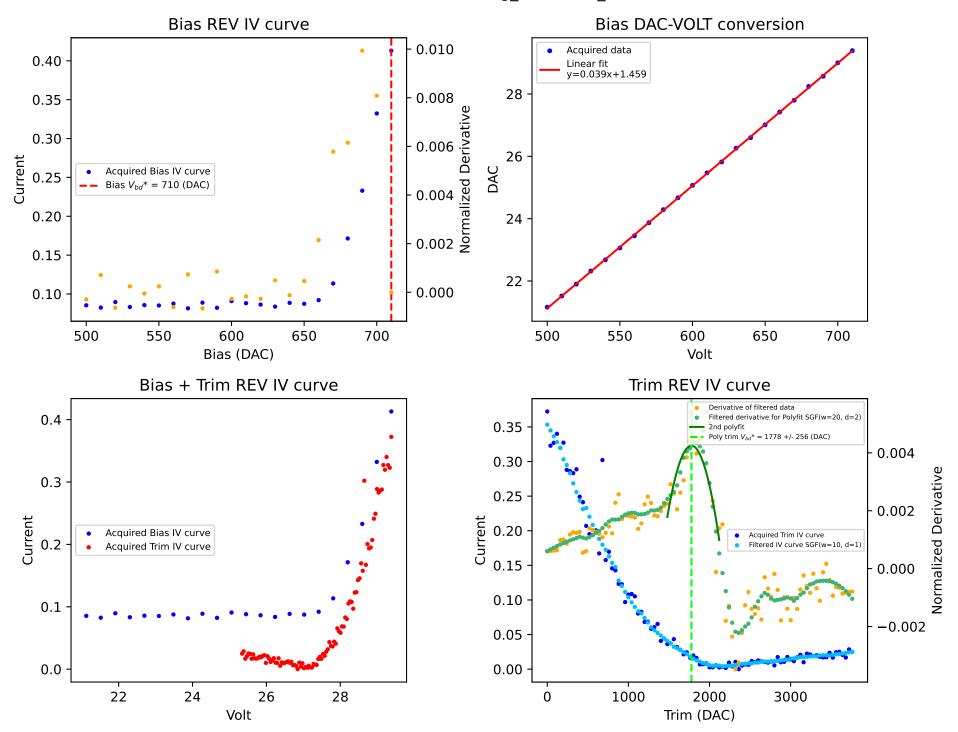
# ENDPOINT:104 APA:1 AFE:0 Config\_CH:3 DAQ\_CH:3 SiPM:FBK

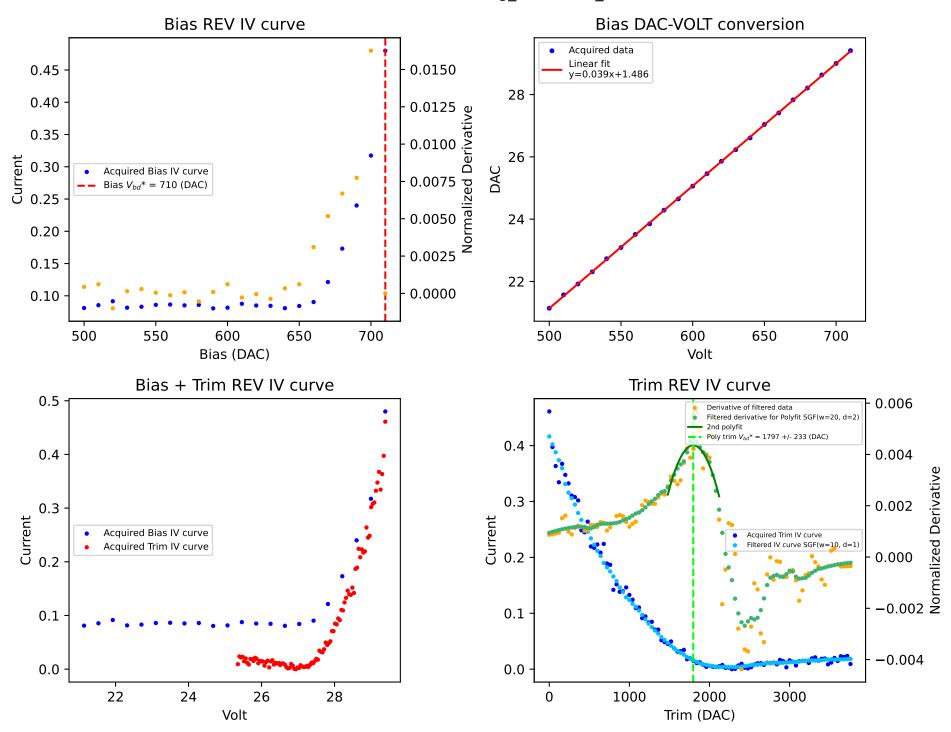


## ENDPOINT:104 APA:1 AFE:0 Config CH:4 DAQ CH:4 SiPM:FBK

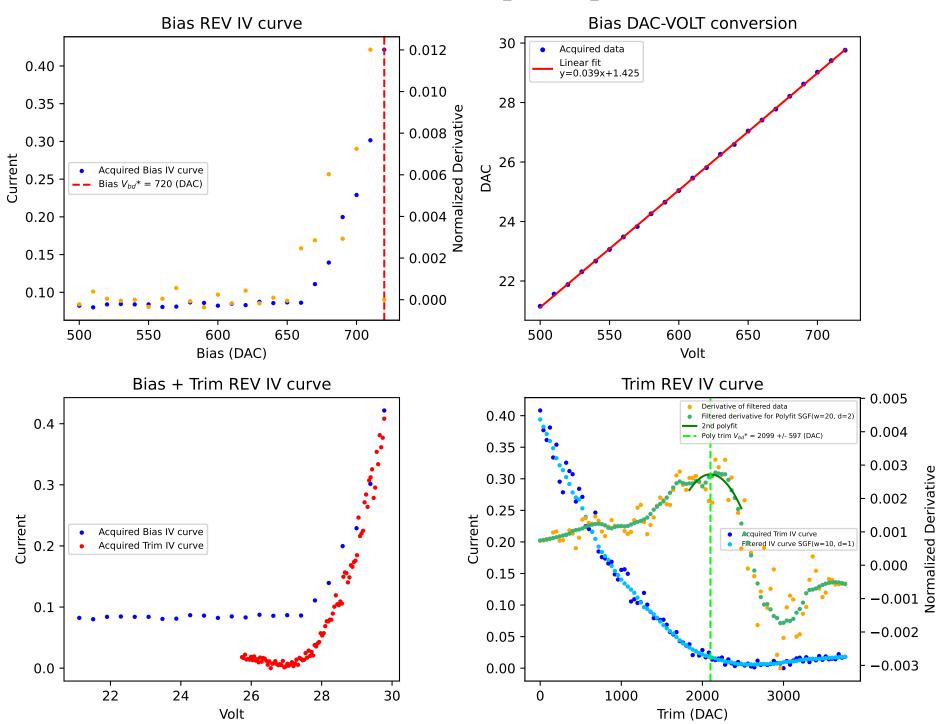


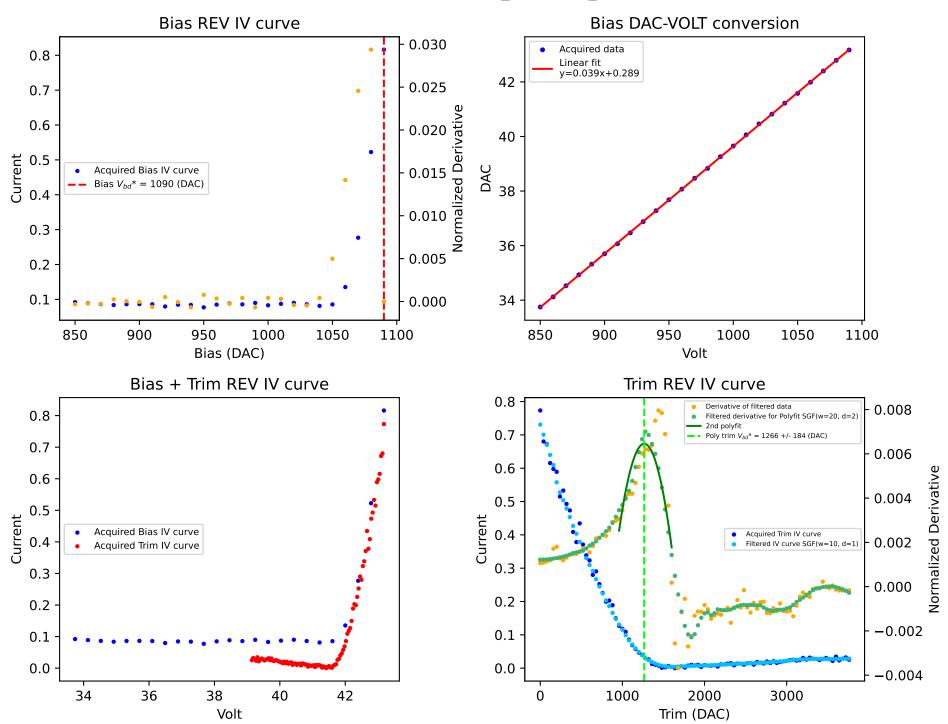
## ENDPOINT:104 APA:1 AFE:0 Config CH:5 DAQ CH:5 SiPM:FBK

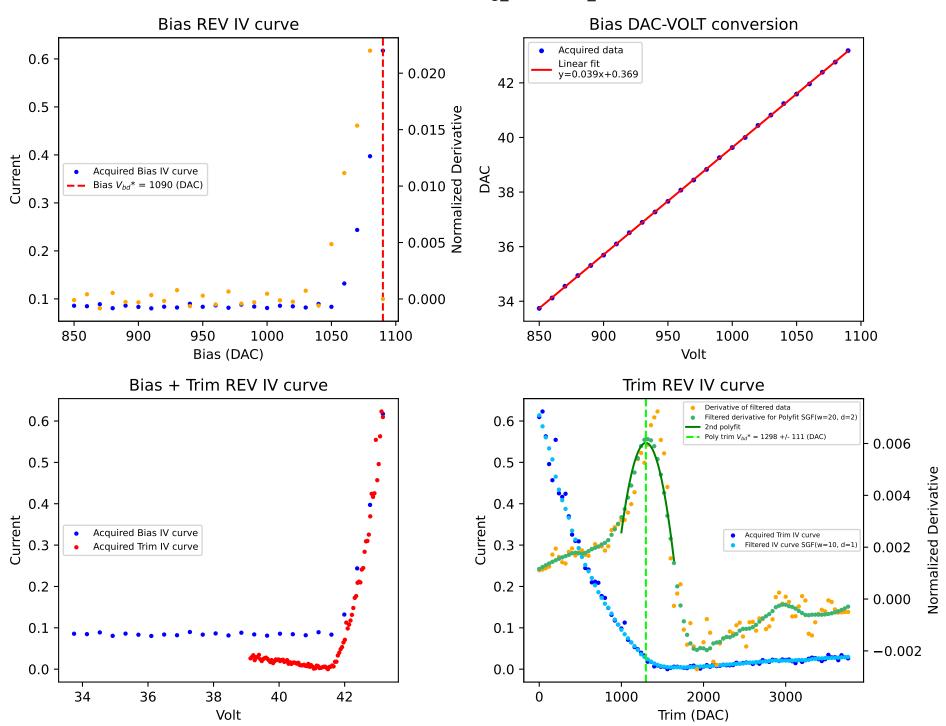




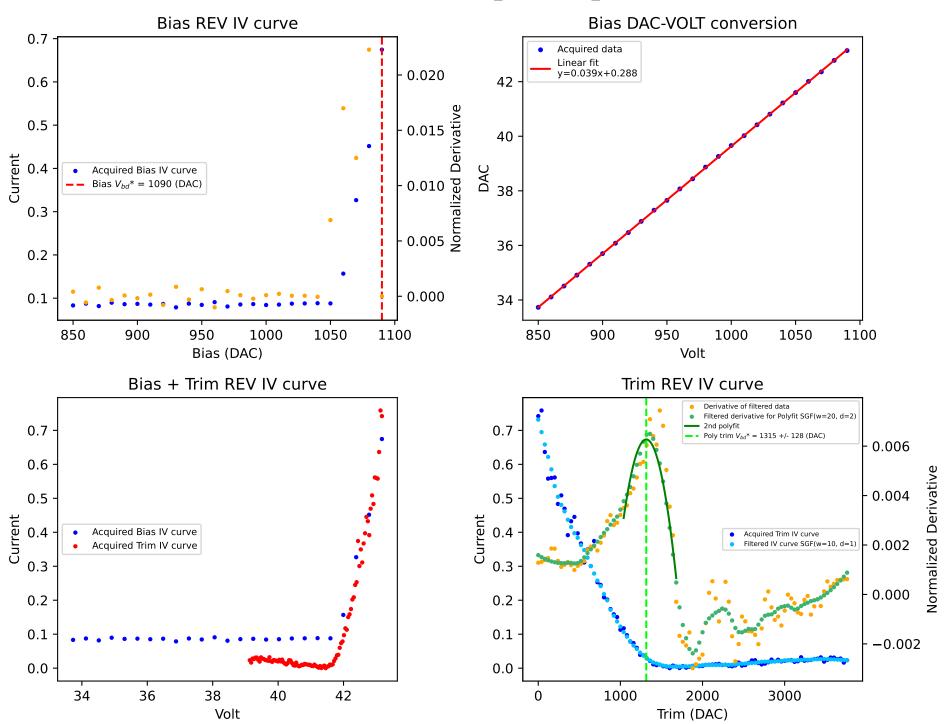
# ENDPOINT:104 APA:1 AFE:0 Config\_CH:7 DAQ\_CH:7 SiPM:FBK



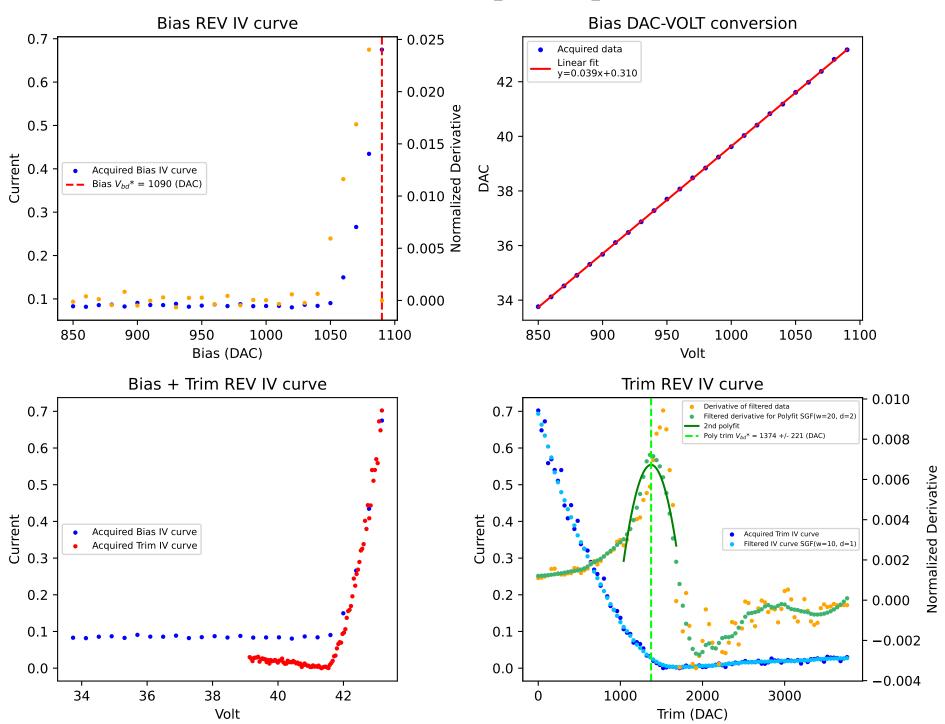




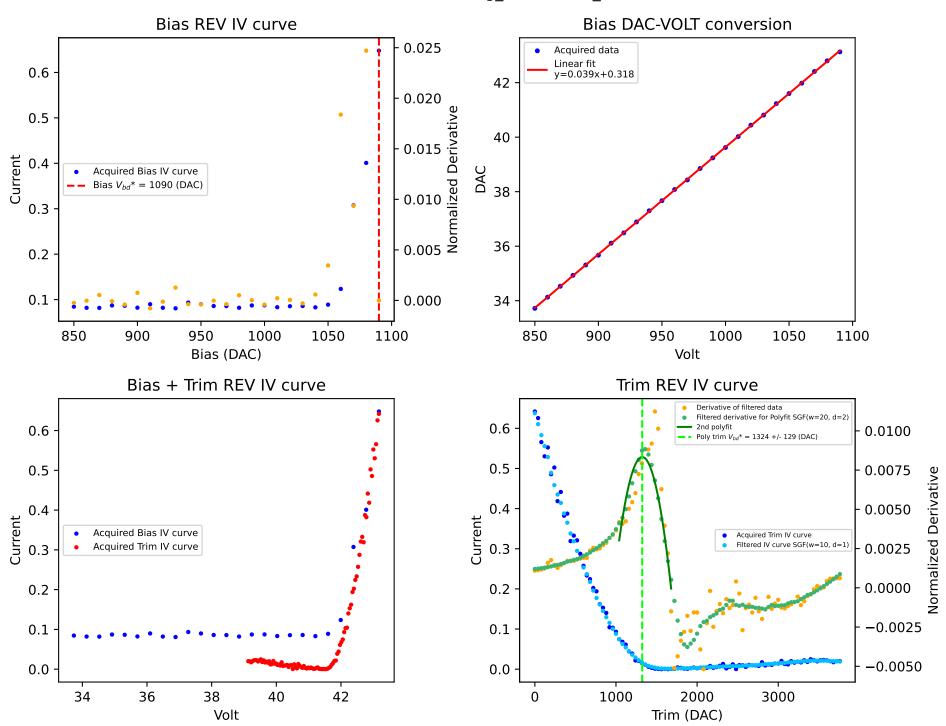
ENDPOINT:104 APA:1 AFE:1 Config\_CH:10 DAQ\_CH:12 SiPM:HPK



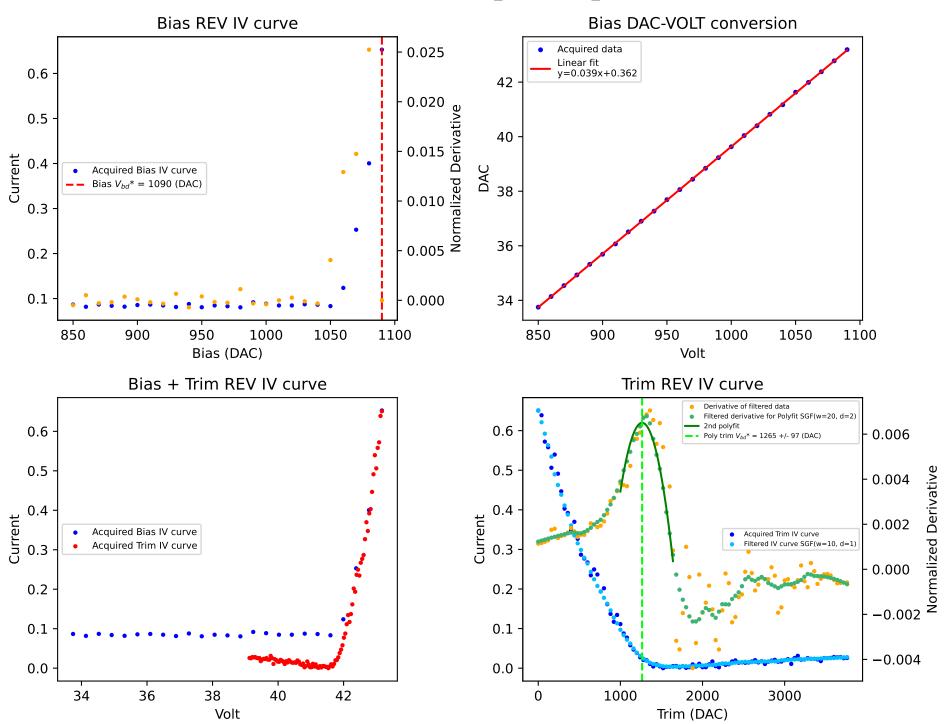
ENDPOINT:104 APA:1 AFE:1 Config\_CH:11 DAQ\_CH:13 SiPM:HPK

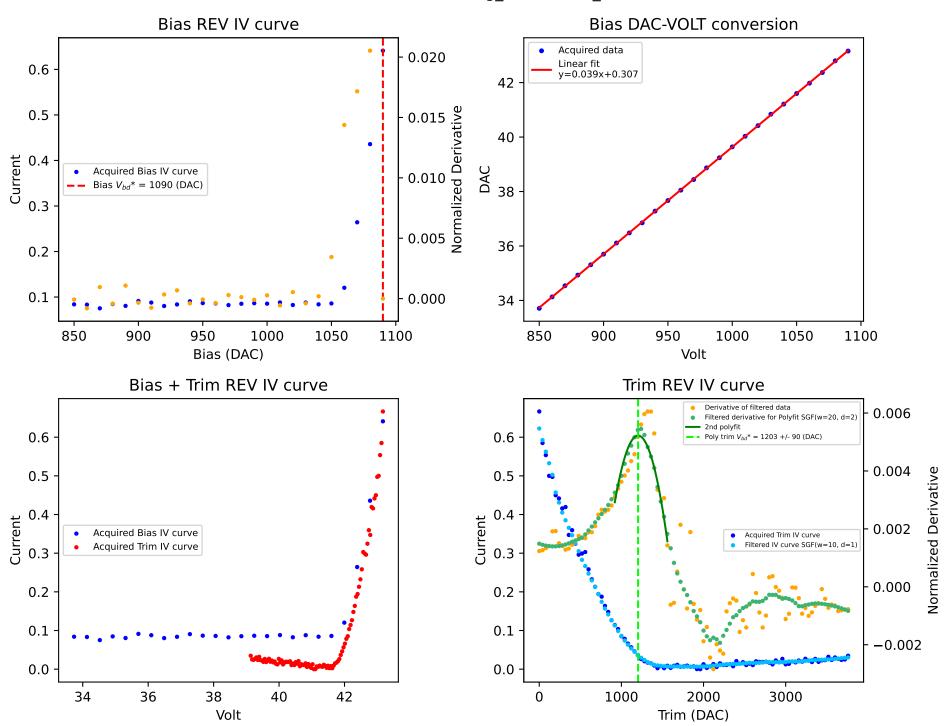


## ENDPOINT:104 APA:1 AFE:1 Config CH:12 DAQ CH:14 SiPM:HPK



ENDPOINT:104 APA:1 AFE:1 Config\_CH:13 DAQ\_CH:15 SiPM:HPK





## ENDPOINT:104 APA:1 AFE:1 Config CH:15 DAQ CH:17 SiPM:HPK

