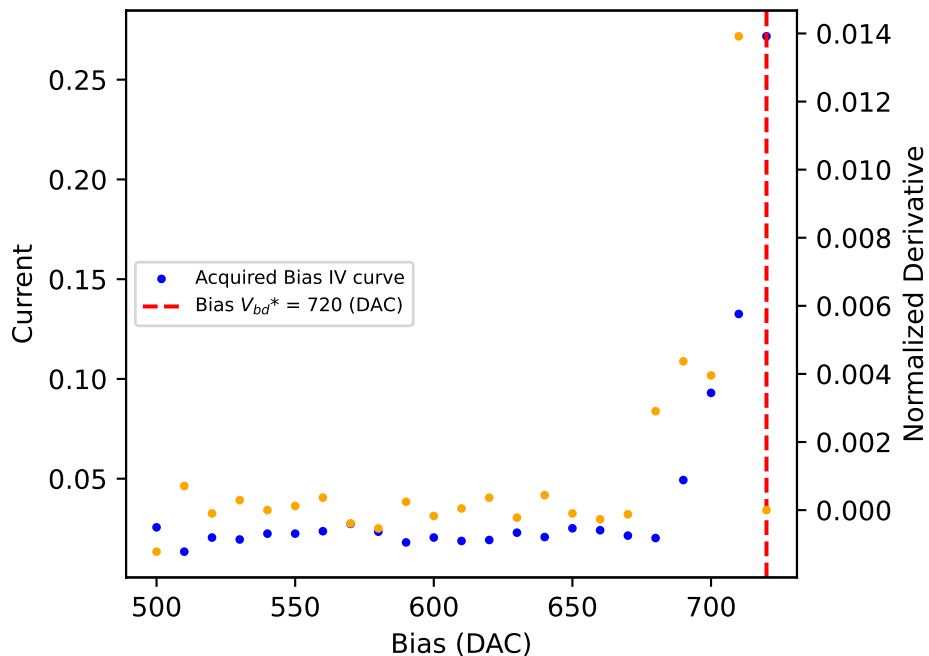
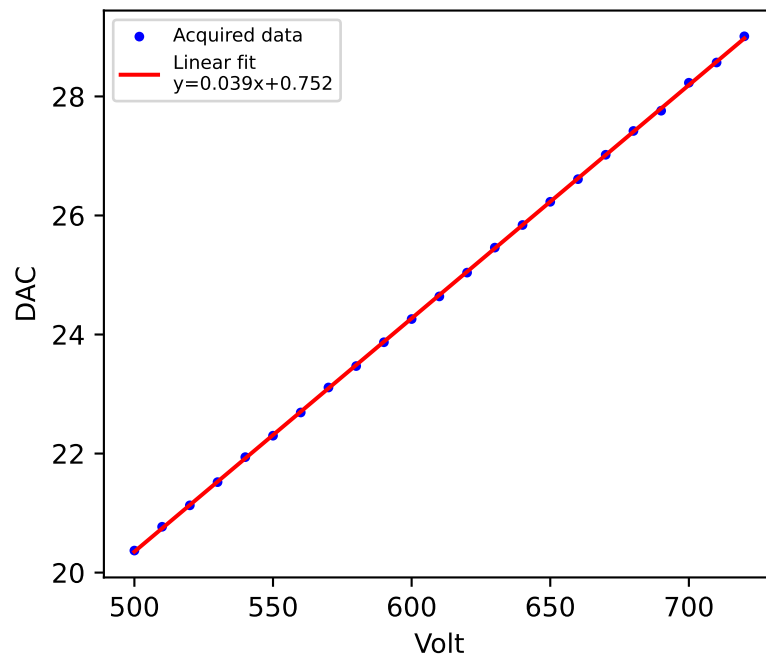


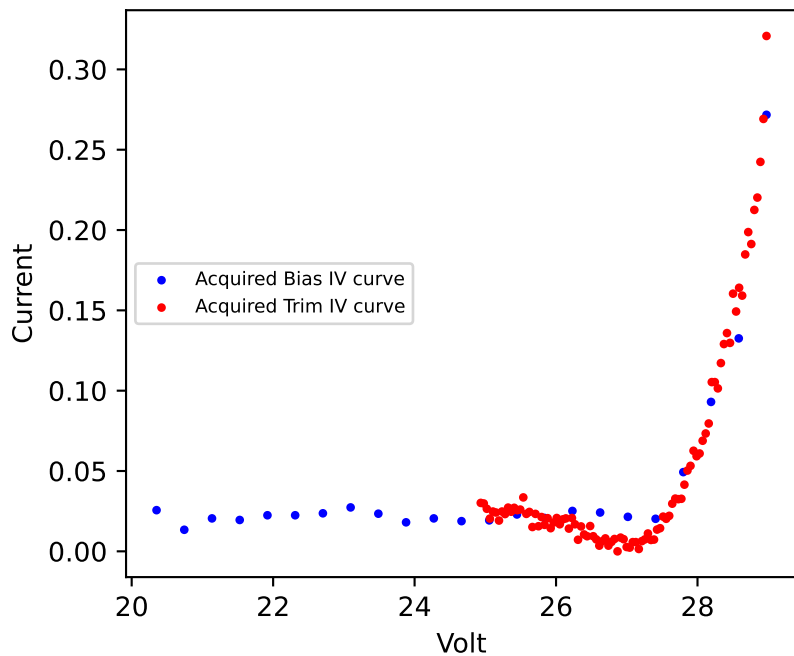
Bias REV IV curve



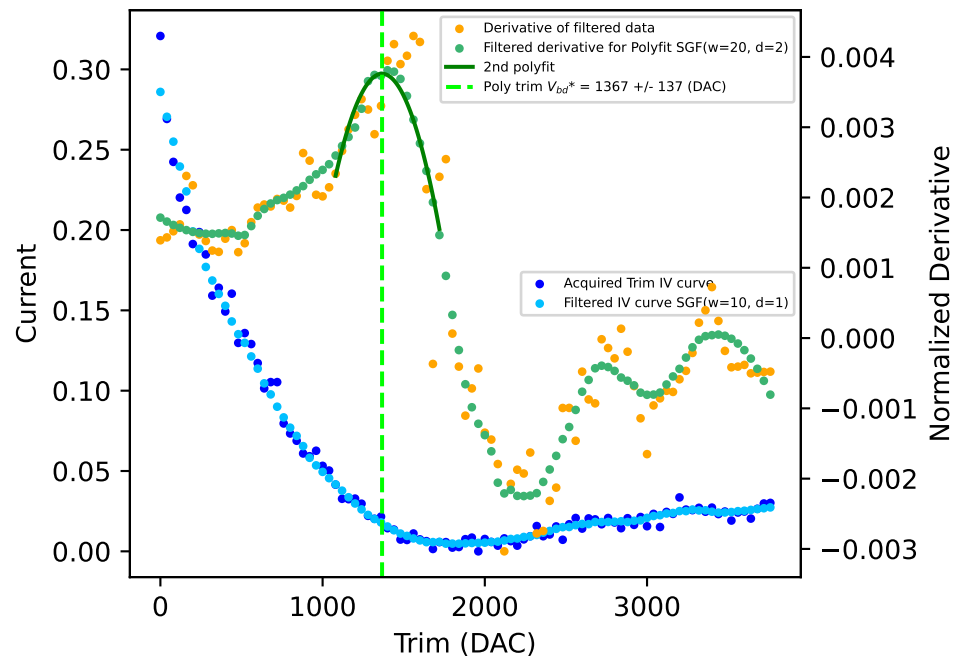
Bias DAC-VOLT conversion



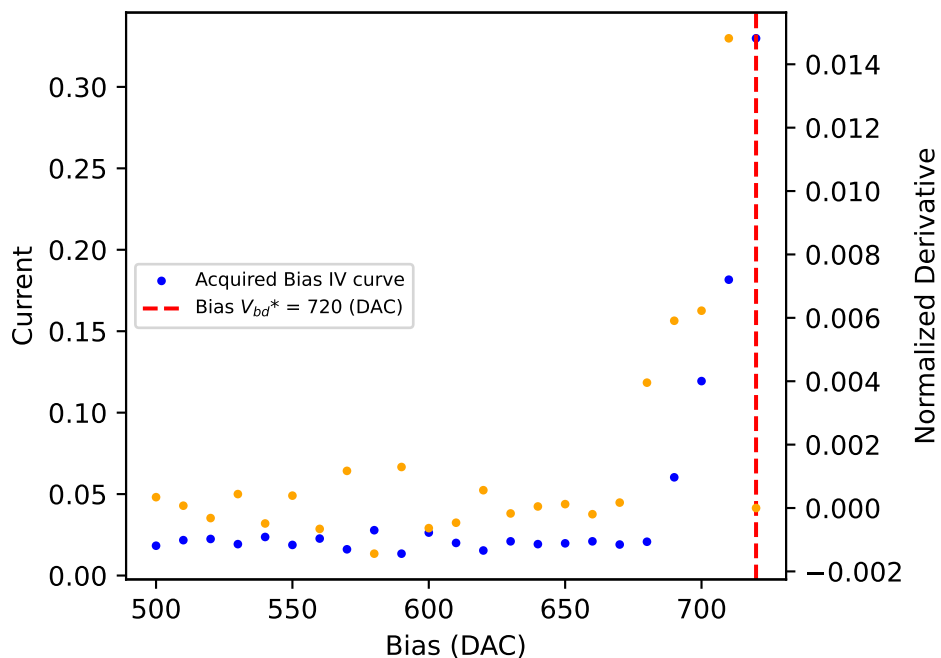
Bias + Trim REV IV curve



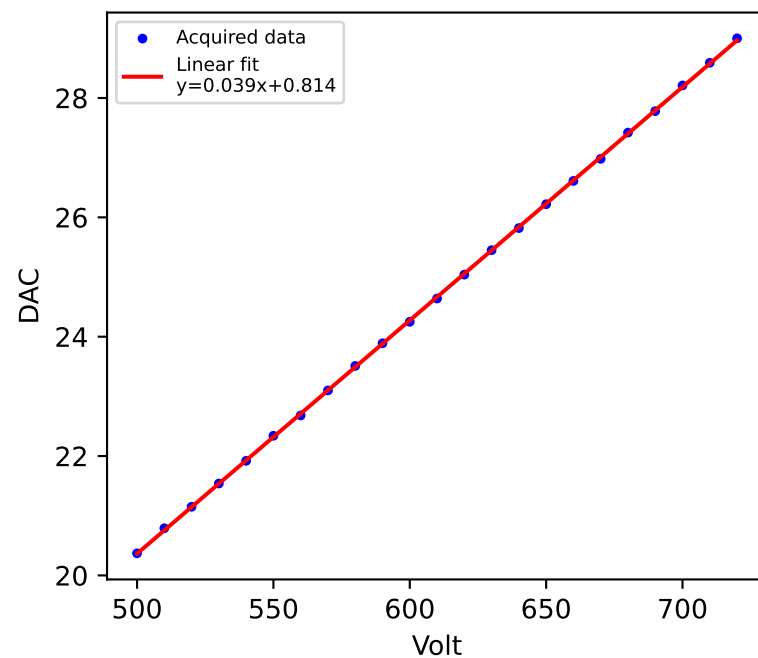
Trim REV IV curve



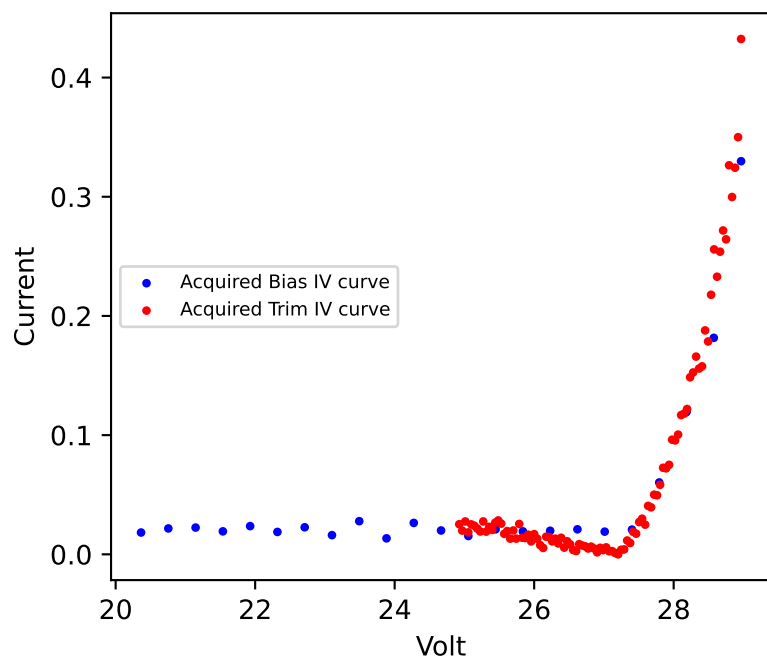
Bias REV IV curve



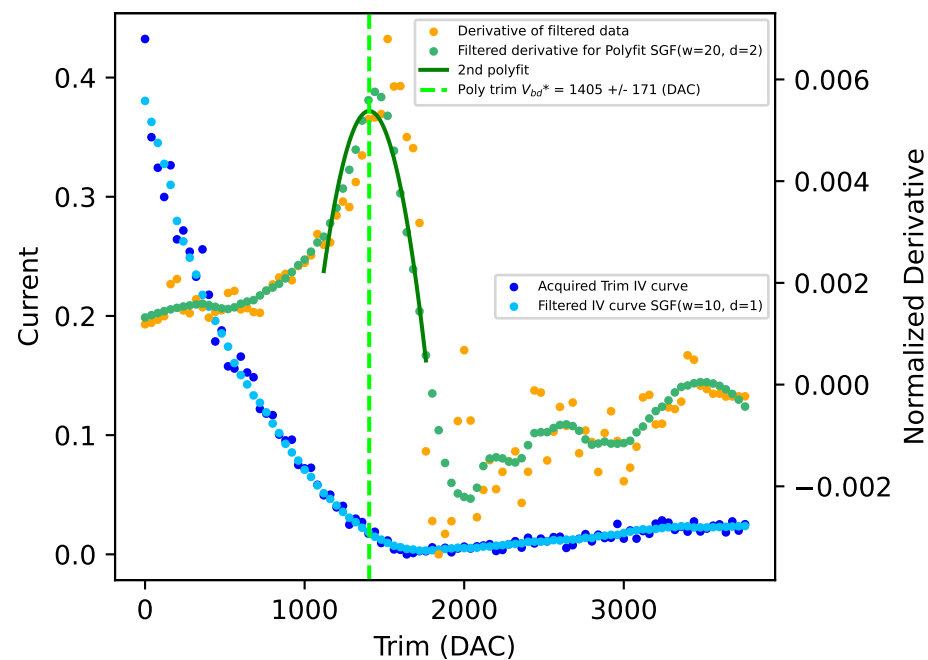
Bias DAC-VOLT conversion



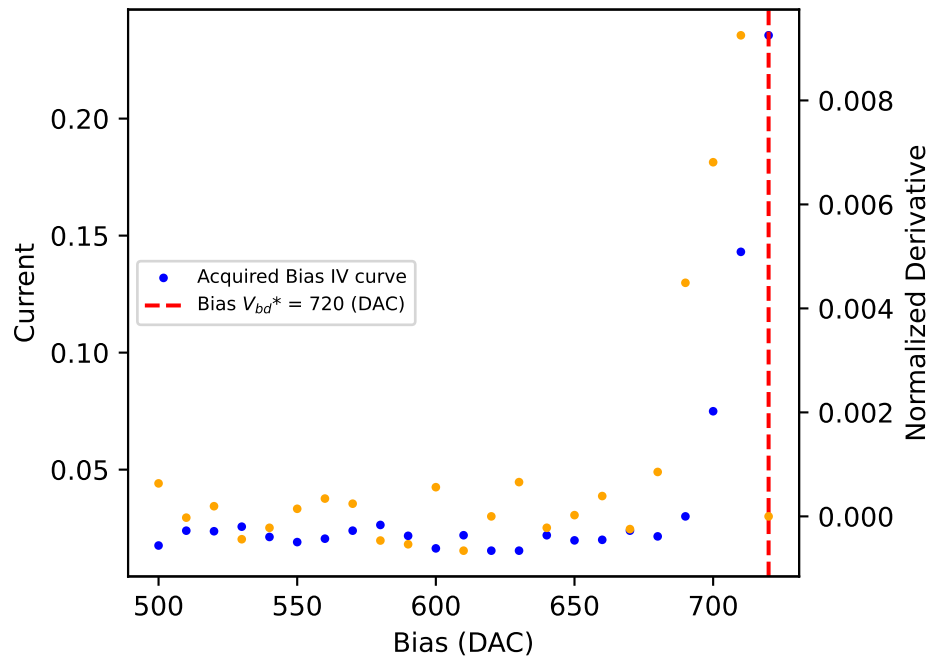
Bias + Trim REV IV curve



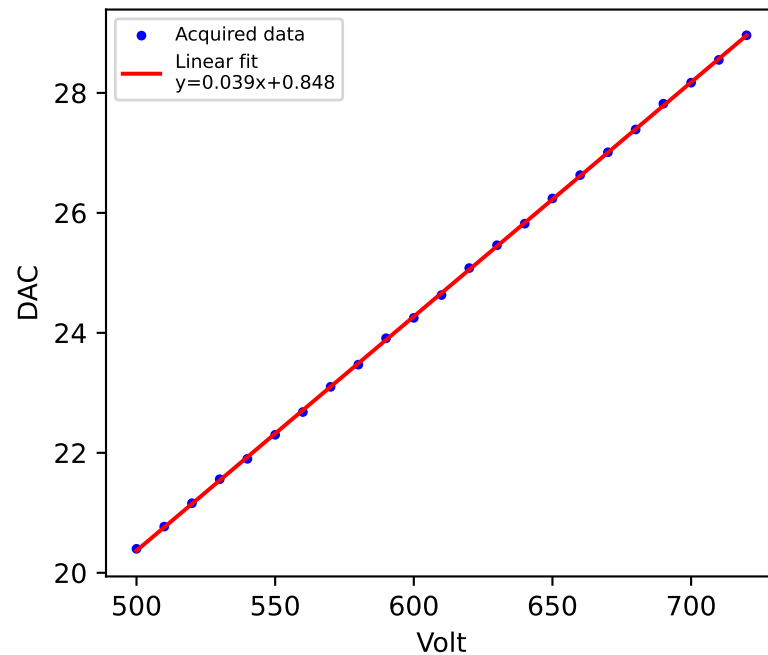
Trim REV IV curve



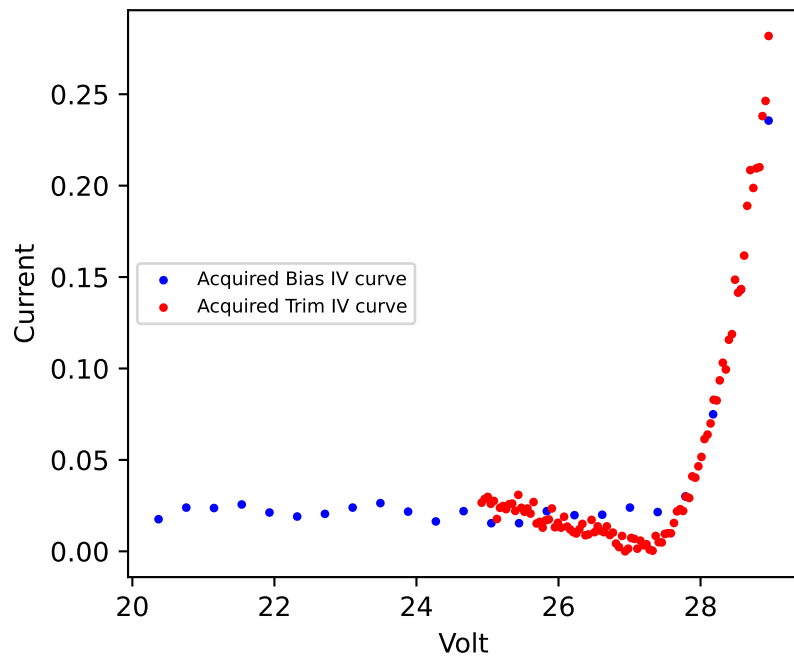
Bias REV IV curve



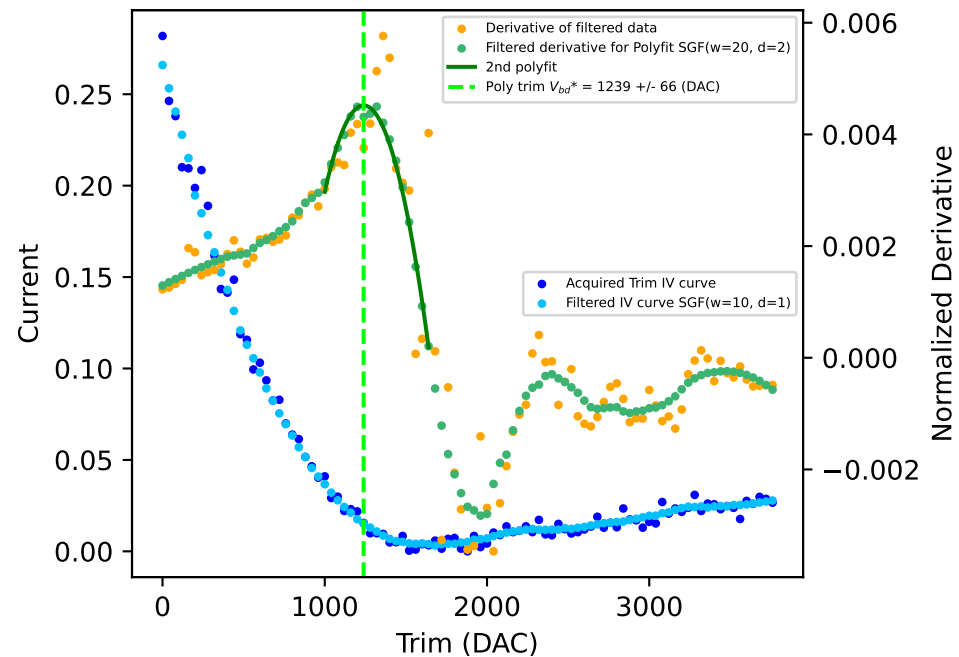
Bias DAC-VOLT conversion



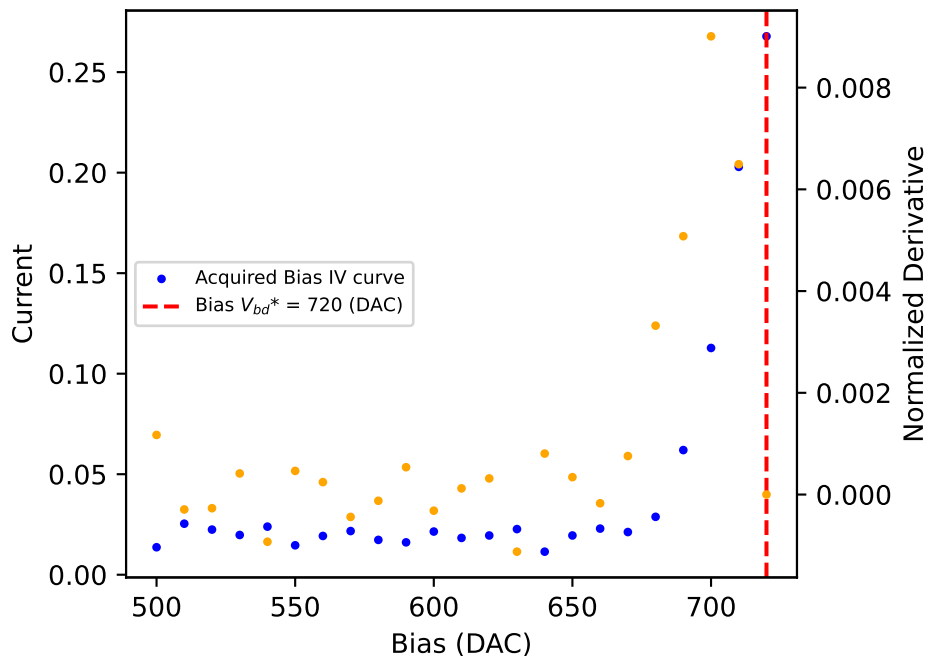
Bias + Trim REV IV curve



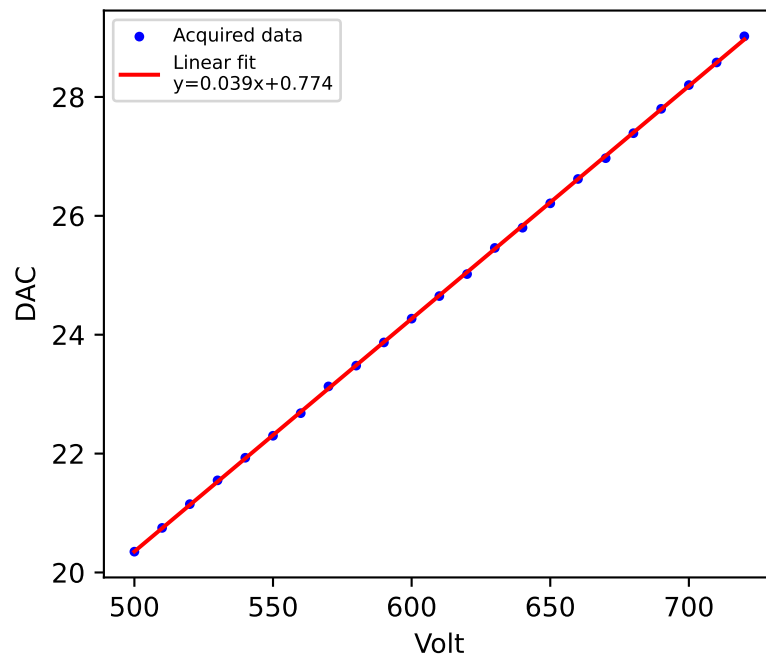
Trim REV IV curve



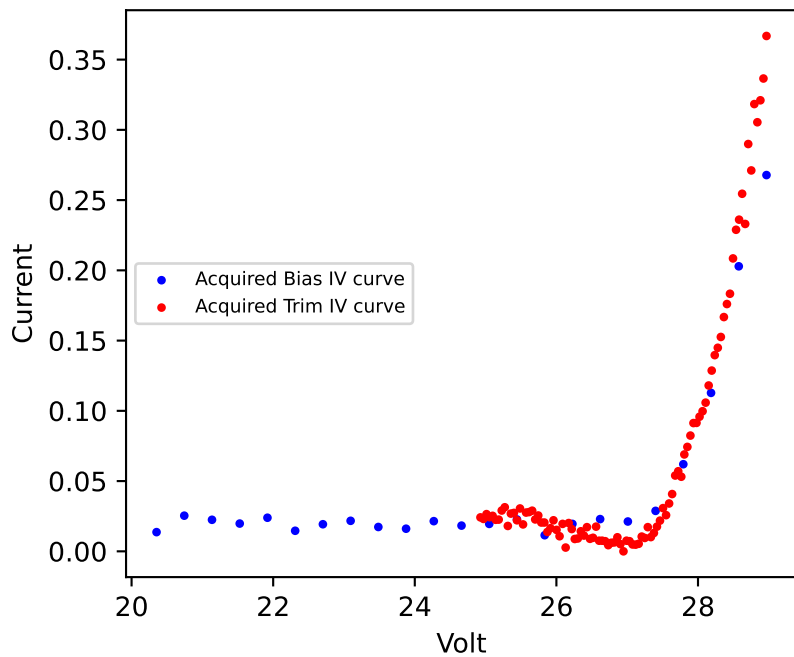
Bias REV IV curve



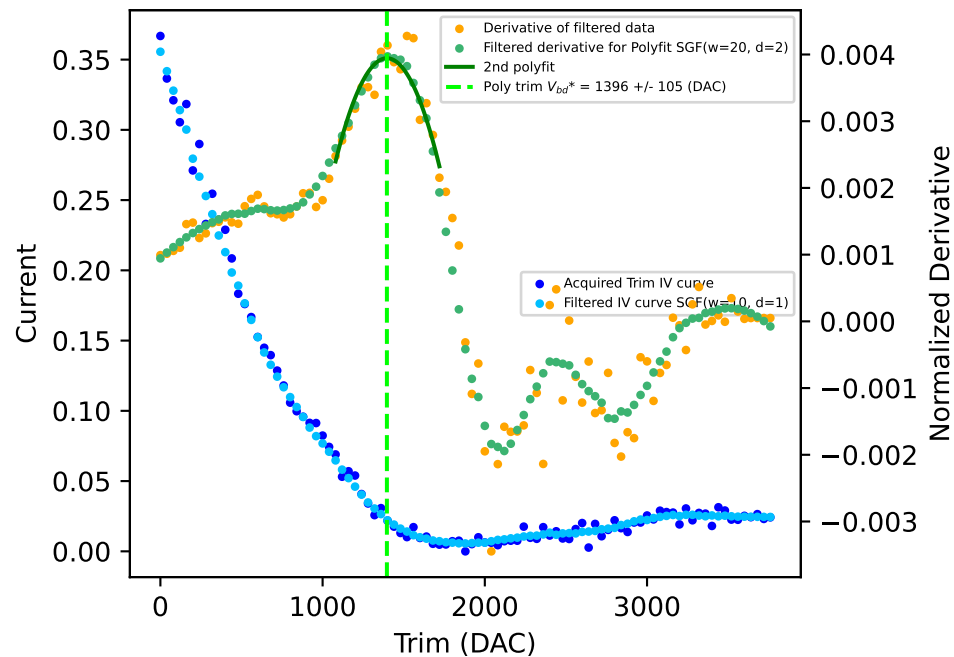
Bias DAC-VOLT conversion



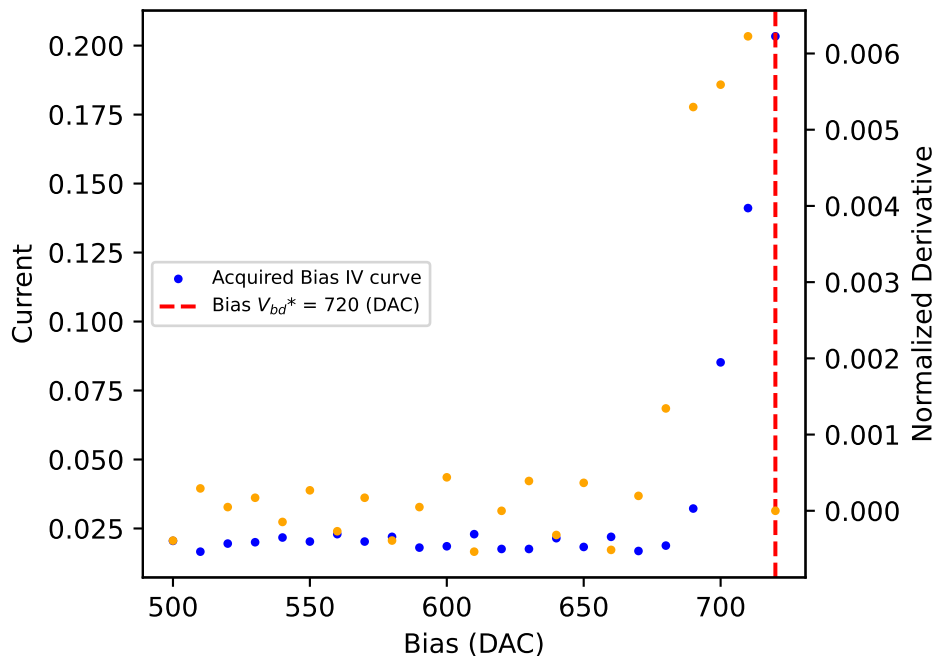
Bias + Trim REV IV curve



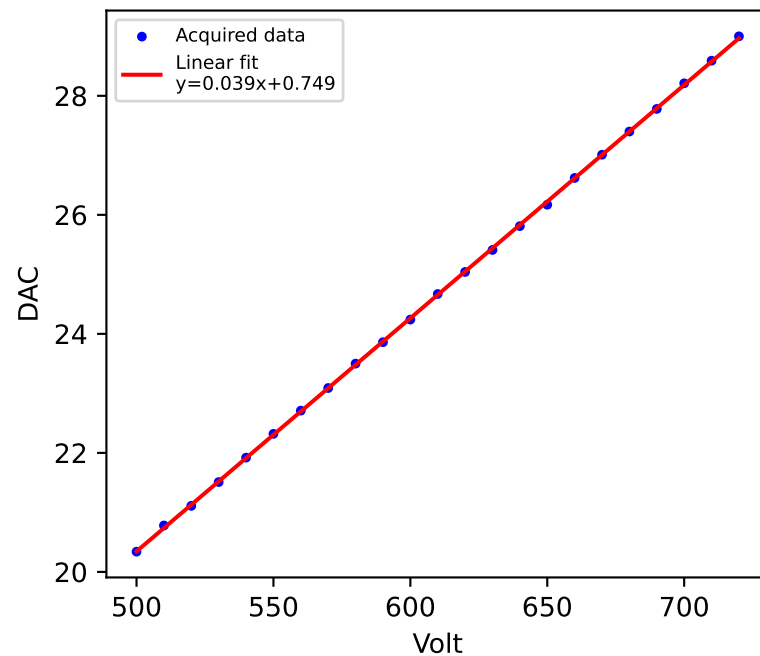
Trim REV IV curve



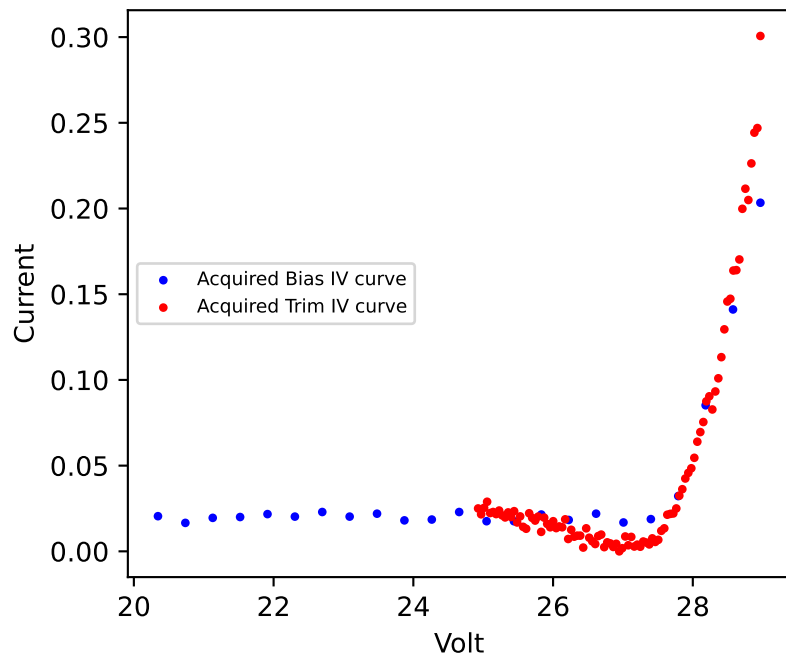
Bias REV IV curve



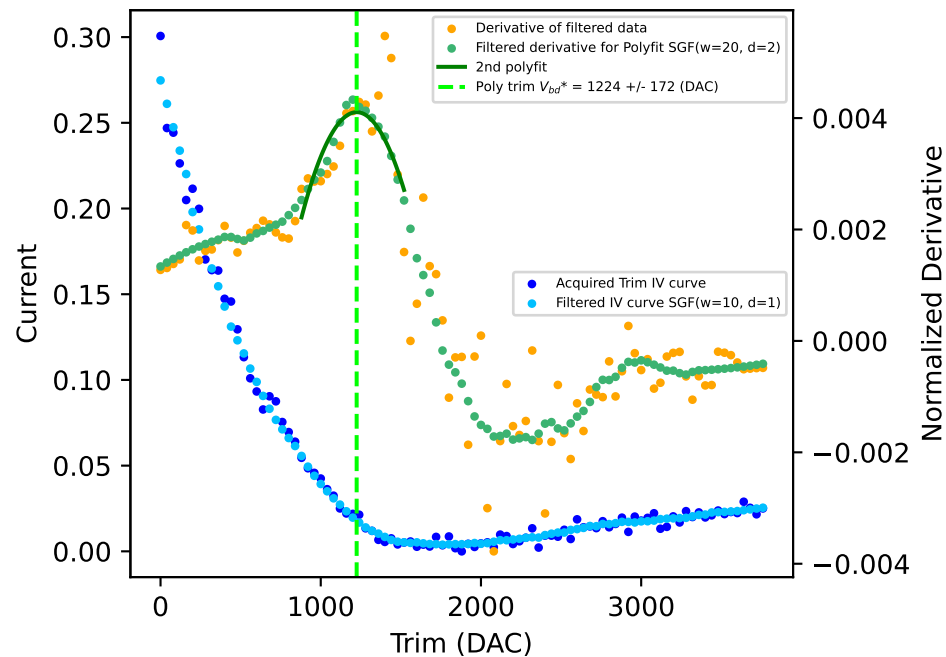
Bias DAC-VOLT conversion



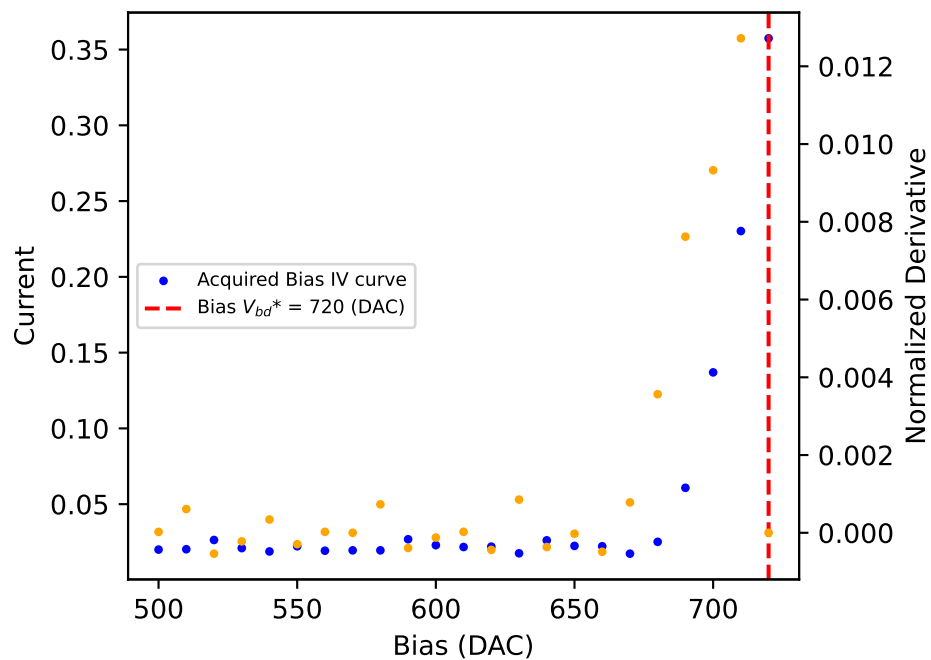
Bias + Trim REV IV curve



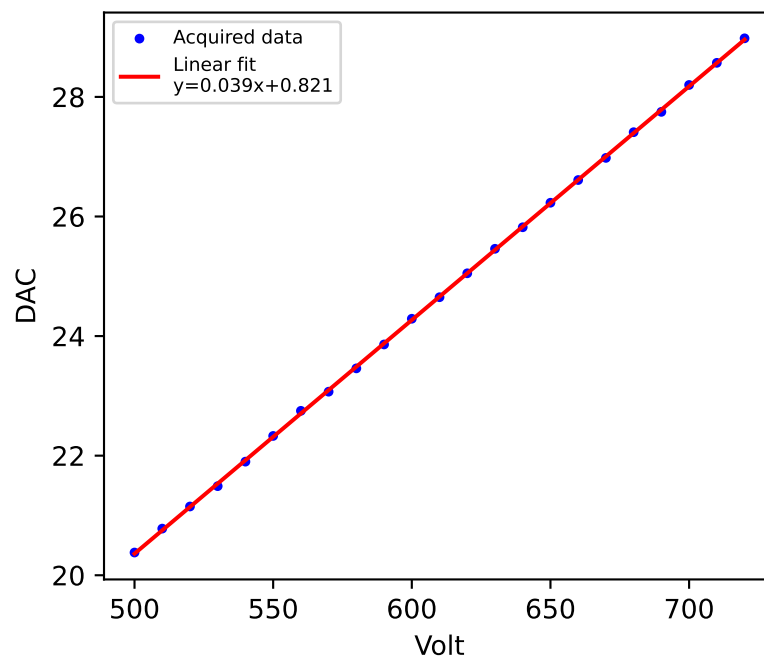
Trim REV IV curve



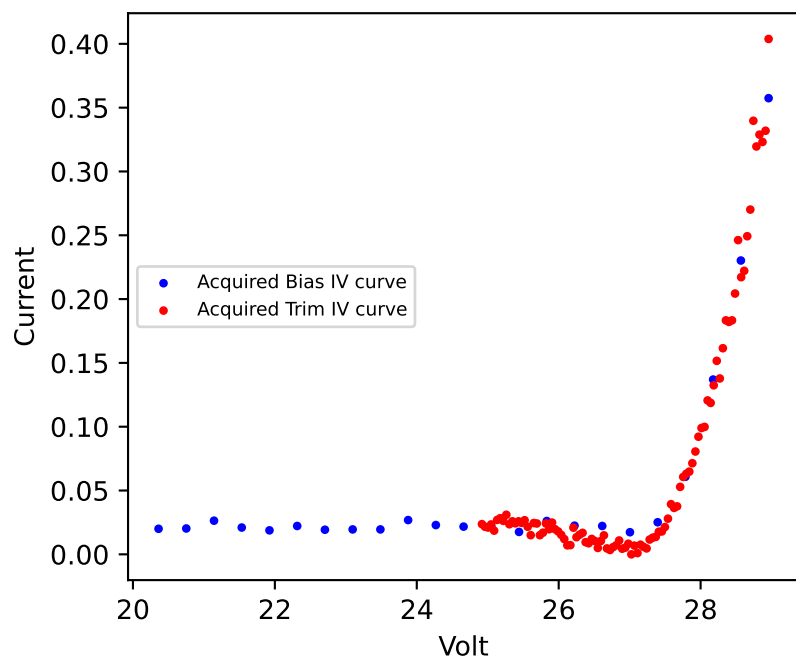
Bias REV IV curve



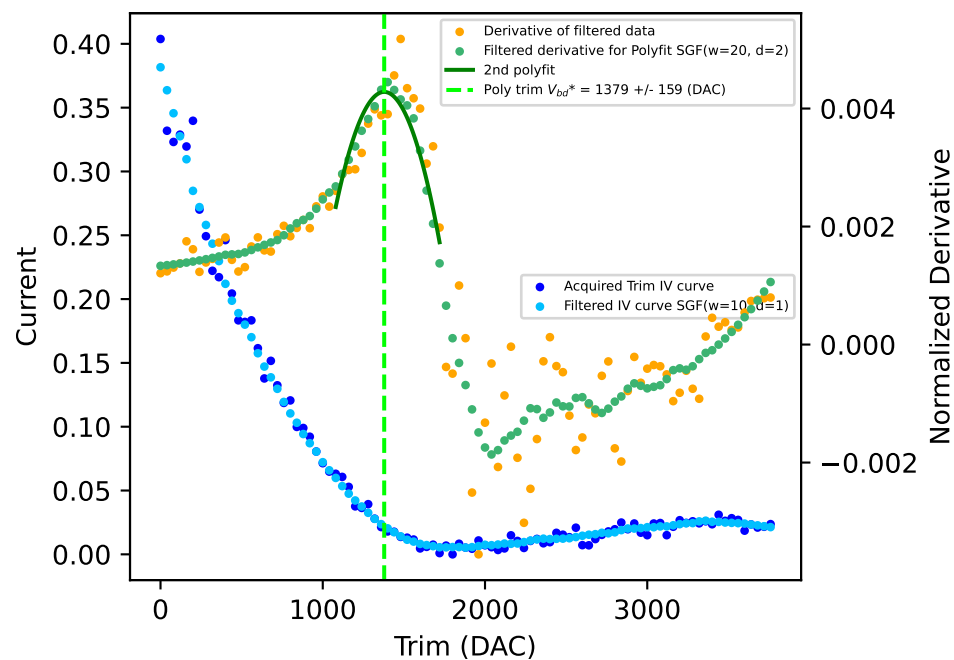
Bias DAC-VOLT conversion



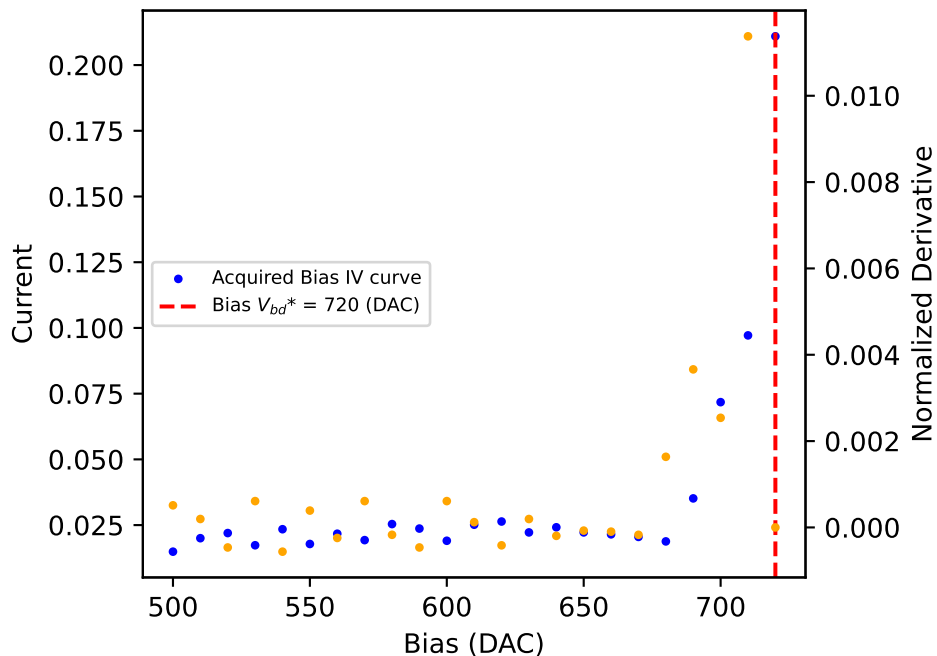
Bias + Trim REV IV curve



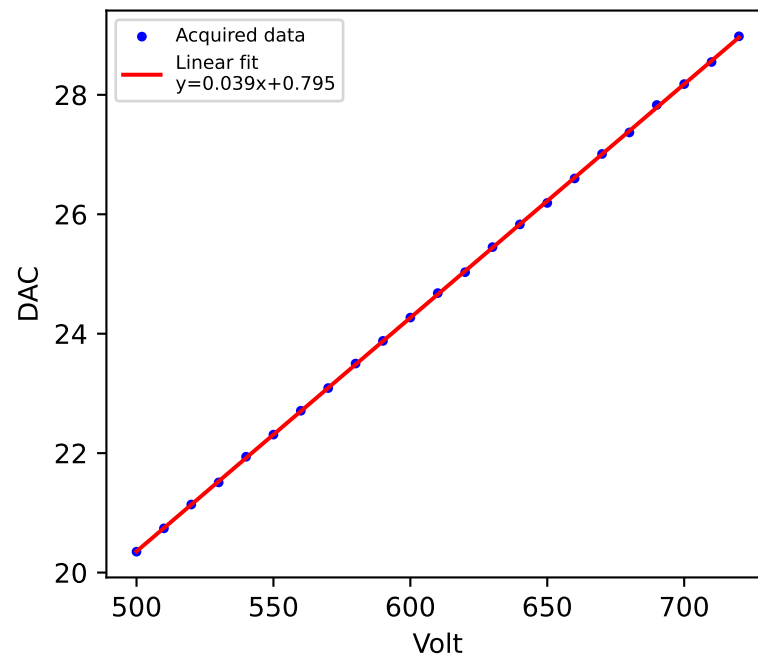
Trim REV IV curve



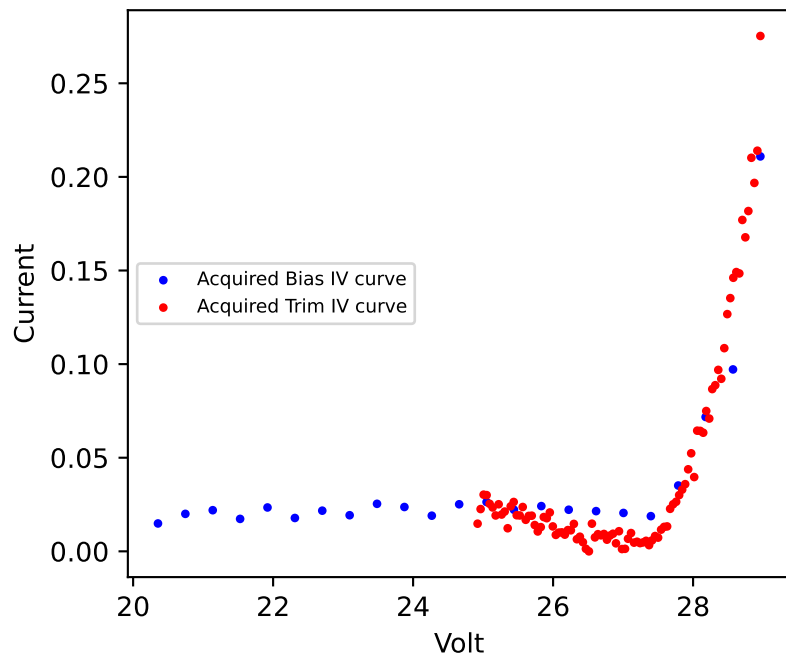
Bias REV IV curve



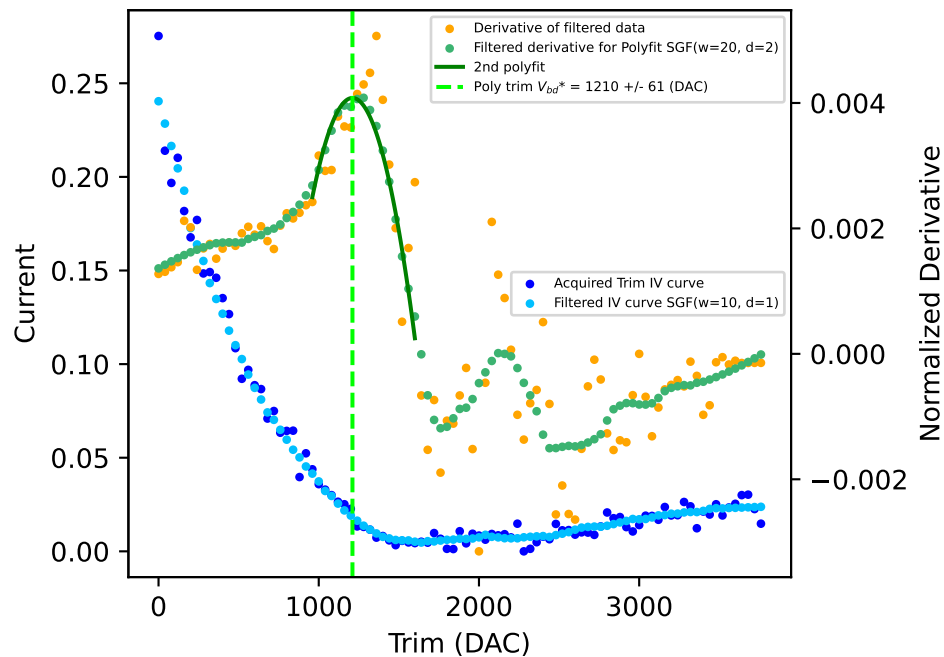
Bias DAC-VOLT conversion



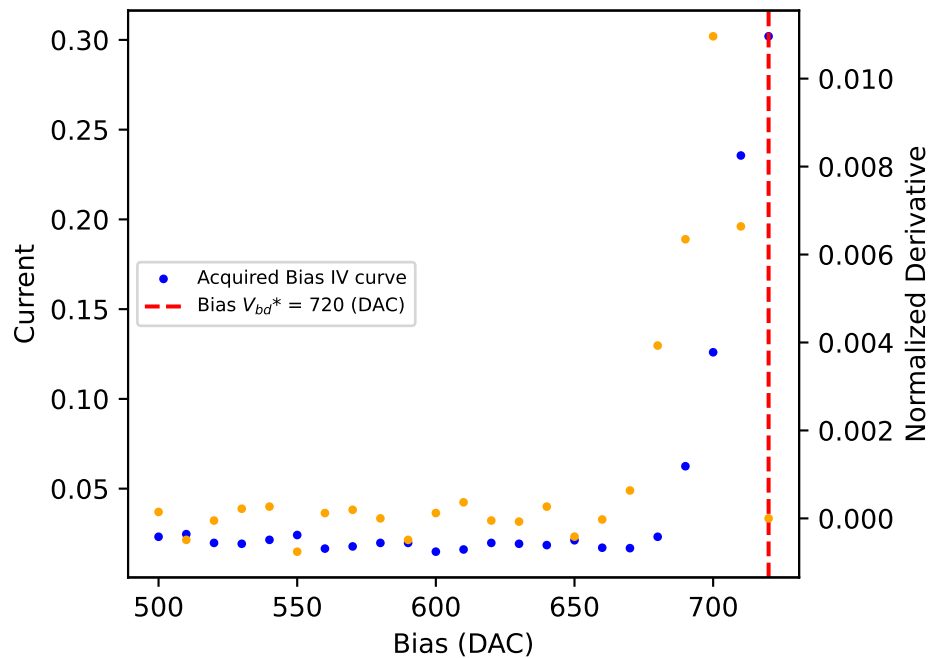
Bias + Trim REV IV curve



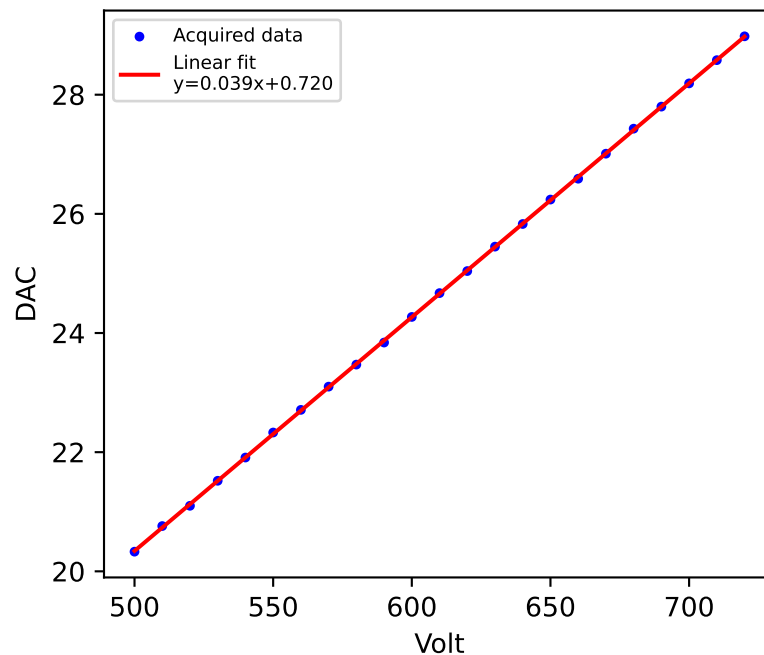
Trim REV IV curve



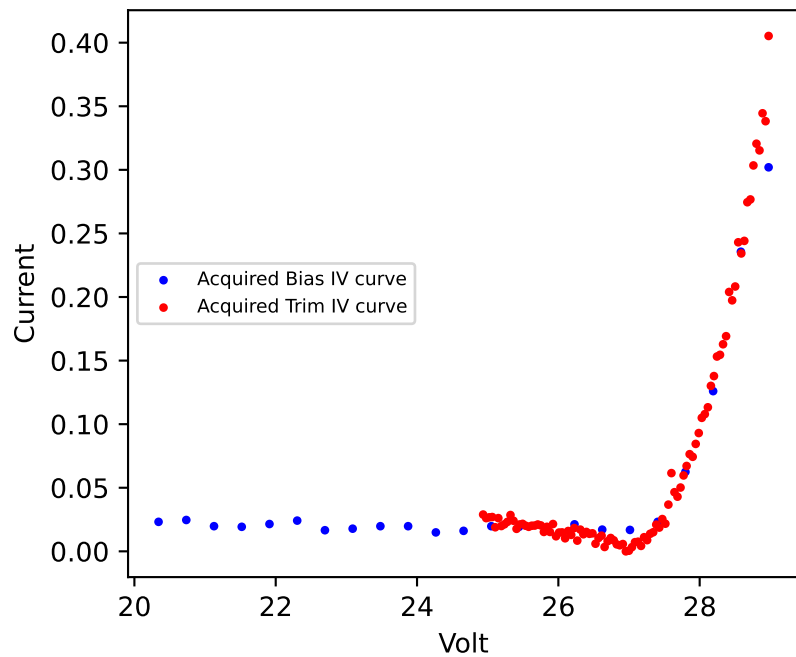
Bias REV IV curve



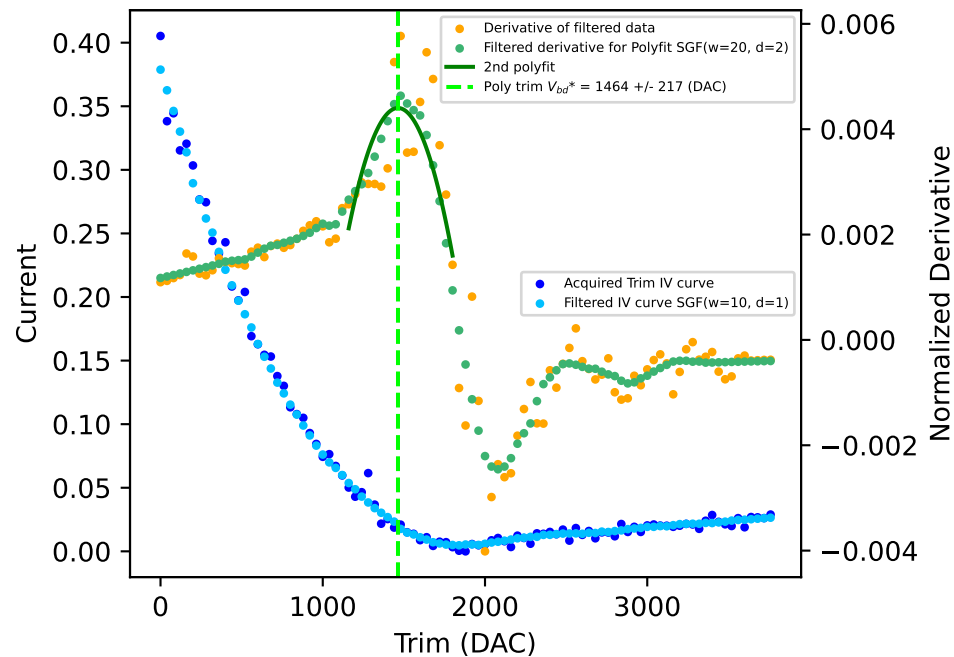
Bias DAC-VOLT conversion



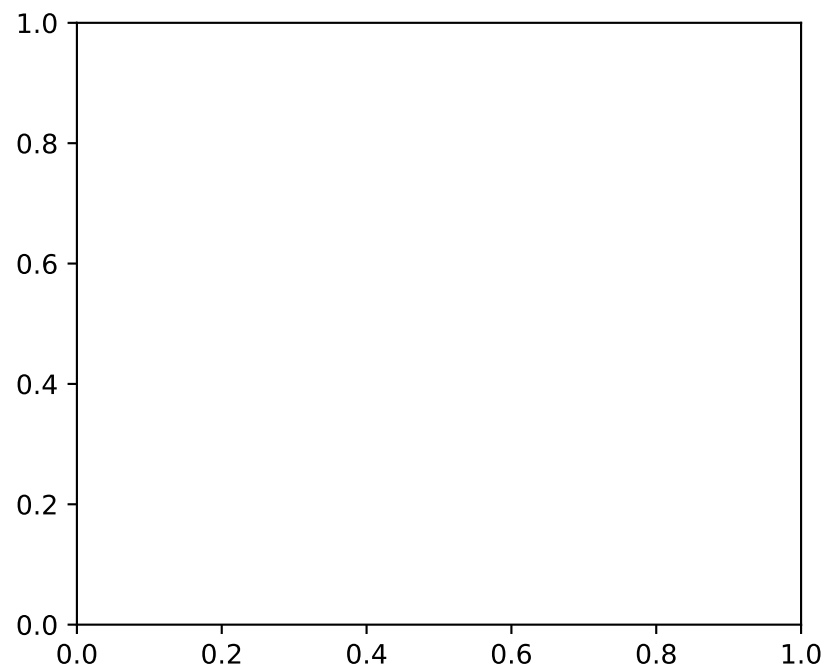
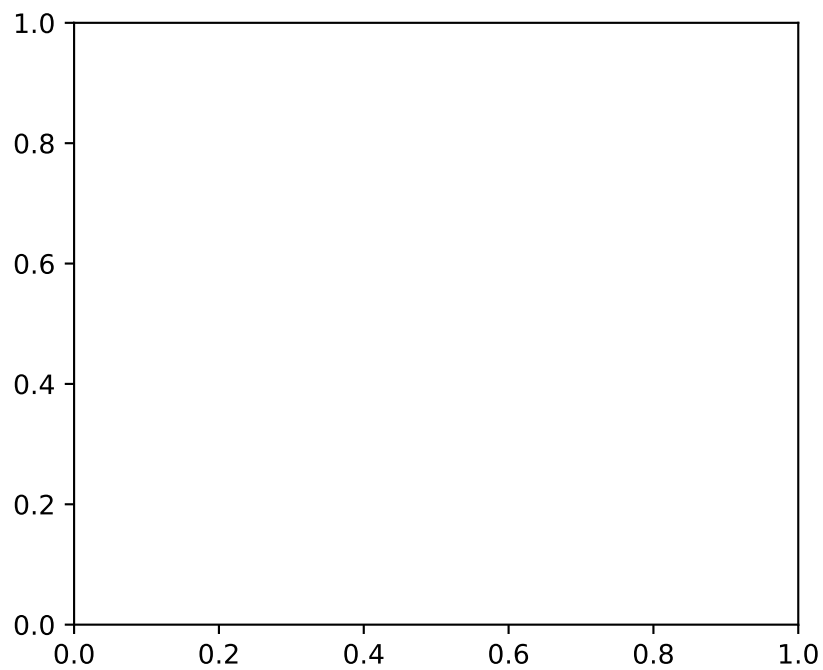
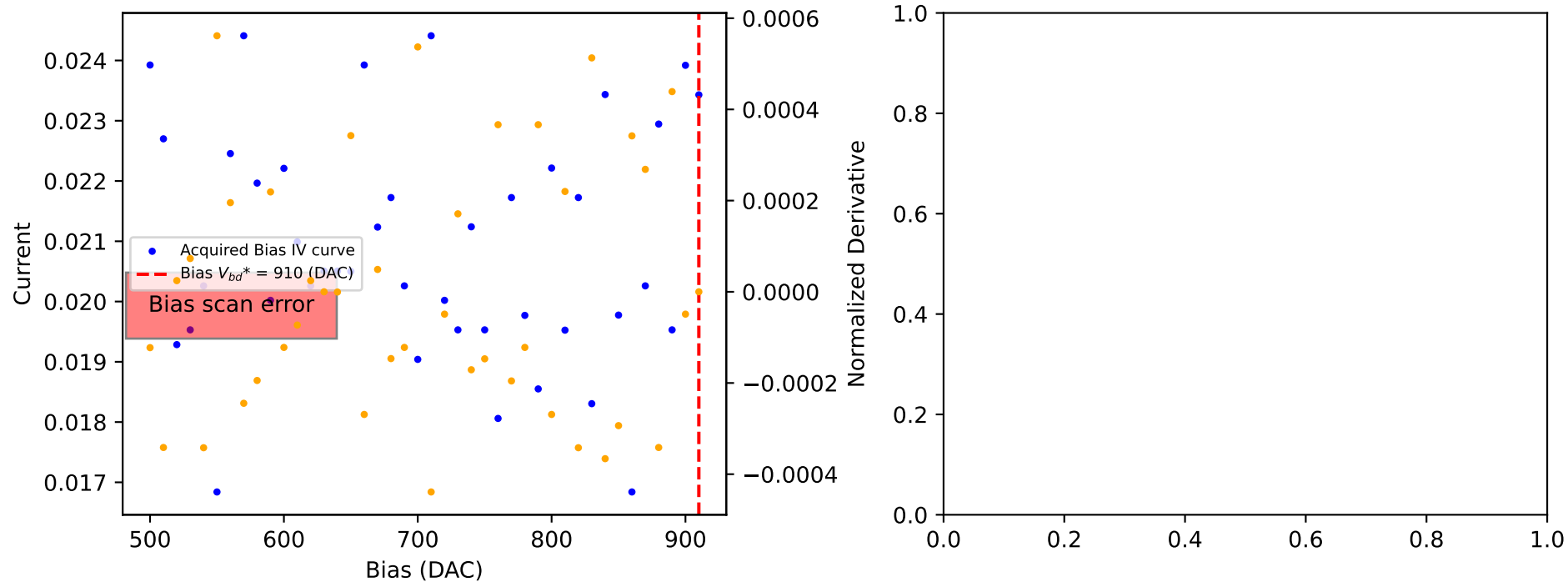
Bias + Trim REV IV curve



Trim REV IV curve



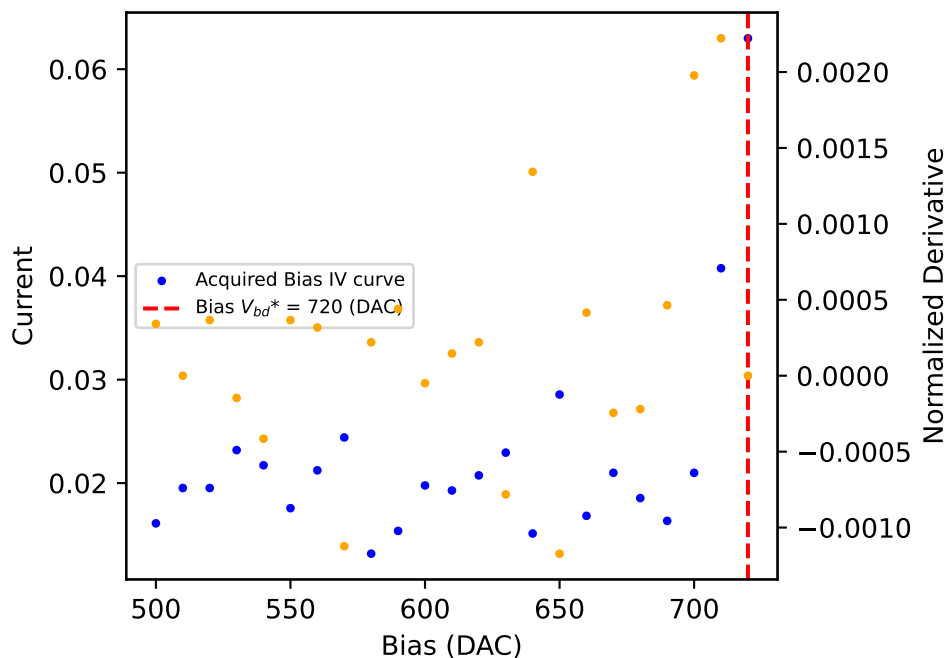
Bias REV IV curve



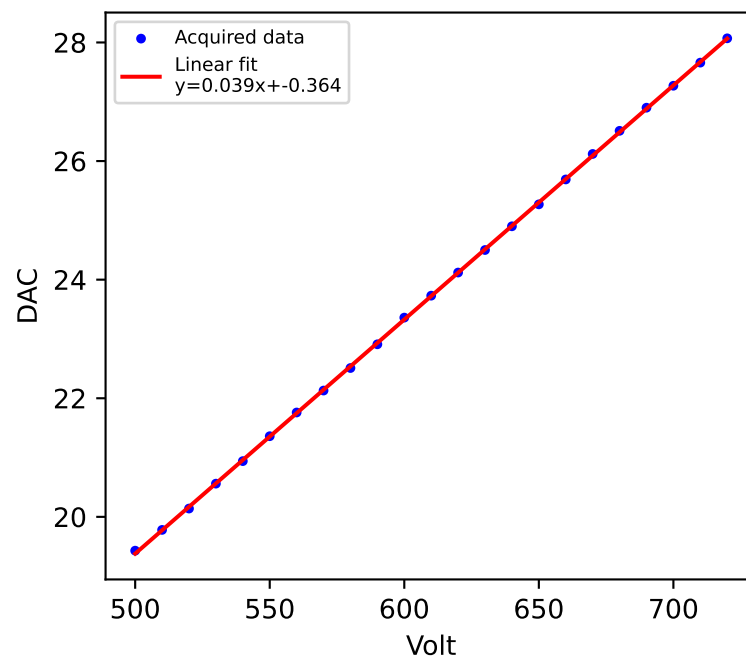
ENDPOINT:109 APA:2 AFE:1 Config_CH:9 CH_daq:11 SiPM:FBK --> MISSING FILE



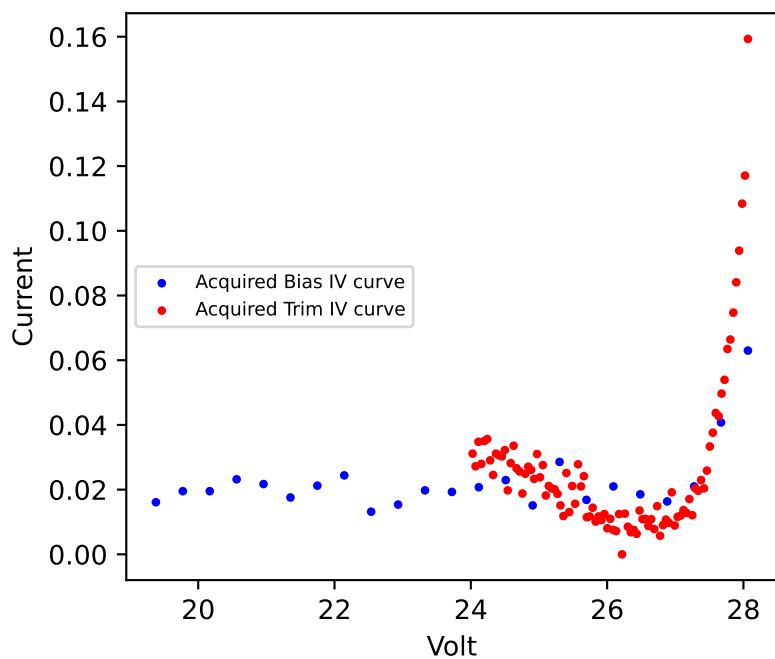
Bias REV IV curve



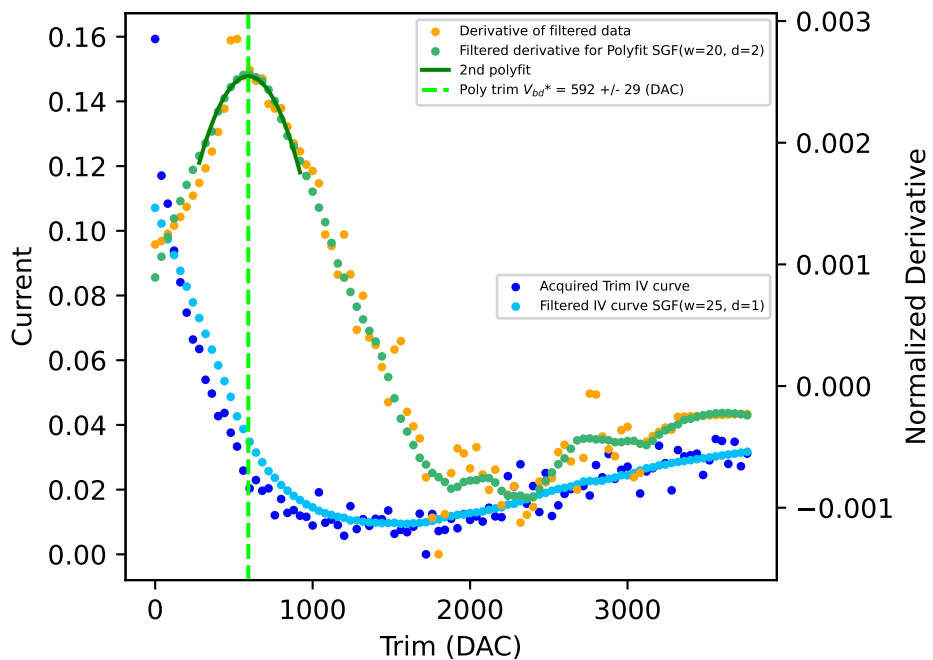
Bias DAC-VOLT conversion



Bias + Trim REV IV curve



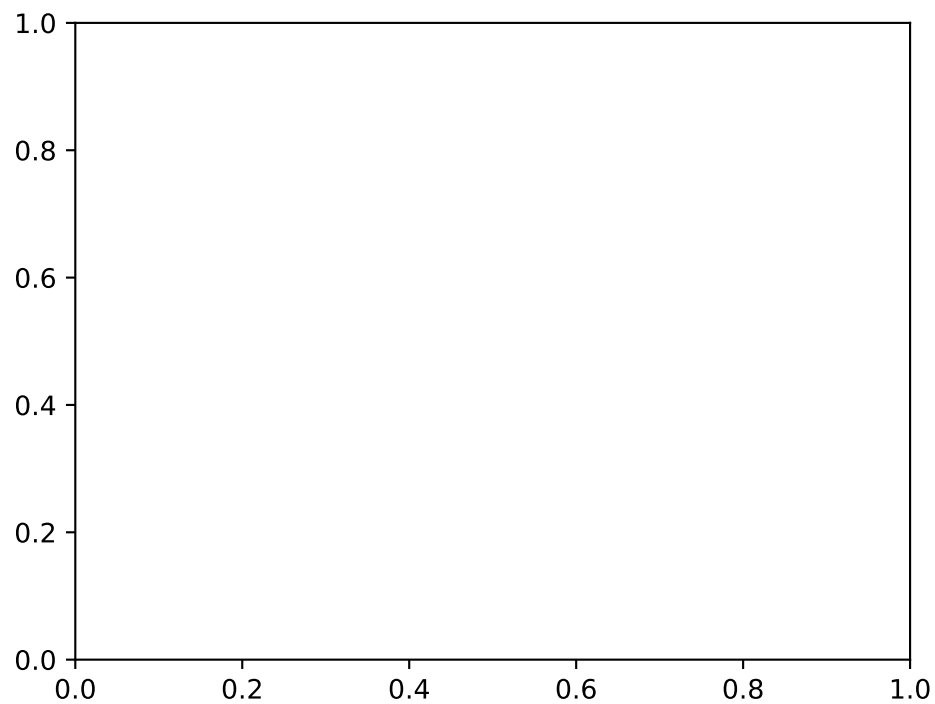
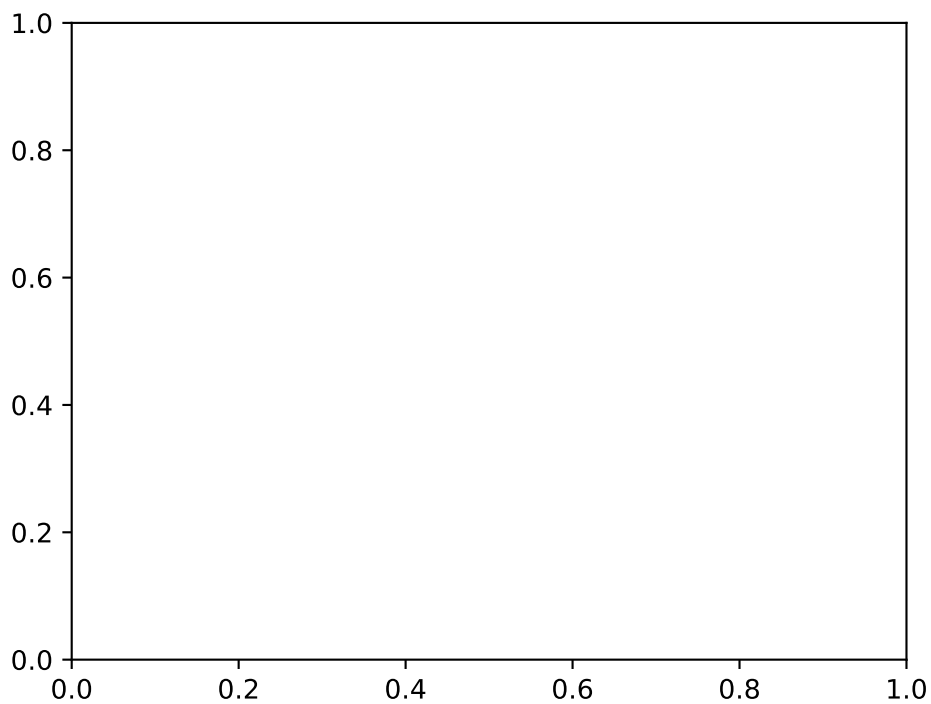
Trim REV IV curve



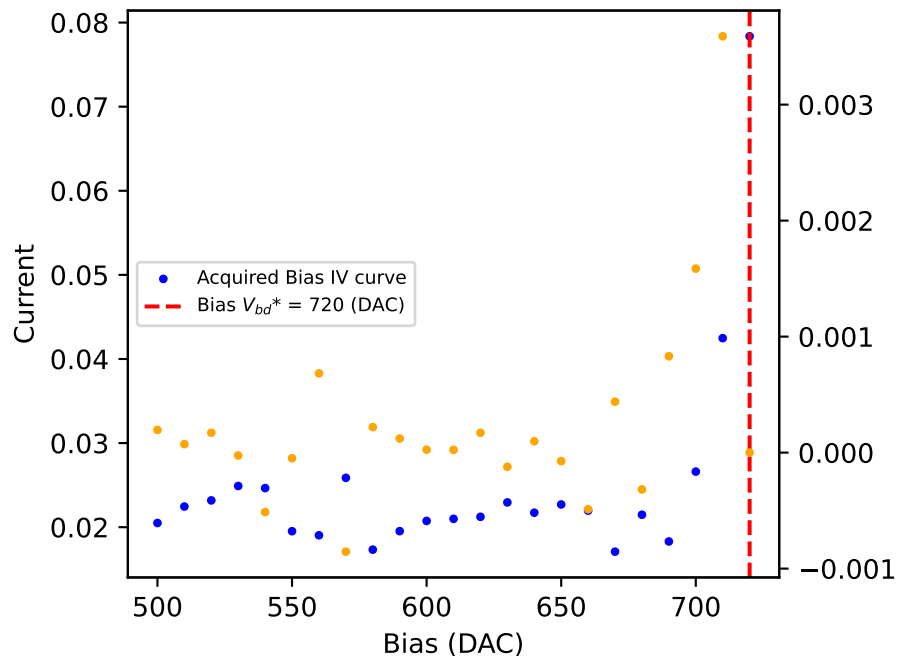
ENDPOINT:109 APA:2 AFE:1 Config_CH:11 CH_daq:13 SiPM:FBK --> MISSING FILE



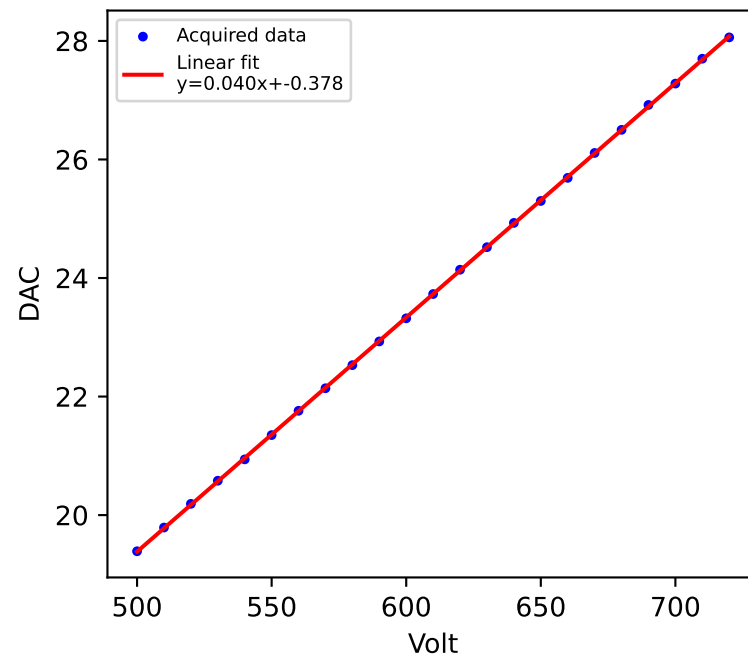
ENDPOINT:109 APA:2 AFE:1 Config_CH:12 CH_daq:14 SiPM:FBK --> MISSING FILE



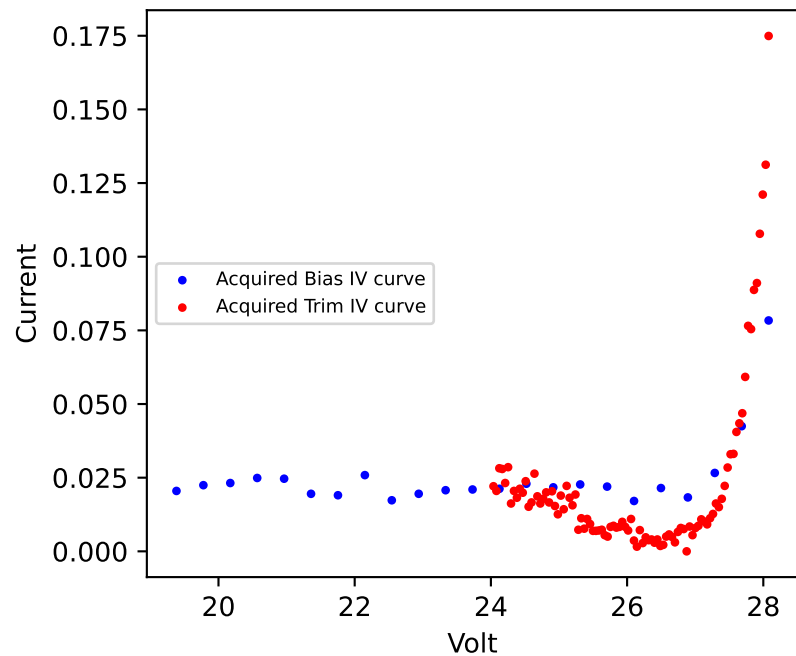
Bias REV IV curve



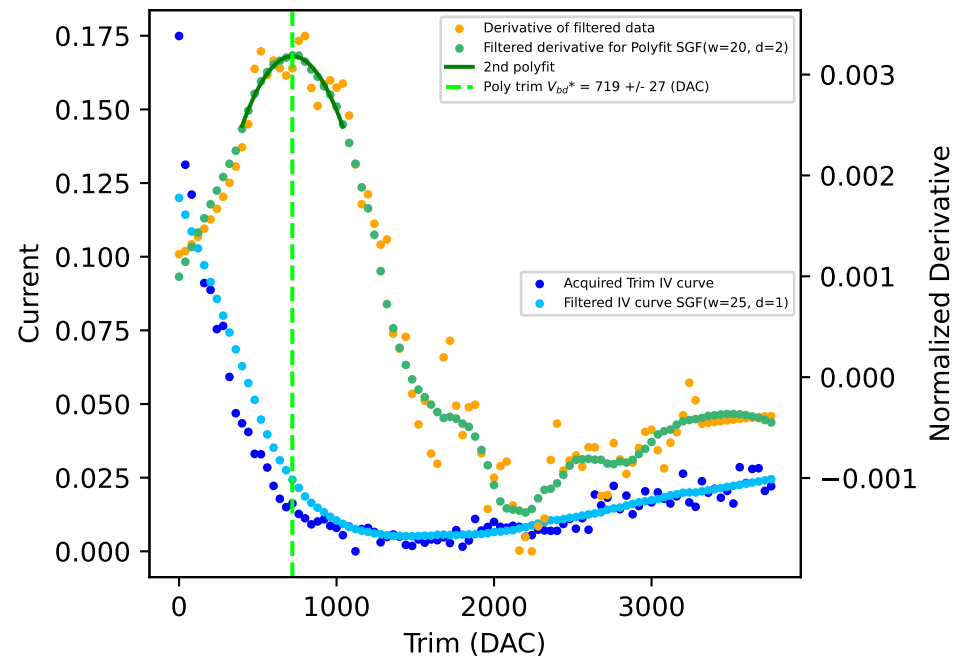
Bias DAC-VOLT conversion



Bias + Trim REV IV curve



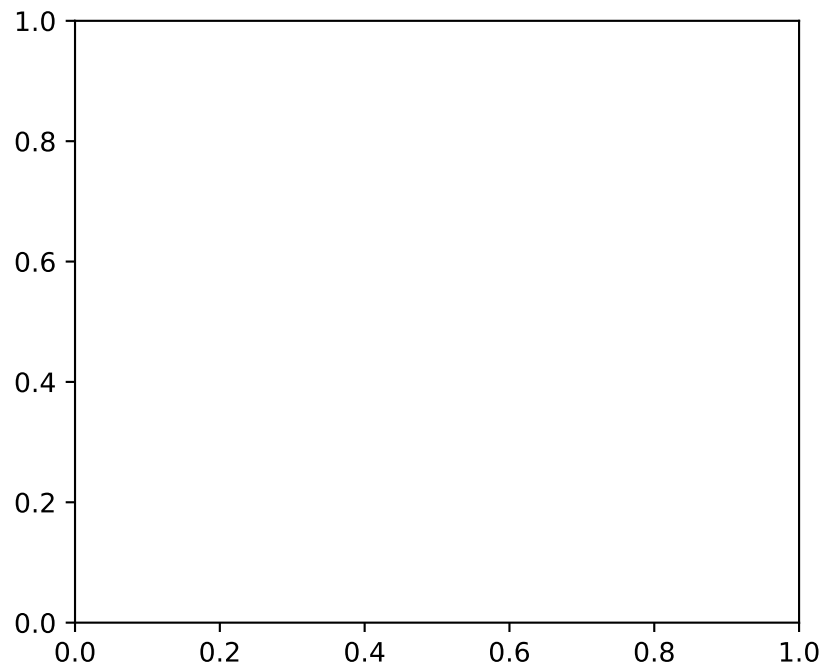
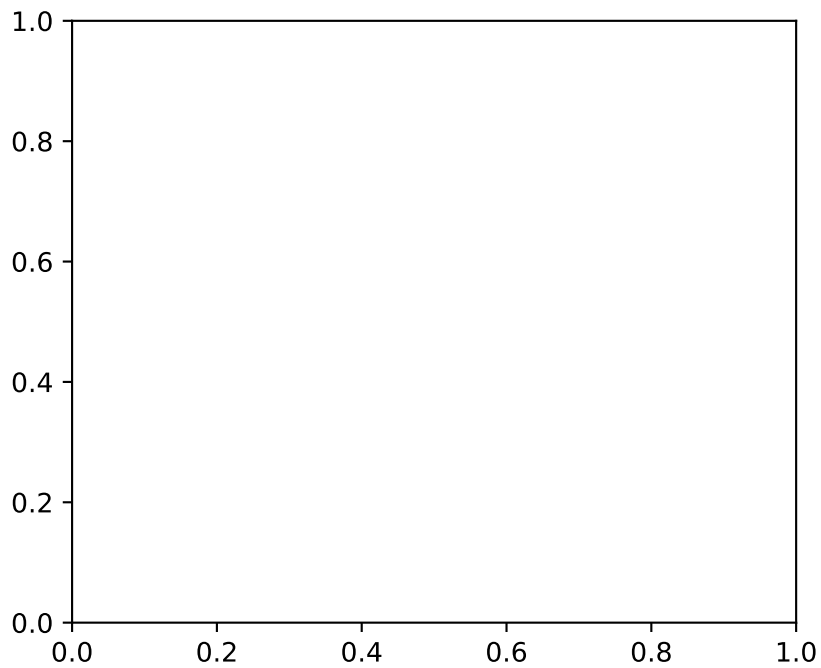
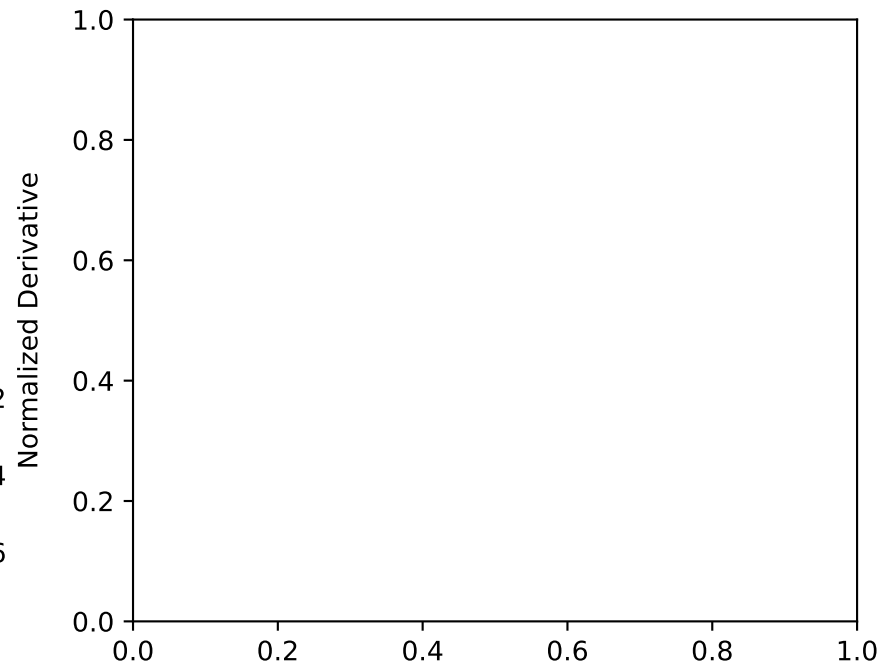
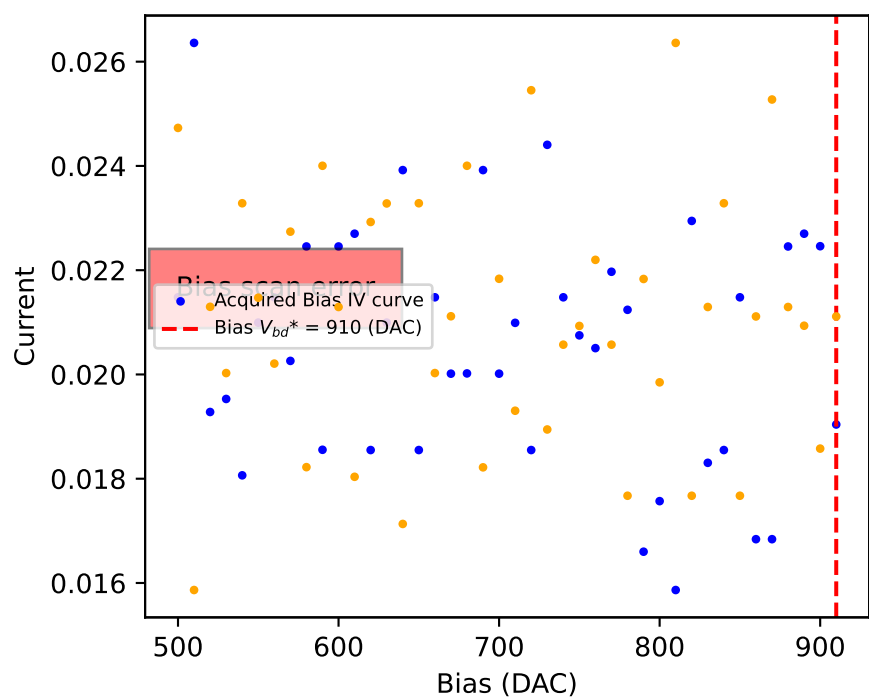
Trim REV IV curve



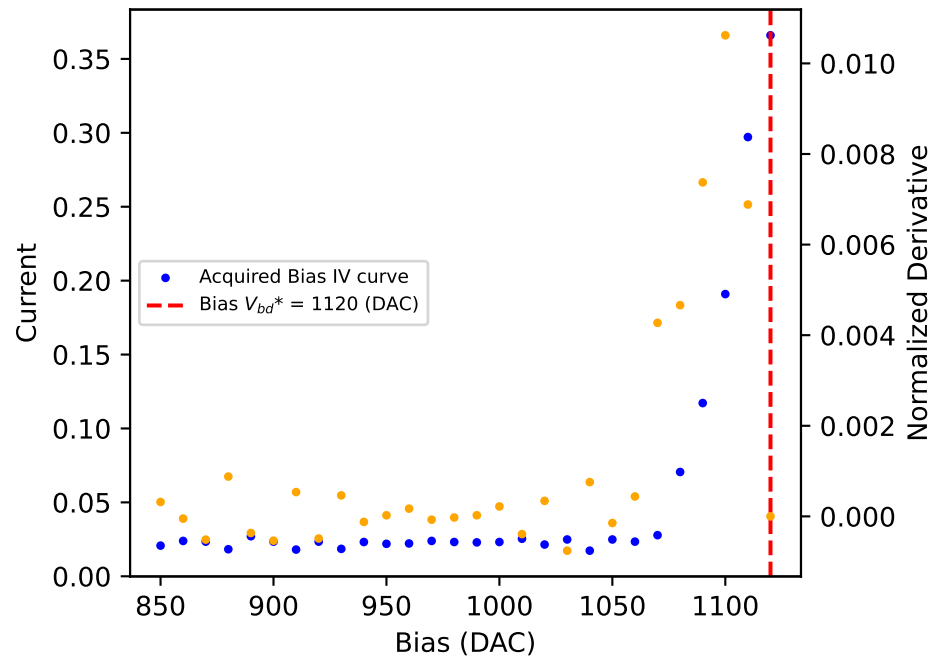
ENDPOINT:109 APA:2 AFE:1 Config_CH:14 CH_daq:16 SiPM:FBK --> MISSING FILE



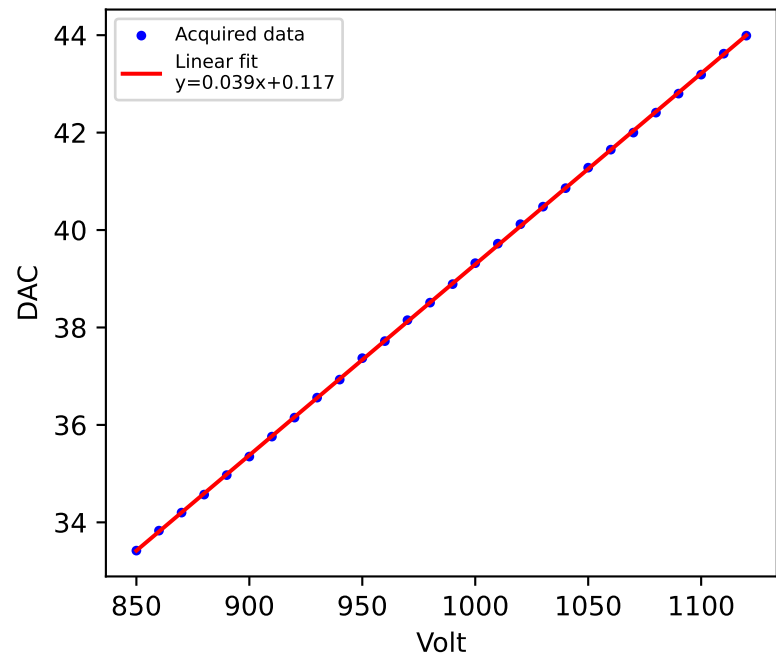
Bias REV IV curve



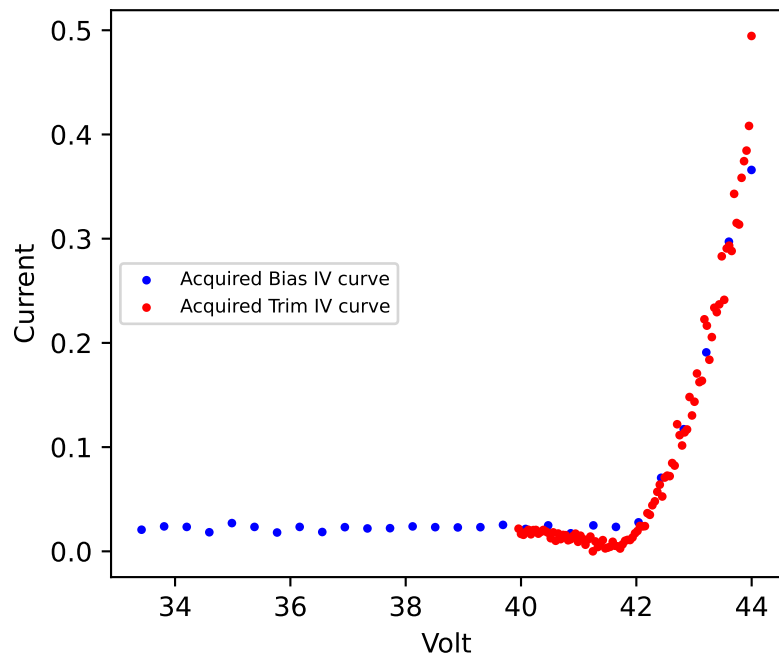
Bias REV IV curve



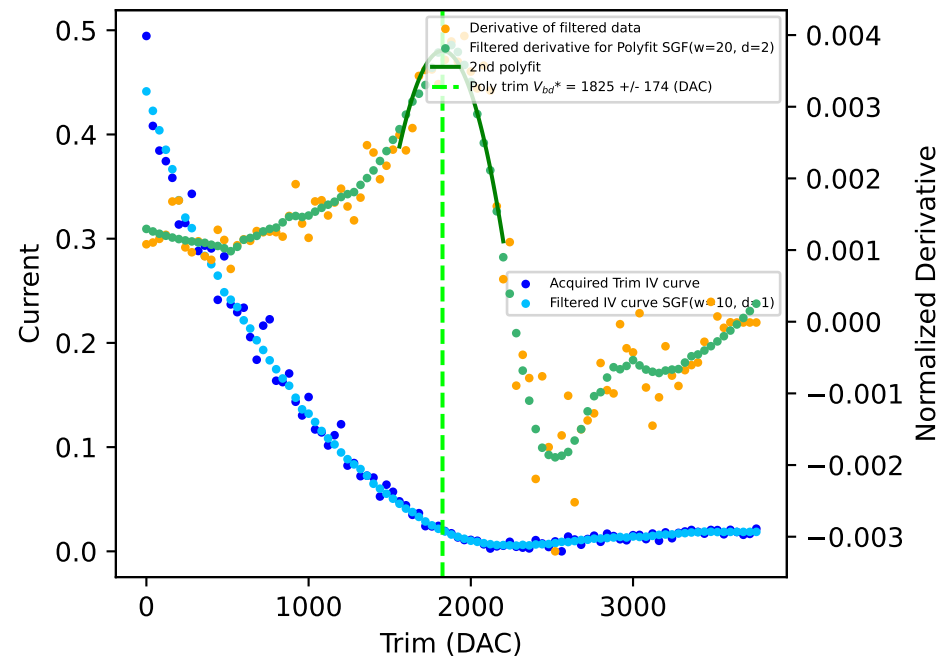
Bias DAC-VOLT conversion



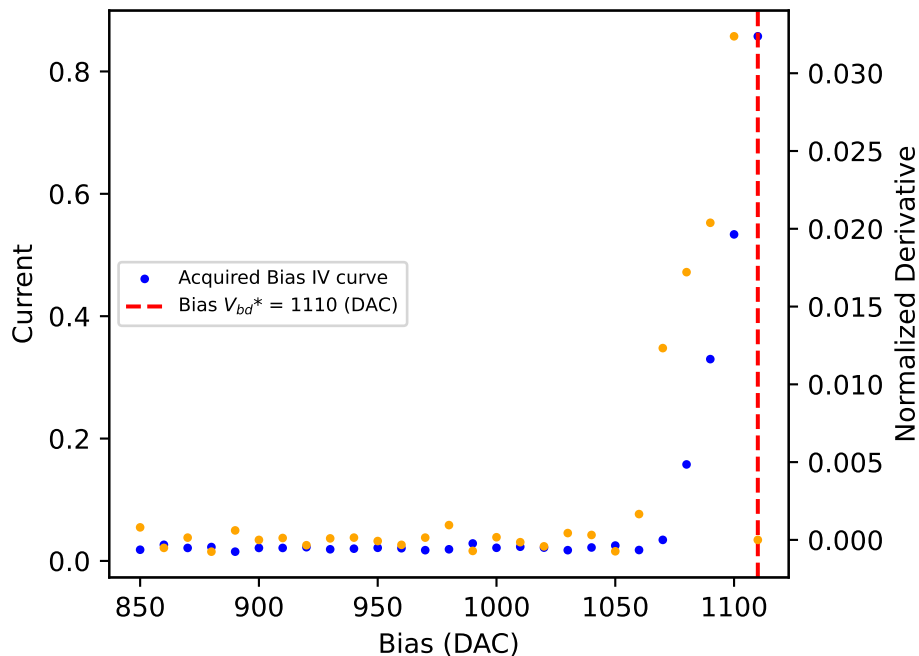
Bias + Trim REV IV curve



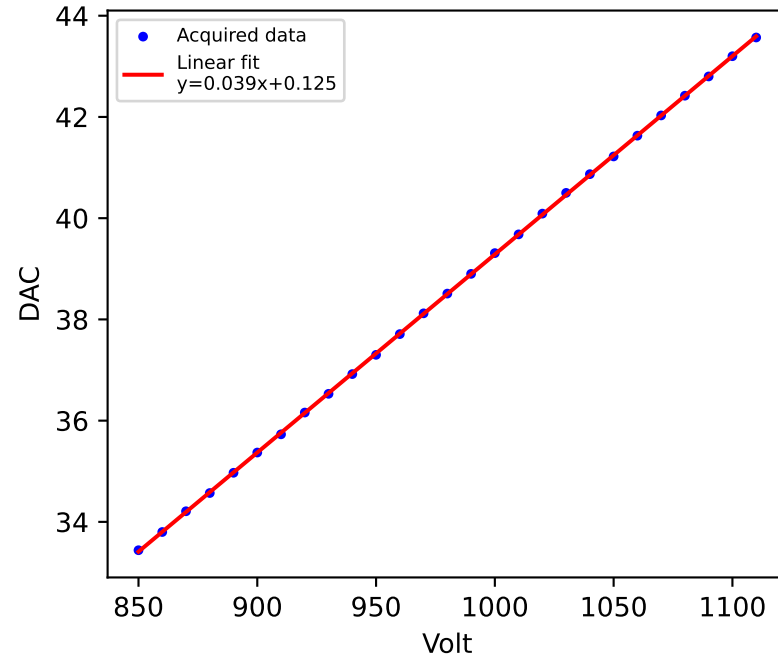
Trim REV IV curve



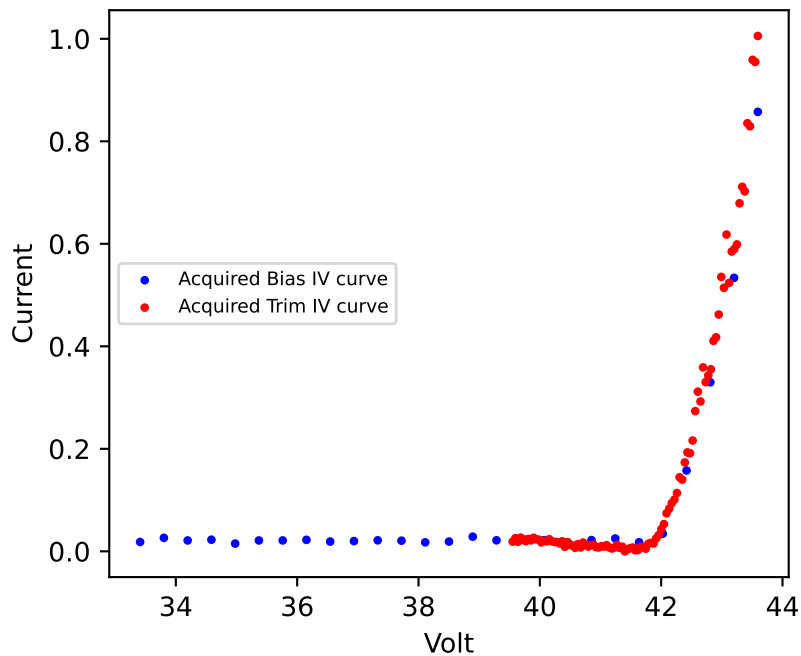
Bias REV IV curve



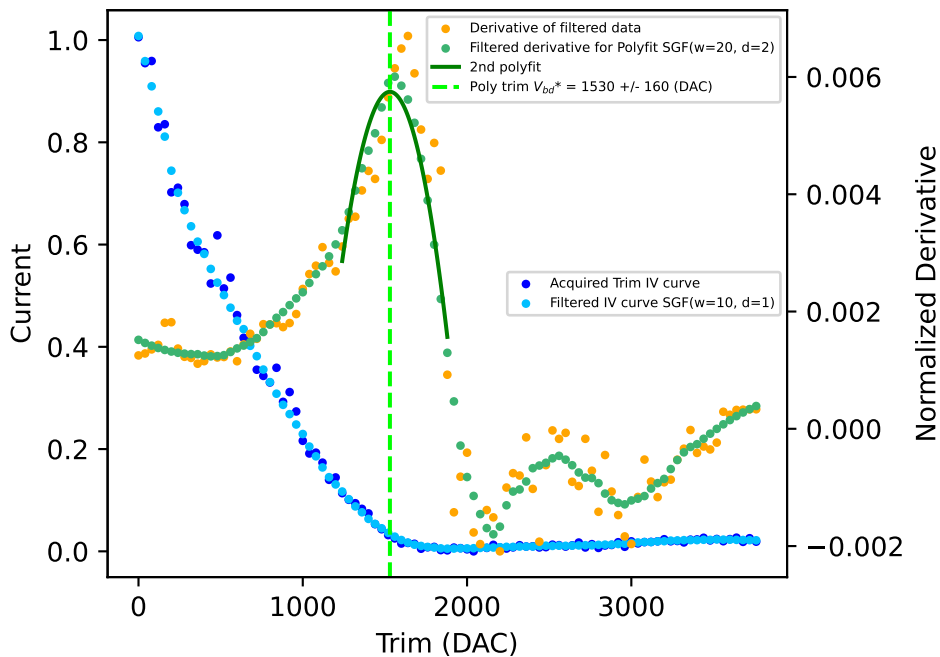
Bias DAC-VOLT conversion



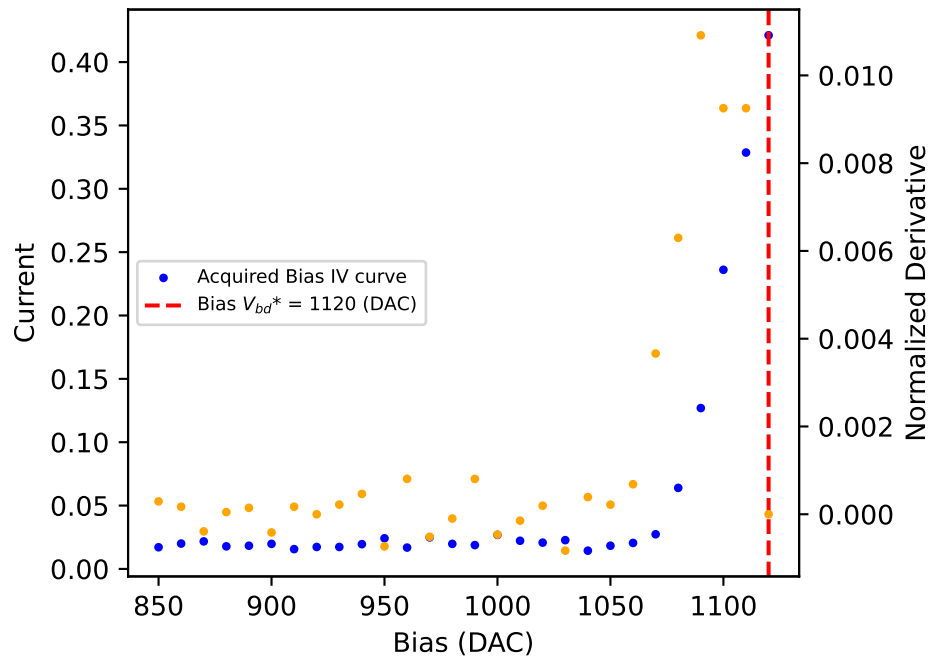
Bias + Trim REV IV curve



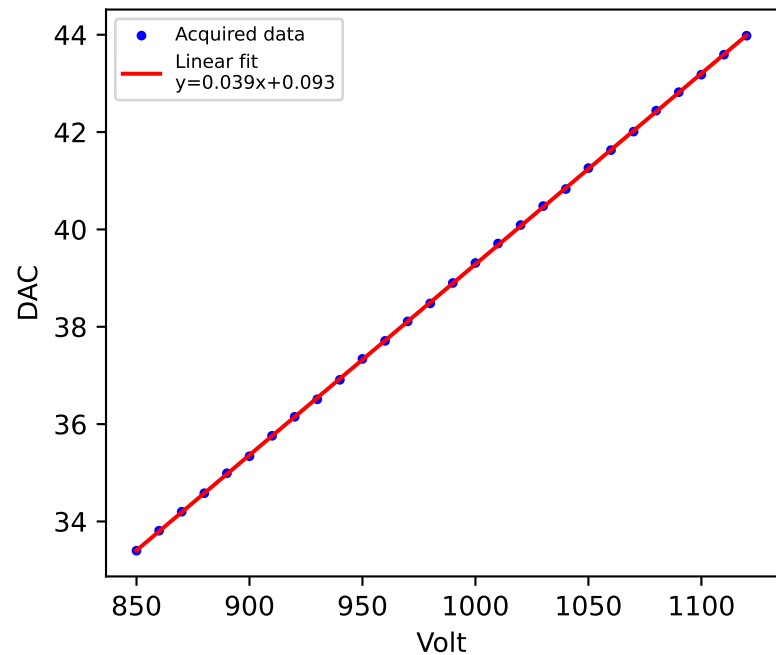
Trim REV IV curve



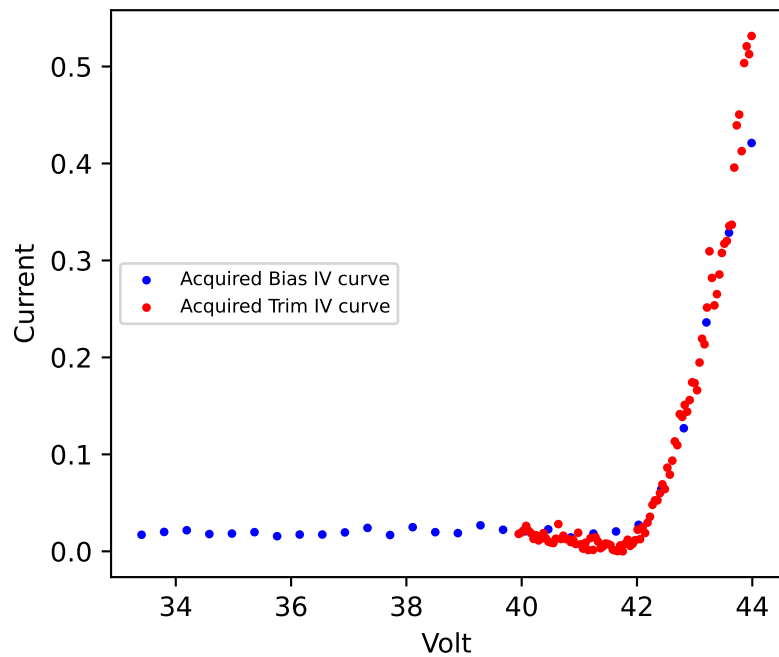
Bias REV IV curve



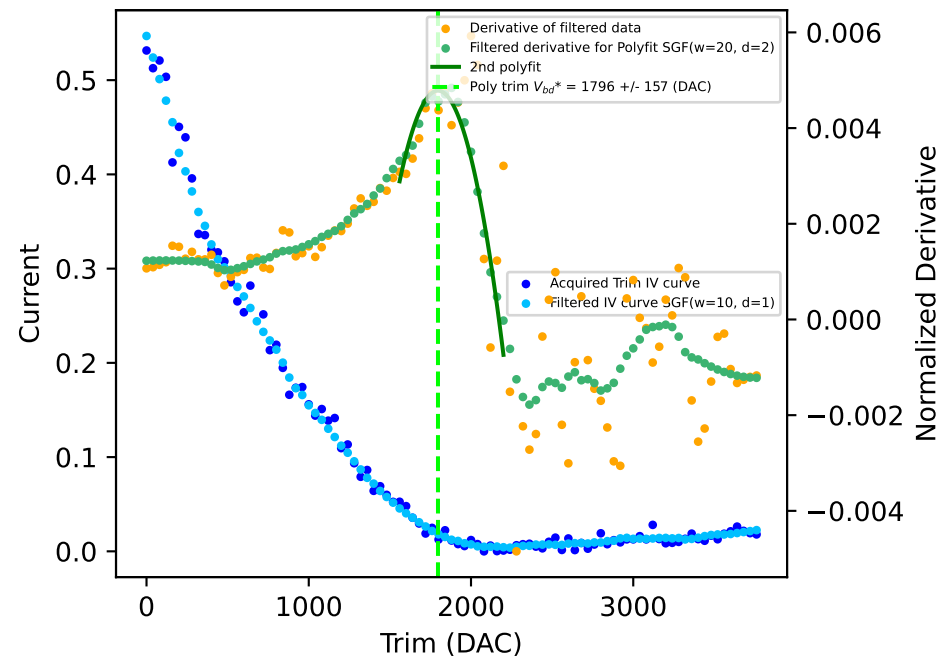
Bias DAC-VOLT conversion



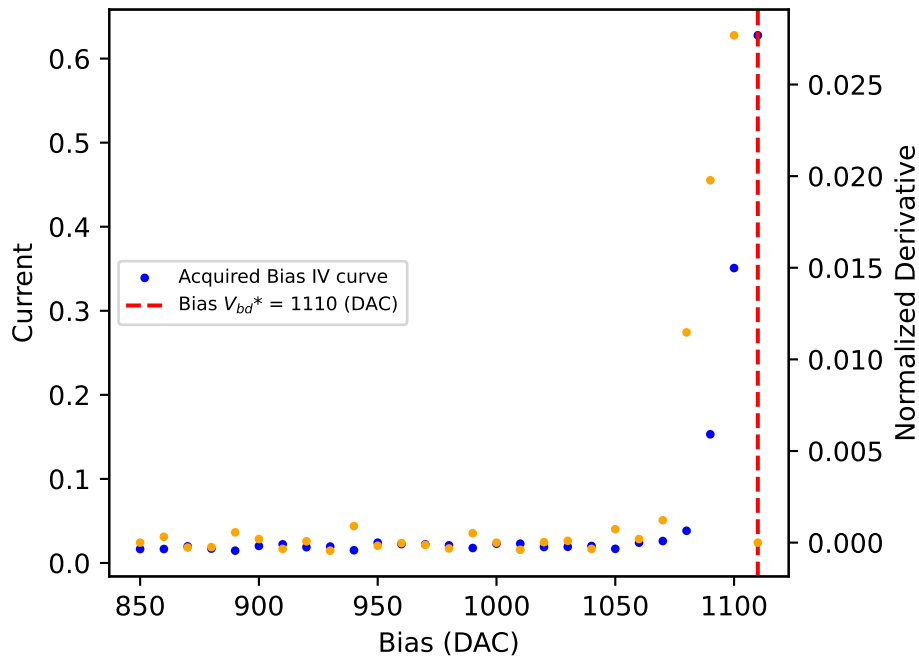
Bias + Trim REV IV curve



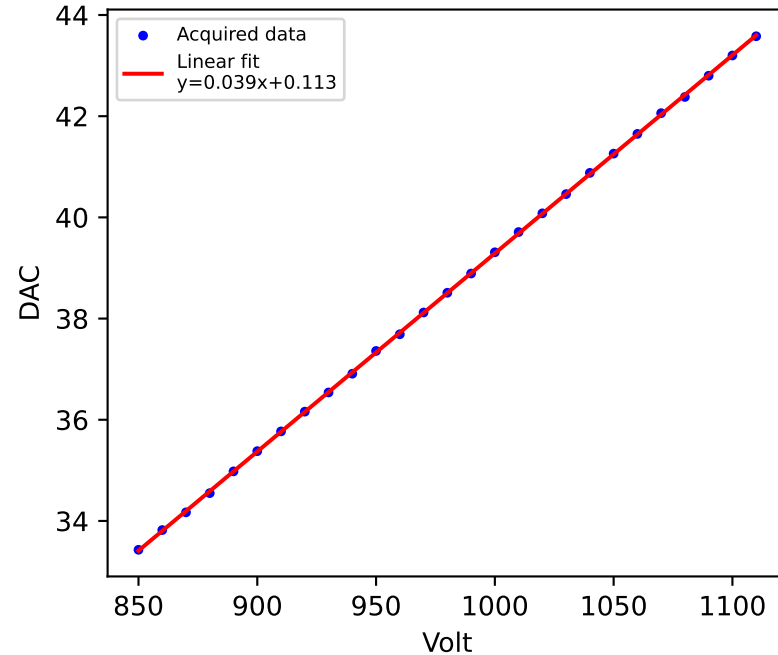
Trim REV IV curve



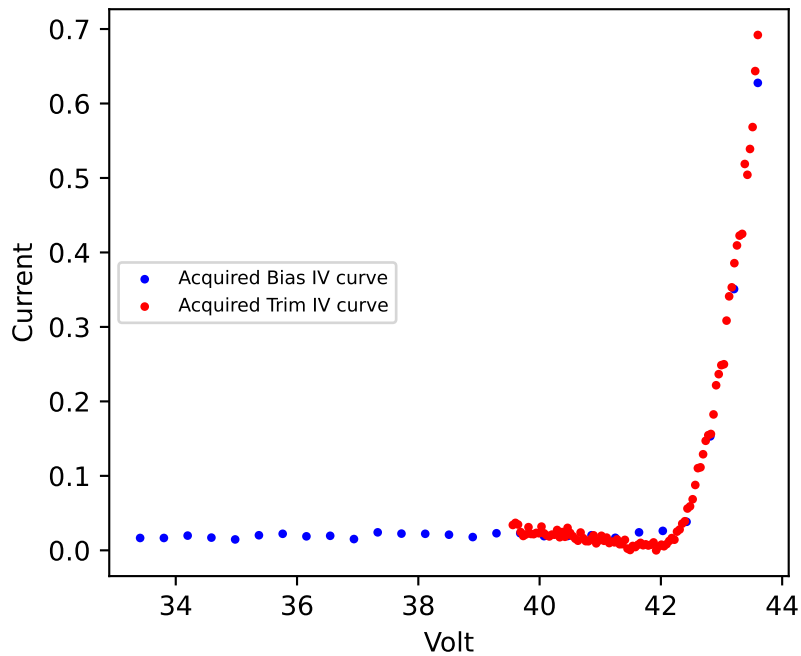
Bias REV IV curve



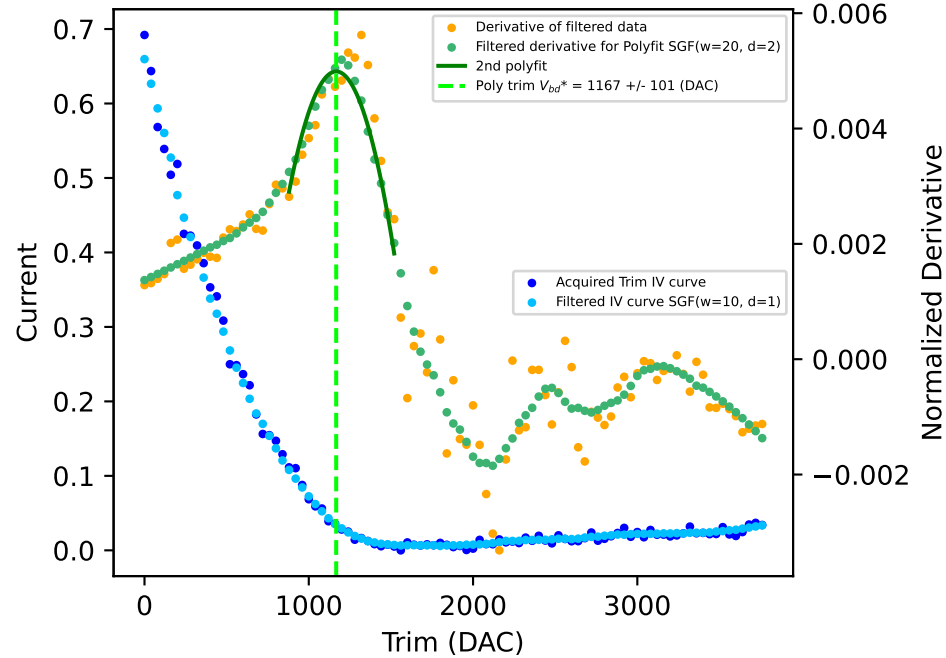
Bias DAC-VOLT conversion



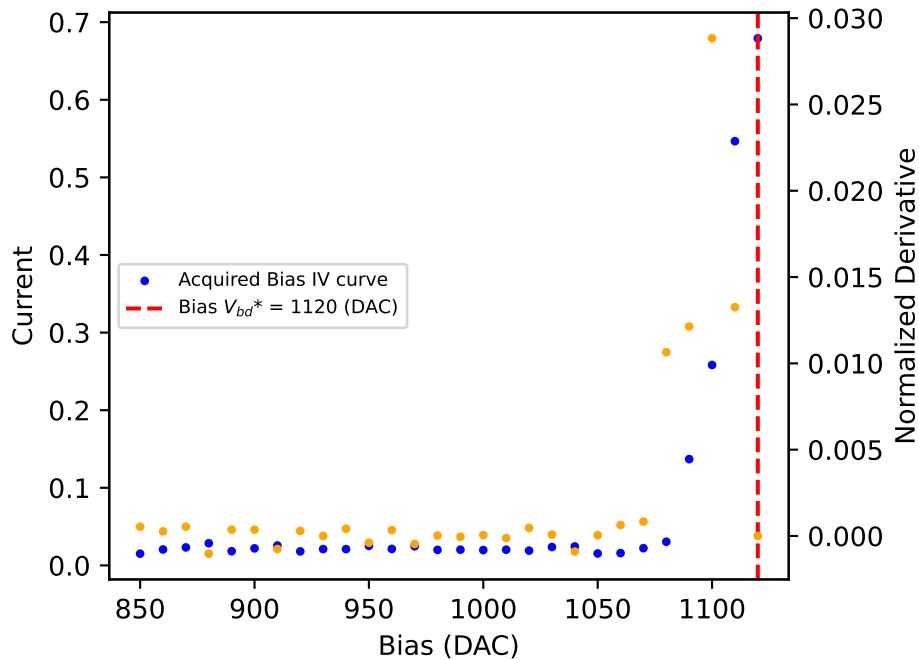
Bias + Trim REV IV curve



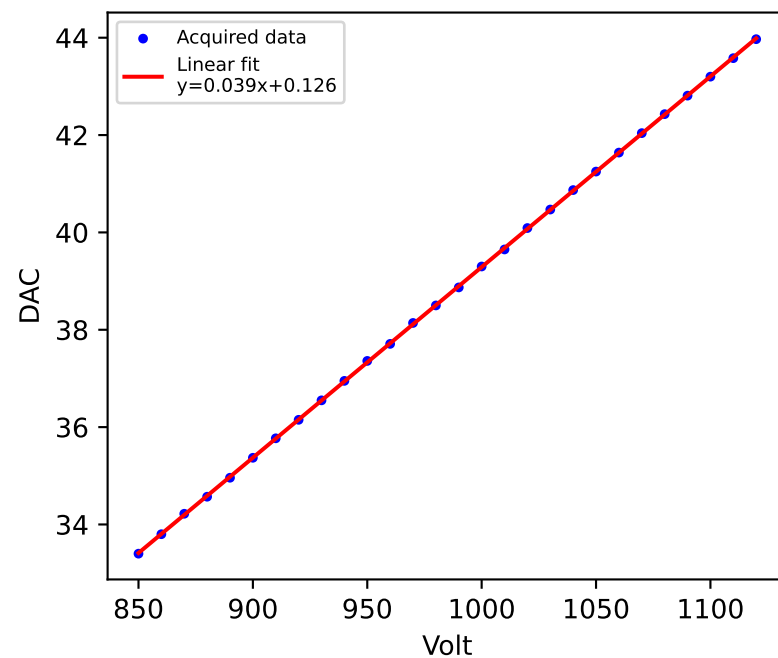
Trim REV IV curve



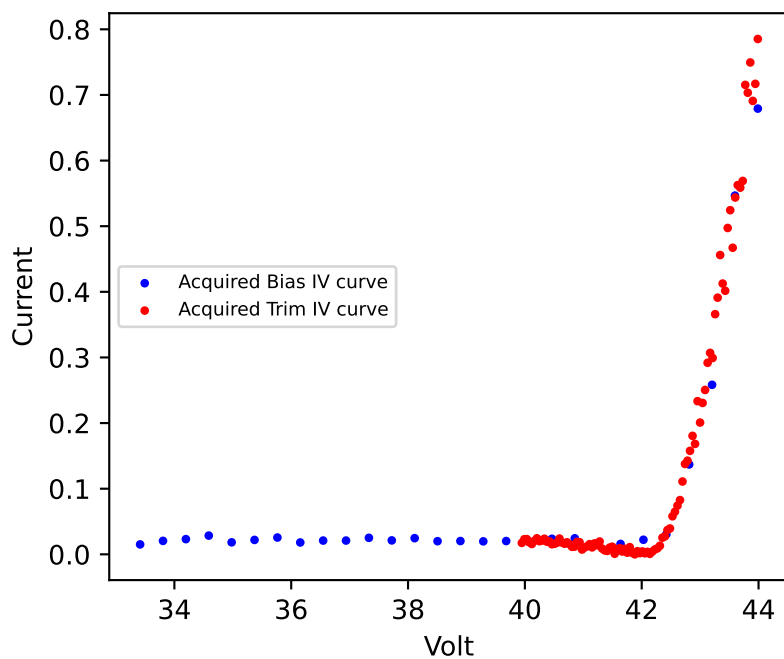
Bias REV IV curve



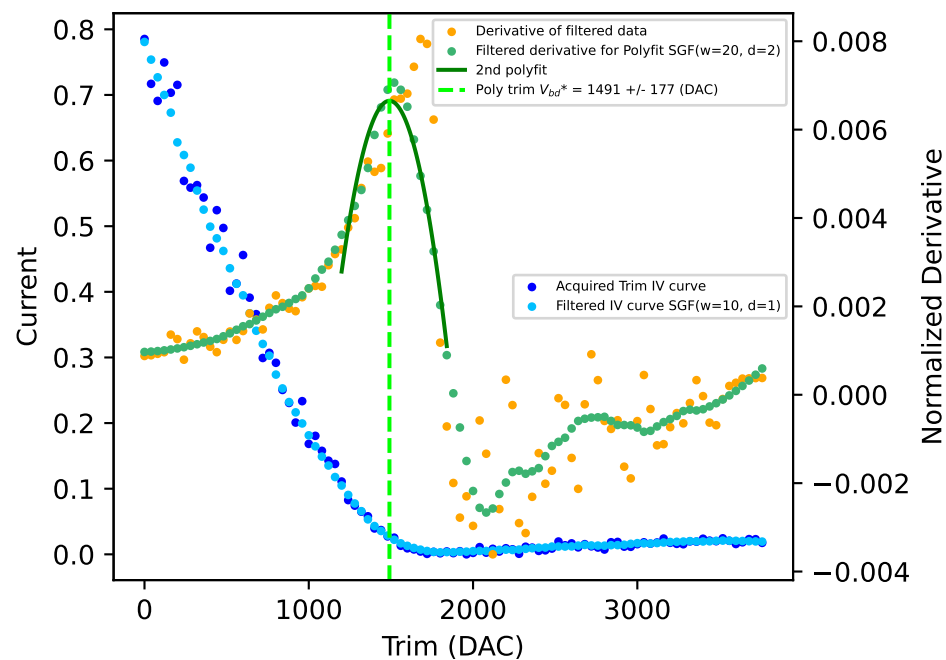
Bias DAC-VOLT conversion



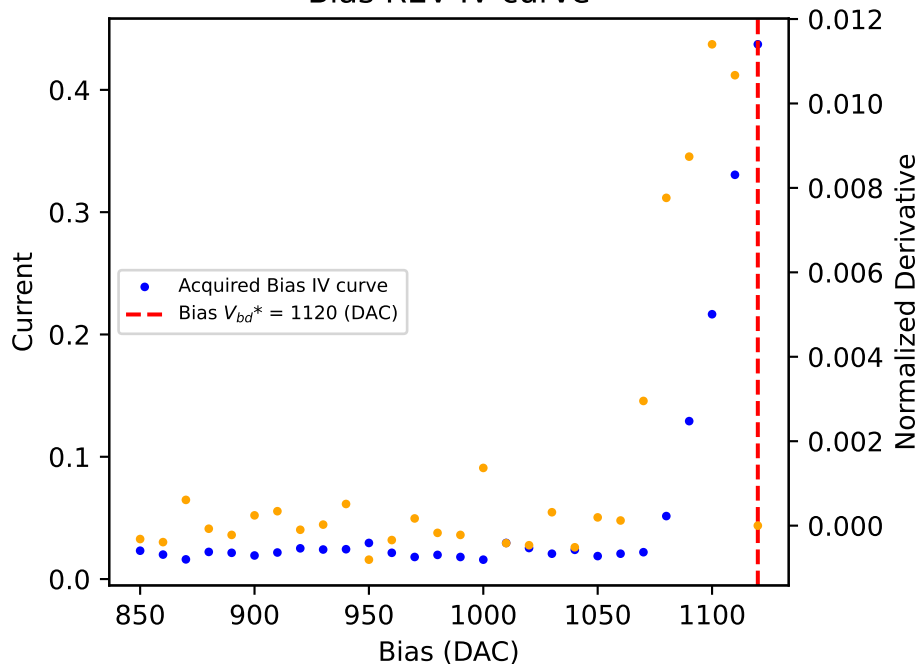
Bias + Trim REV IV curve



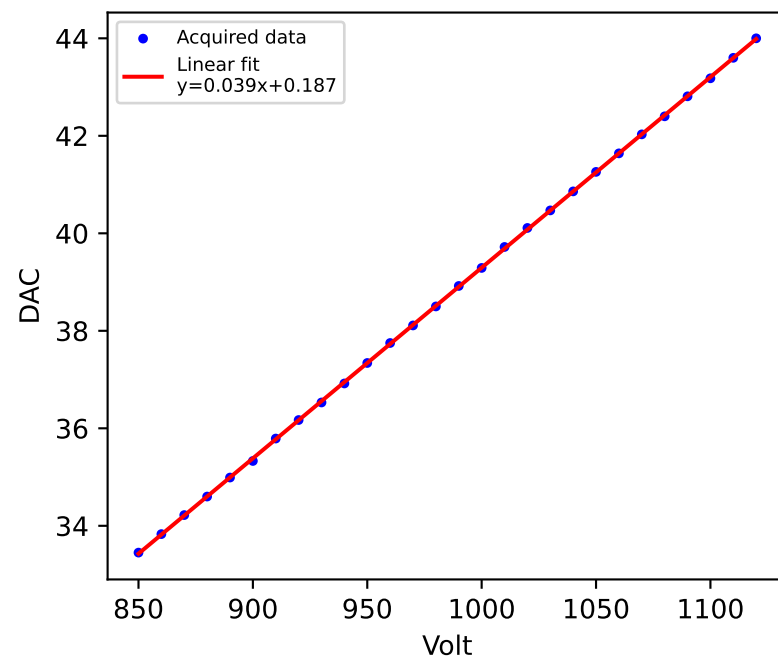
Trim REV IV curve



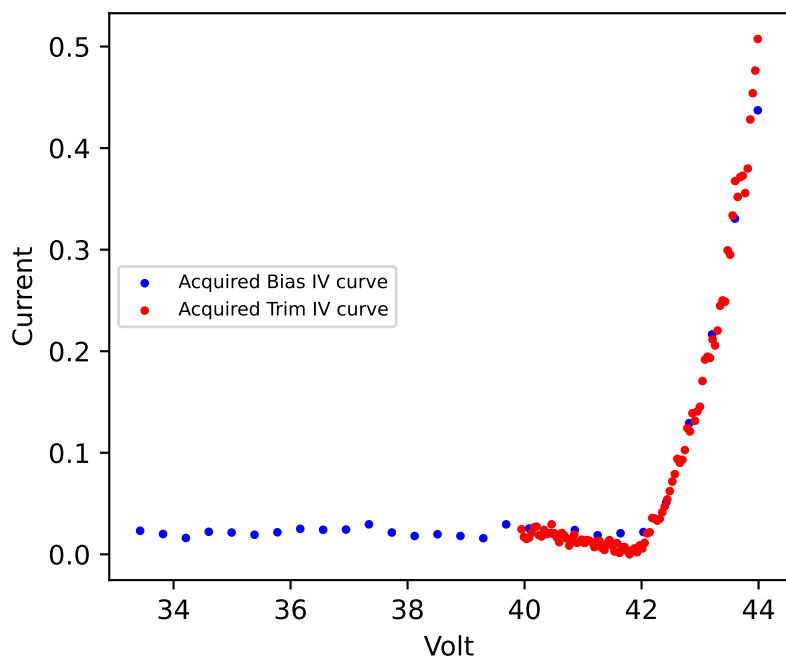
Bias REV IV curve



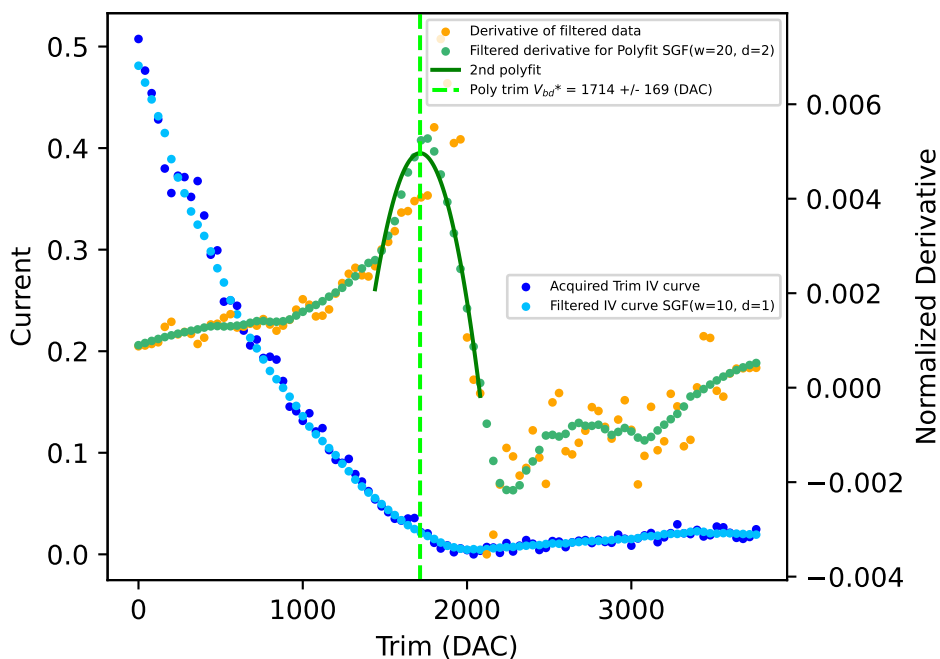
Bias DAC-VOLT conversion



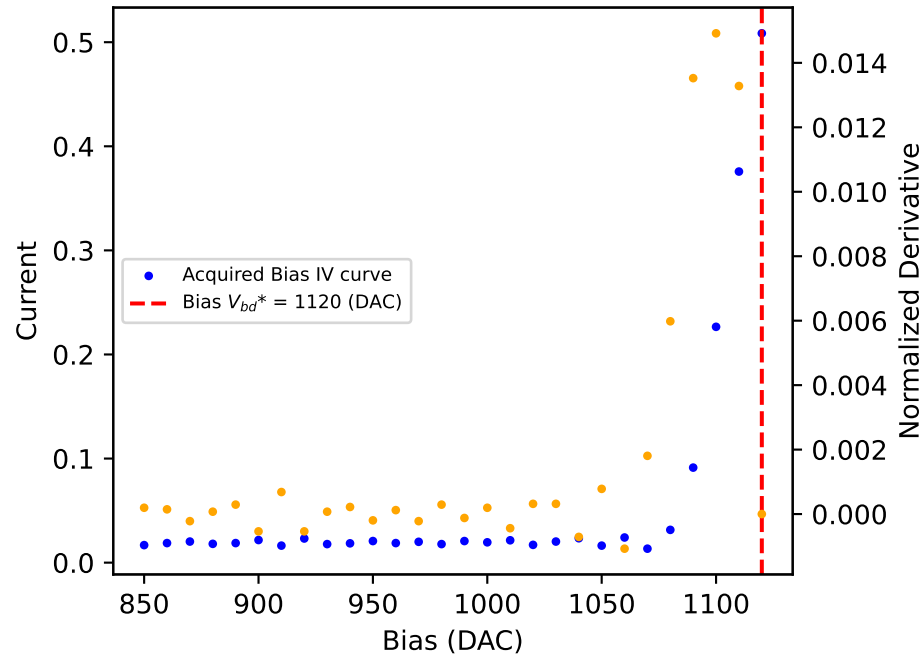
Bias + Trim REV IV curve



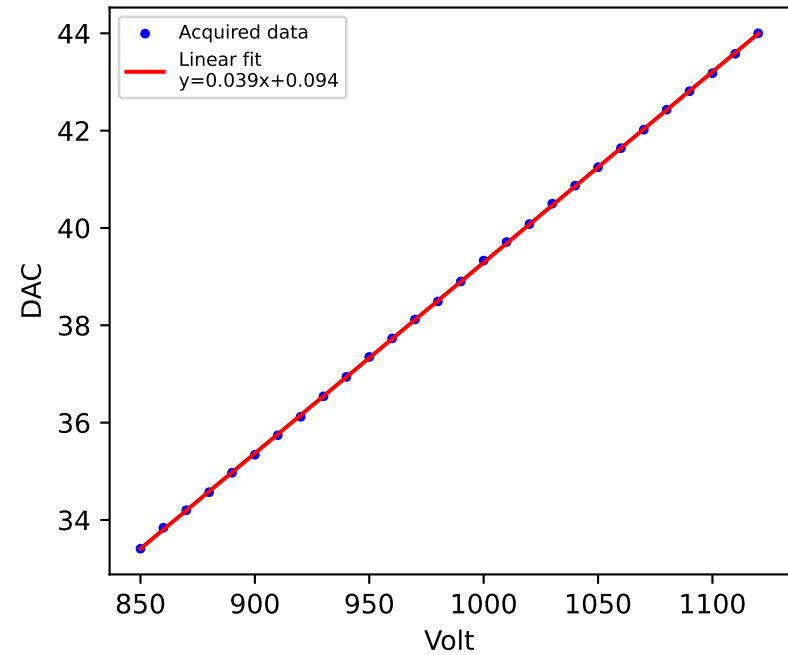
Trim REV IV curve



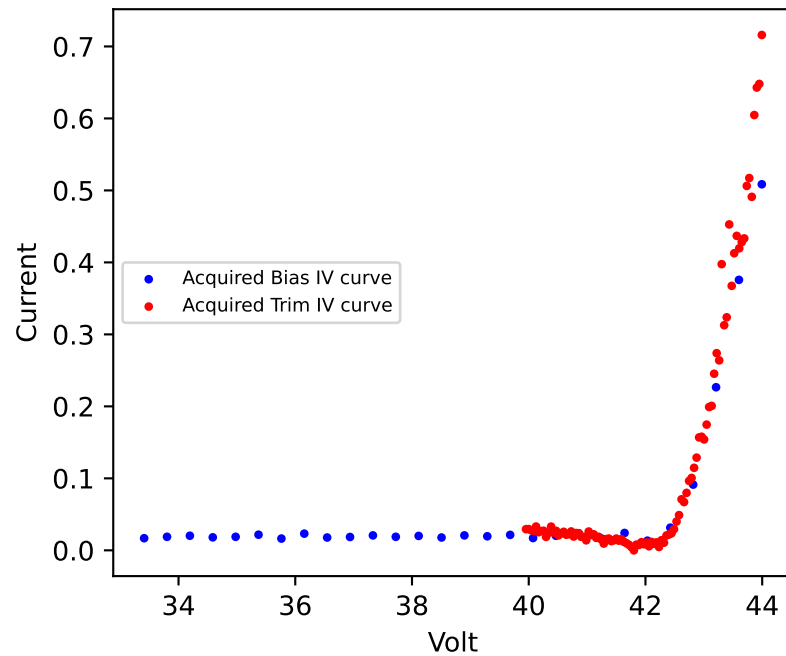
Bias REV IV curve



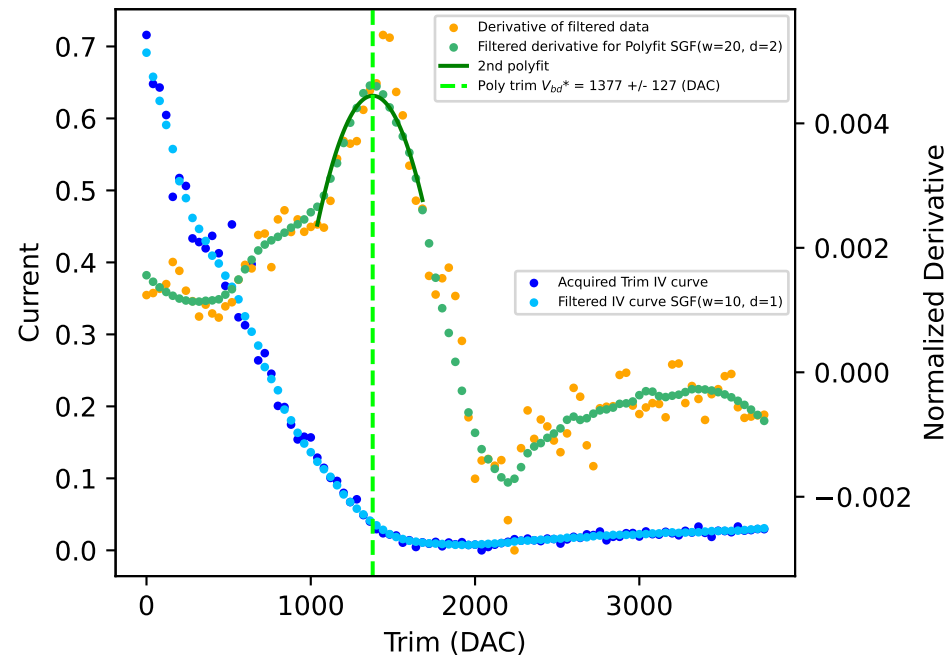
Bias DAC-VOLT conversion



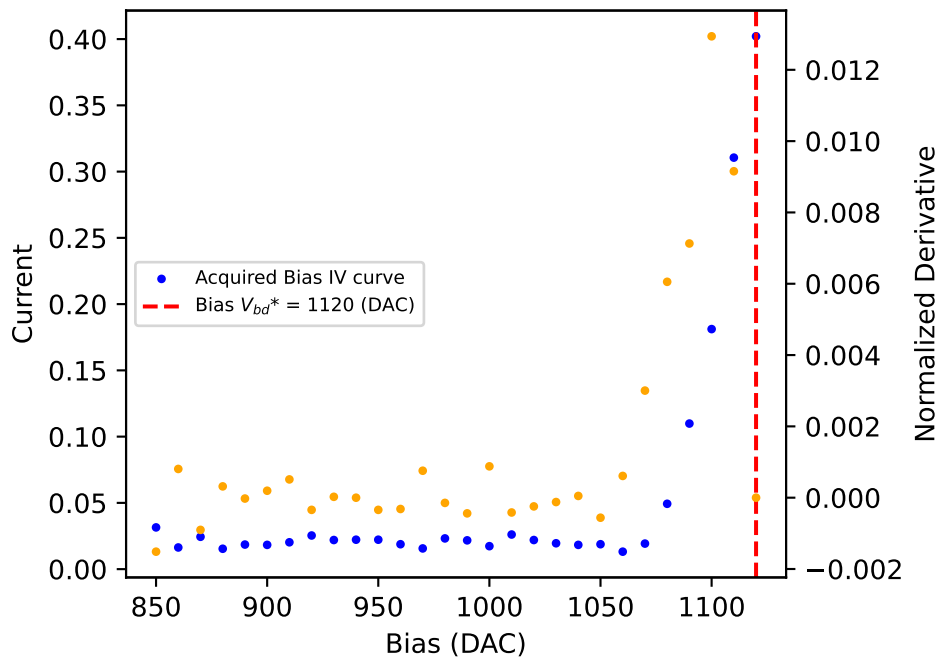
Bias + Trim REV IV curve



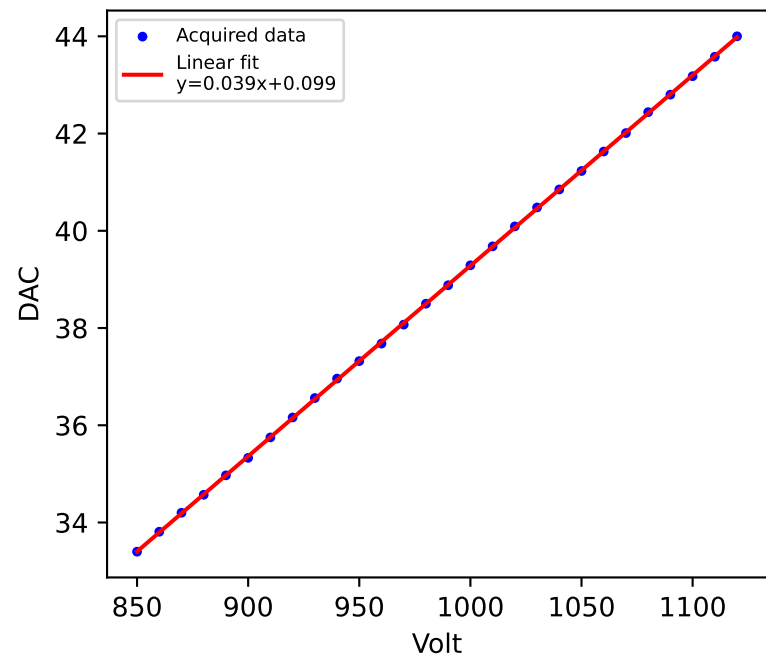
Trim REV IV curve



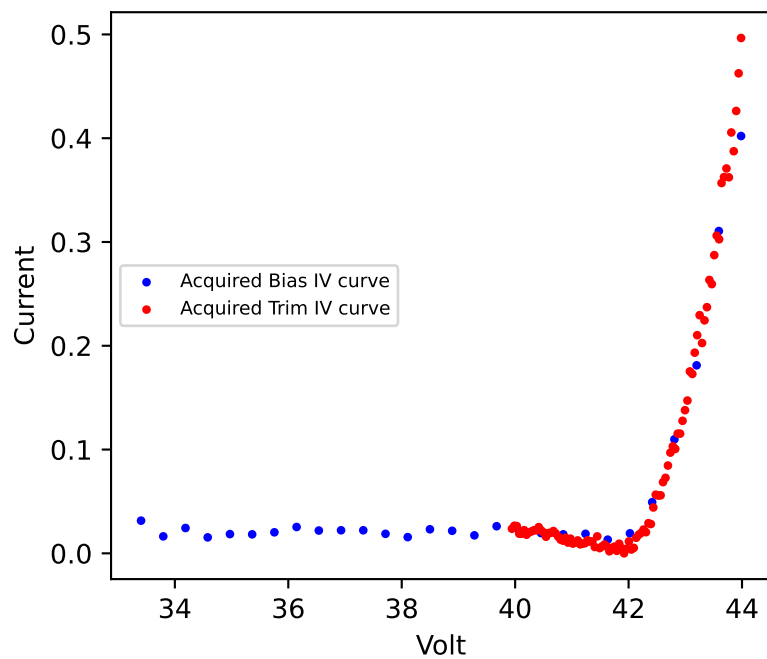
Bias REV IV curve



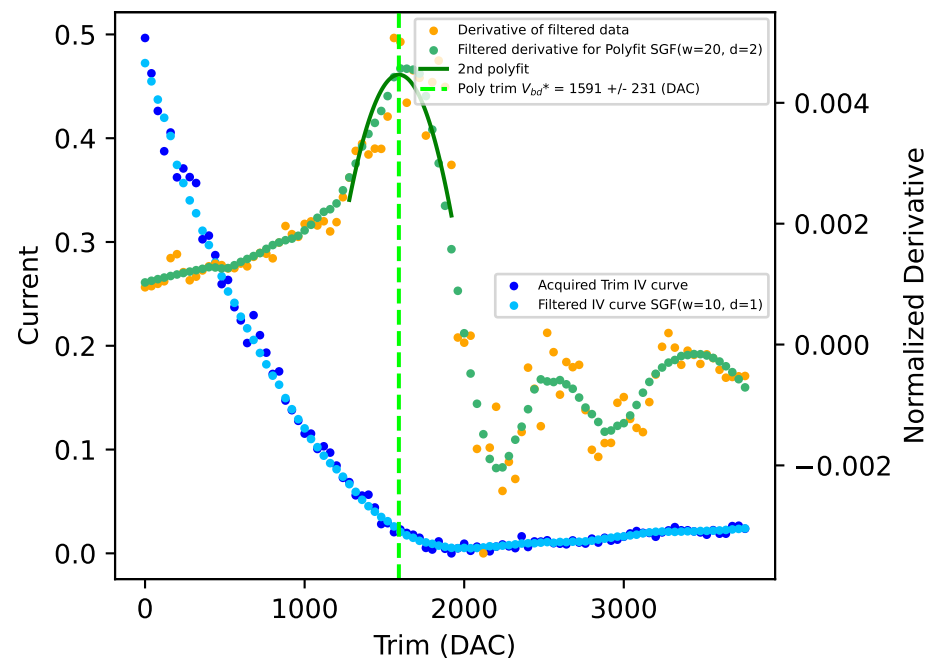
Bias DAC-VOLT conversion



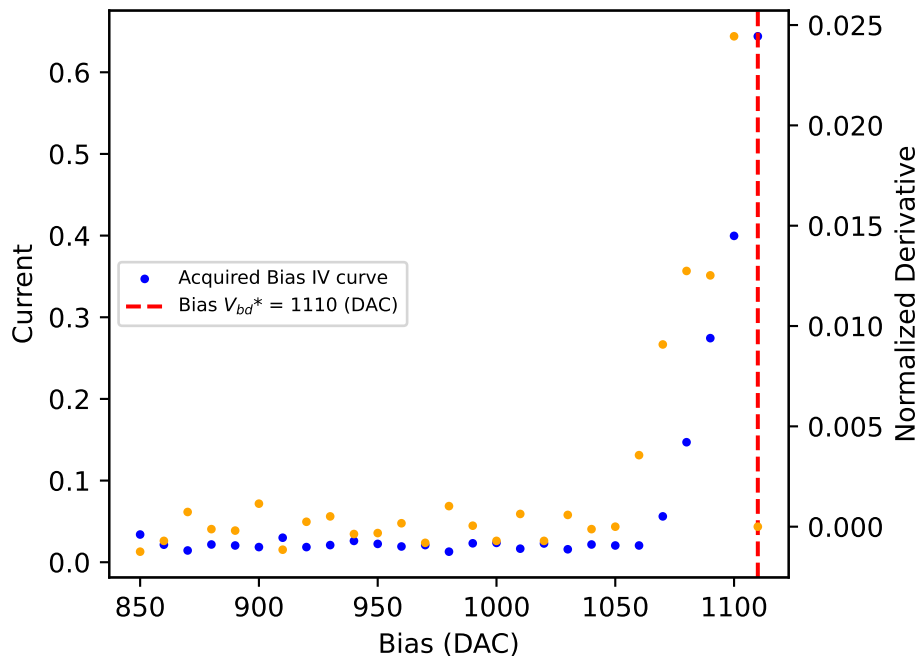
Bias + Trim REV IV curve



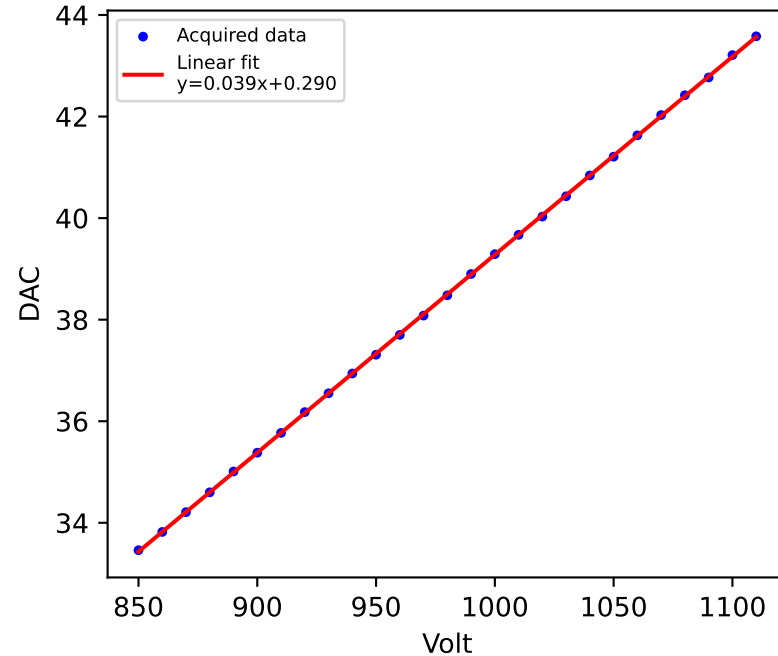
Trim REV IV curve



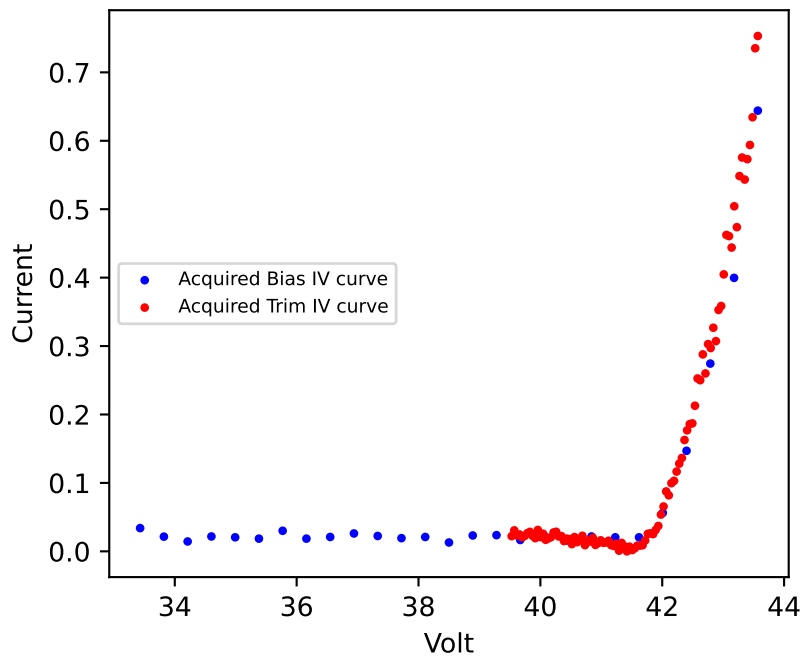
Bias REV IV curve



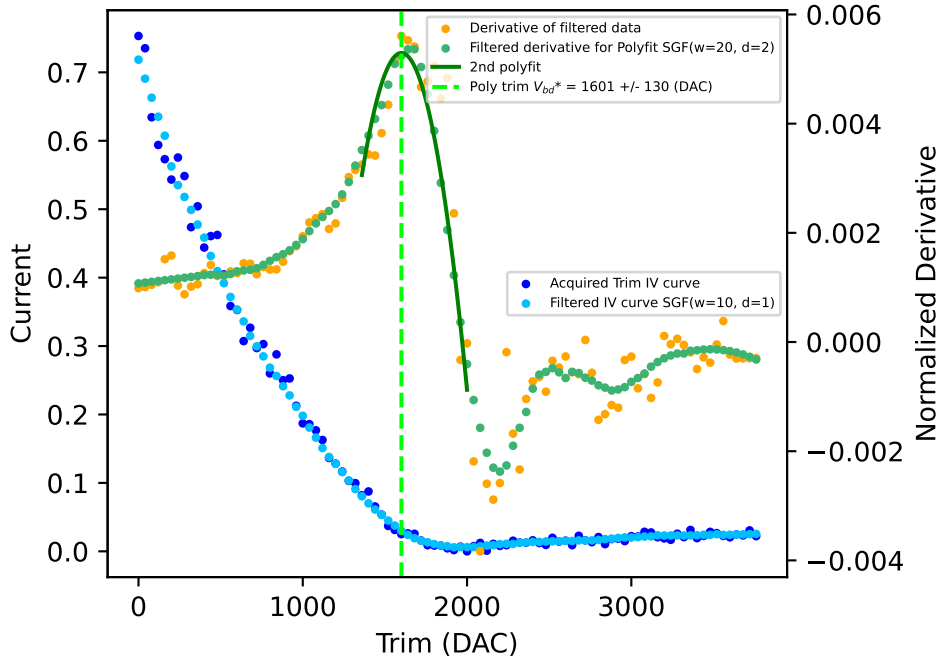
Bias DAC-VOLT conversion



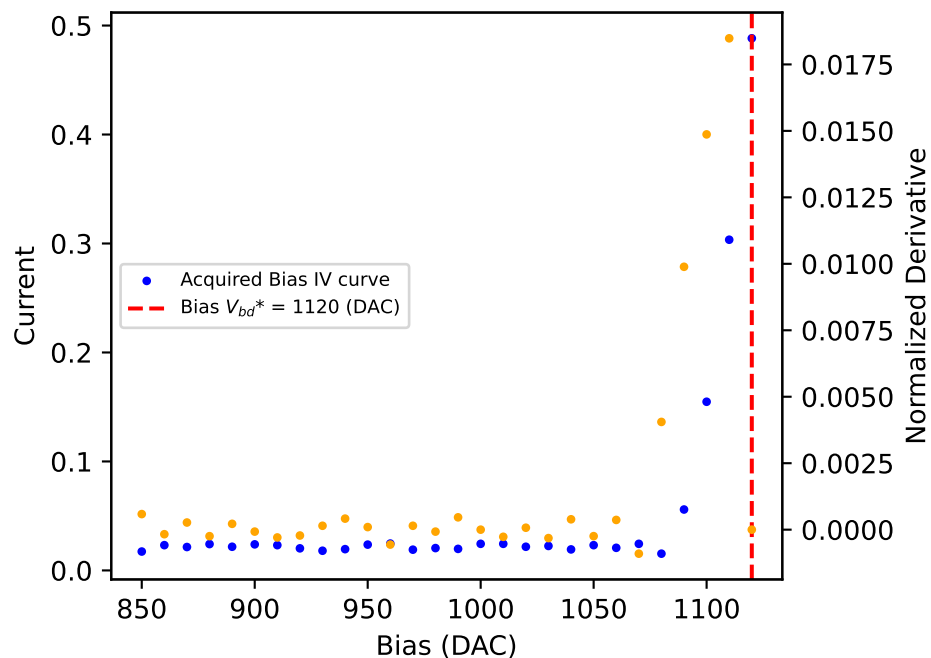
Bias + Trim REV IV curve



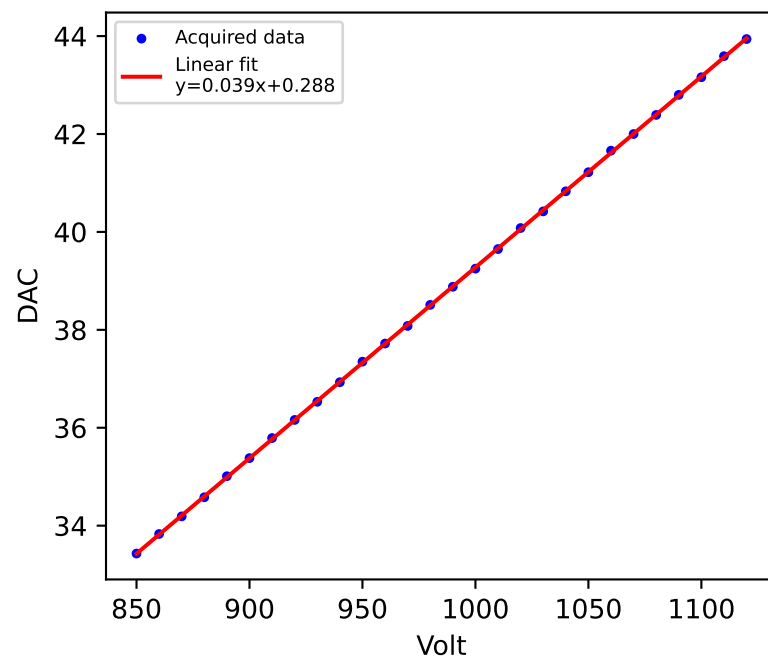
Trim REV IV curve



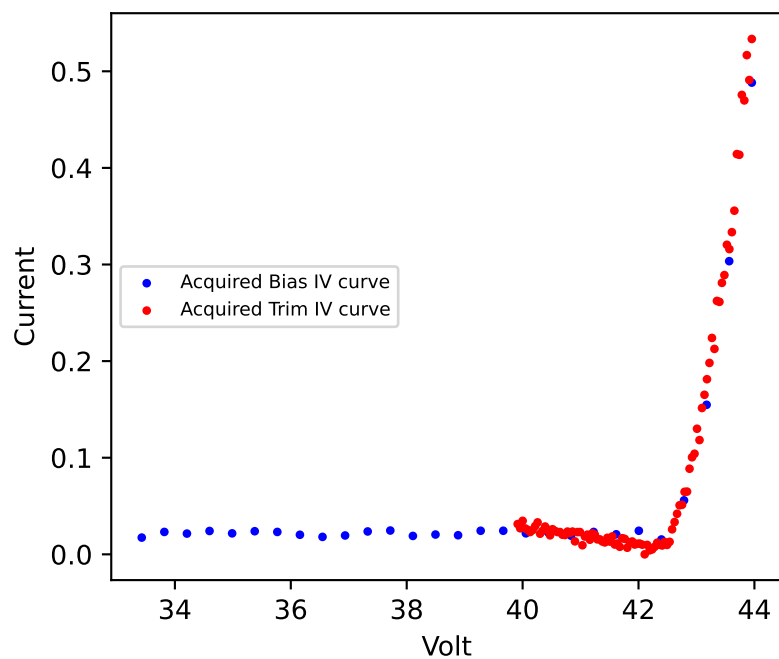
Bias REV IV curve



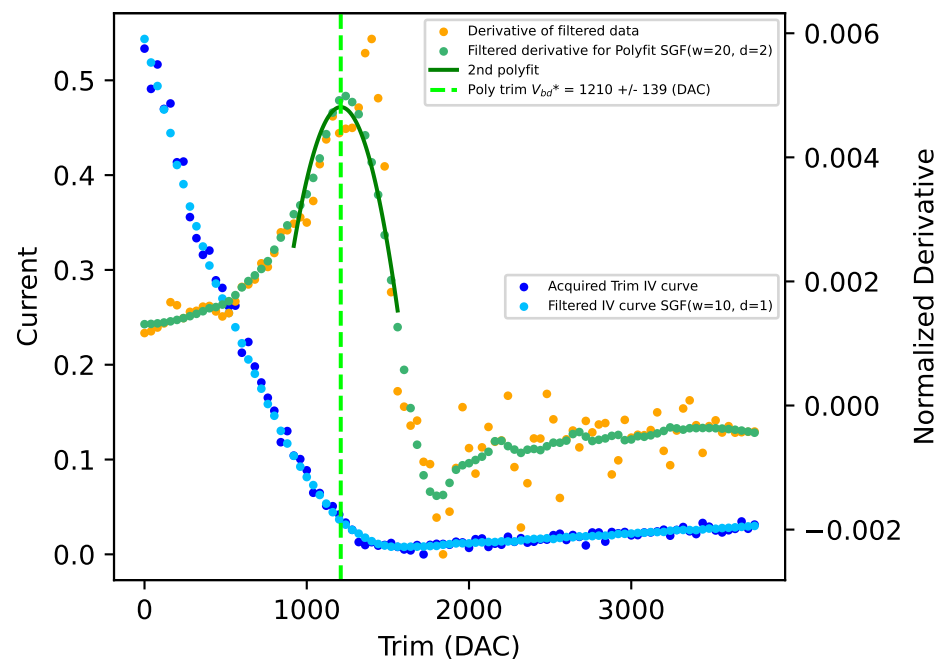
Bias DAC-VOLT conversion



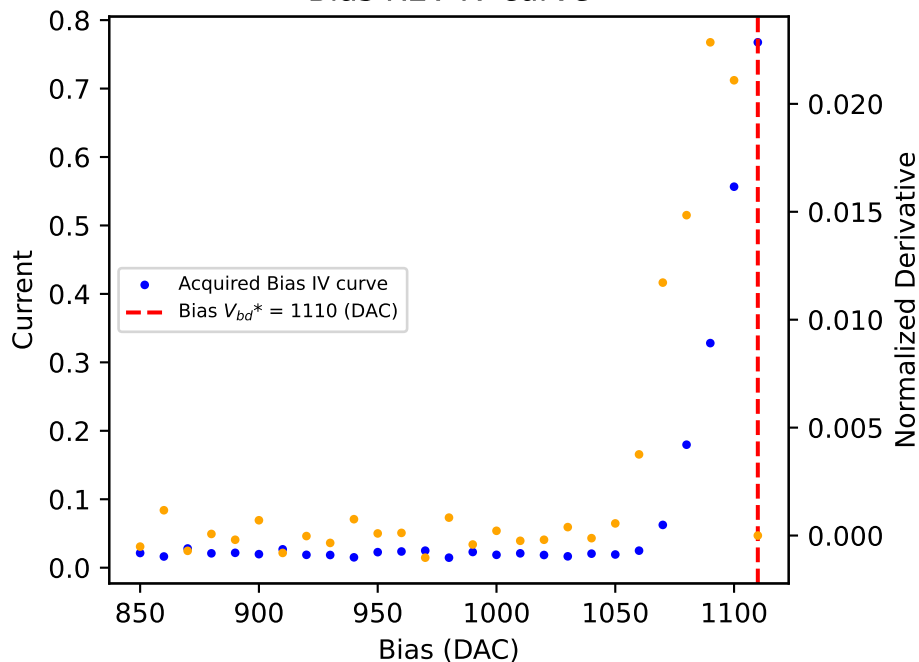
Bias + Trim REV IV curve



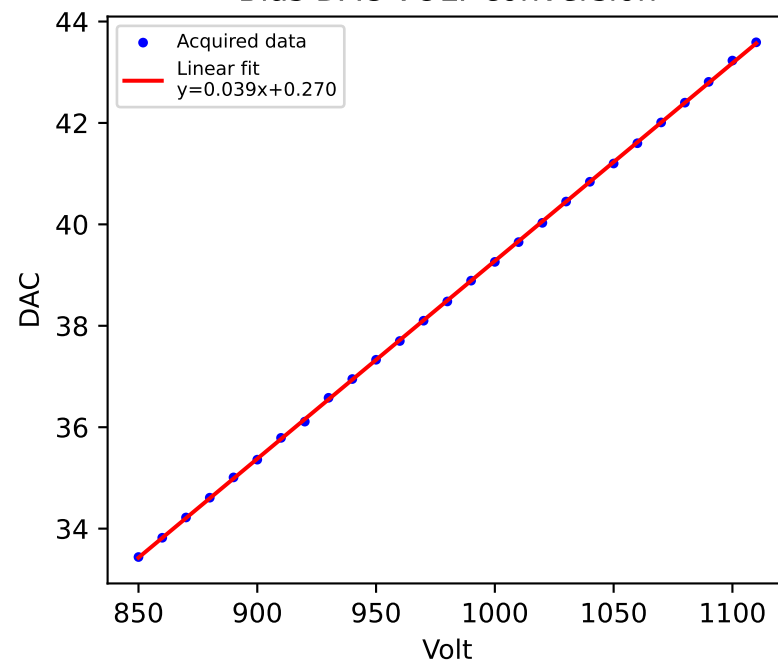
Trim REV IV curve



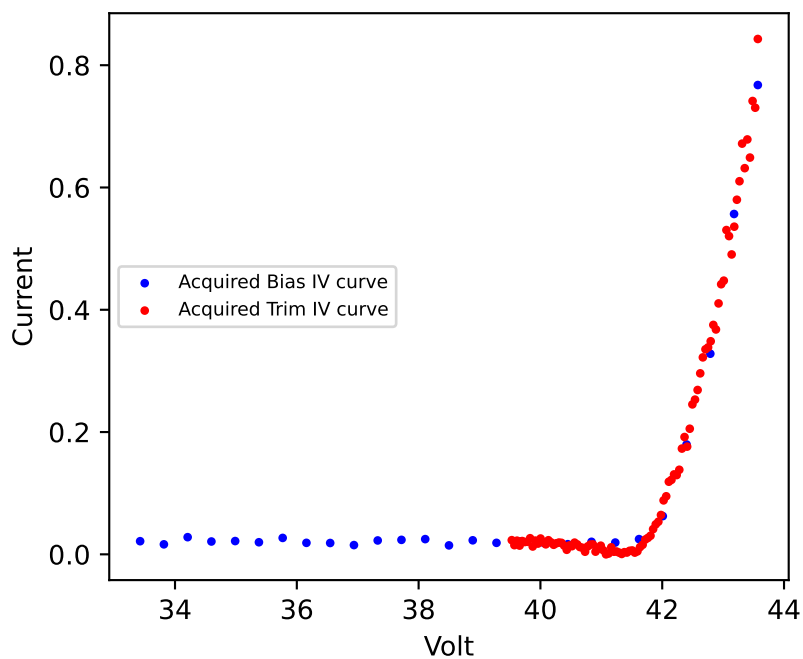
Bias REV IV curve



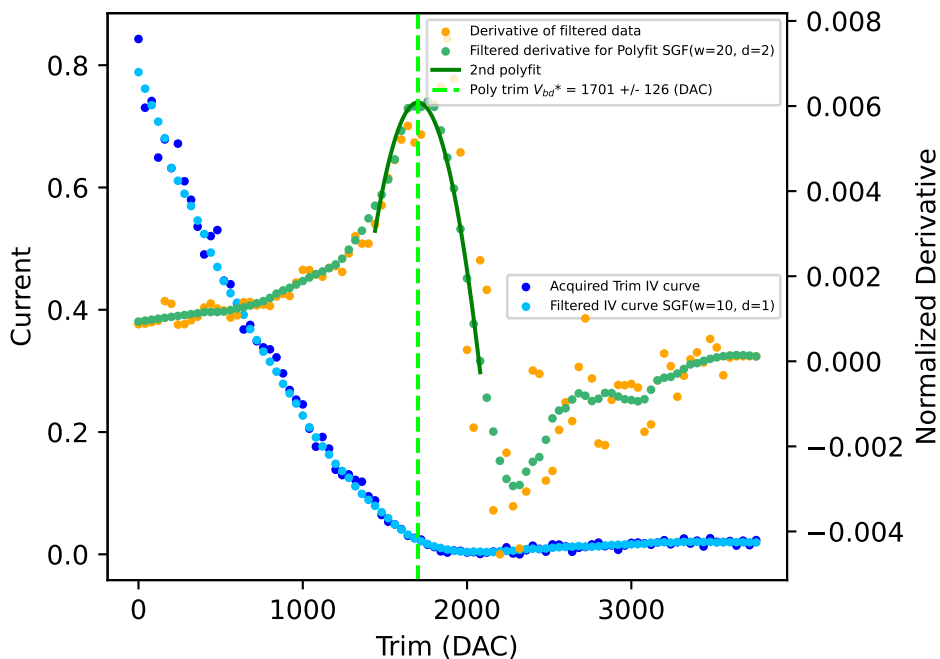
Bias DAC-VOLT conversion



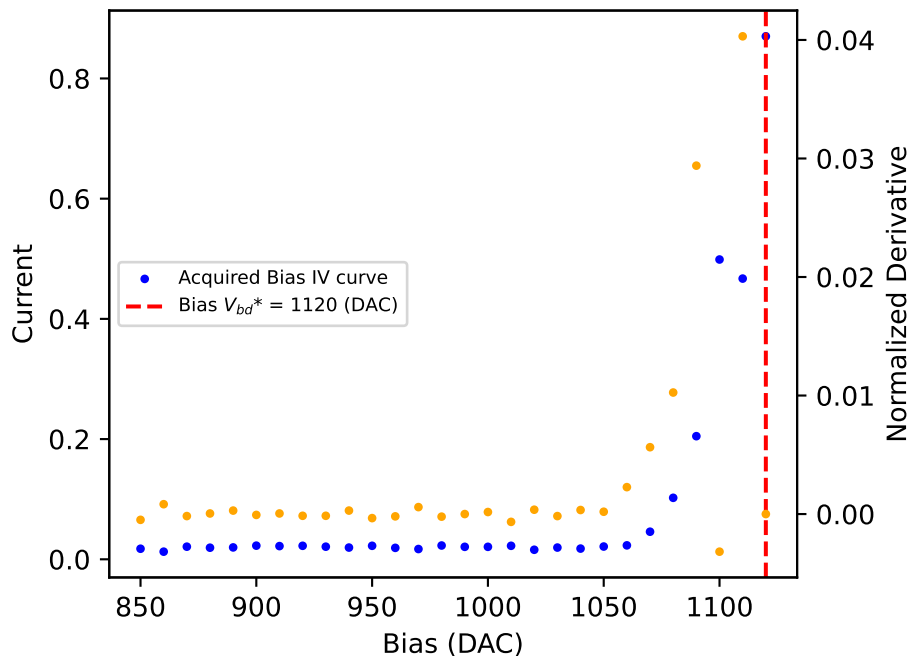
Bias + Trim REV IV curve



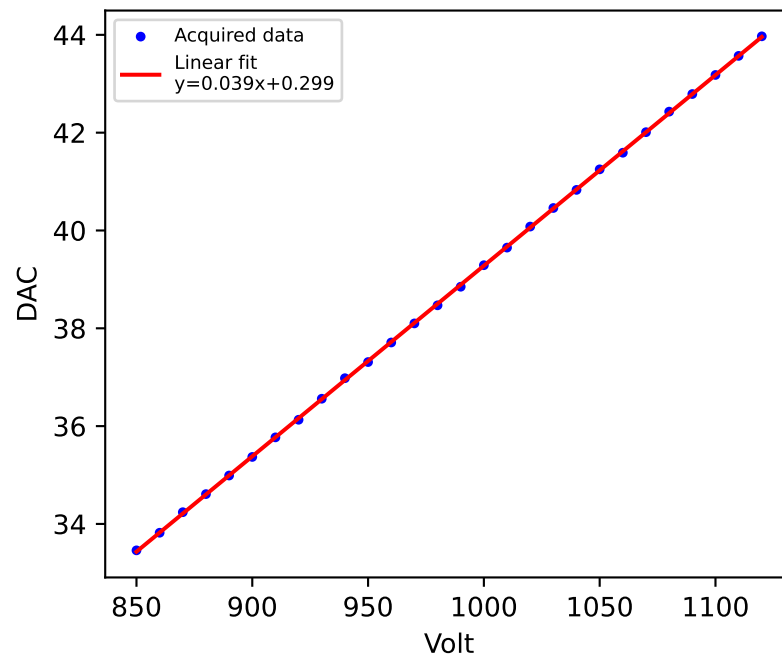
Trim REV IV curve



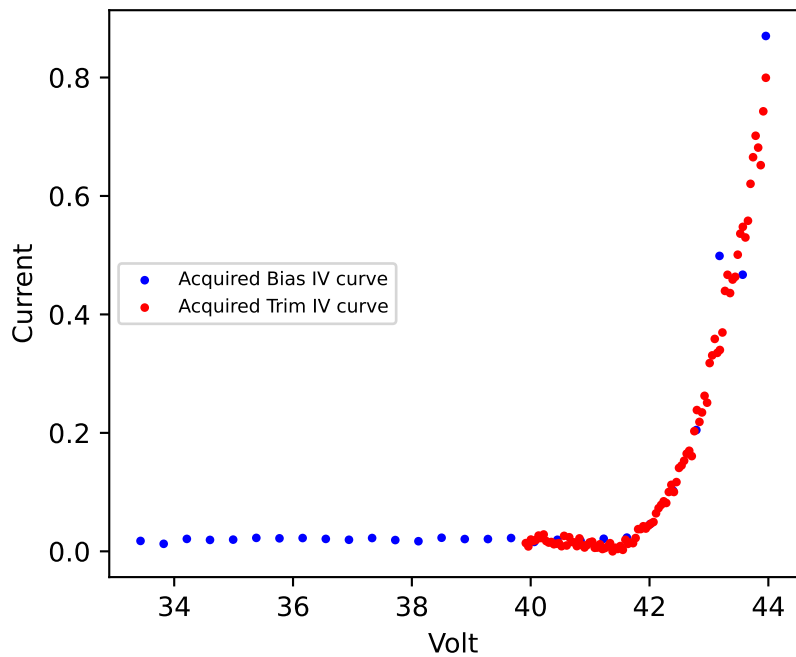
Bias REV IV curve



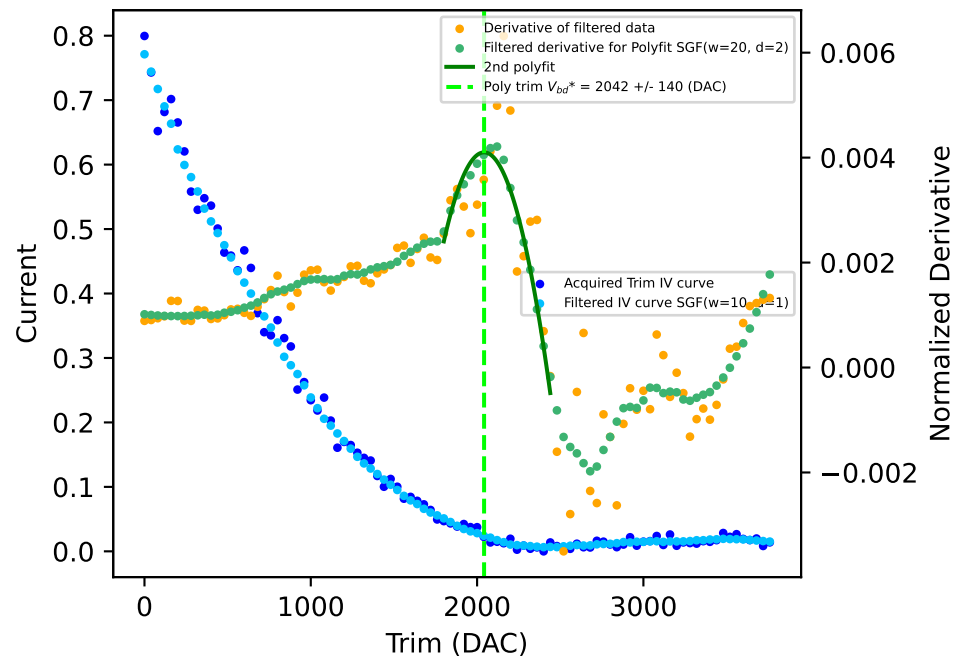
Bias DAC-VOLT conversion



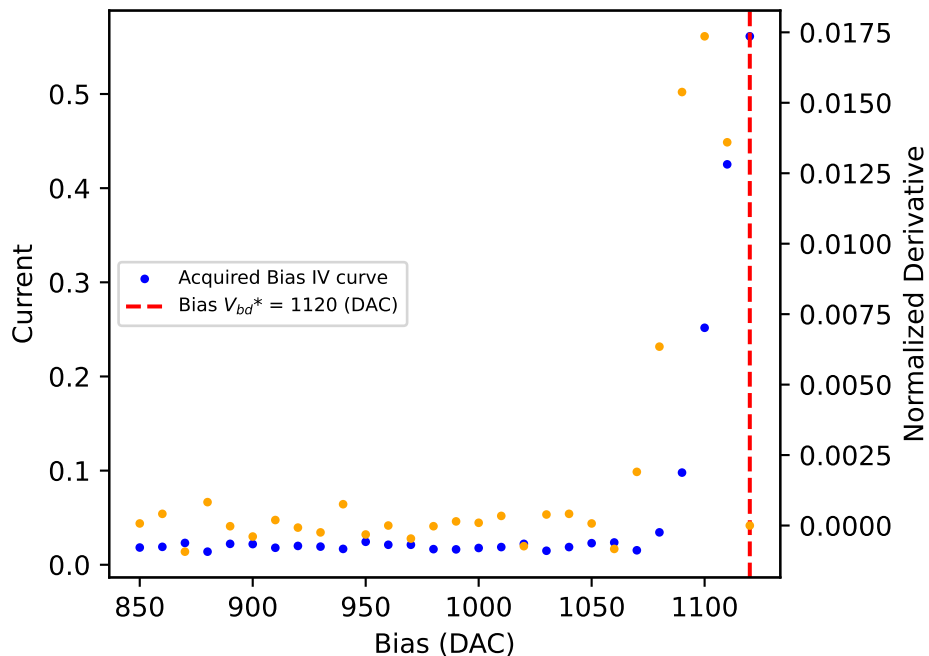
Bias + Trim REV IV curve



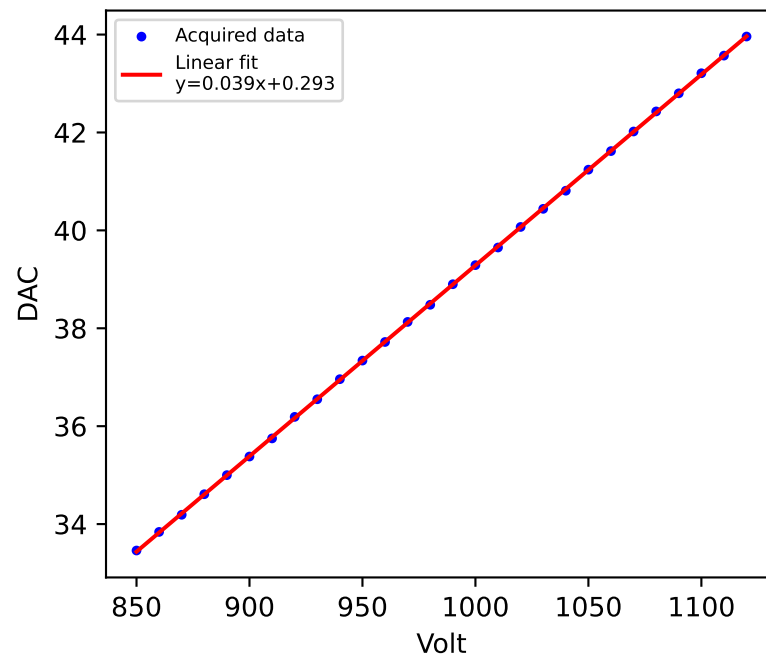
Trim REV IV curve



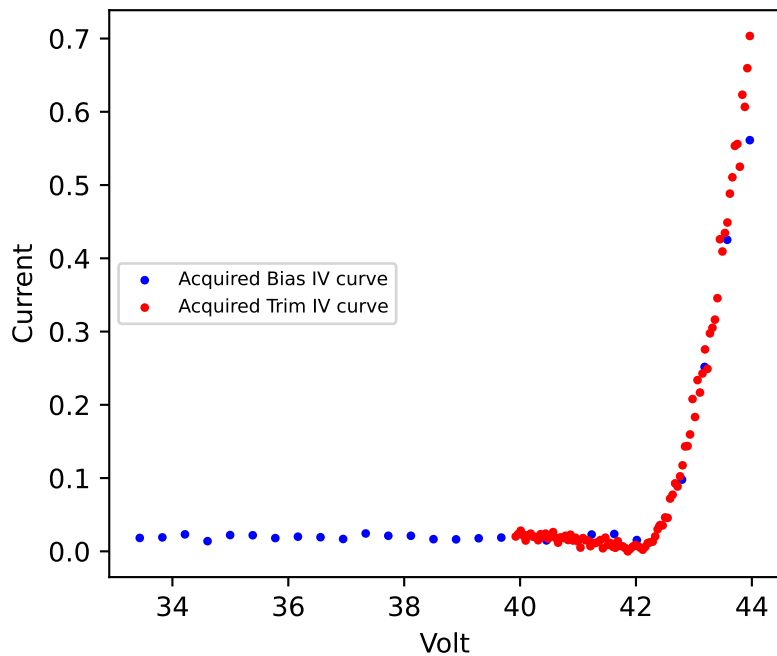
Bias REV IV curve



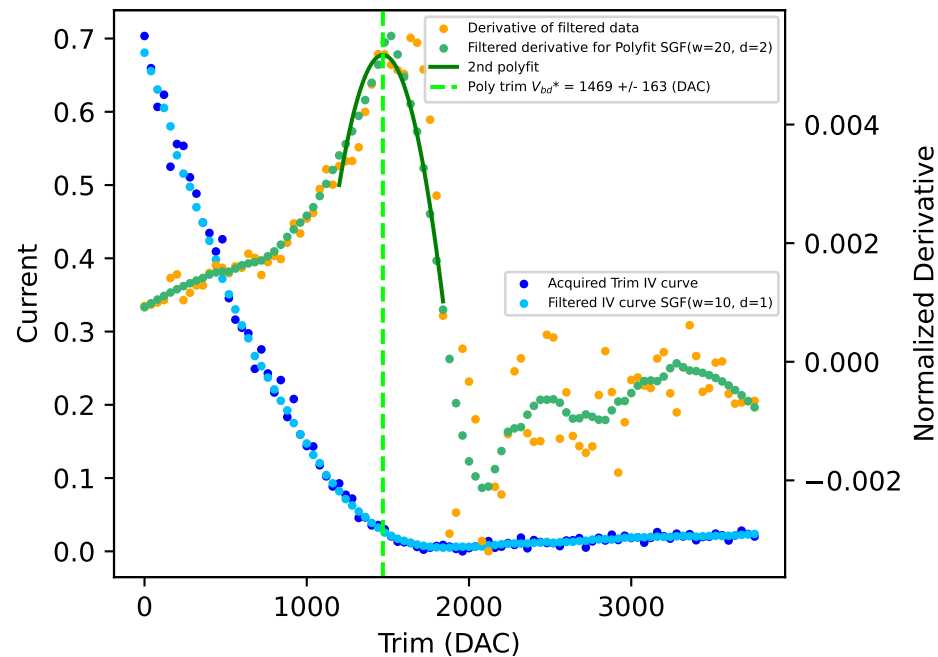
Bias DAC-VOLT conversion



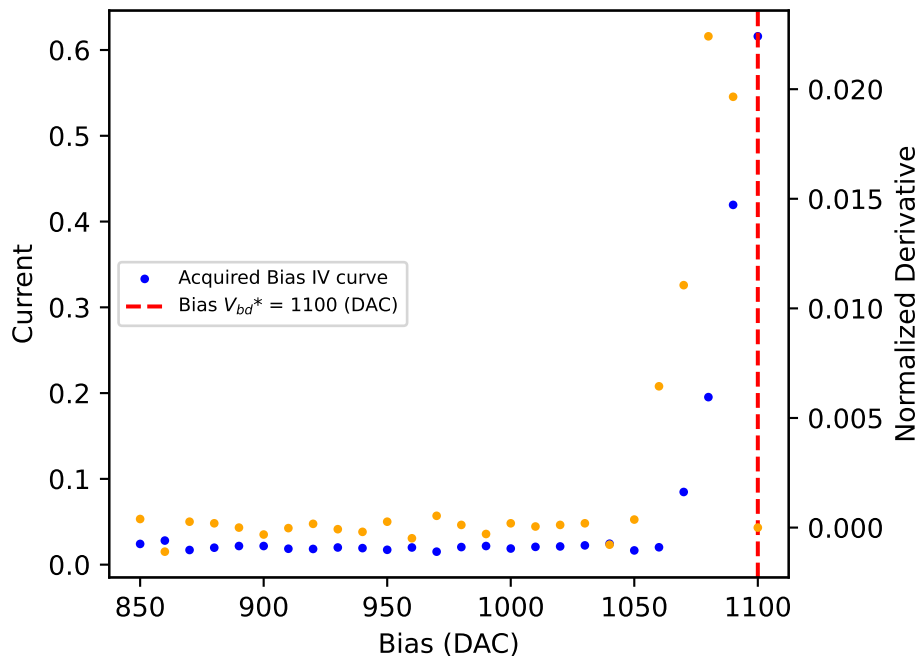
Bias + Trim REV IV curve



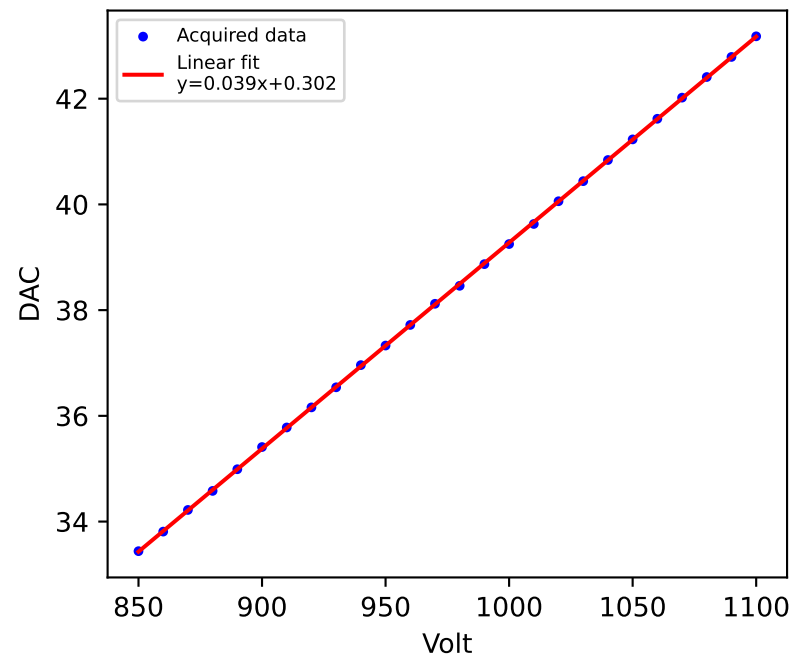
Trim REV IV curve



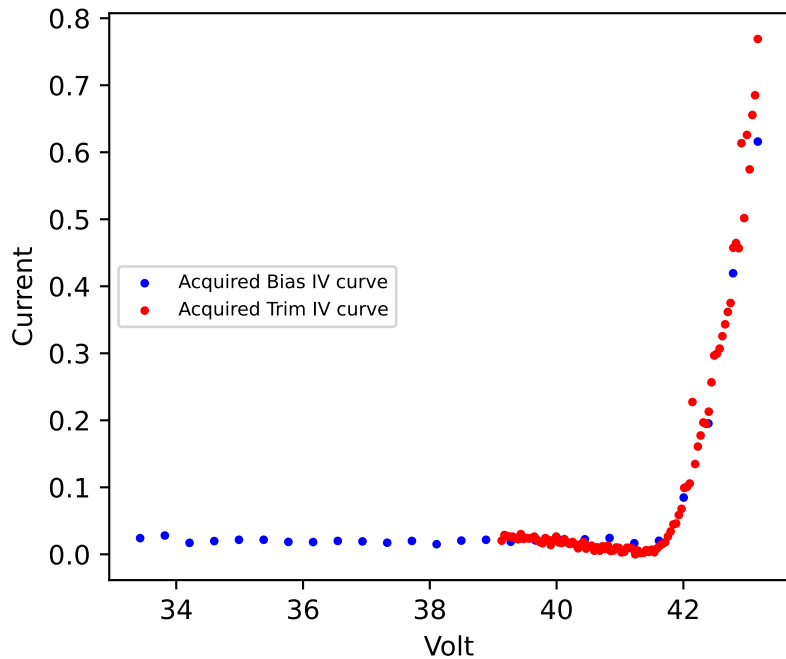
Bias REV IV curve



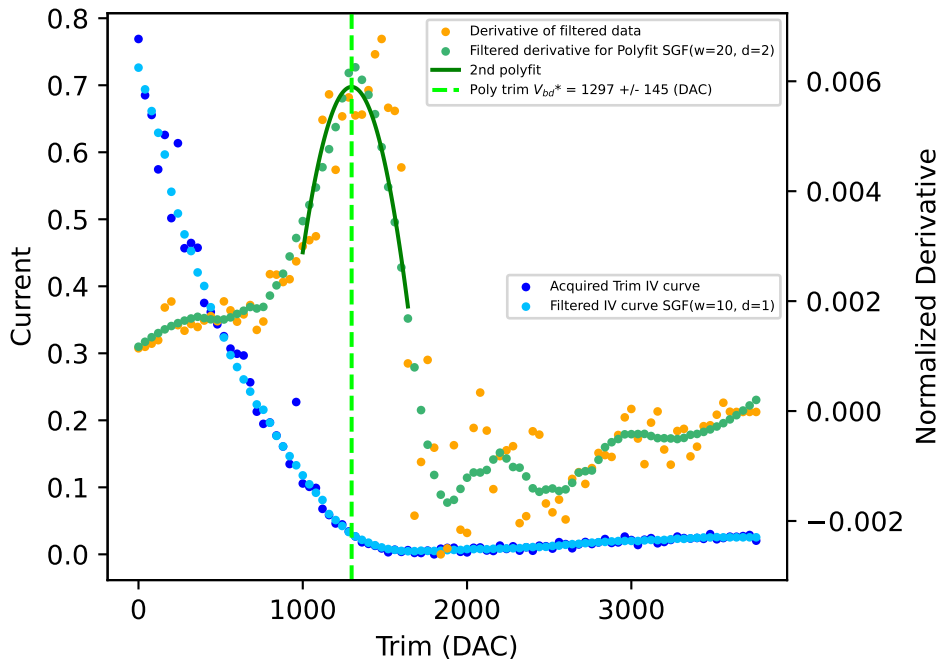
Bias DAC-VOLT conversion



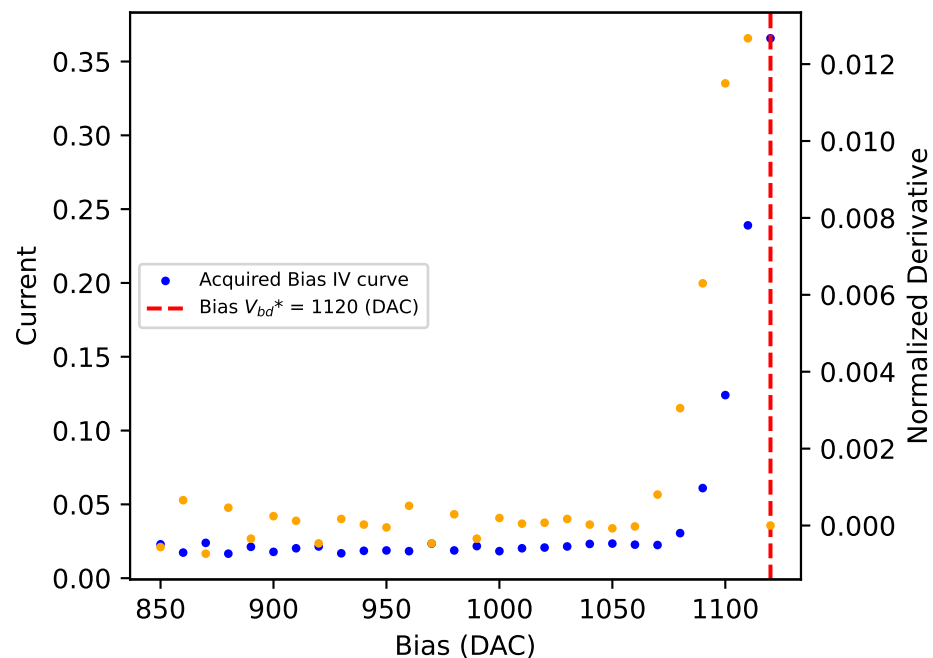
Bias + Trim REV IV curve



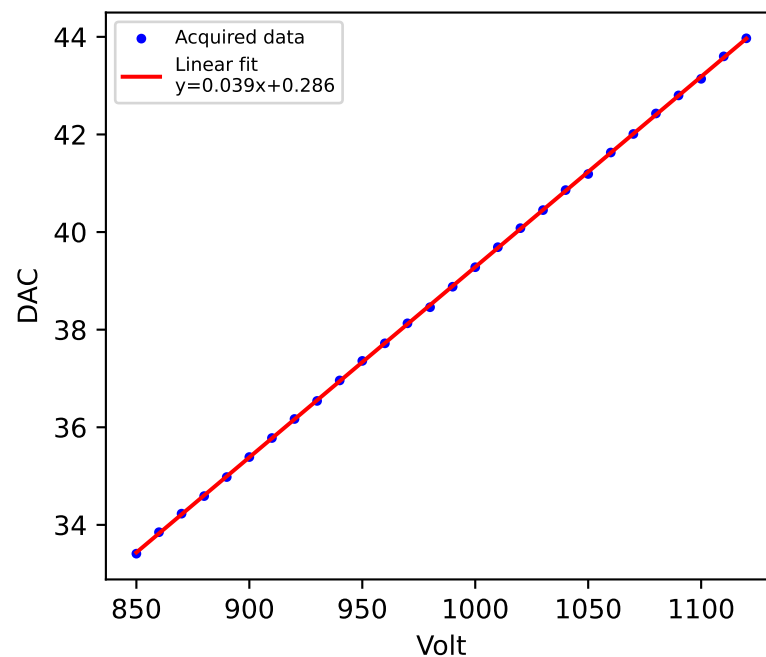
Trim REV IV curve



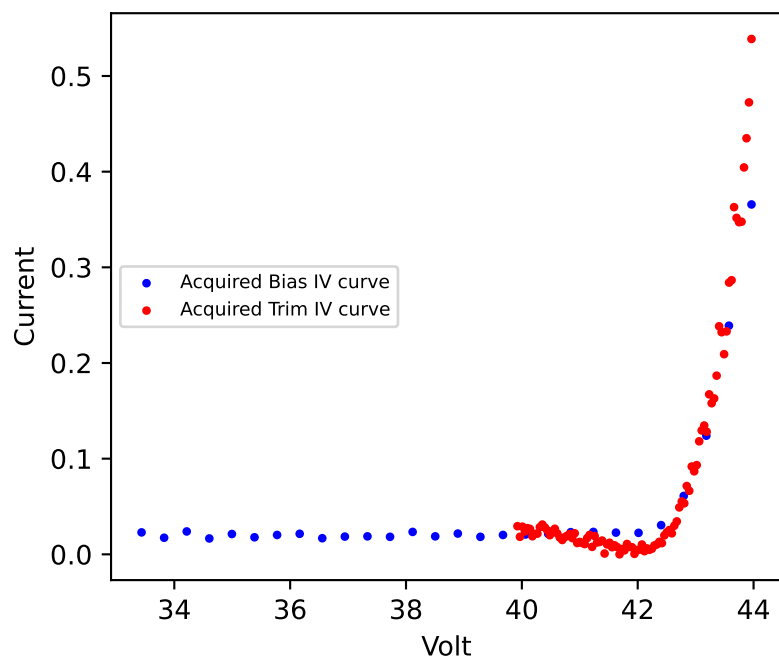
Bias REV IV curve



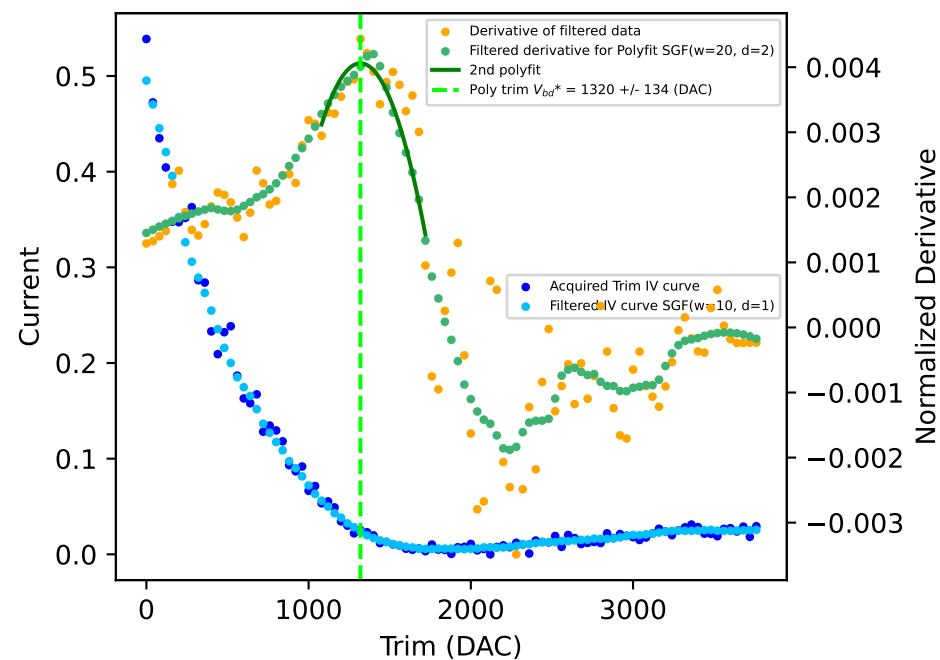
Bias DAC-VOLT conversion



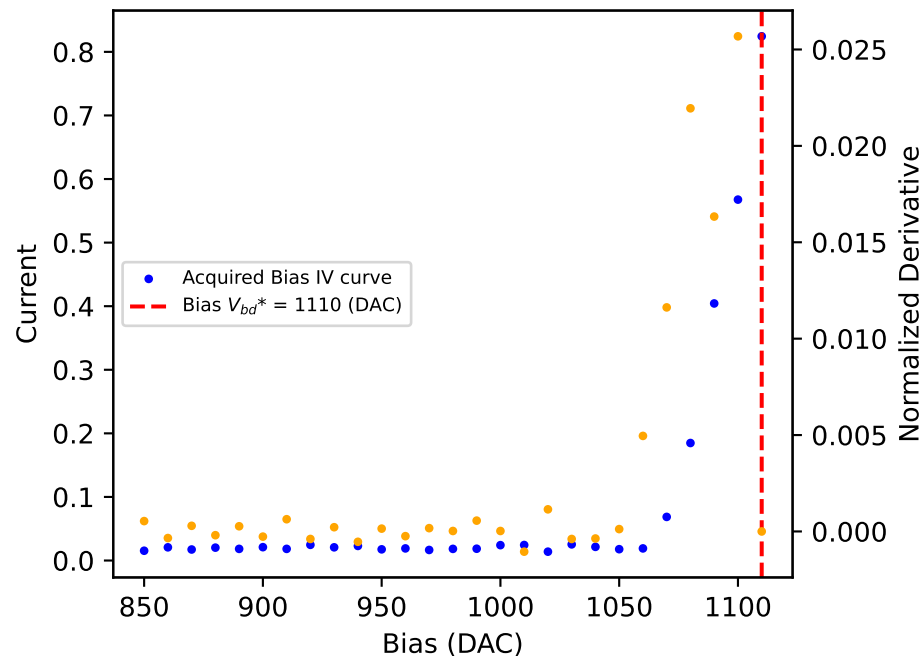
Bias + Trim REV IV curve



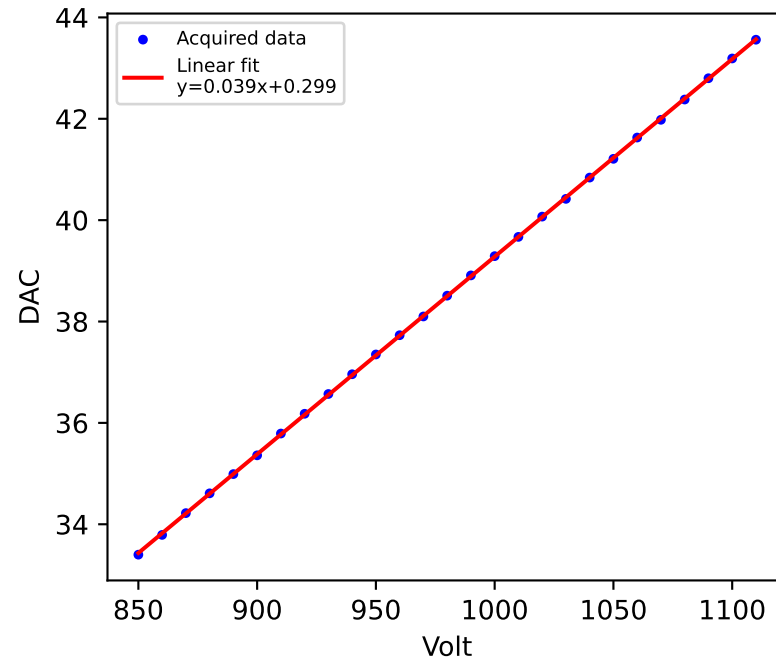
Trim REV IV curve



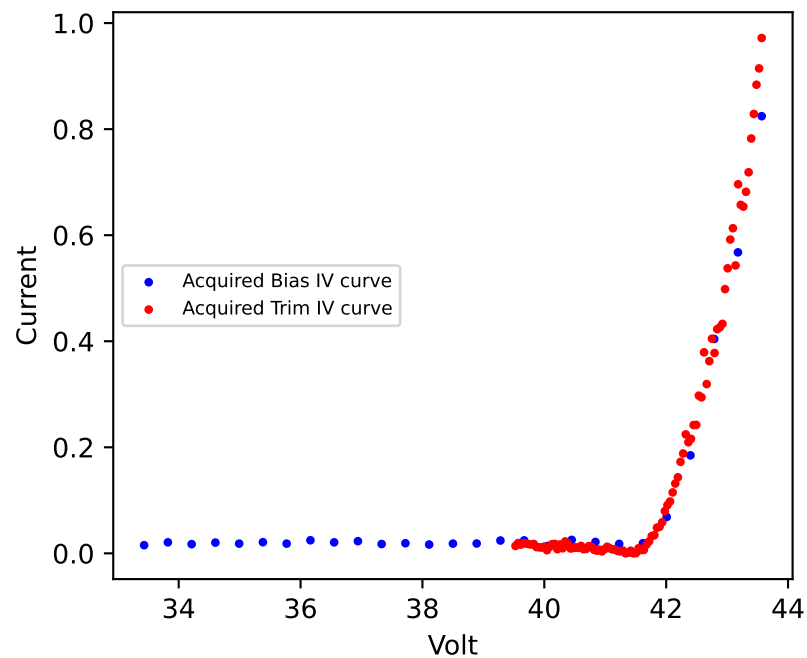
Bias REV IV curve



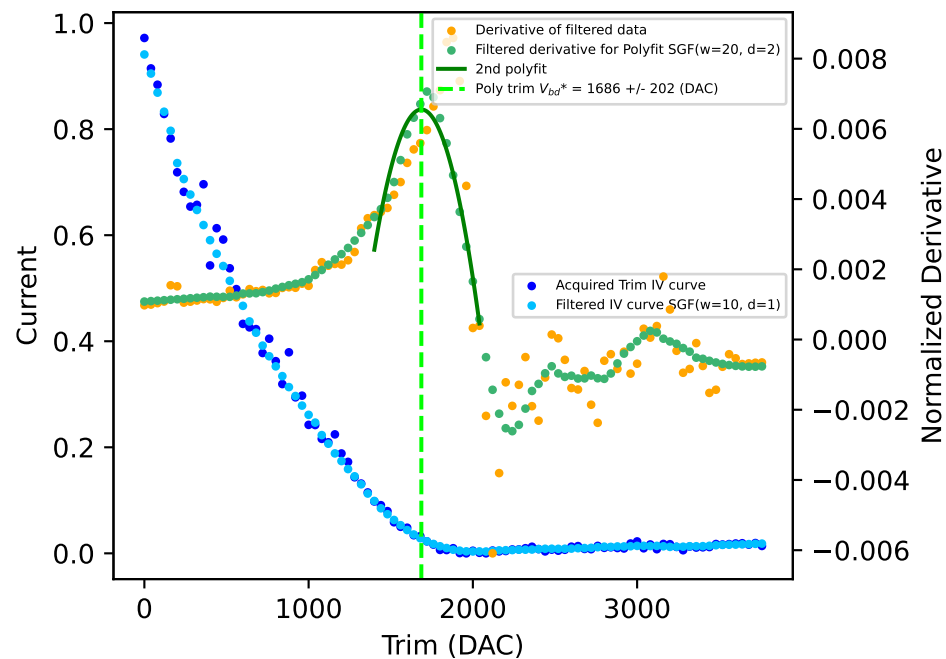
Bias DAC-VOLT conversion



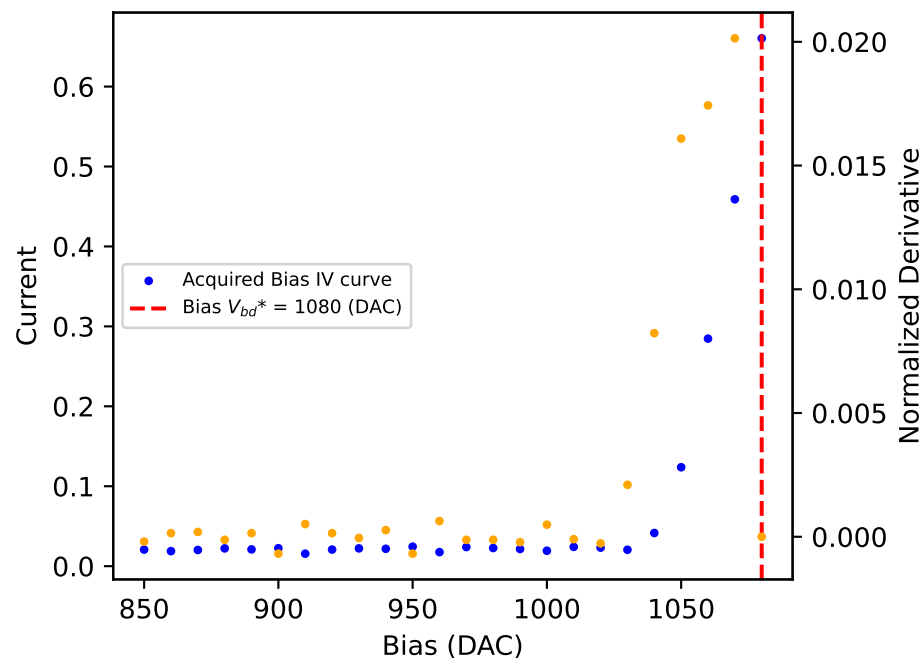
Bias + Trim REV IV curve



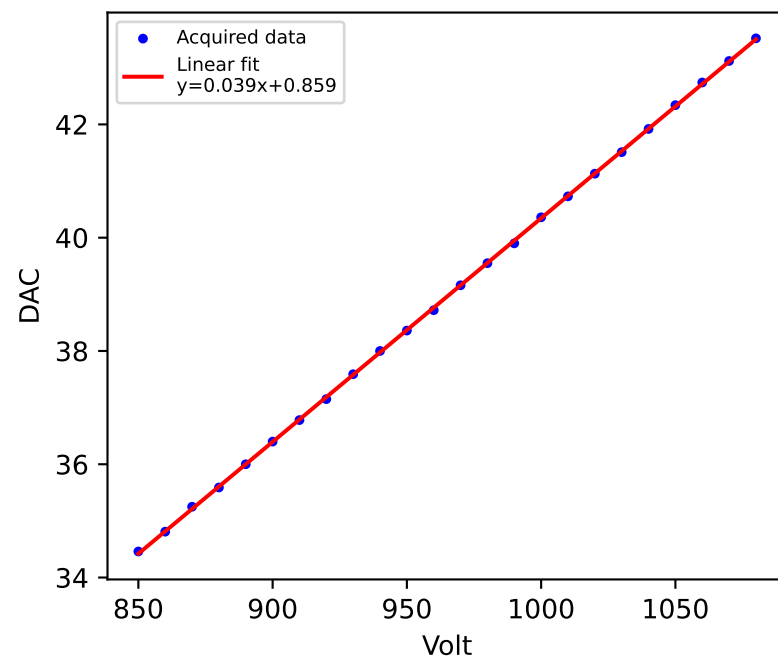
Trim REV IV curve



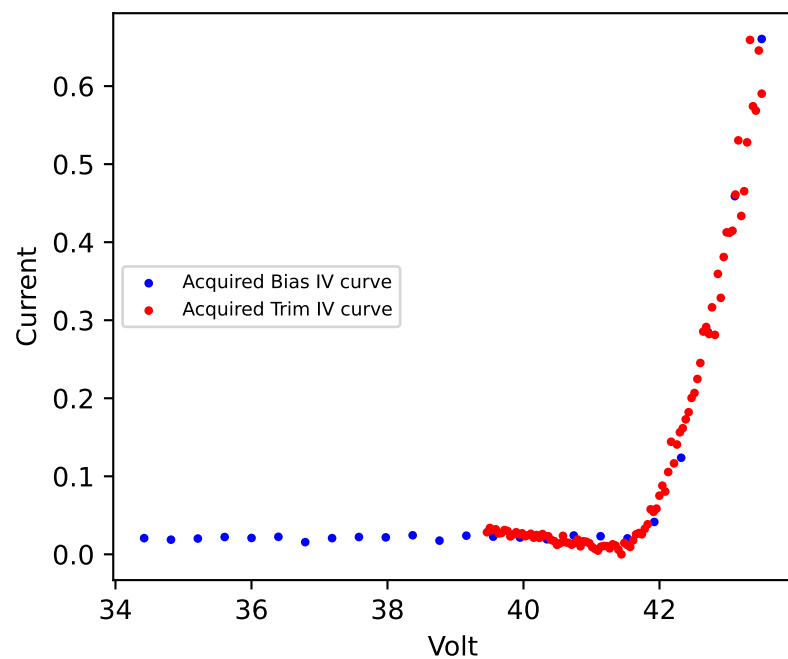
Bias REV IV curve



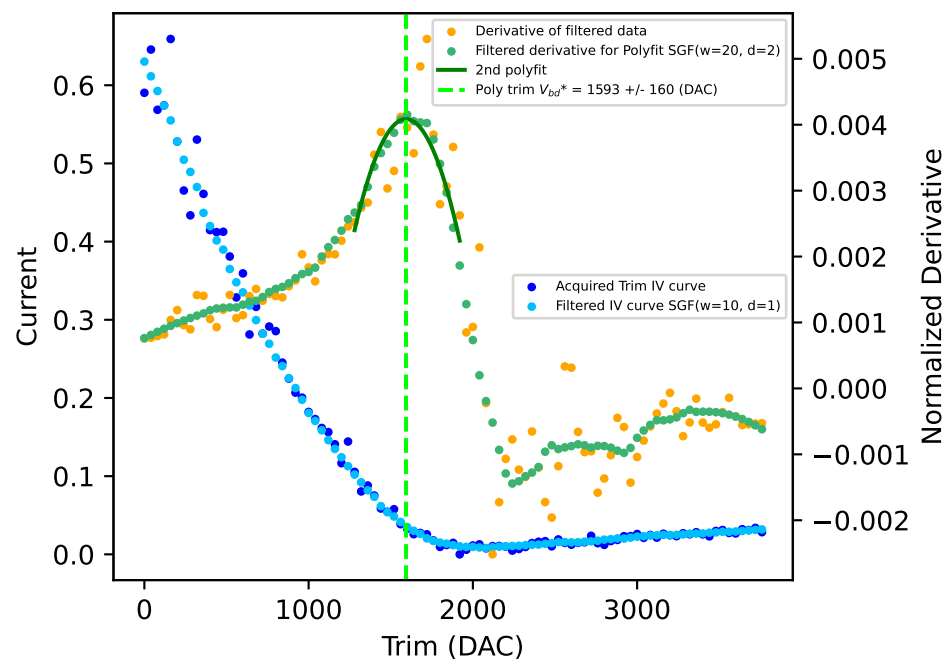
Bias DAC-VOLT conversion



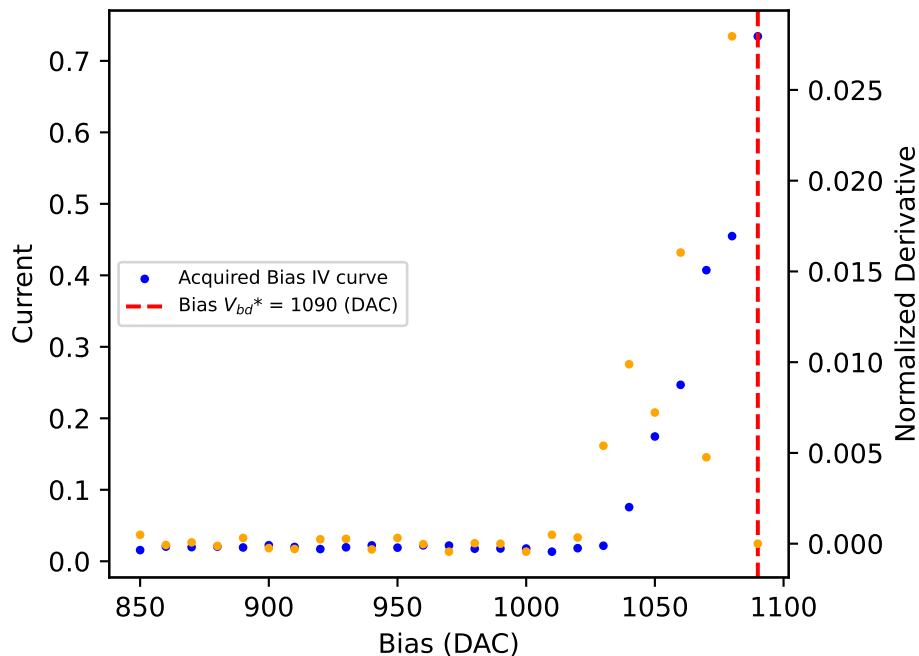
Bias + Trim REV IV curve



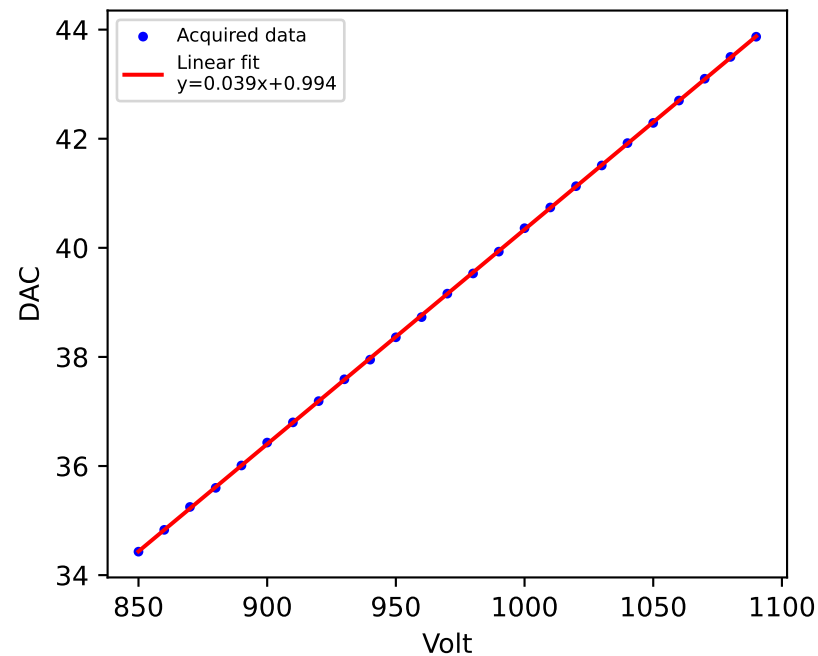
Trim REV IV curve



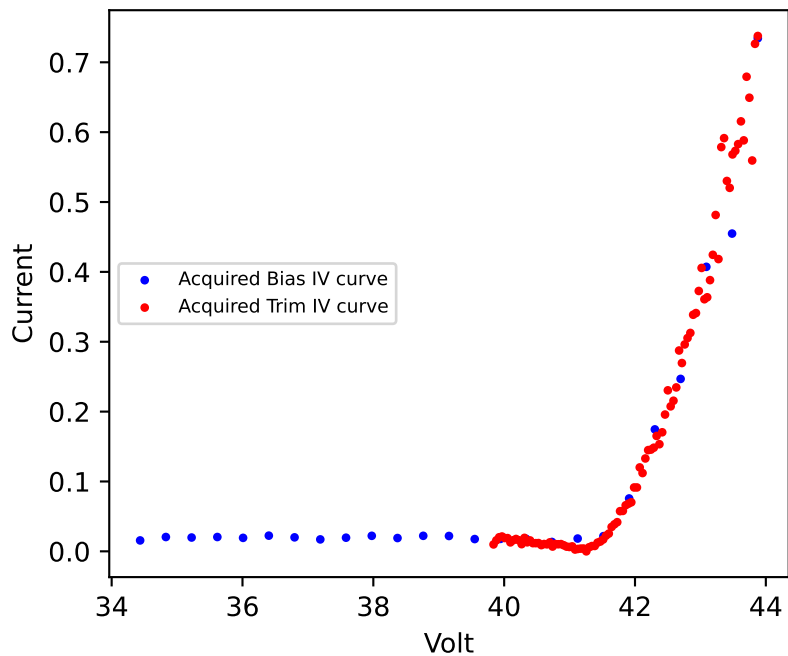
Bias REV IV curve



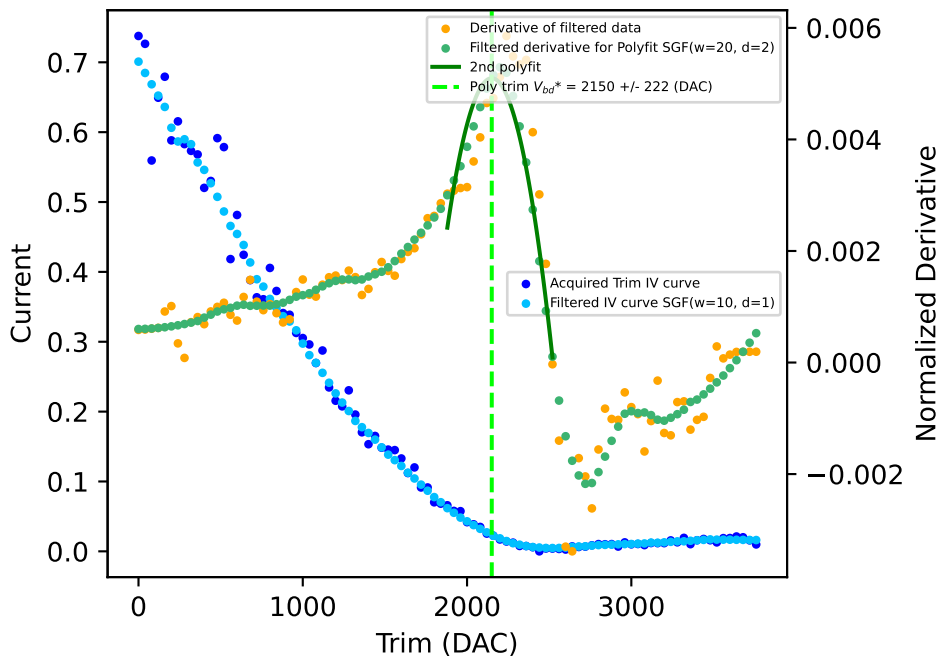
Bias DAC-VOLT conversion



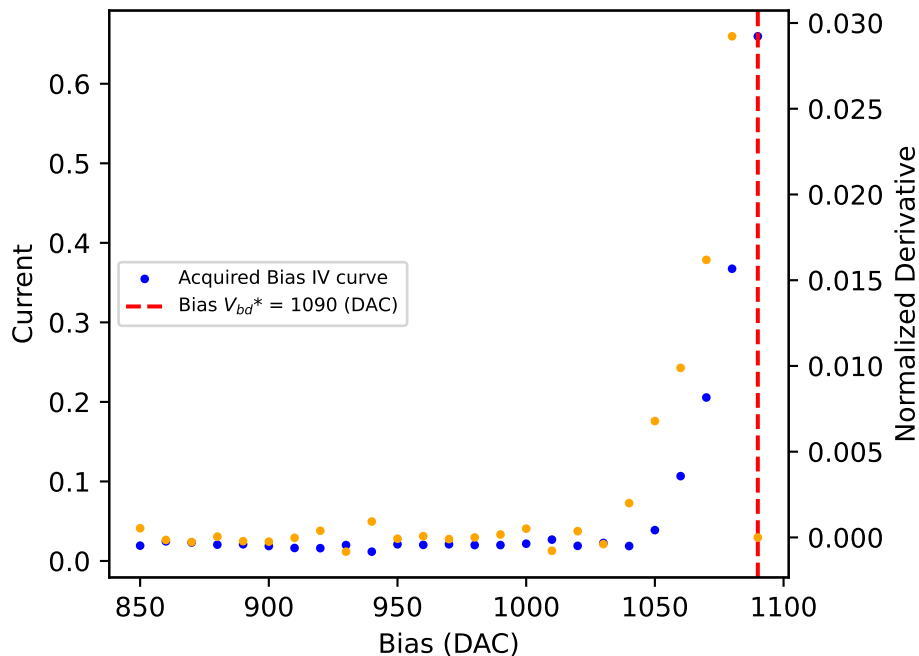
Bias + Trim REV IV curve



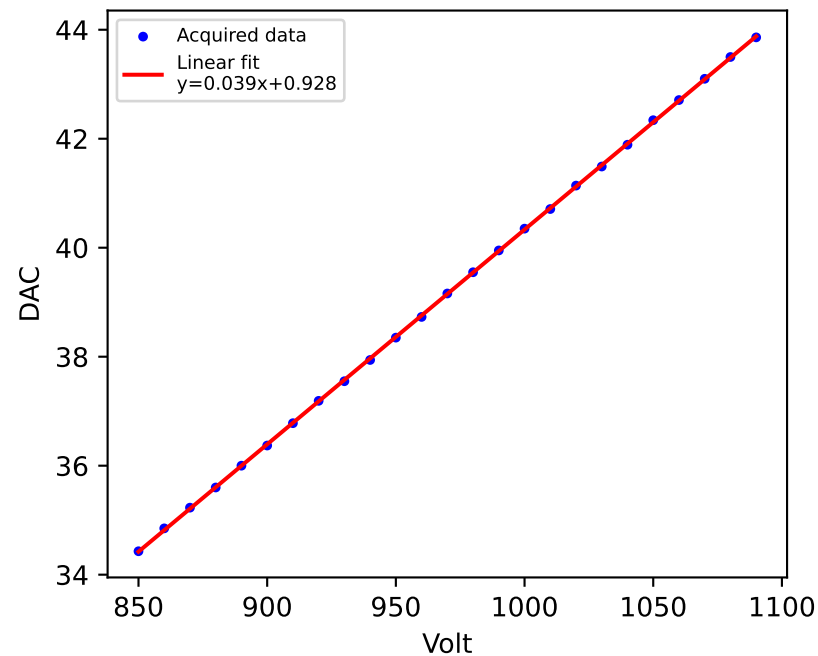
Trim REV IV curve



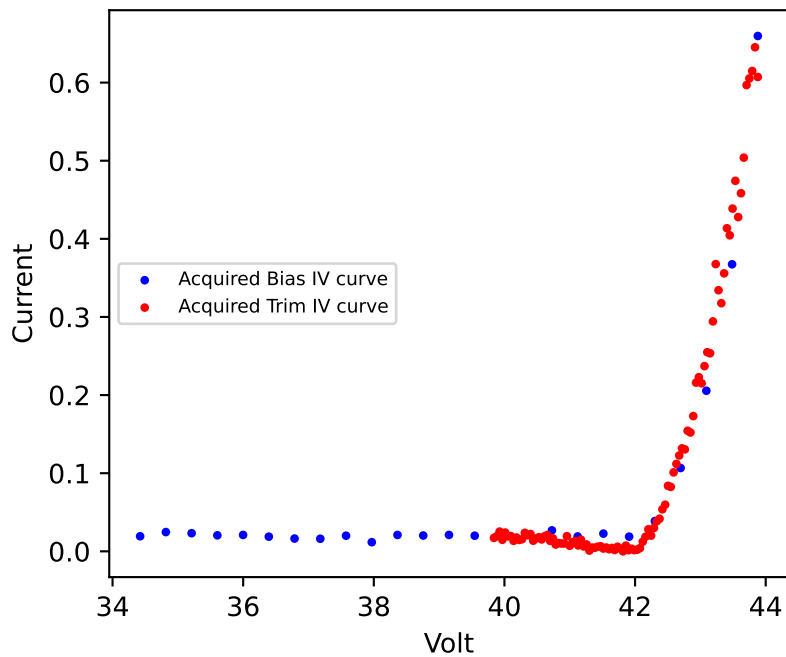
Bias REV IV curve



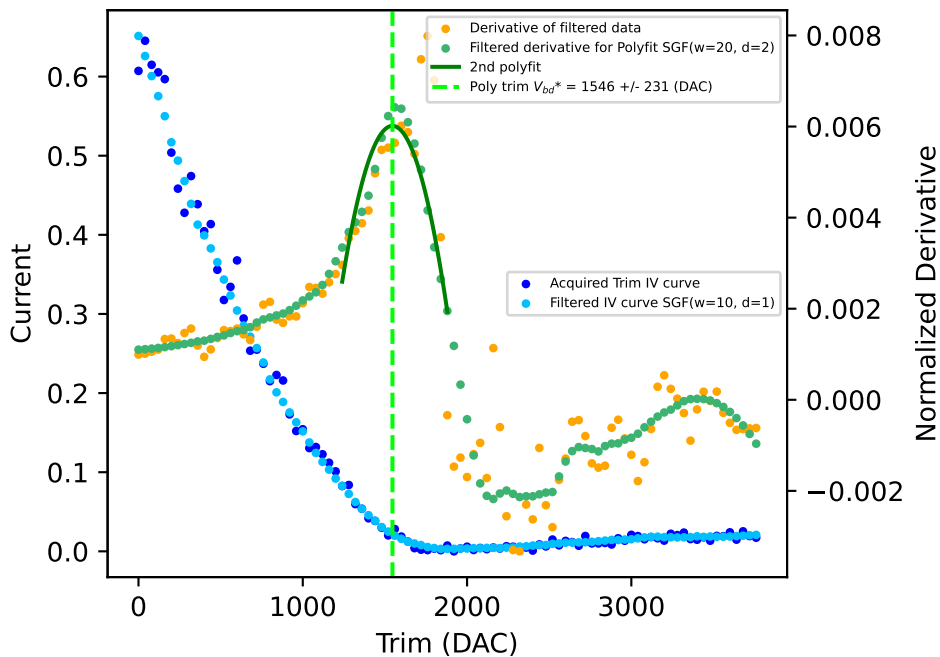
Bias DAC-VOLT conversion



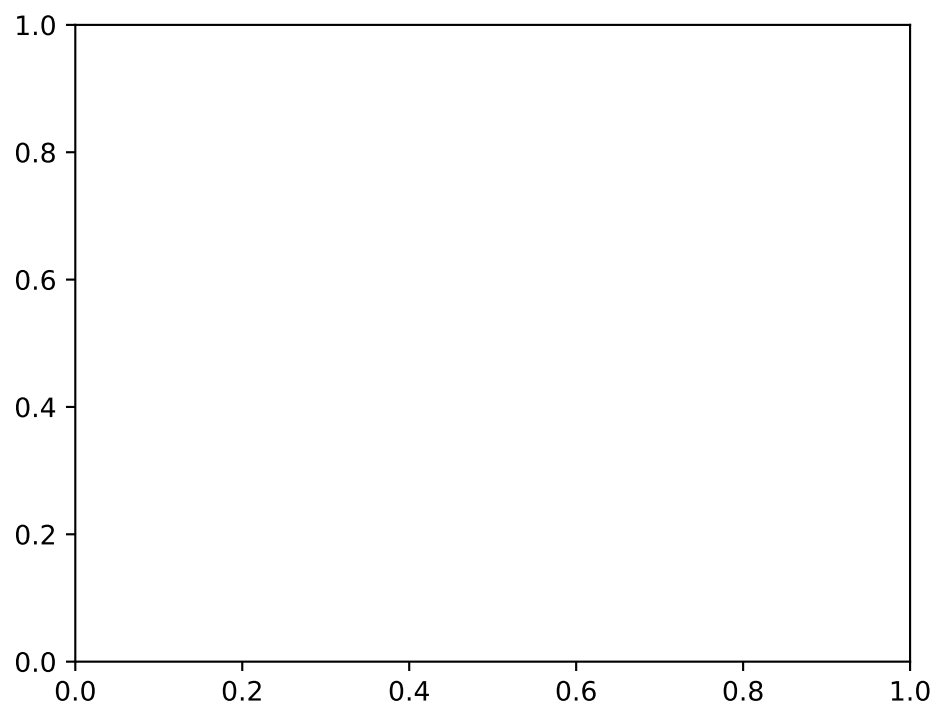
Bias + Trim REV IV curve



Trim REV IV curve



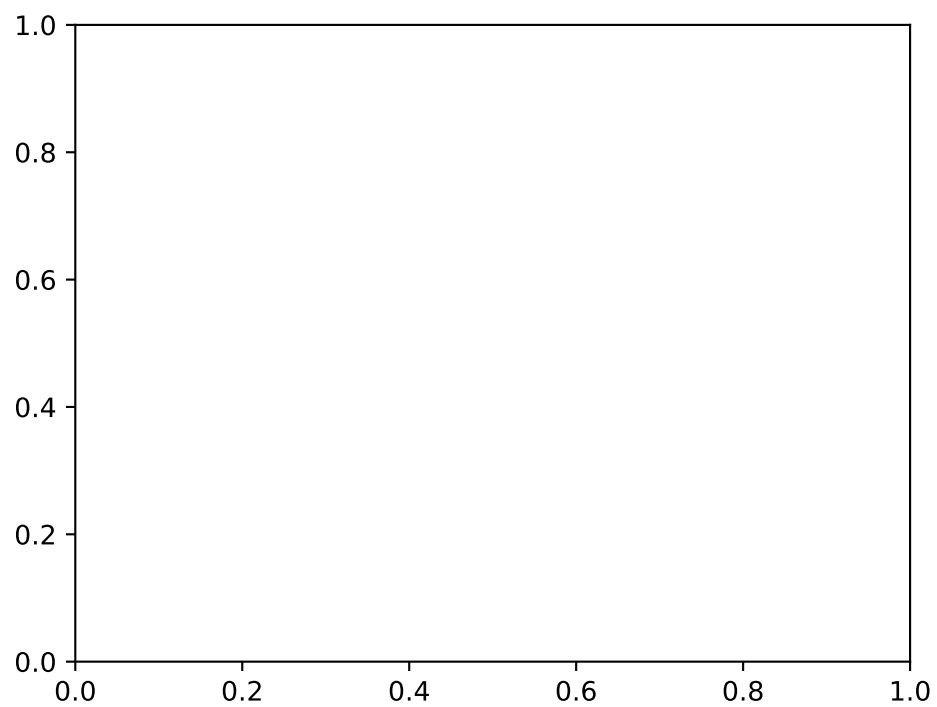
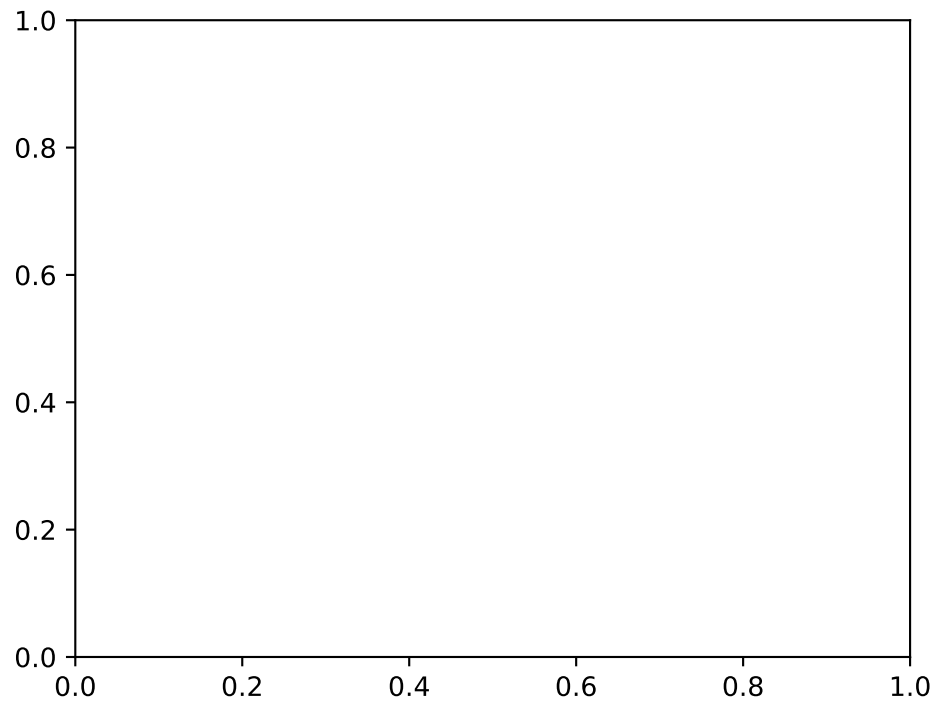
ENDPOINT:109 APA:2 AFE:4 Config_CH:35 CH_daq:43 SiPM:HPK --> MISSING FILE



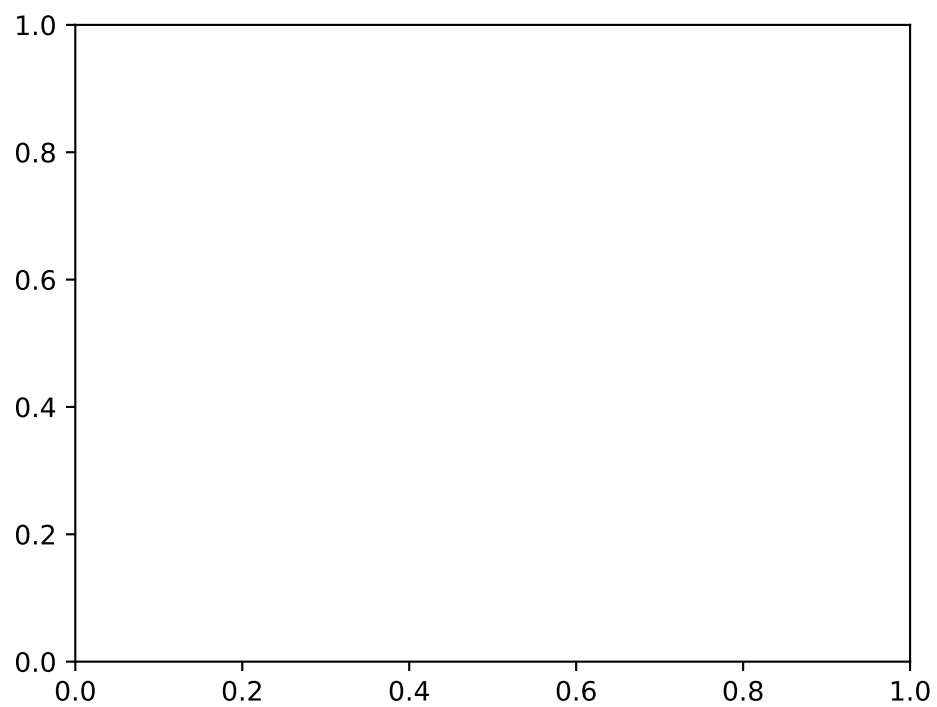
ENDPOINT:109 APA:2 AFE:4 Config_CH:36 CH_daq:44 SiPM:HPK --> MISSING FILE



ENDPOINT:109 APA:2 AFE:4 Config_CH:37 CH_daq:45 SiPM:HPK --> MISSING FILE



ENDPOINT:109 APA:2 AFE:4 Config_CH:38 CH_daq:46 SiPM:HPK --> MISSING FILE



ENDPOINT:109 APA:2 AFE:4 Config_CH:39 CH_daq:47 SiPM:HPK --> MISSING FILE

