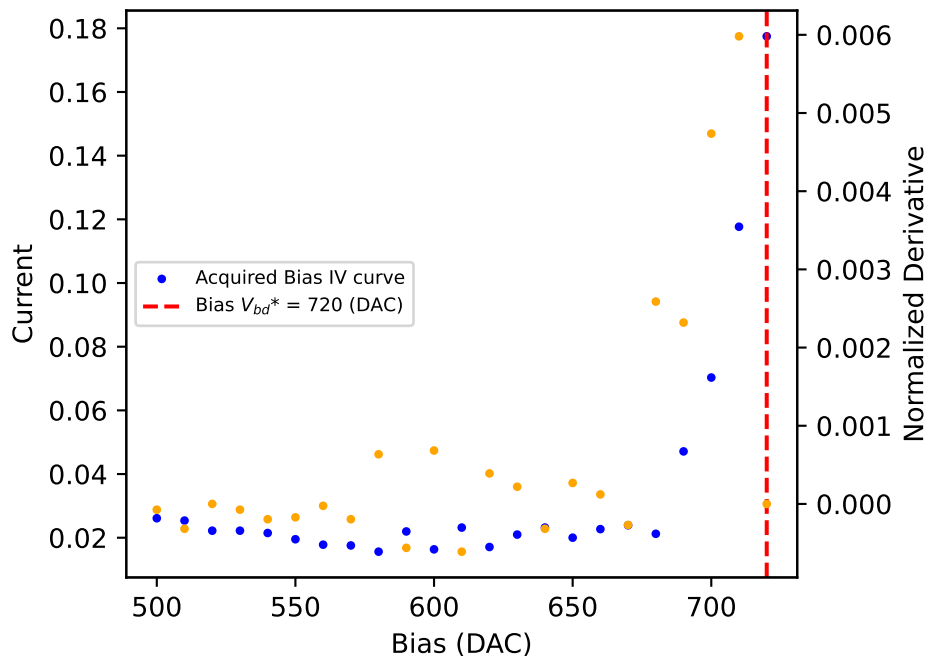
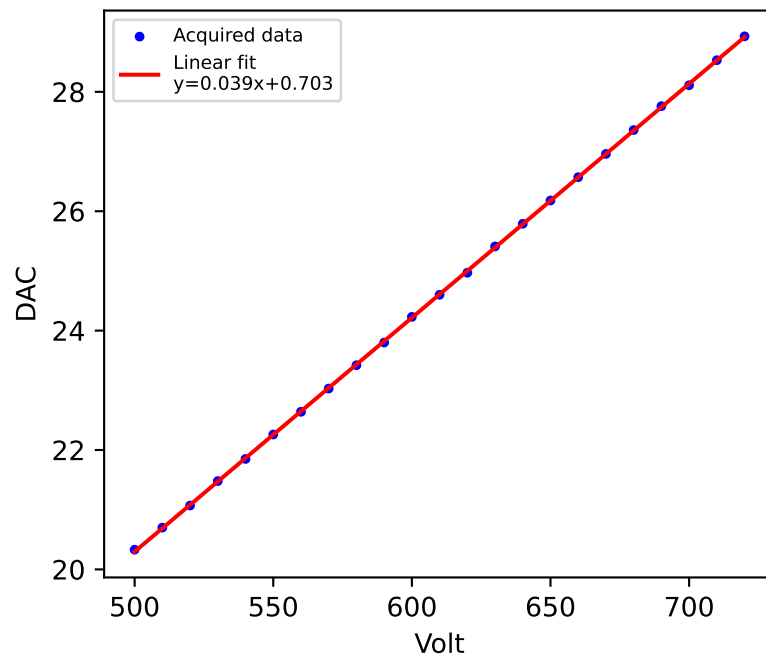


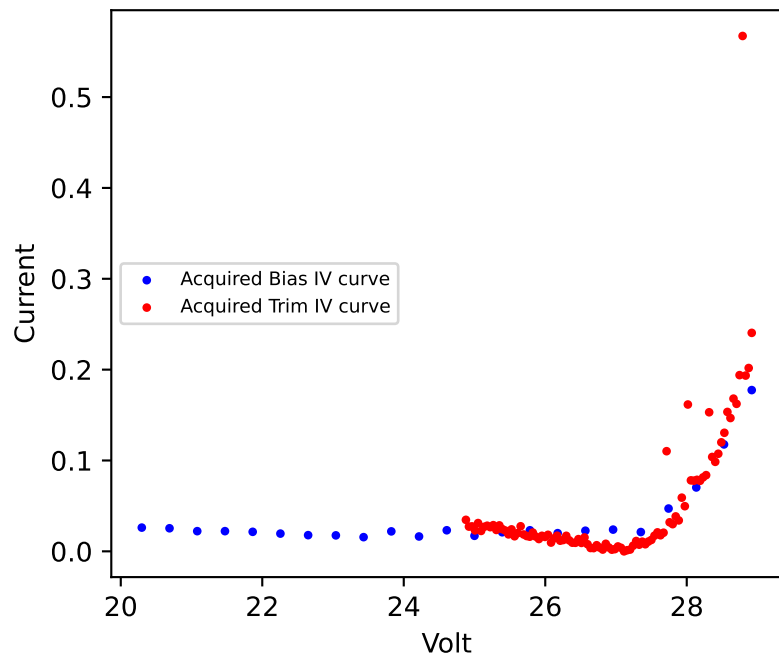
Bias REV IV curve



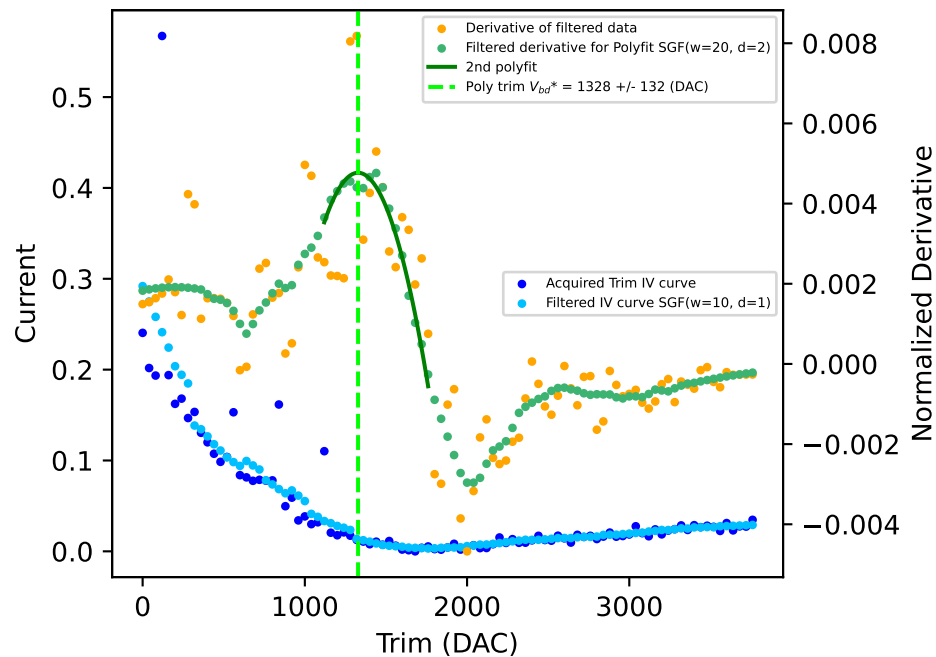
Bias DAC-VOLT conversion



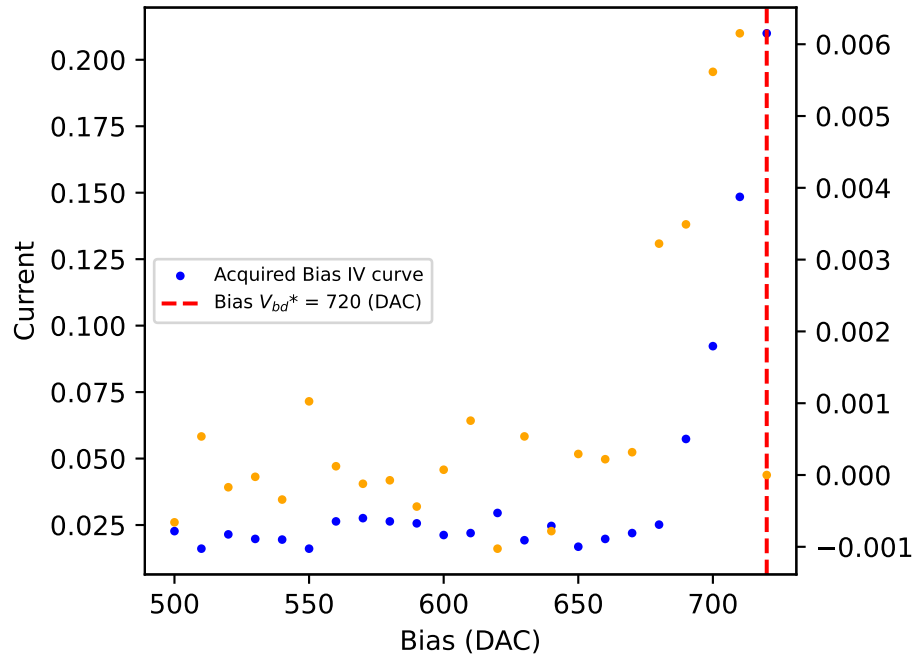
Bias + Trim REV IV curve



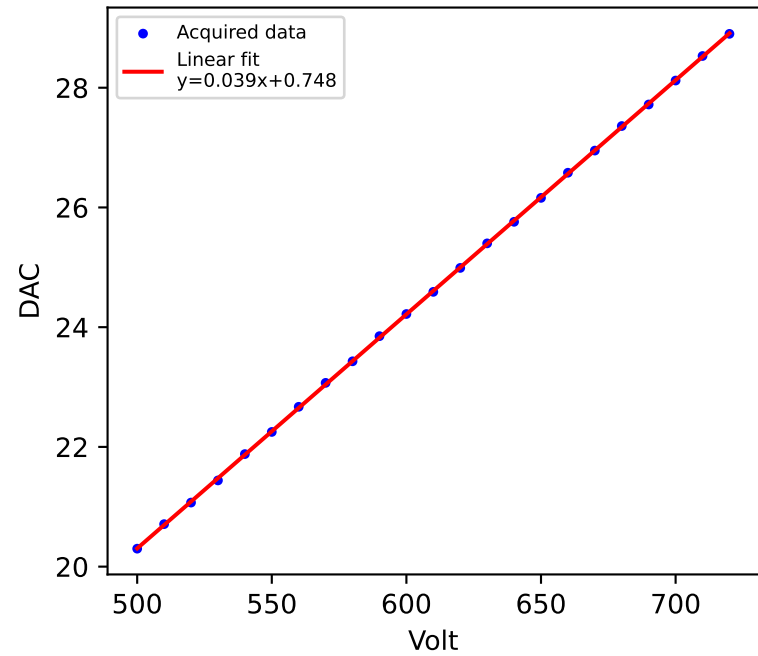
Trim REV IV curve



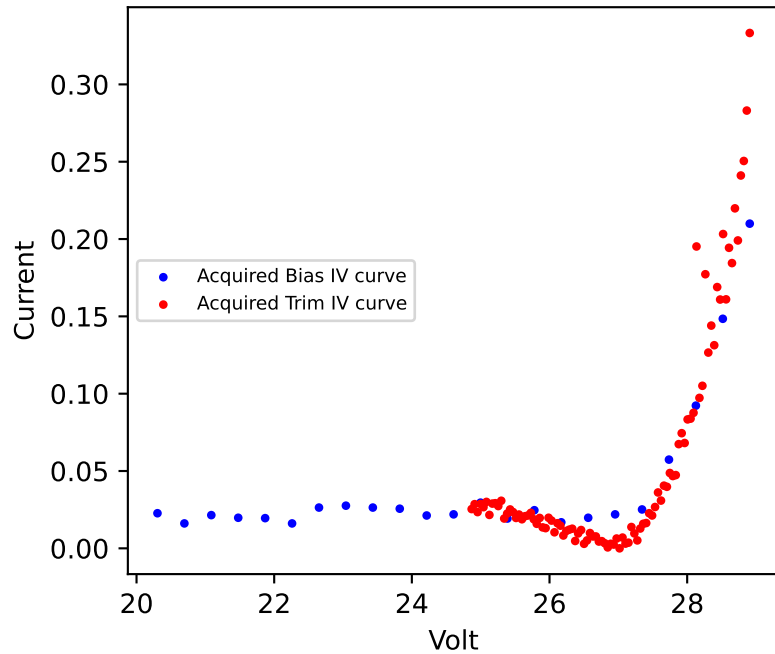
Bias REV IV curve



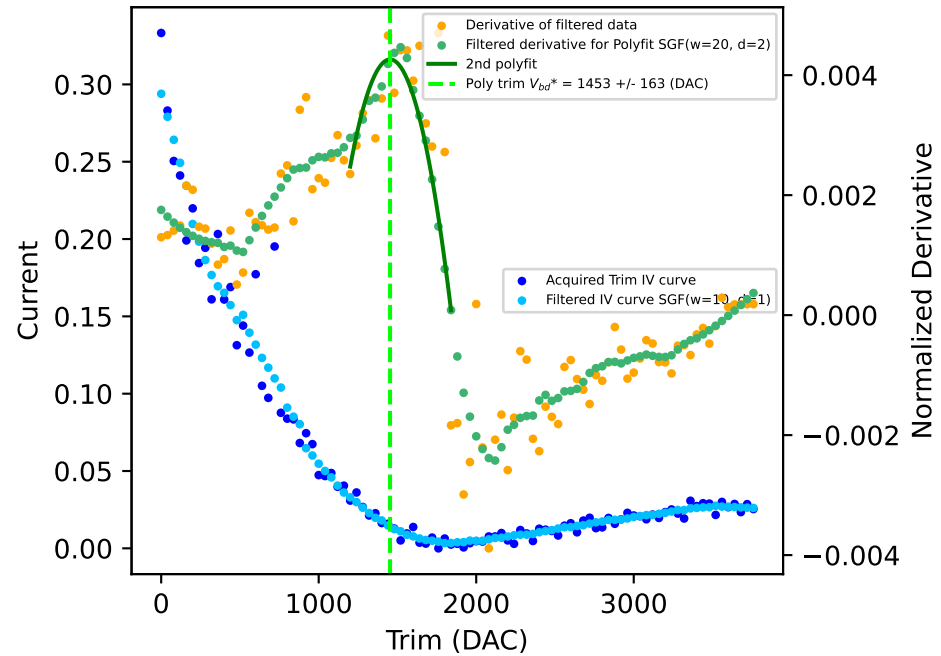
Bias DAC-VOLT conversion



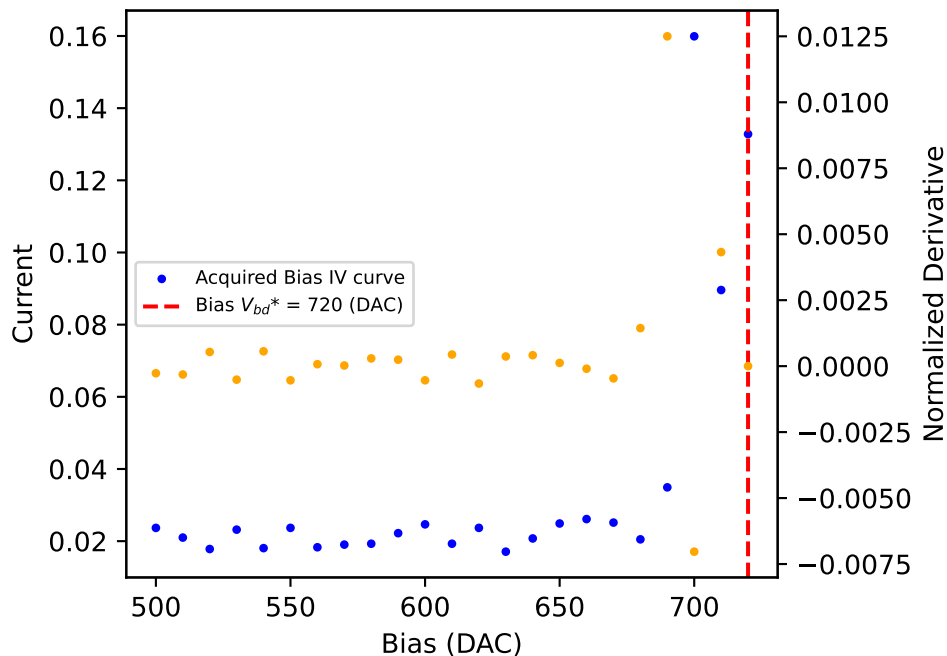
Bias + Trim REV IV curve



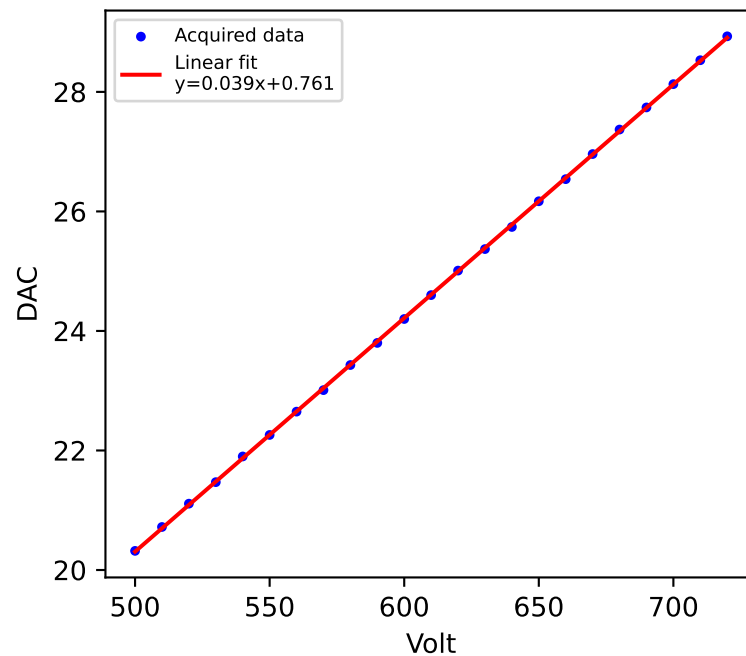
Trim REV IV curve



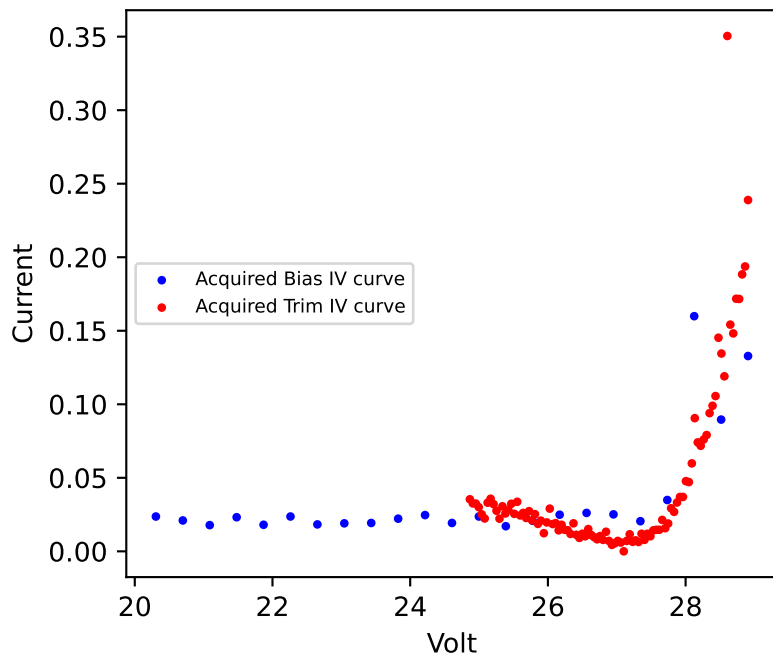
Bias REV IV curve



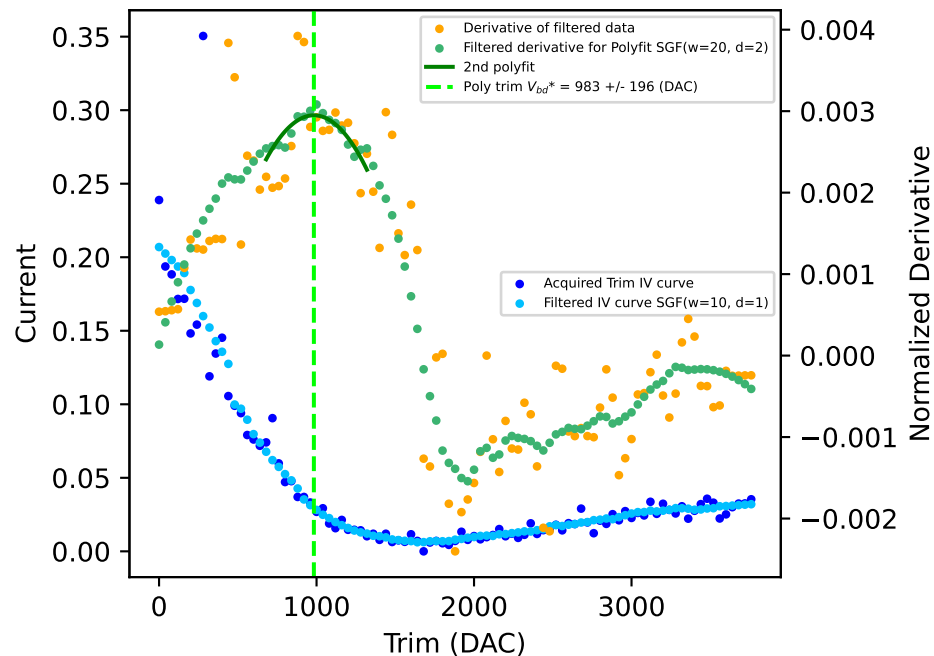
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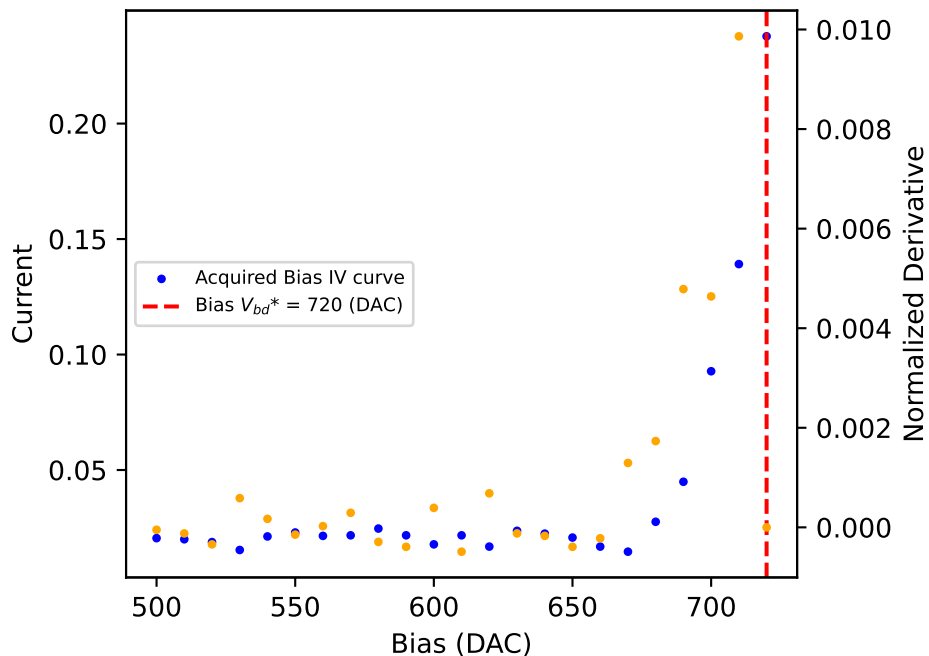
Bias + Trim REV IV curve



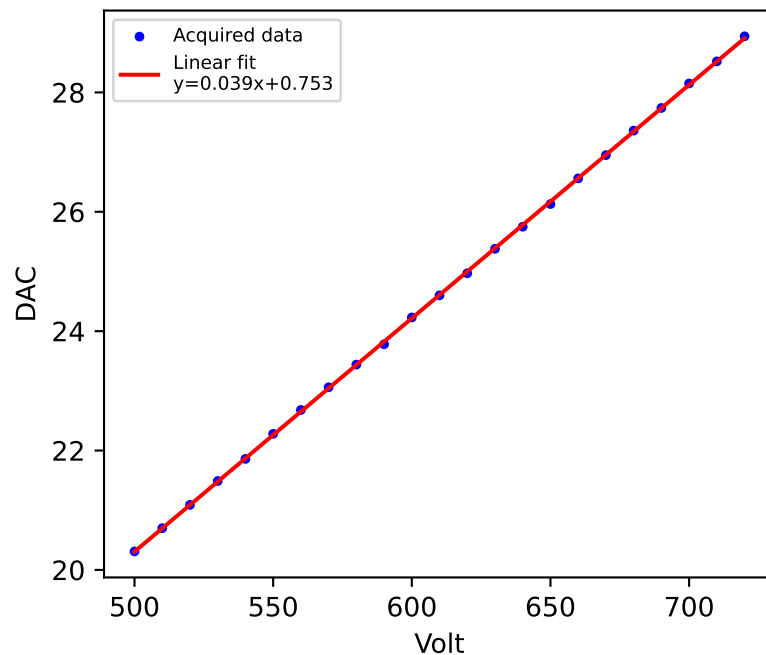
Trim REV IV curve



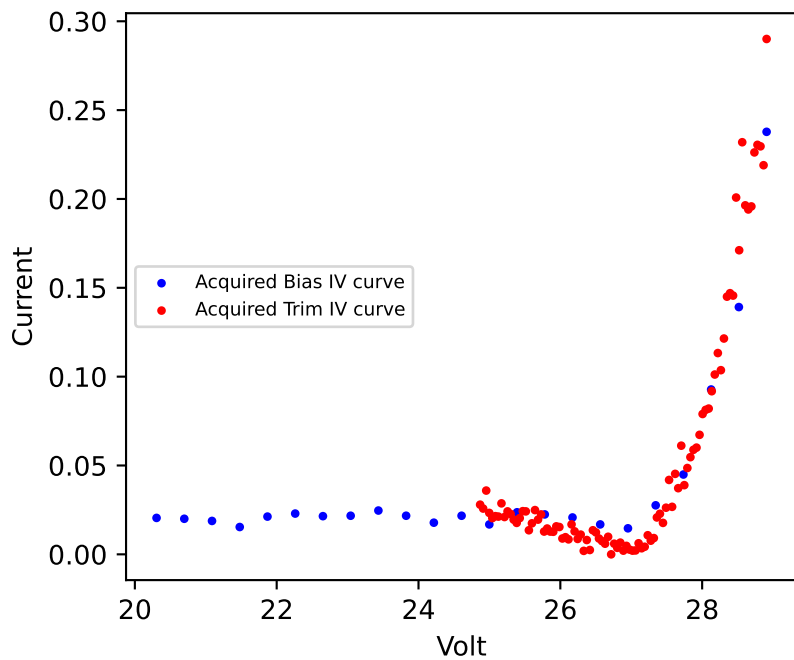
Bias REV IV curve



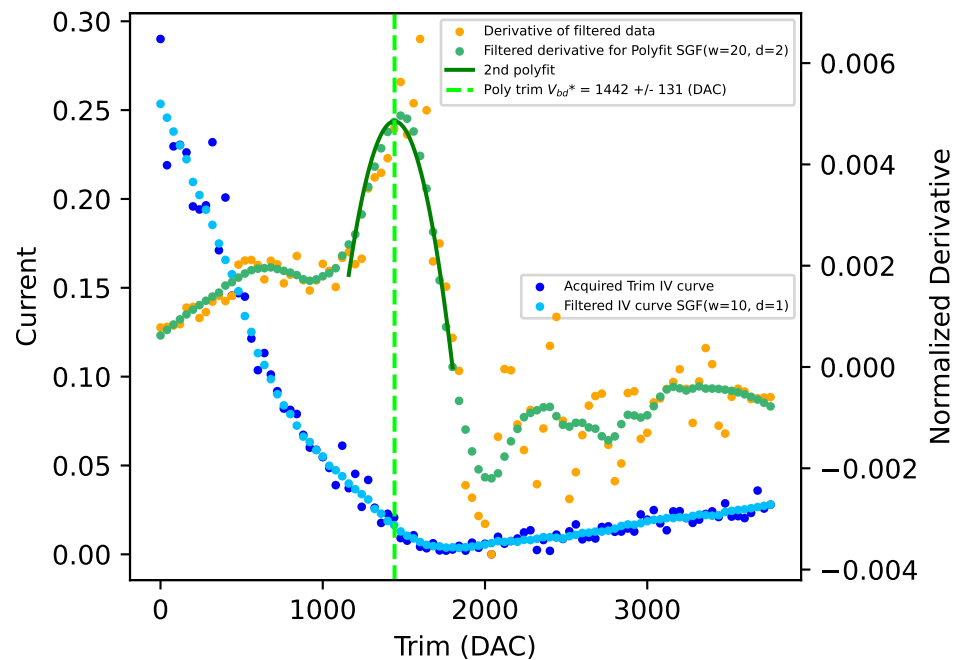
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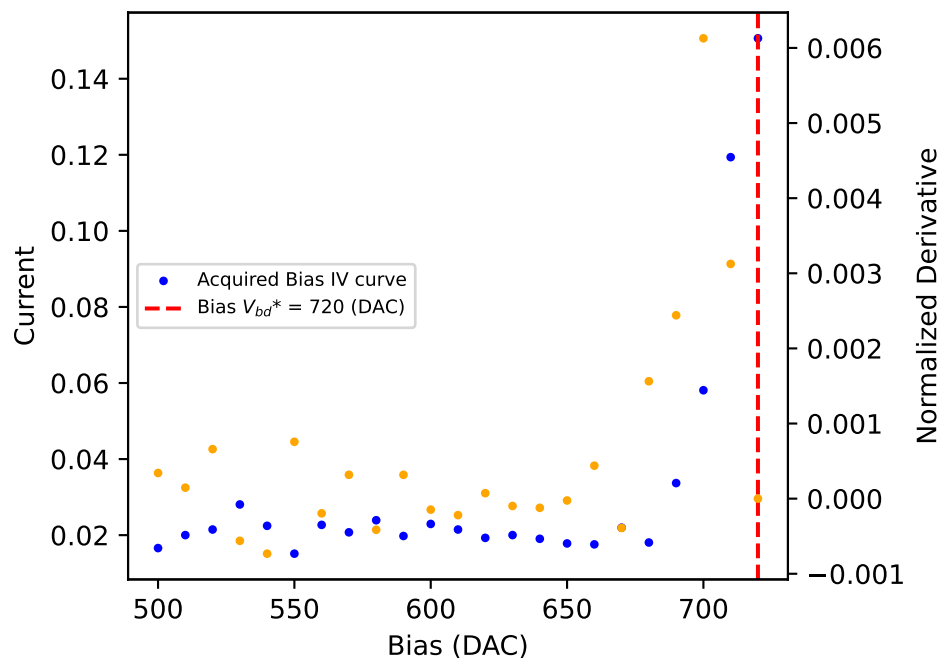
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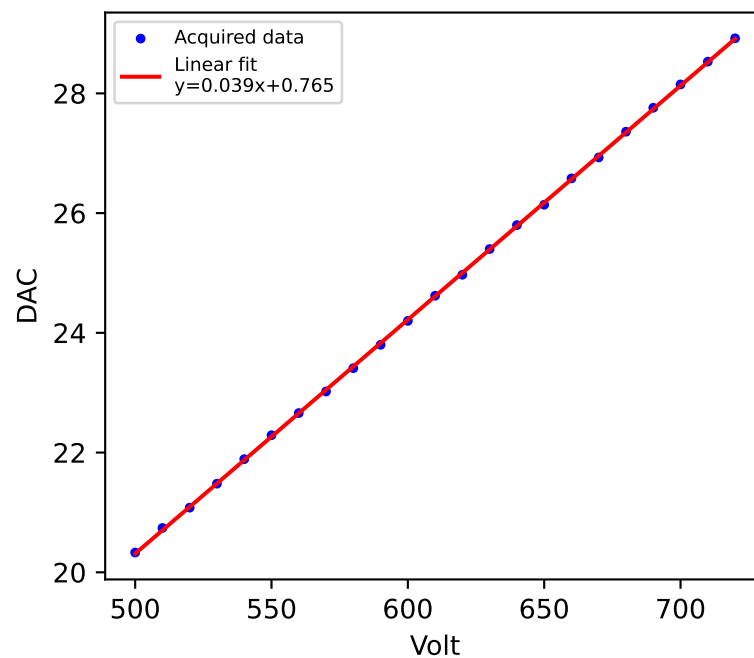
Trim REV IV curve



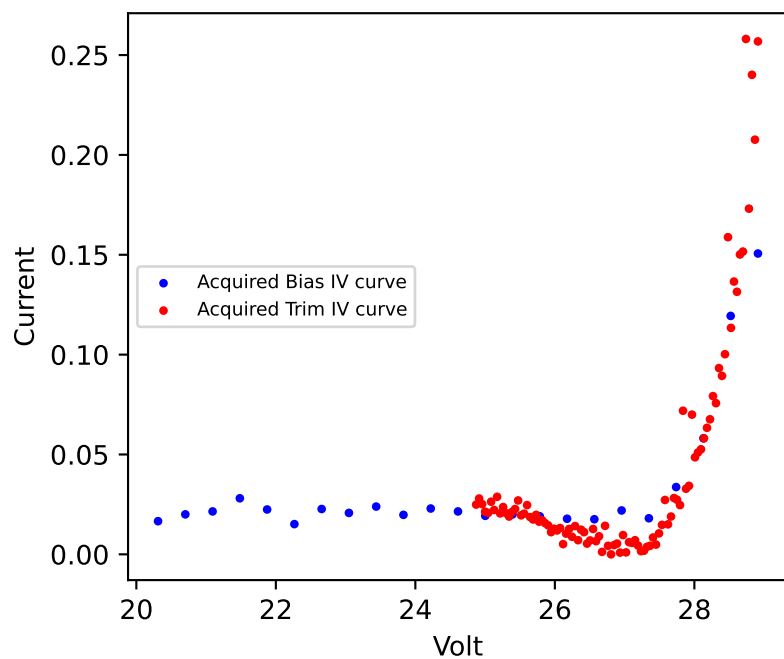
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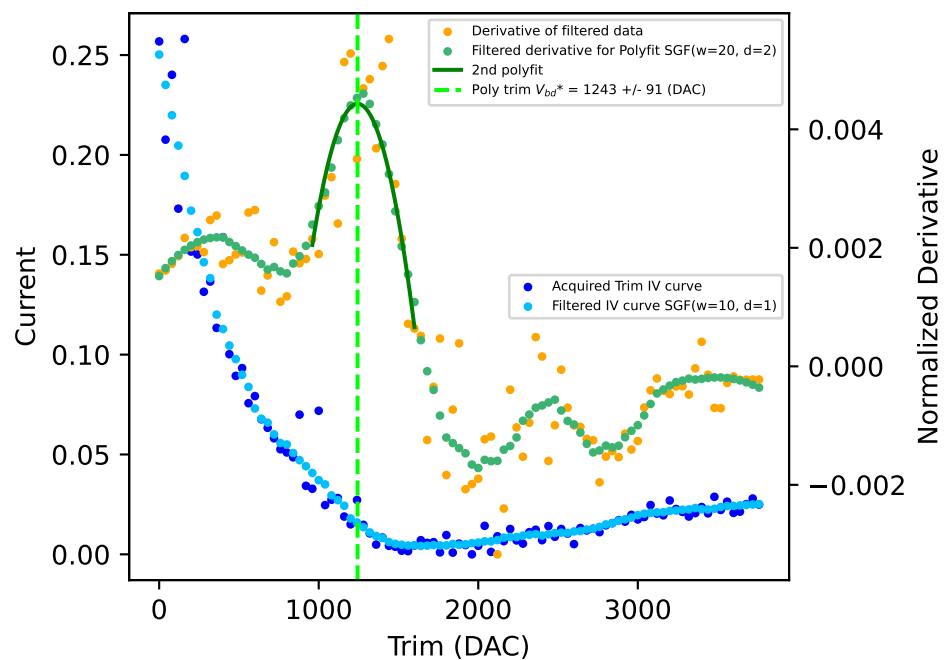
Bias DAC-VOLT conversion



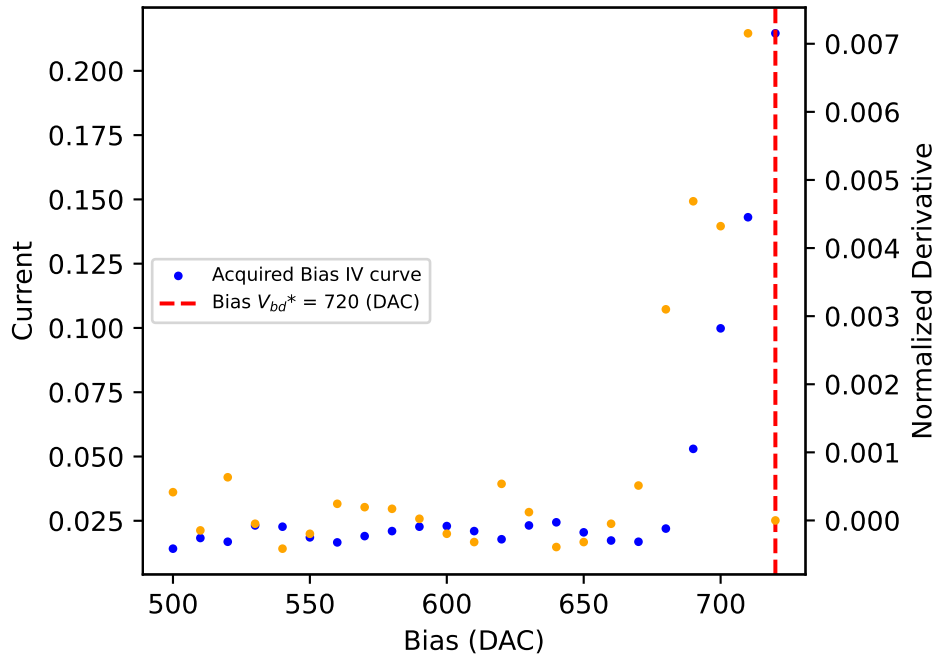
Bias + Trim REV IV curve



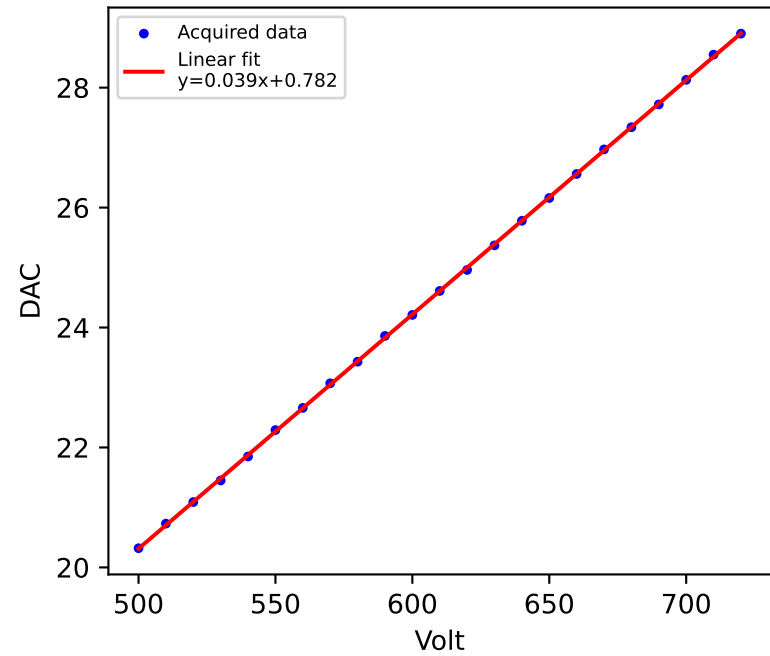
Trim REV IV curve



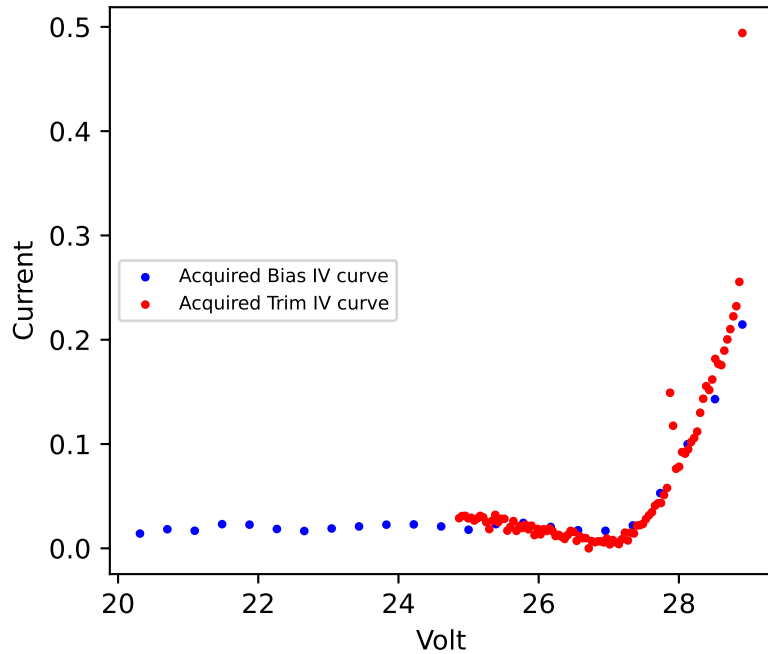
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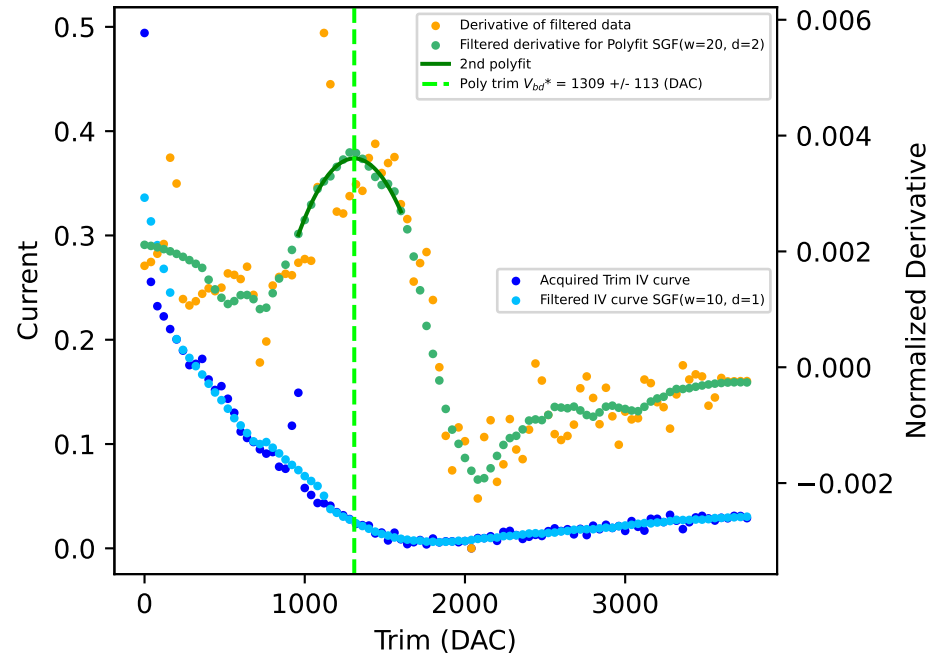
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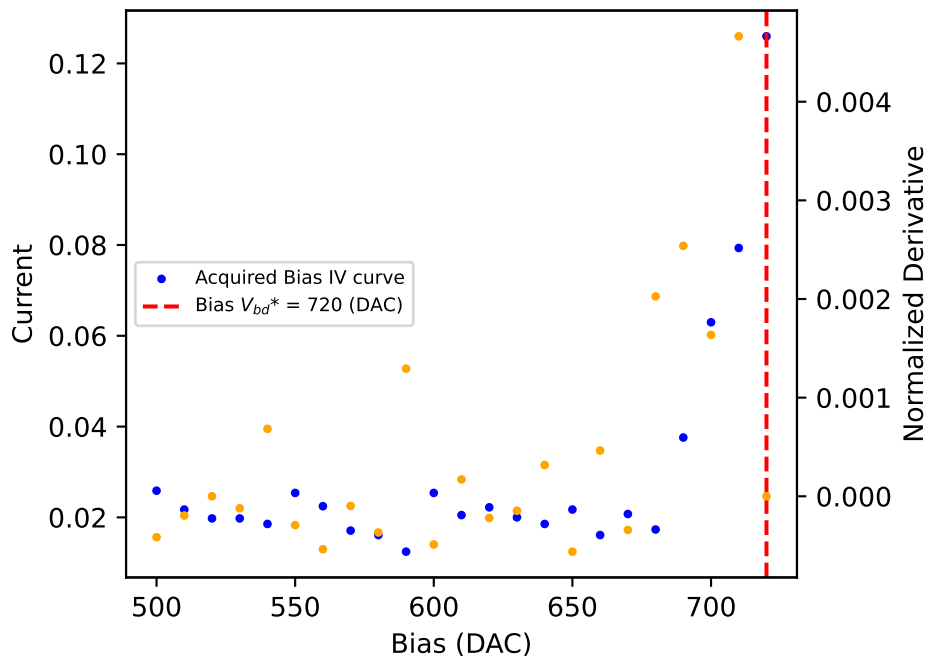
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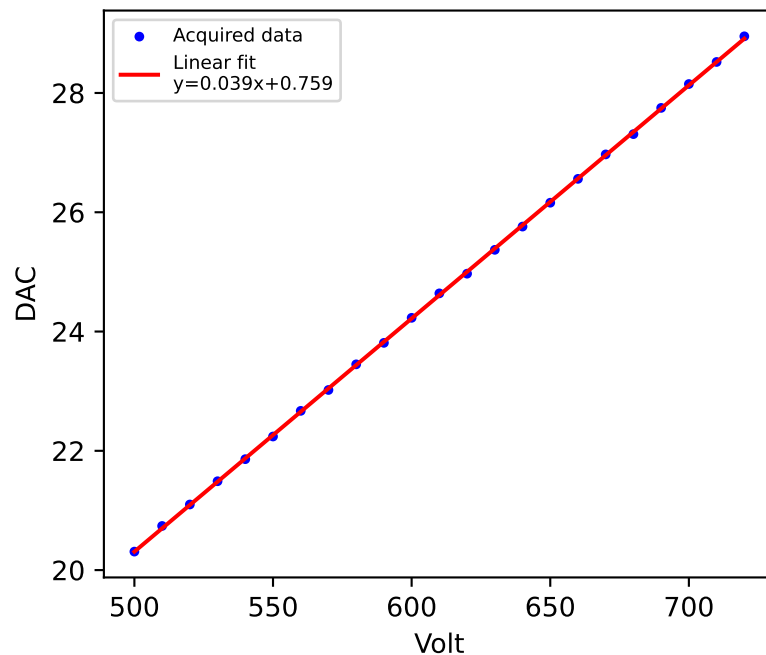
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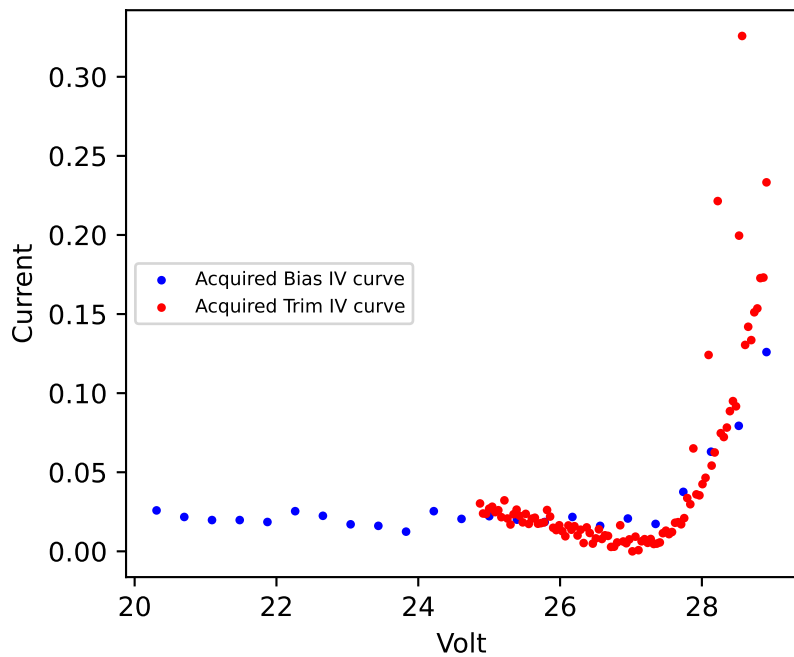
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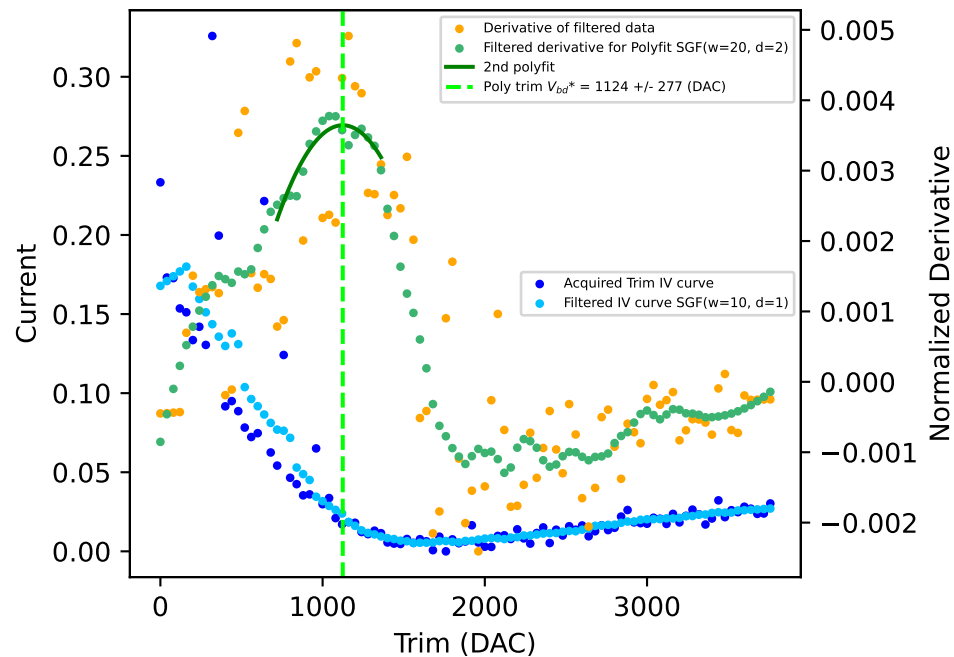
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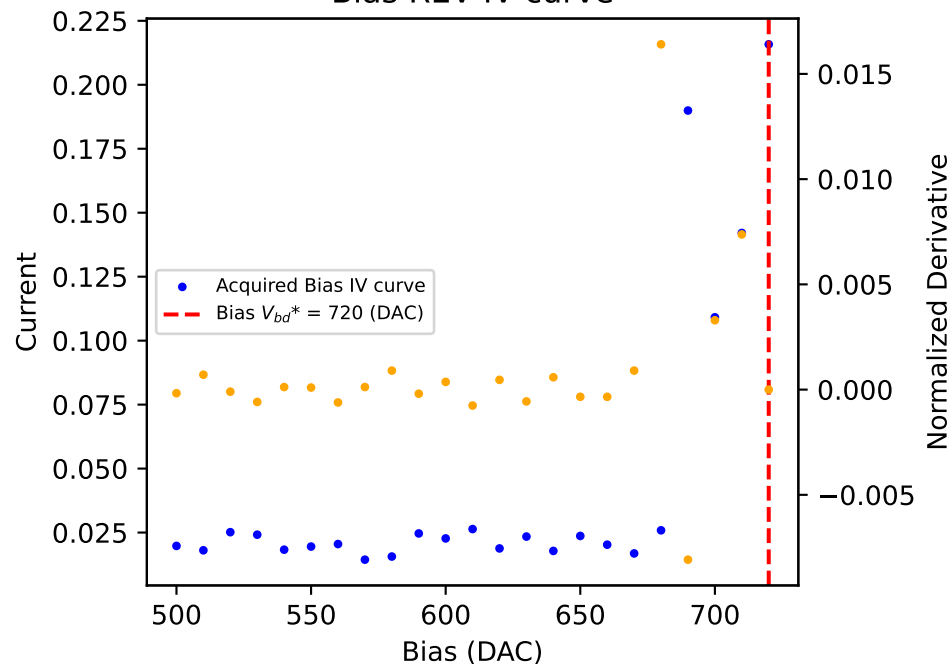
Bias + Trim REV IV curve



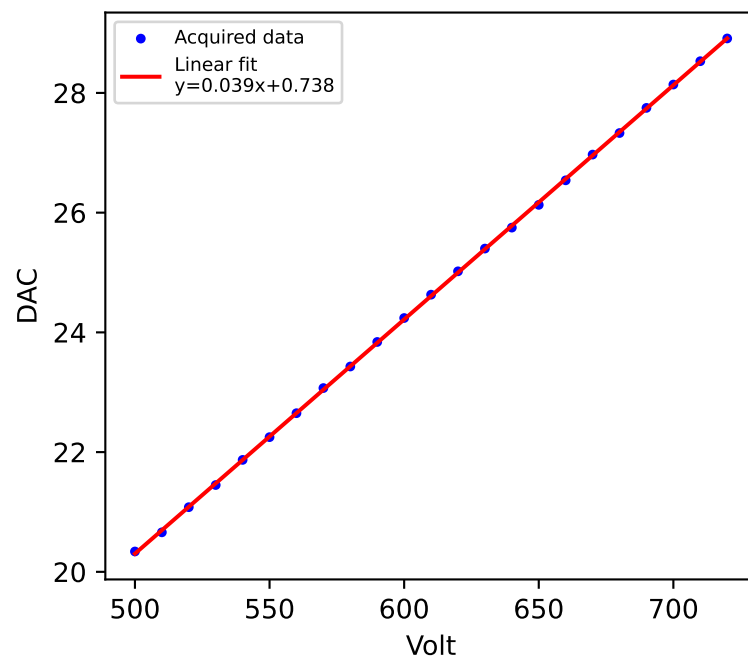
Trim REV IV curve



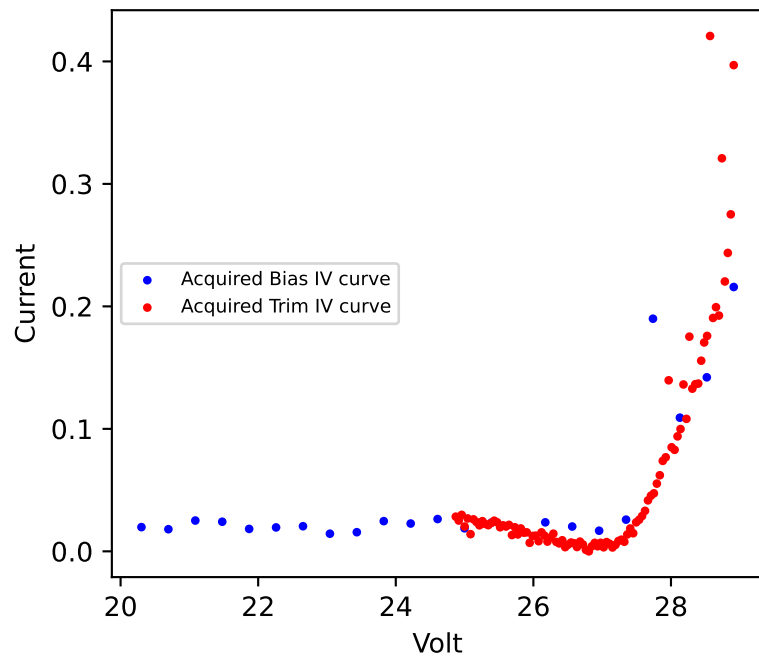
Bias REV IV curve



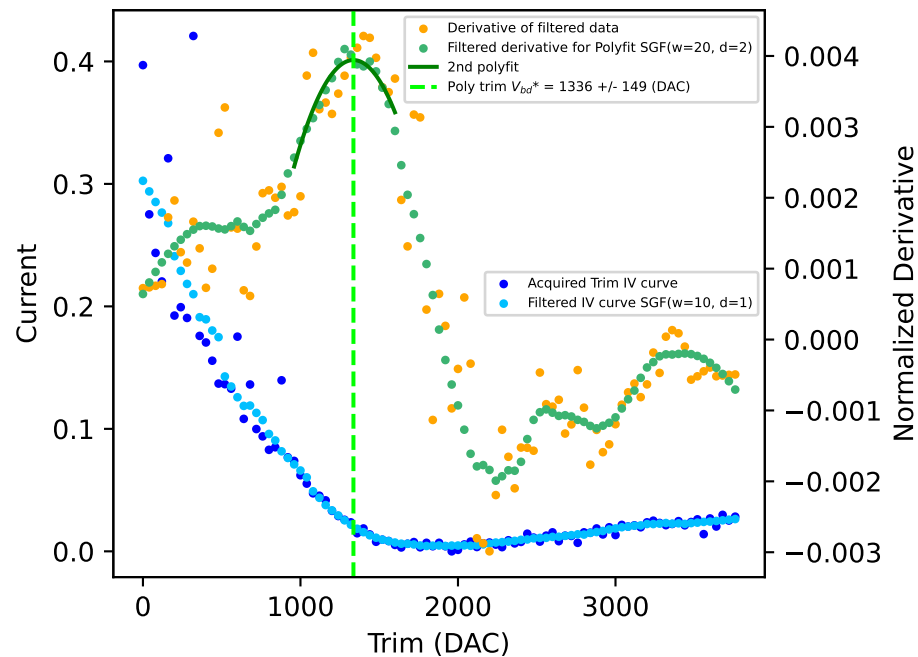
Bias DAC-VOLT conversion



Bias + Trim REV IV curve

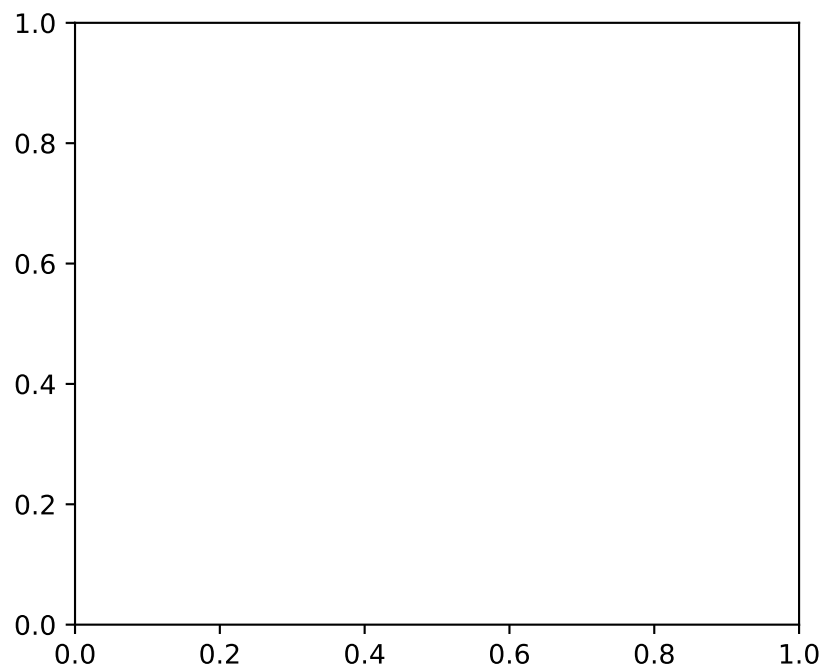
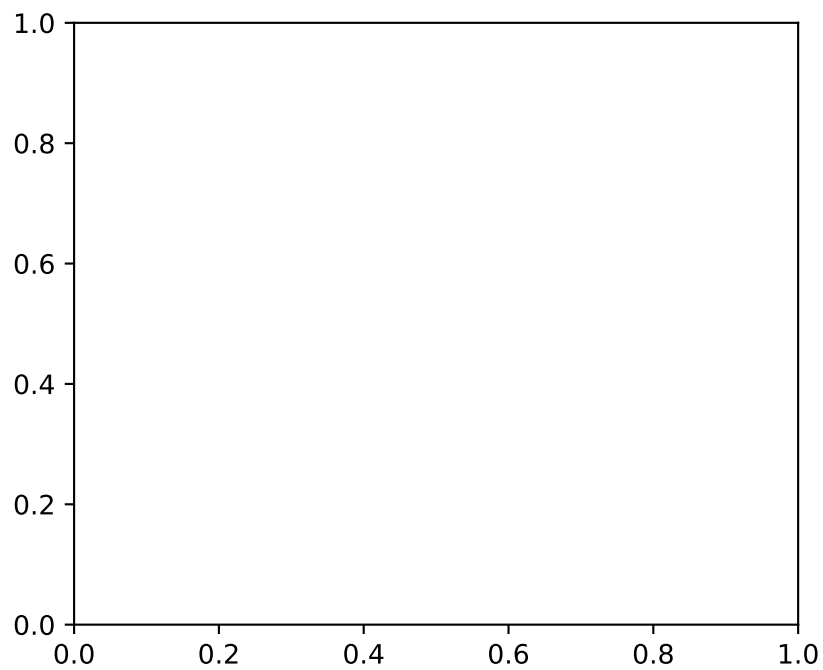
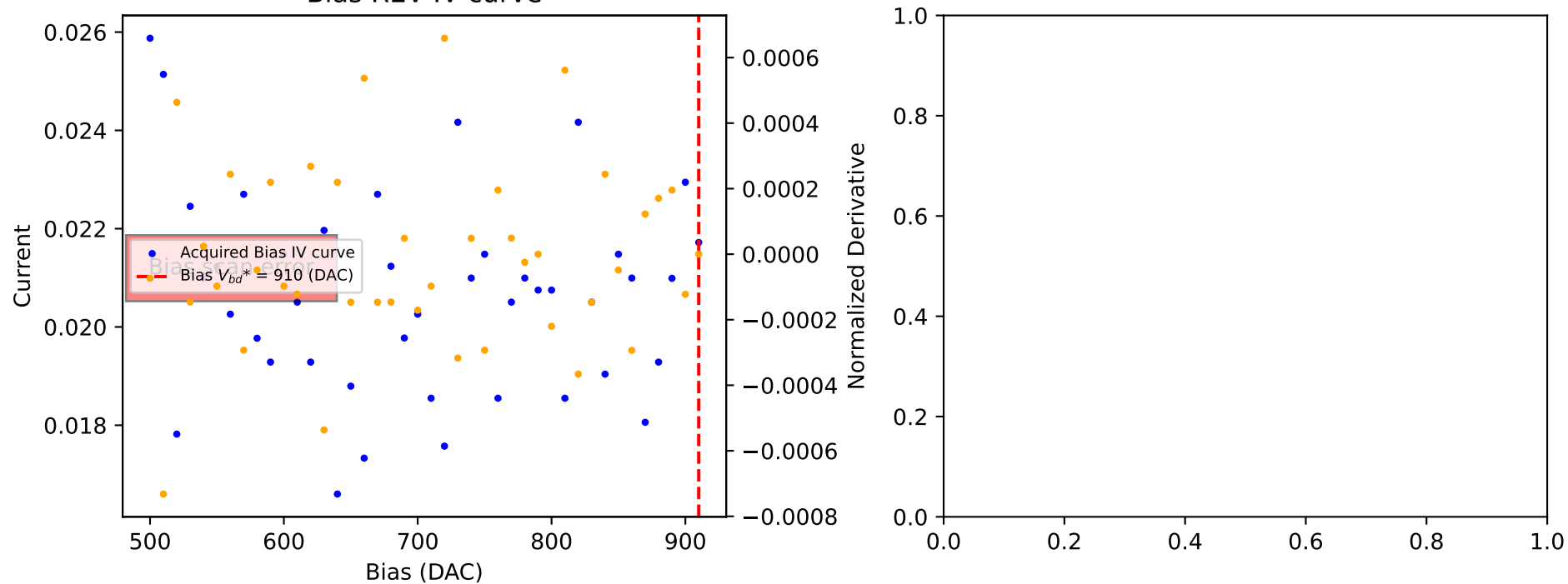


Trim REV IV curve





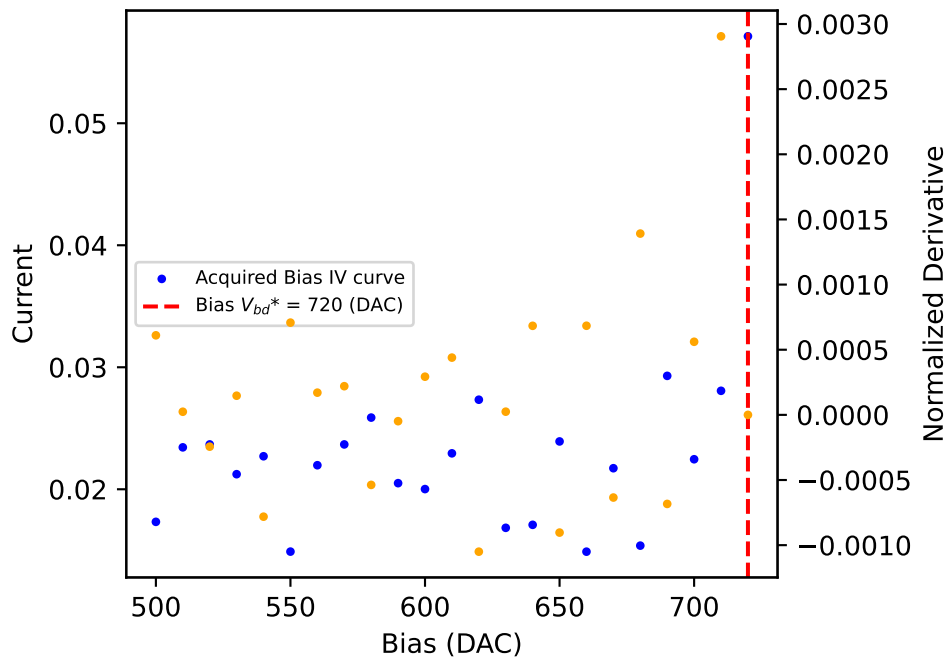
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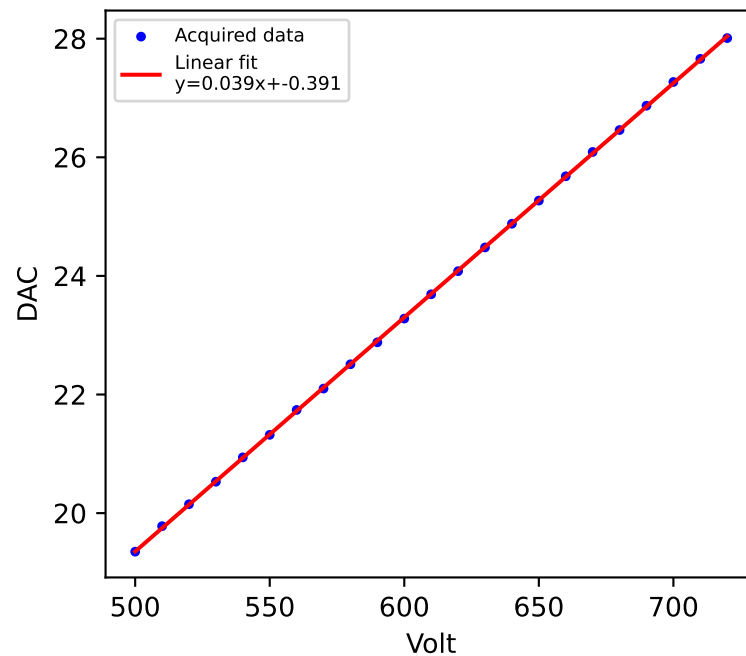
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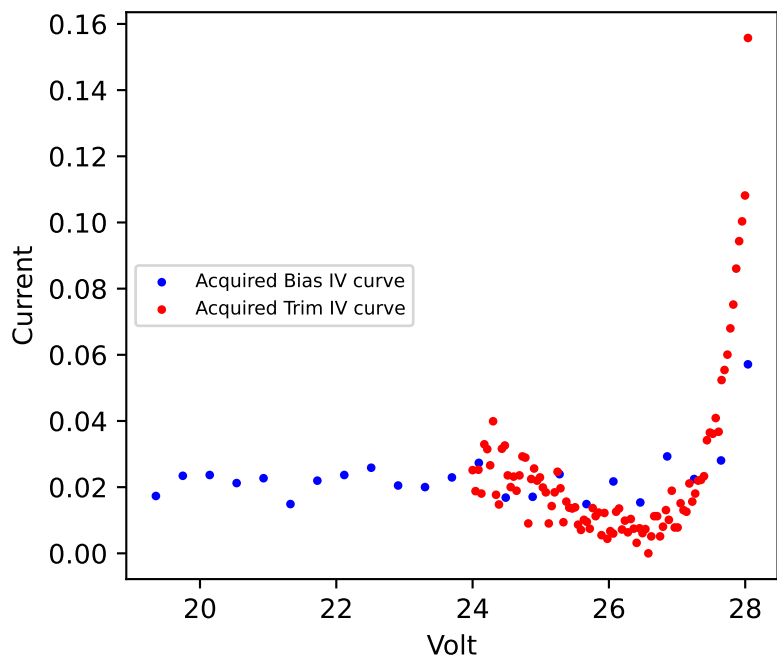
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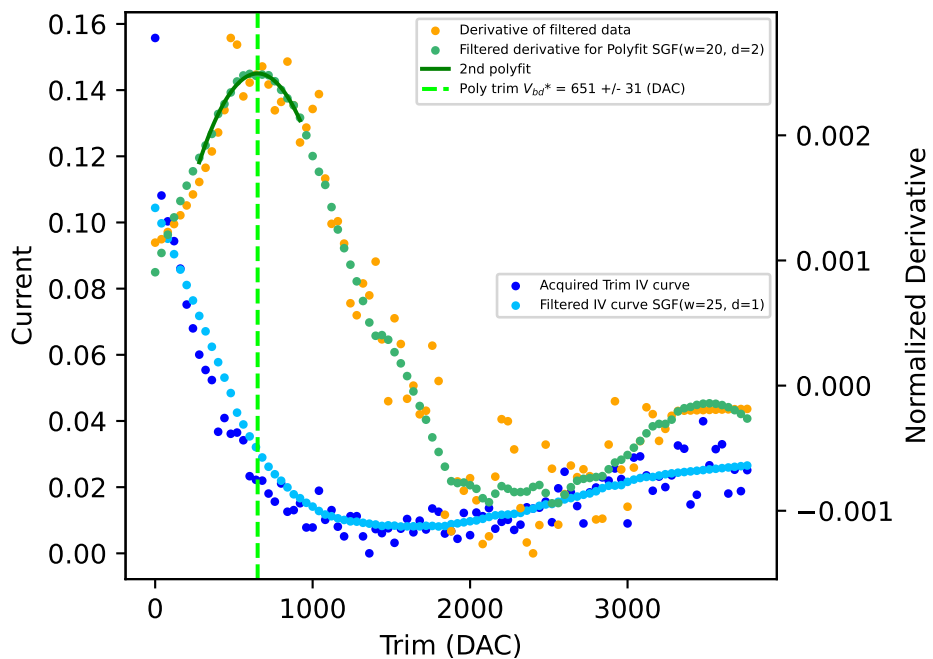
Bias DAC-VOLT conversion



Bias + Trim REV IV curve



Trim REV IV curve



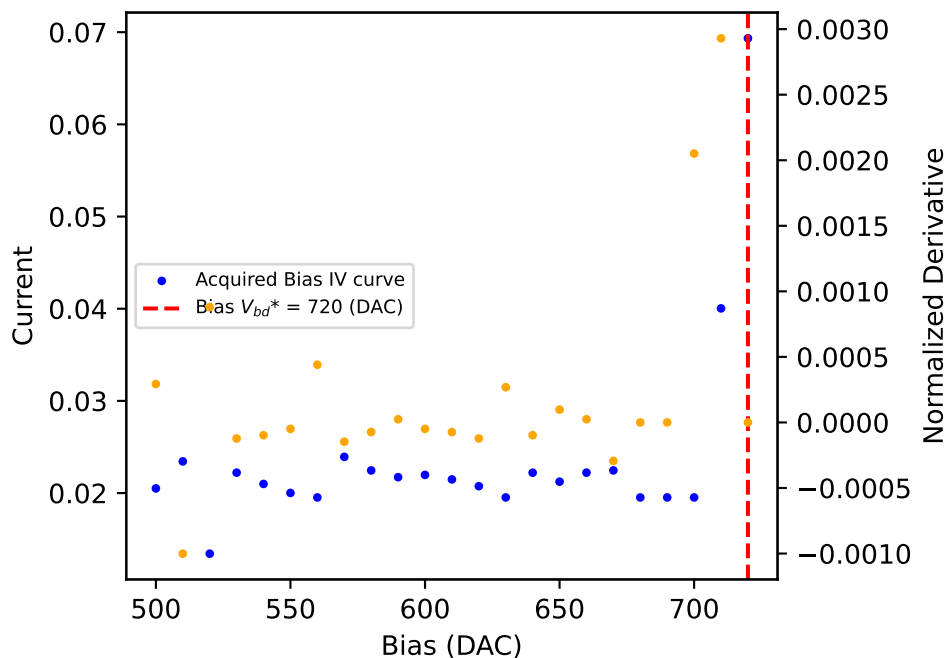
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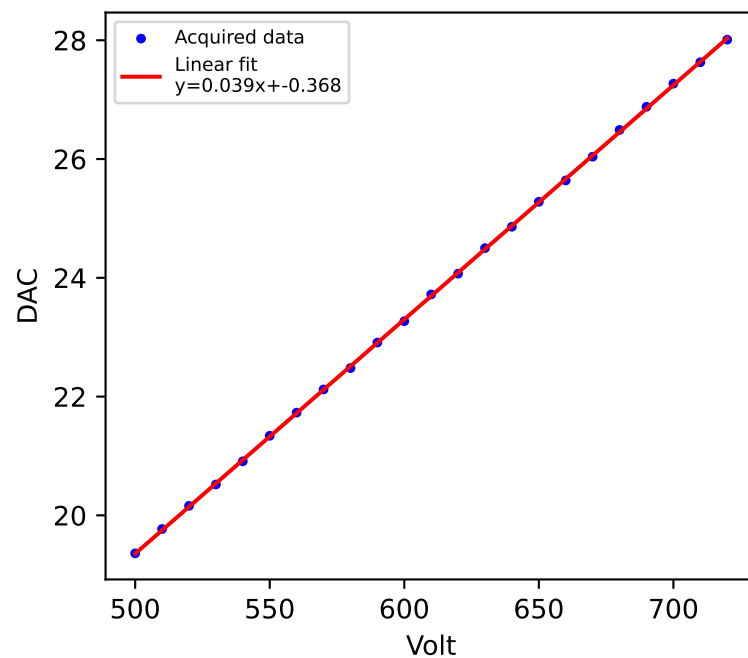
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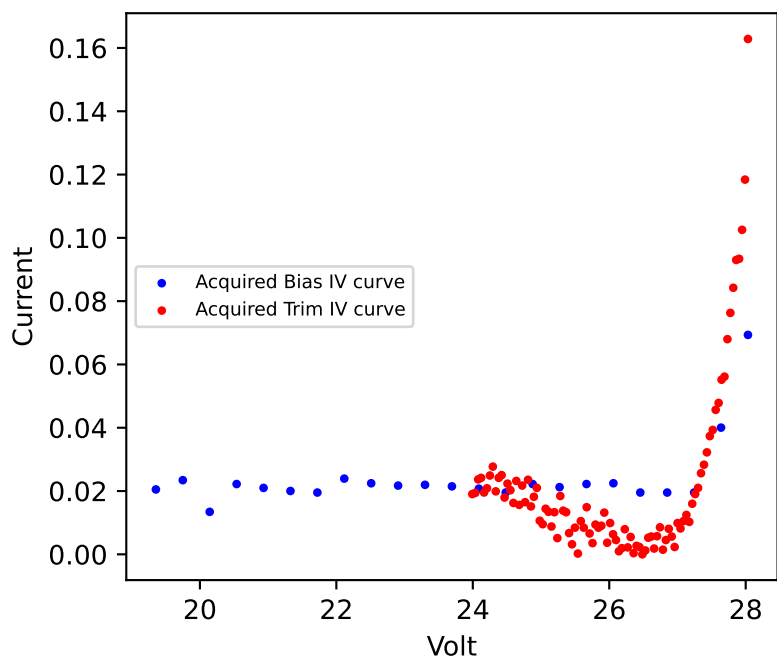
Bias REV IV curve



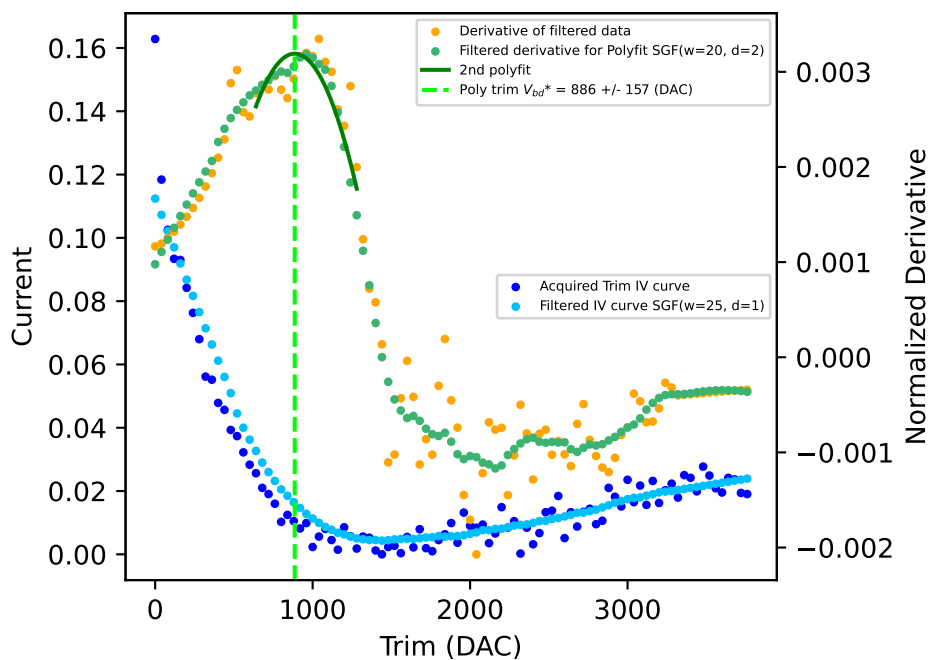
Bias DAC-VOLT conversion



Bias + Trim REV IV curve



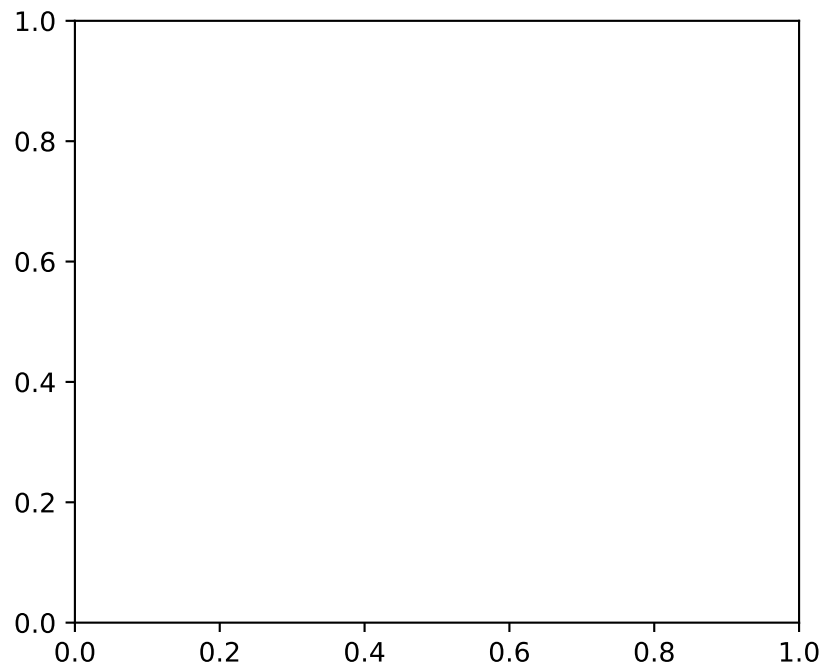
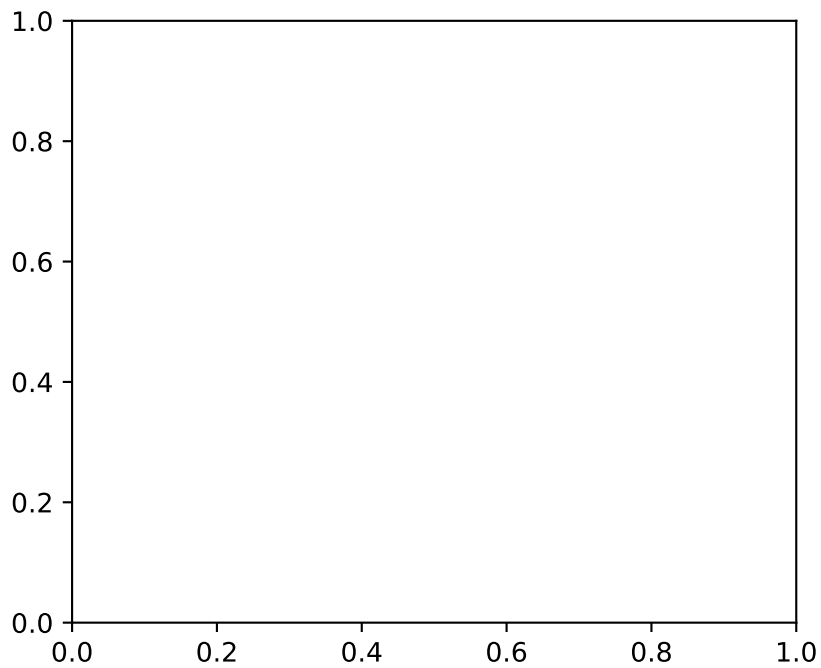
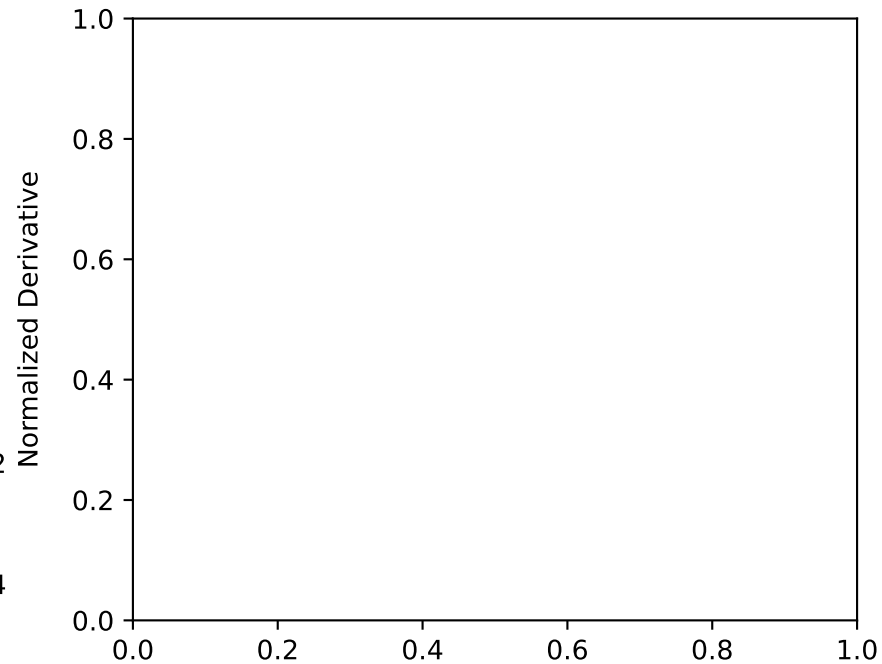
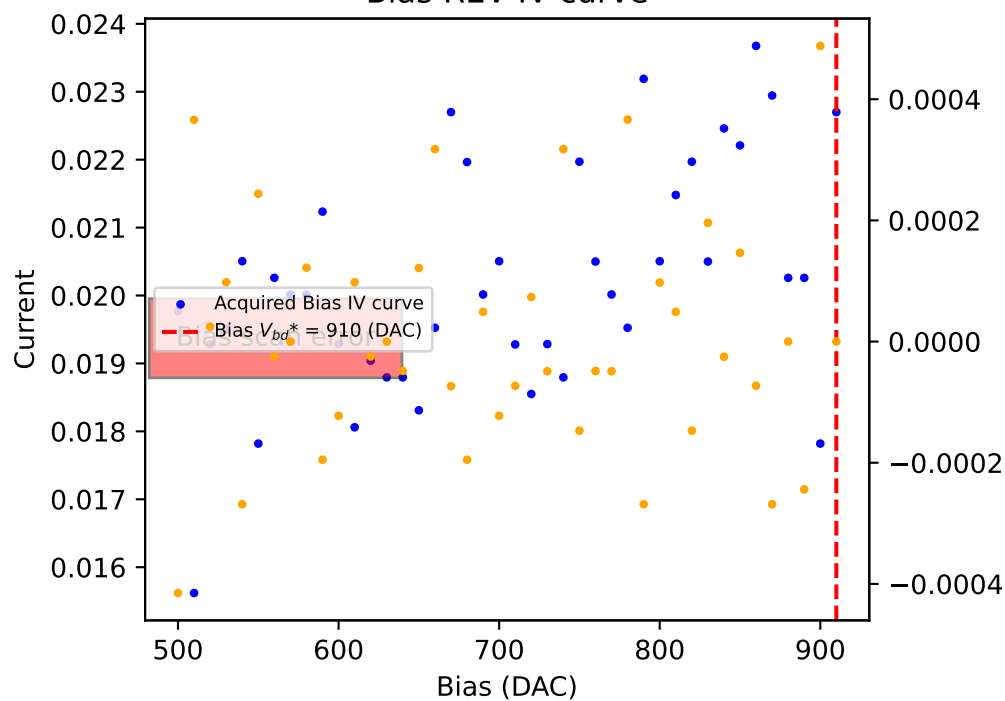
Trim REV IV curve



ENDPOINT:109 APA:2 AFE:1 Config\_CH:14 CH\_daq:16 SiPM:FBK --> MISSING FILE

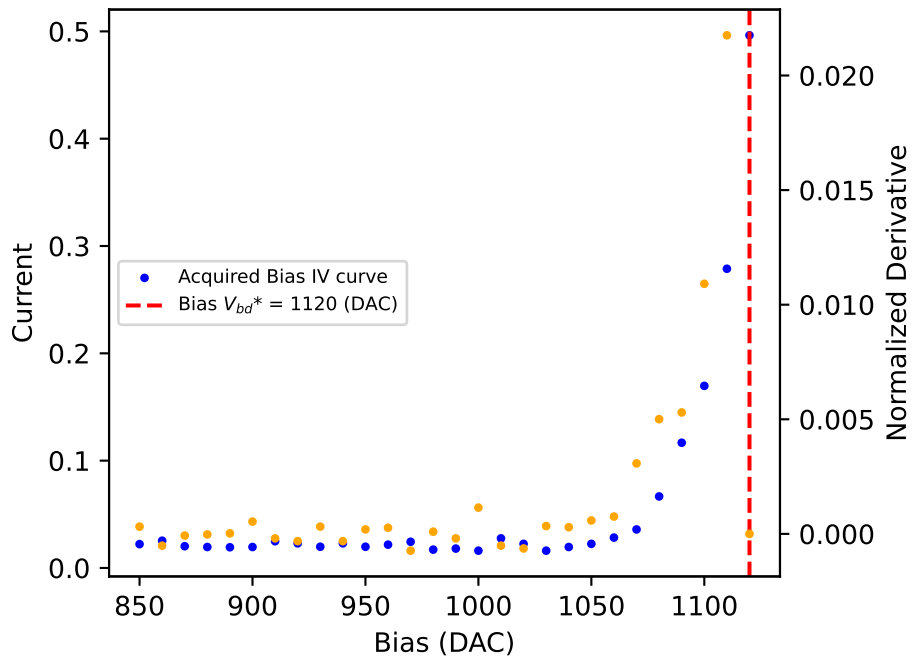


Bias REV IV curve

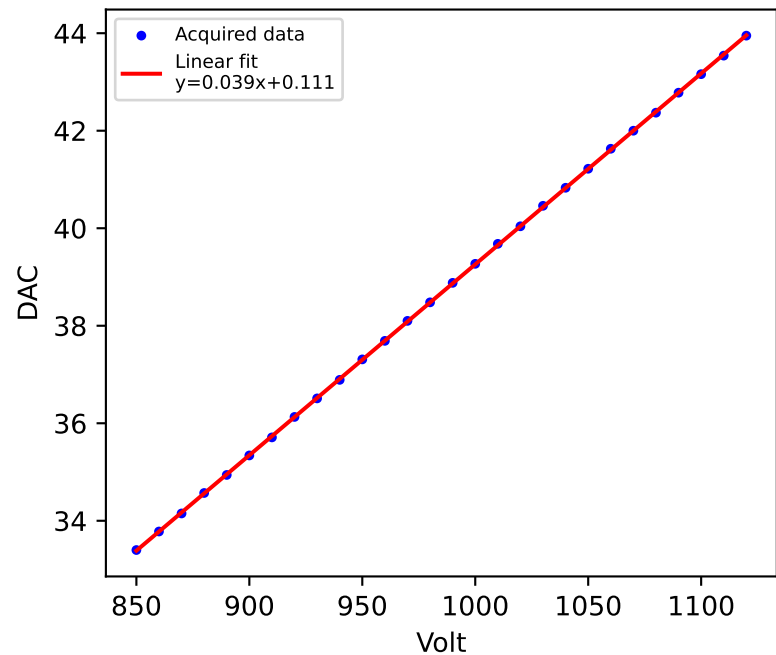




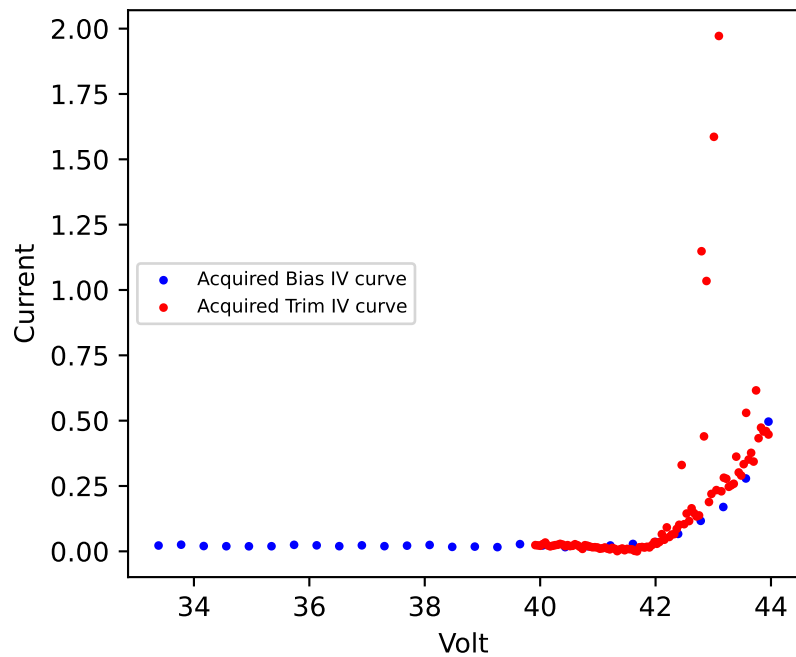
Bias REV IV curve



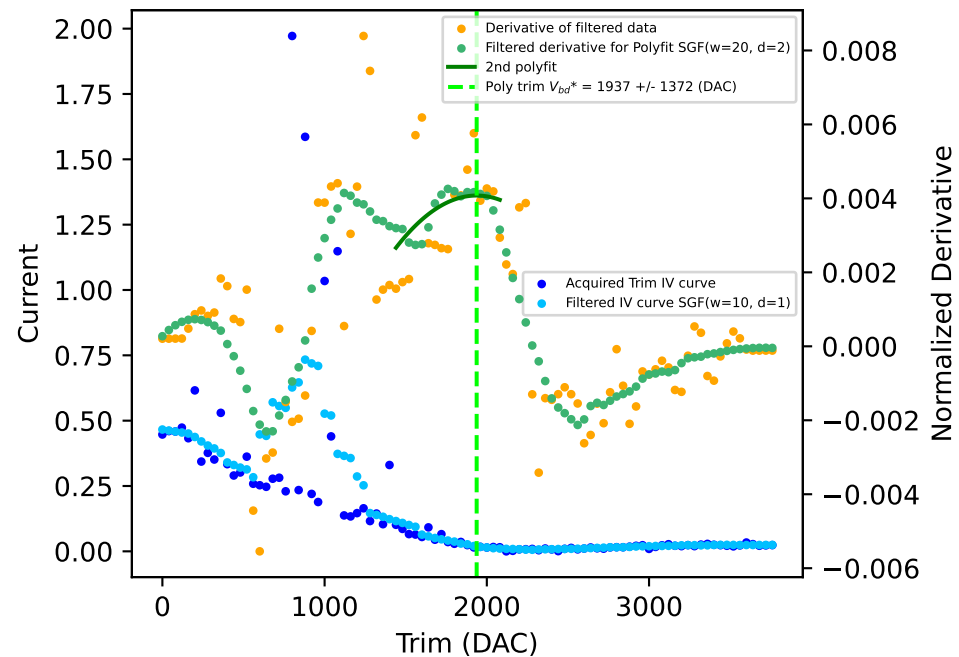
Bias DAC-VOLT conversion



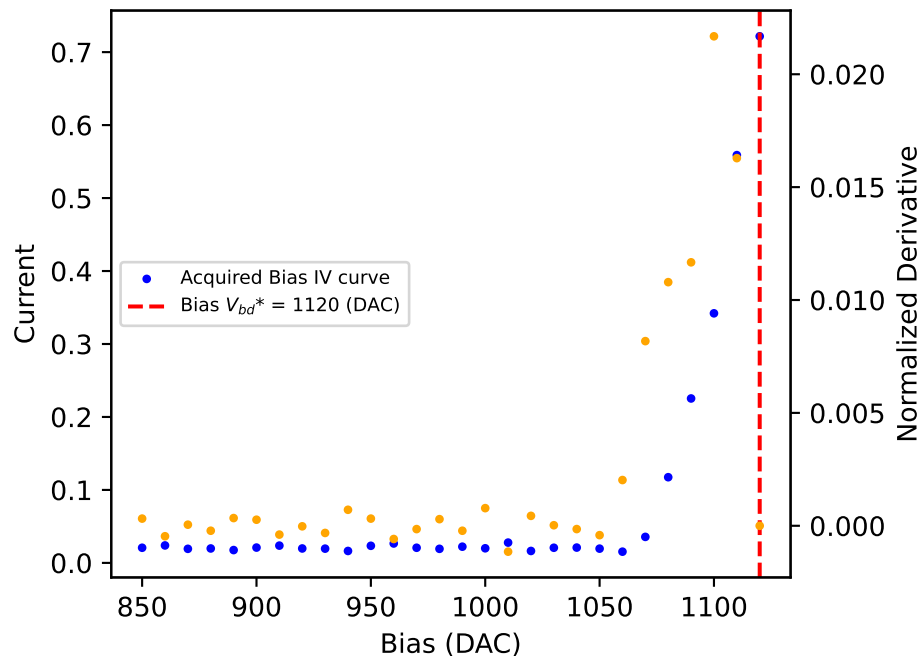
Bias + Trim REV IV curve



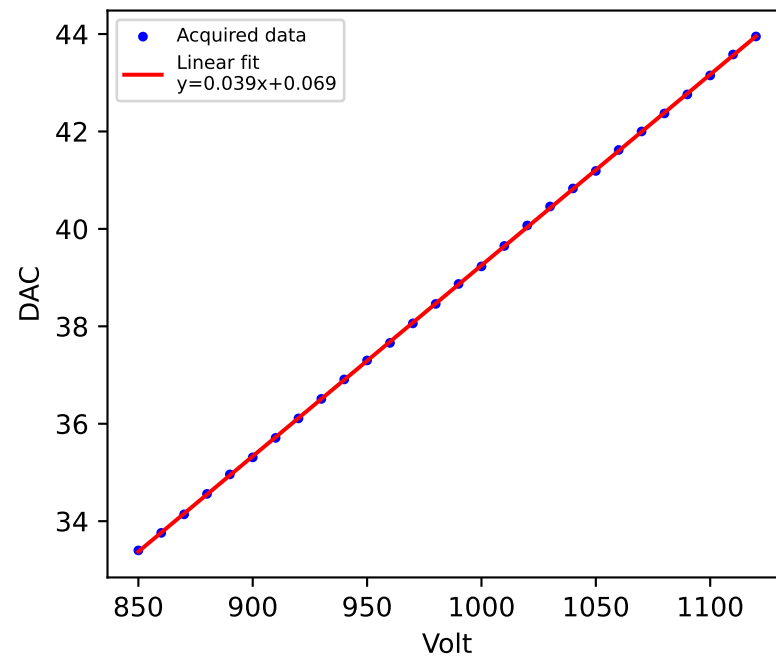
Trim REV IV curve



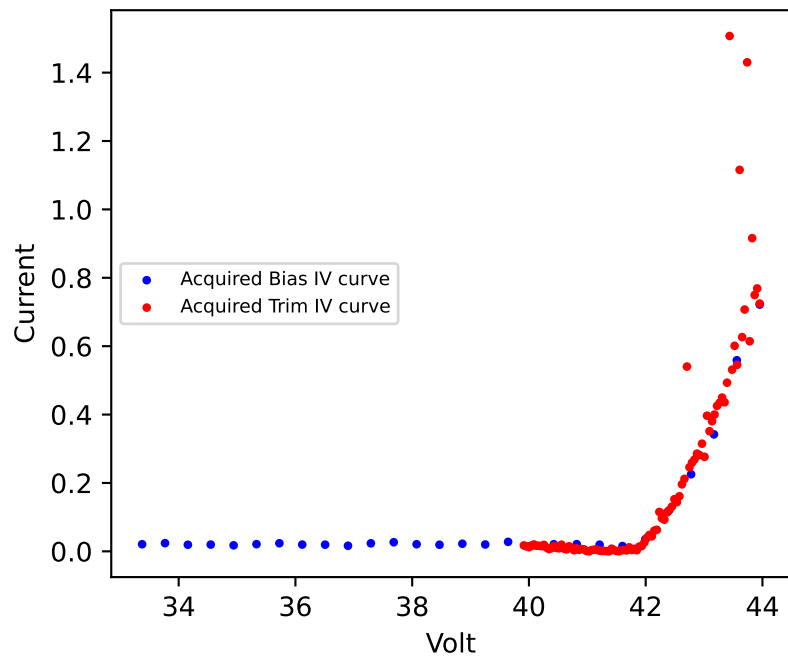
Bias REV IV curve



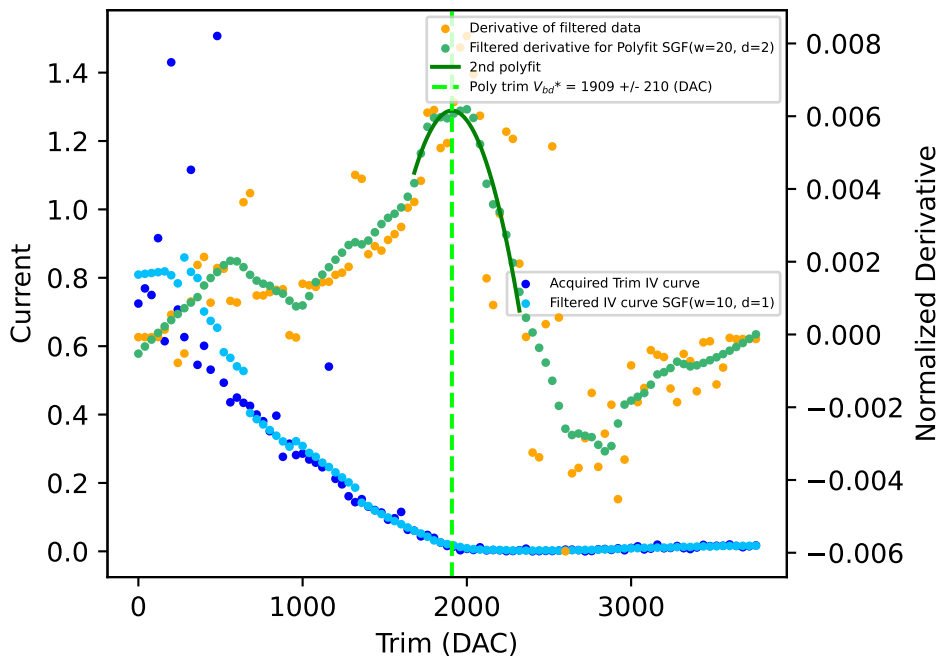
Bias DAC-VOLT conversion



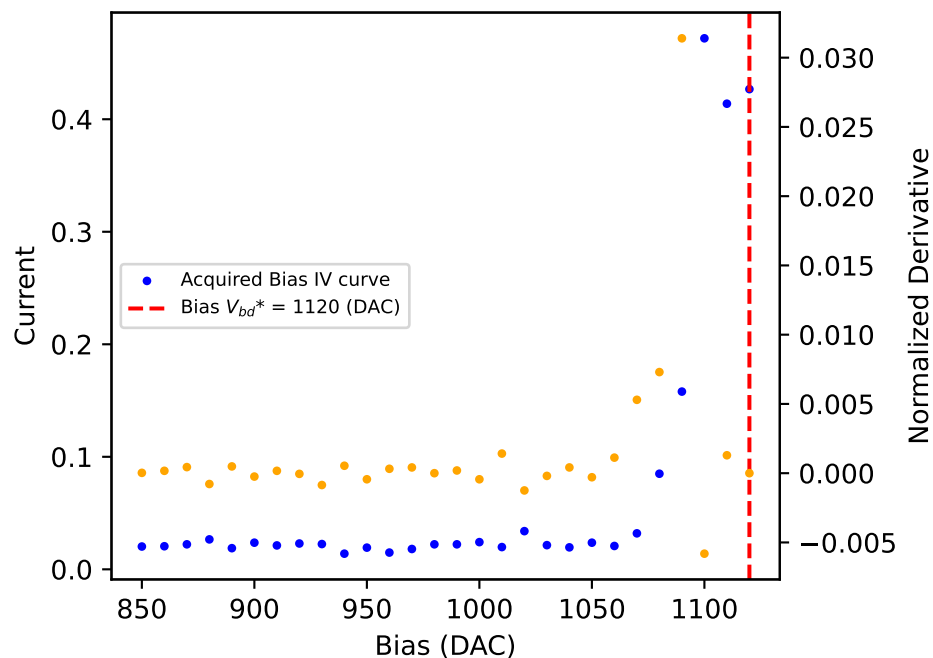
Bias + Trim REV IV curve



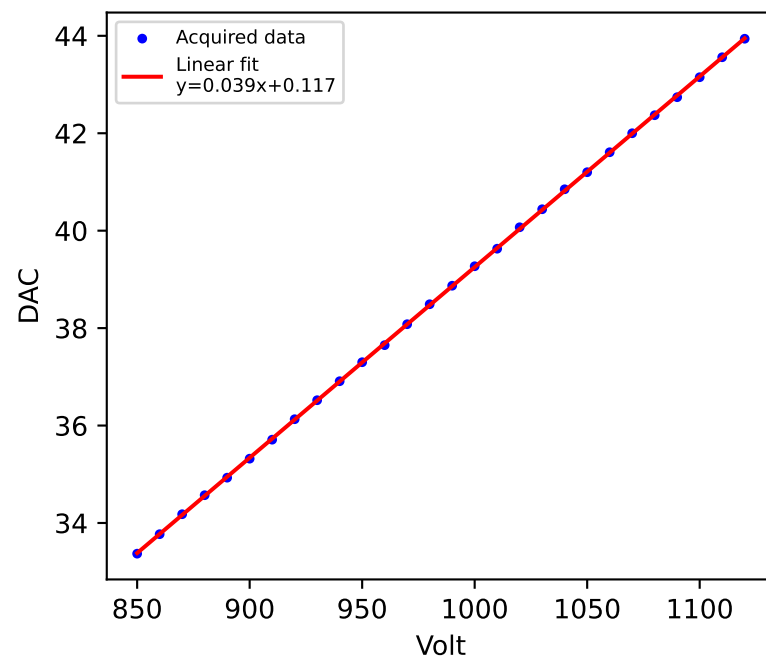
Trim REV IV curve



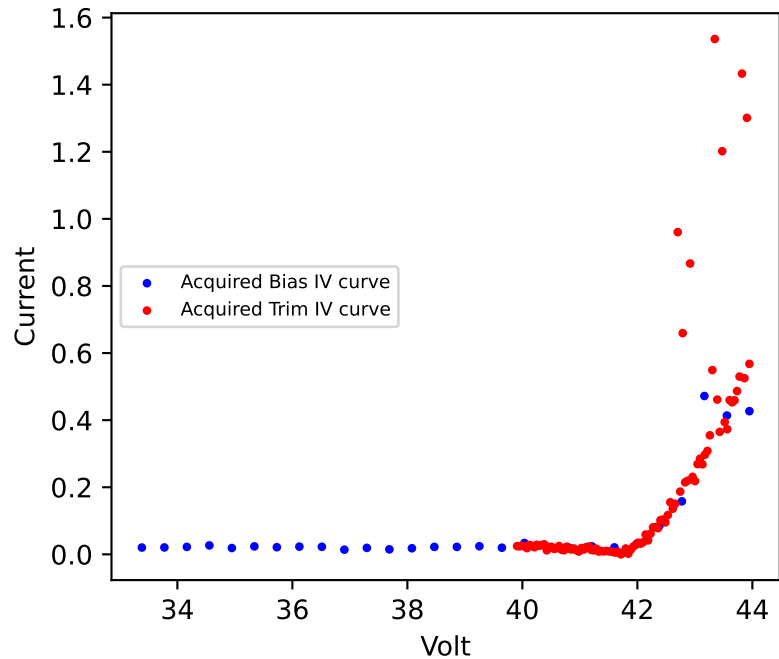
Bias REV IV curve



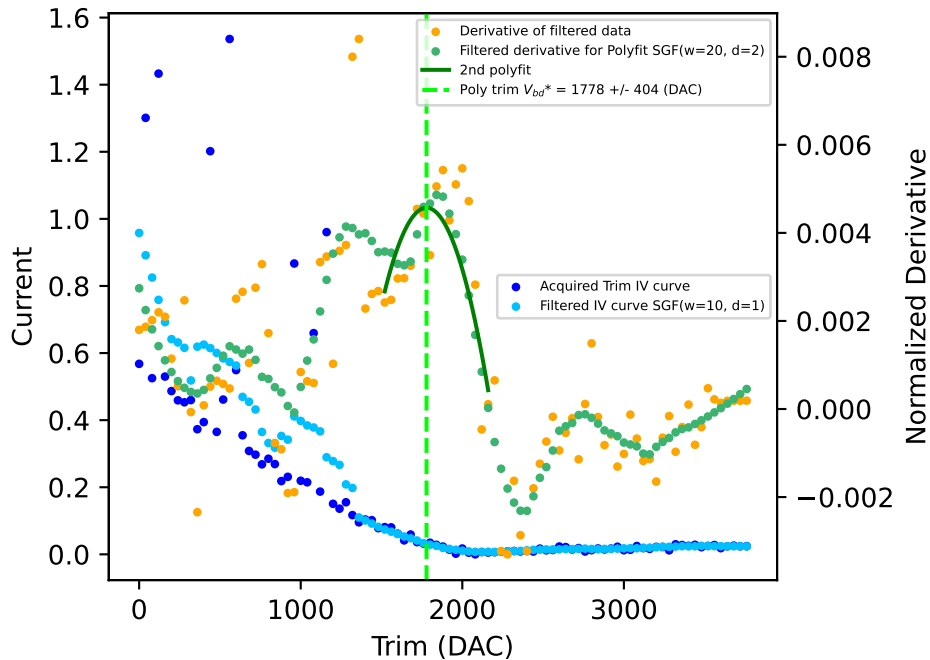
Bias DAC-VOLT conversion



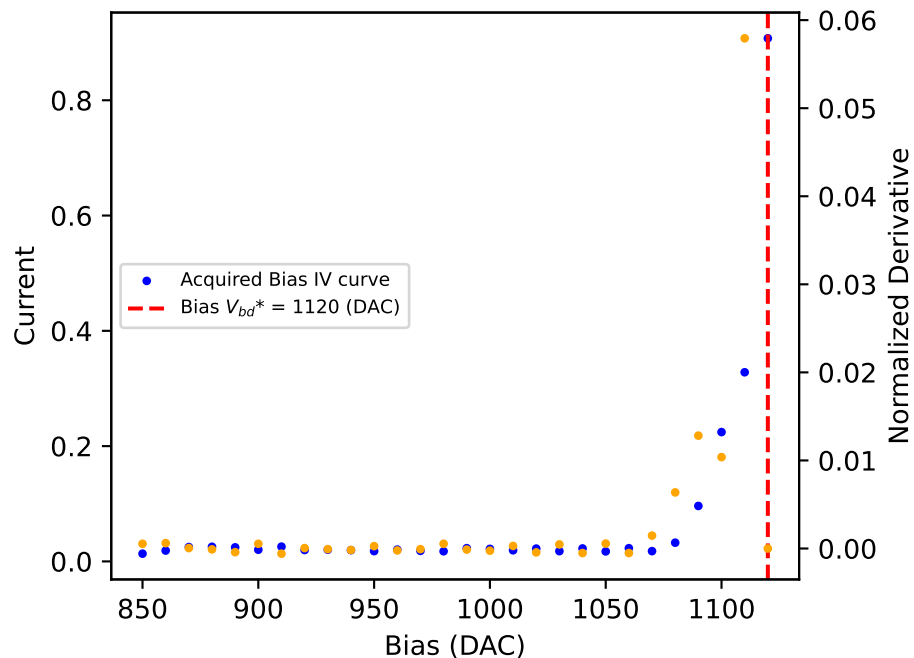
Bias + Trim REV IV curve



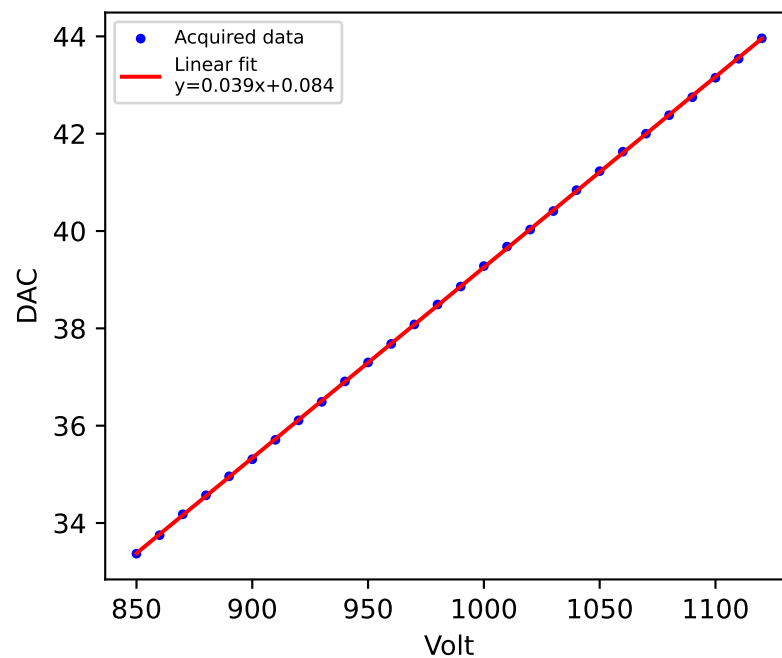
Trim REV IV curve



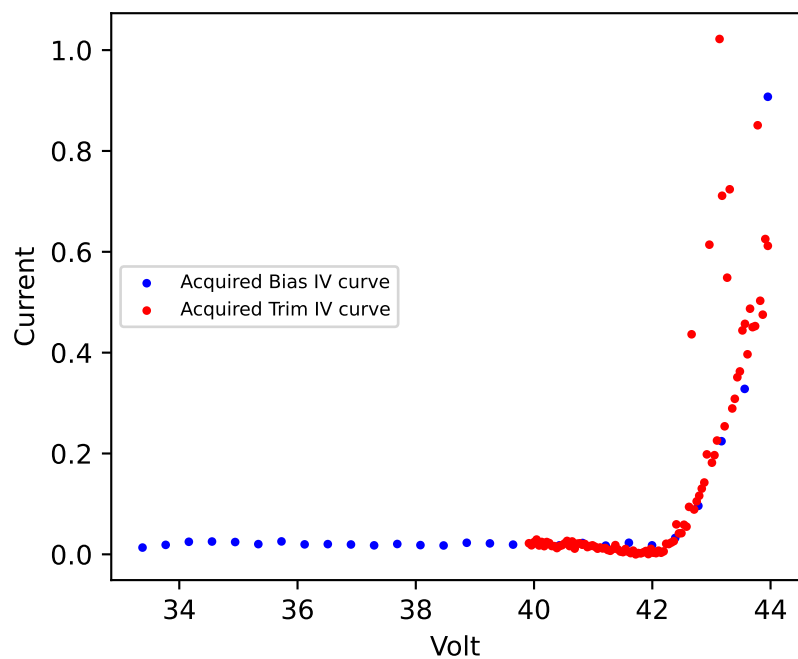
Bias REV IV curve



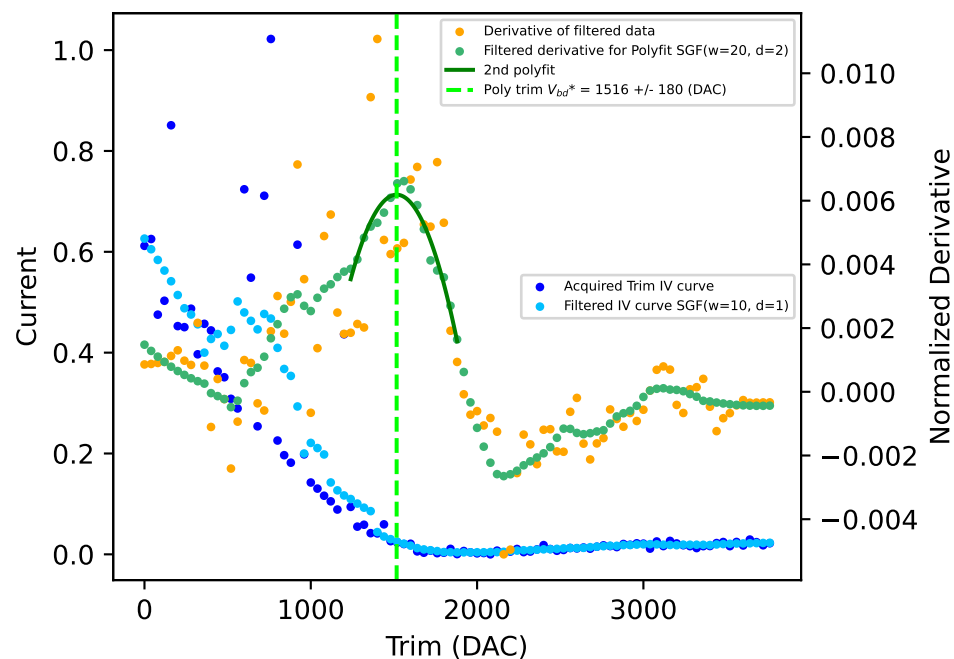
Bias DAC-VOLT conversion



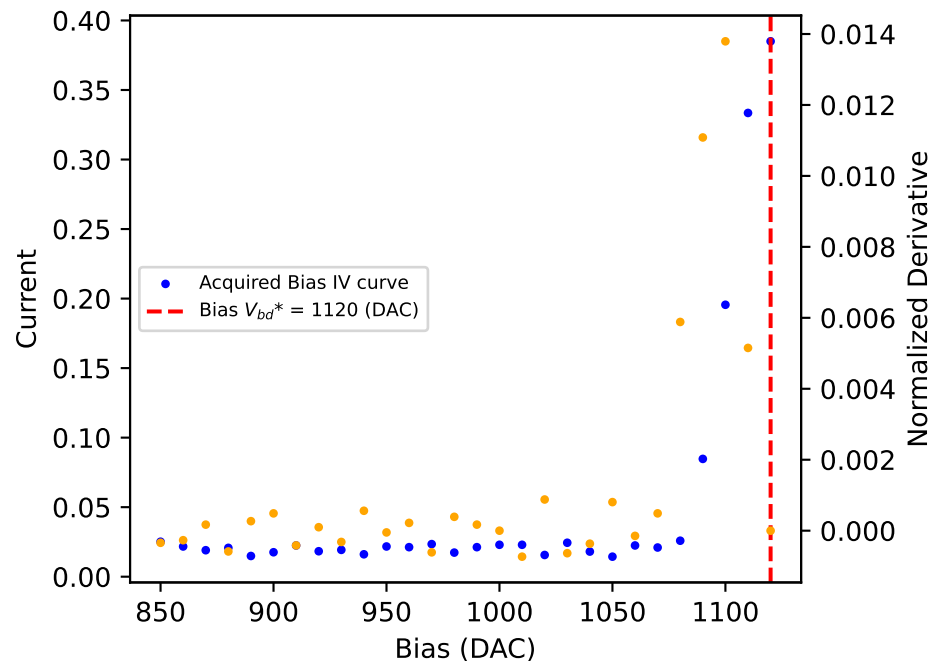
Bias + Trim REV IV curve



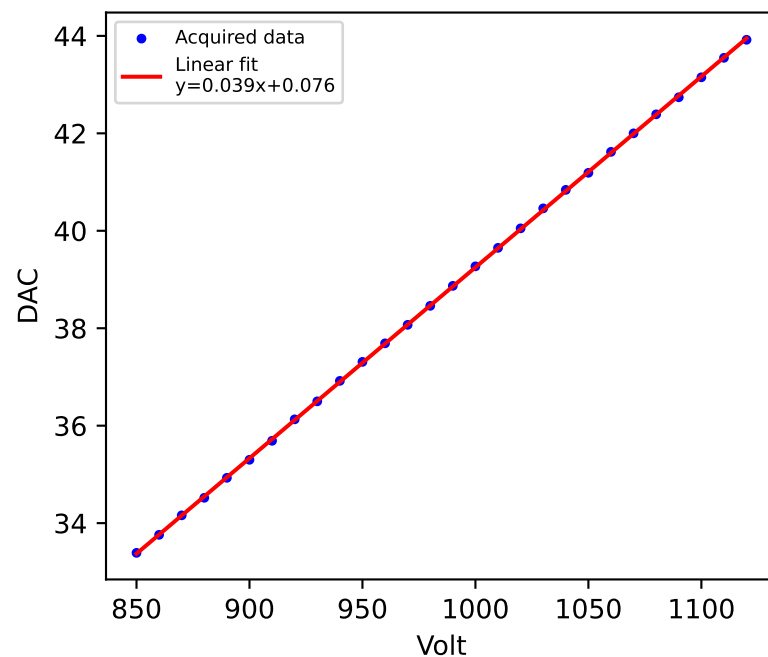
Trim REV IV curve



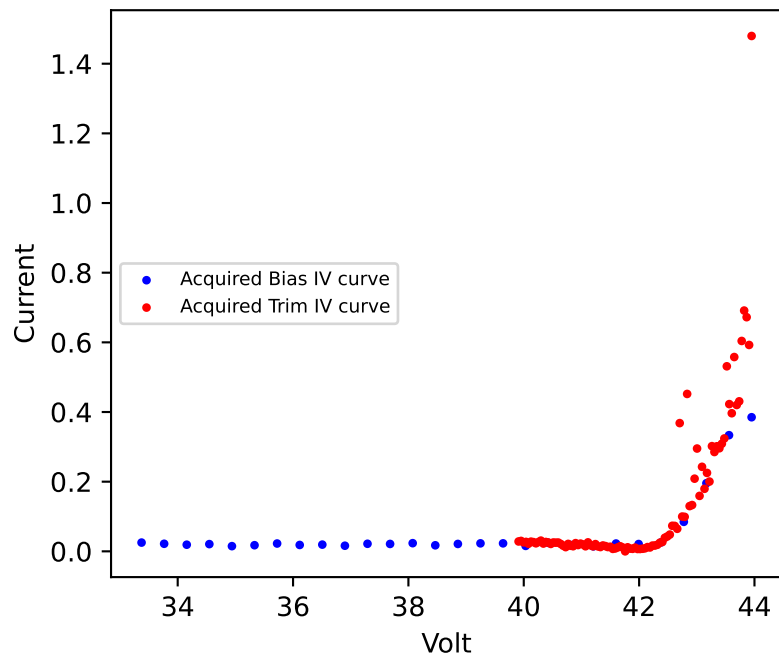
Bias REV IV curve



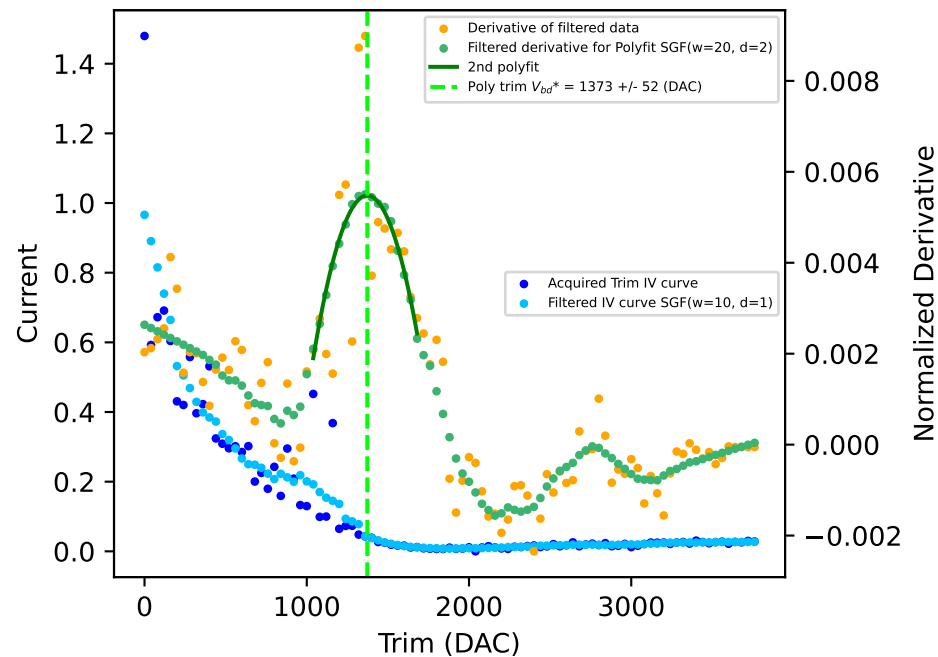
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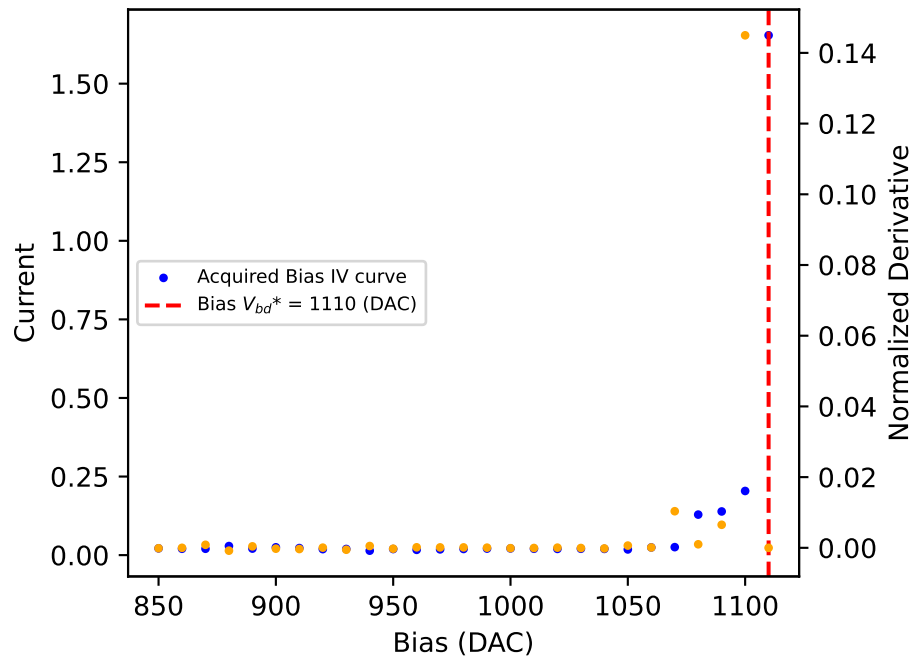
Bias + Trim REV IV curve



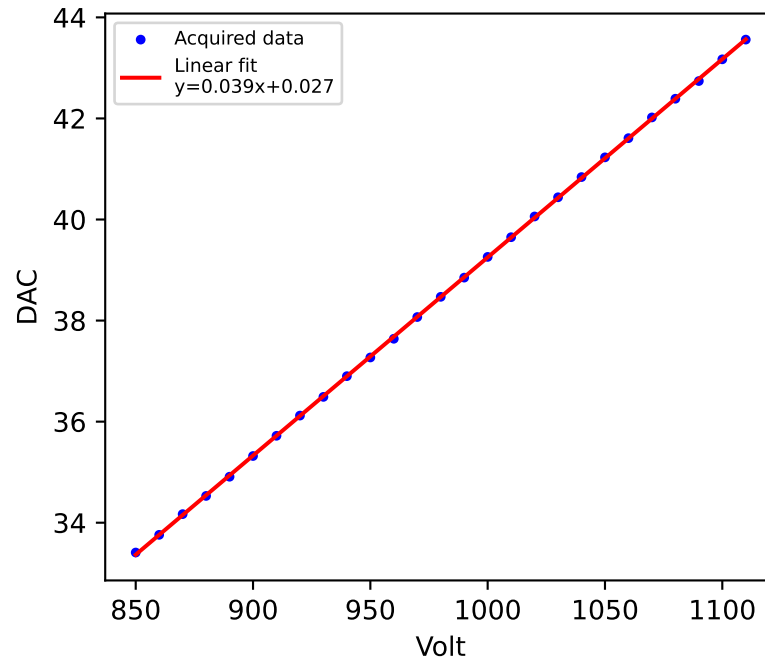
Trim REV IV curve



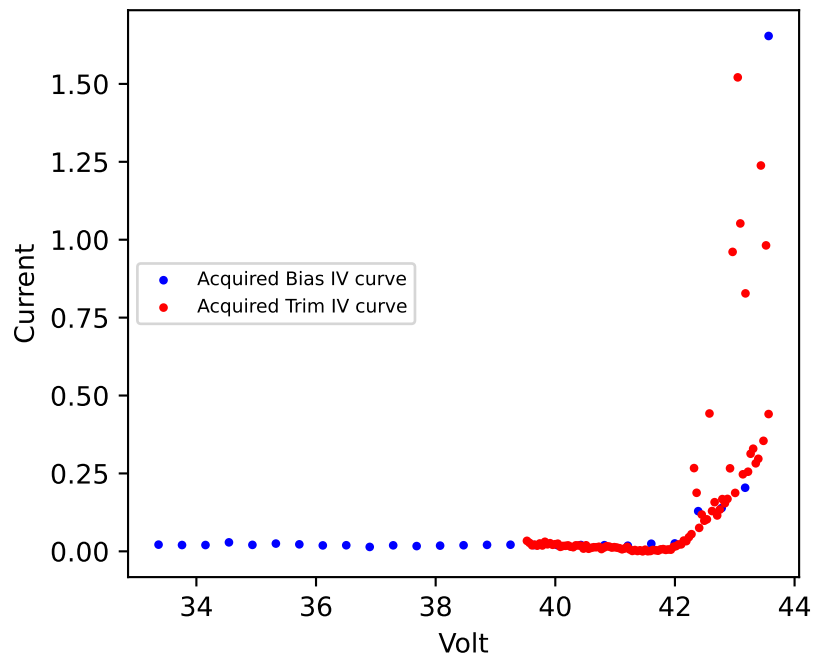
Bias REV IV curve



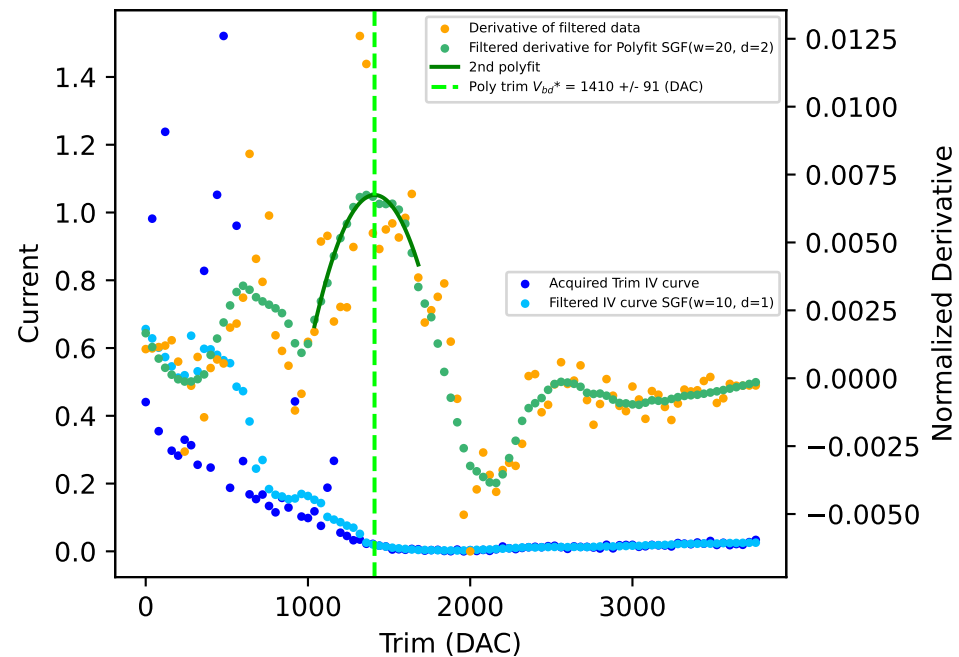
Bias DAC-VOLT conversion



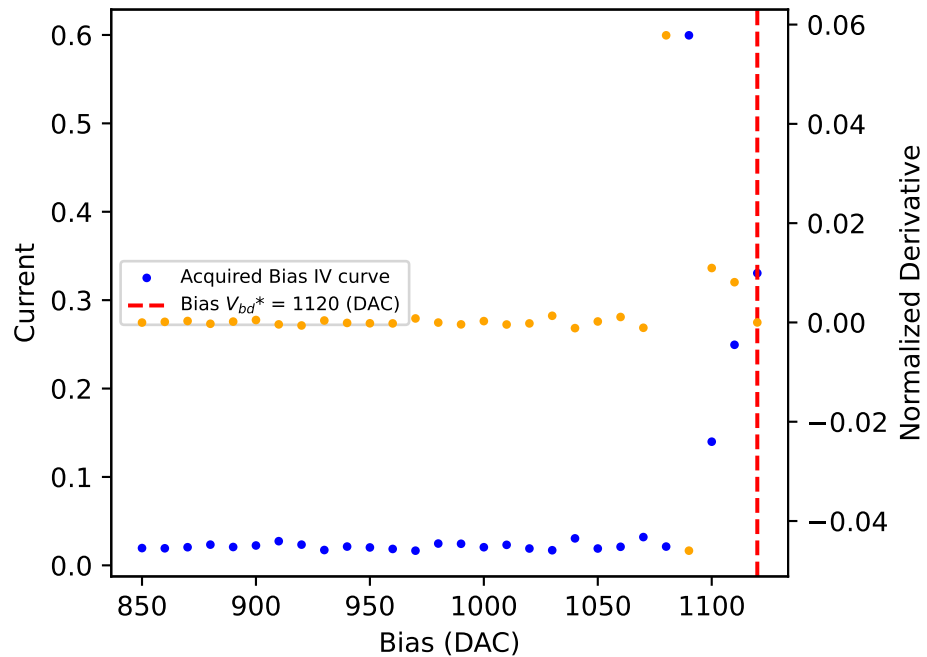
Bias + Trim REV IV curve



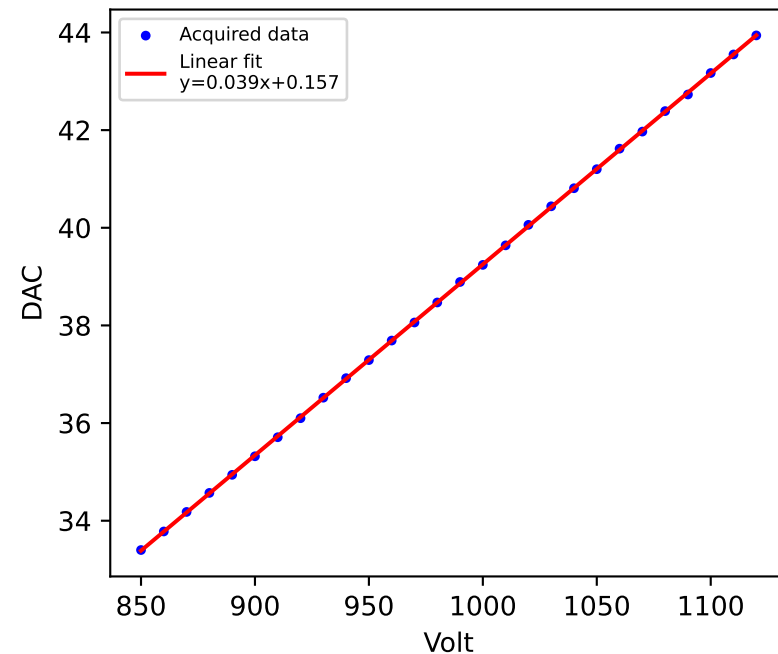
Trim REV IV curve



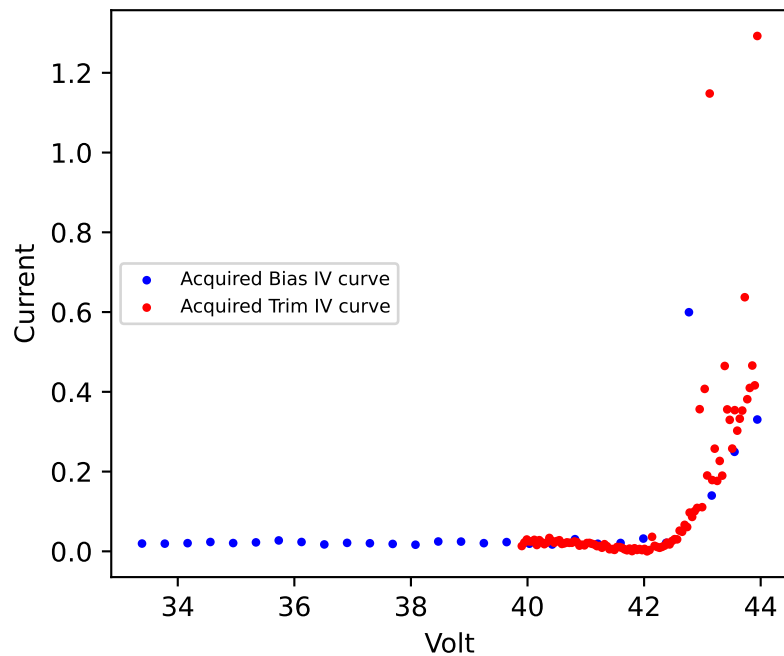
Bias REV IV curve



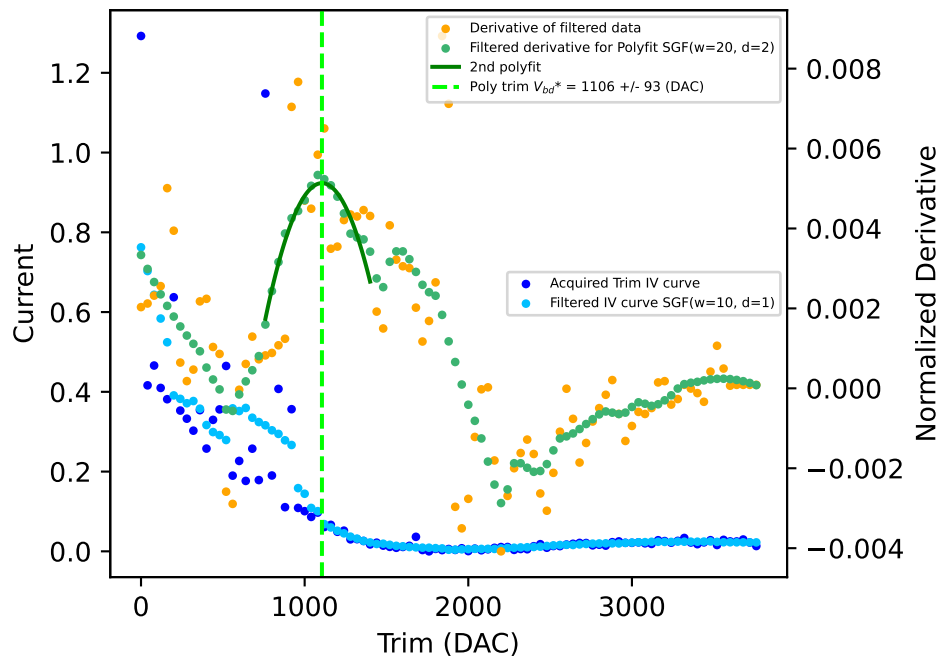
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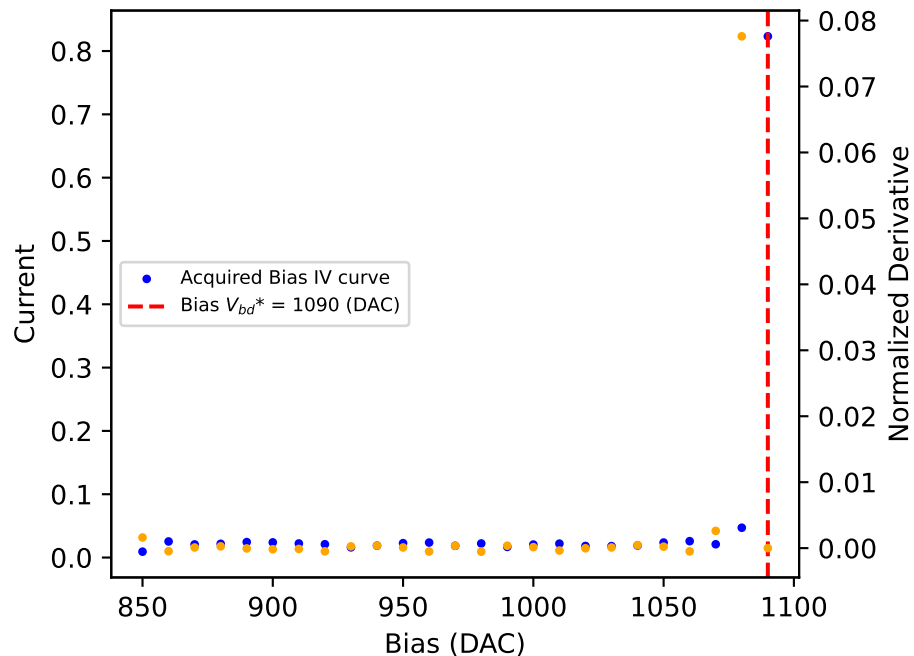
Bias + Trim REV IV curve



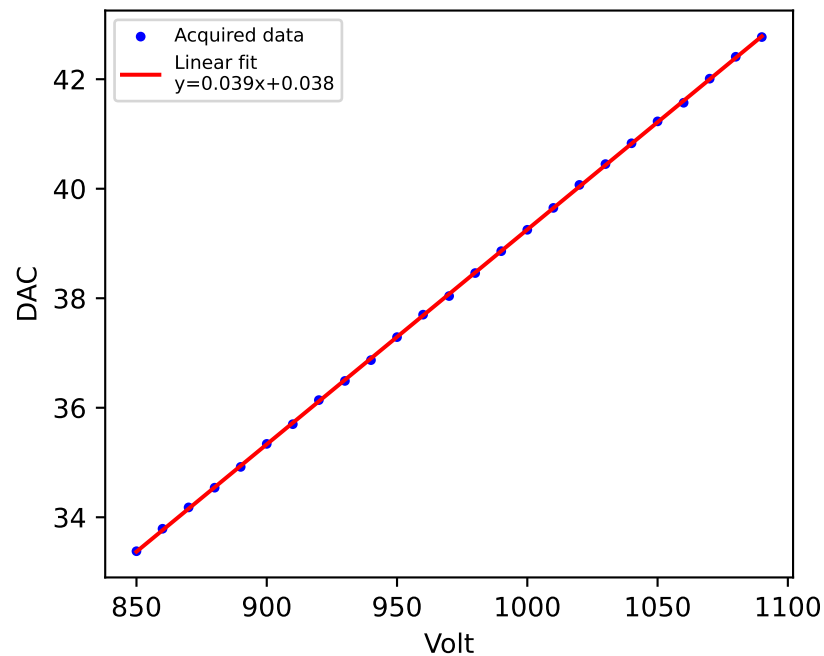
Trim REV IV curve



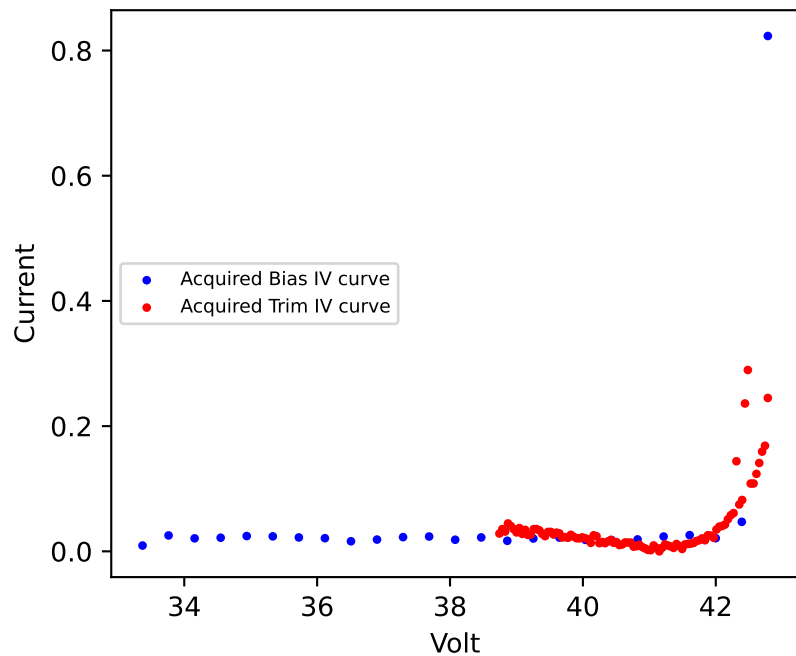
Bias REV IV curve



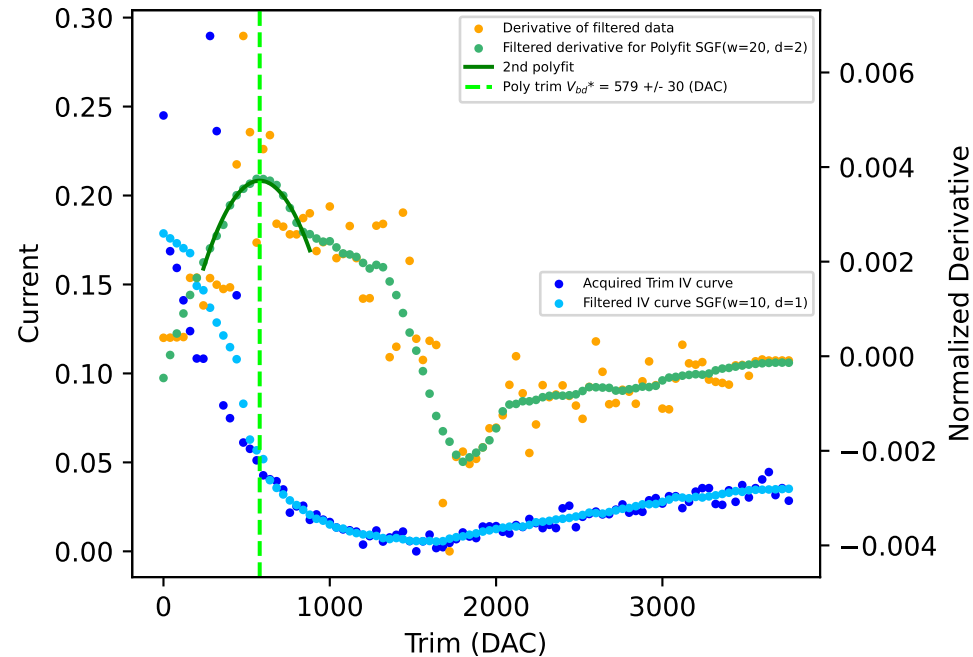
Bias DAC-VOLT conversion



Bias + Trim REV IV curve

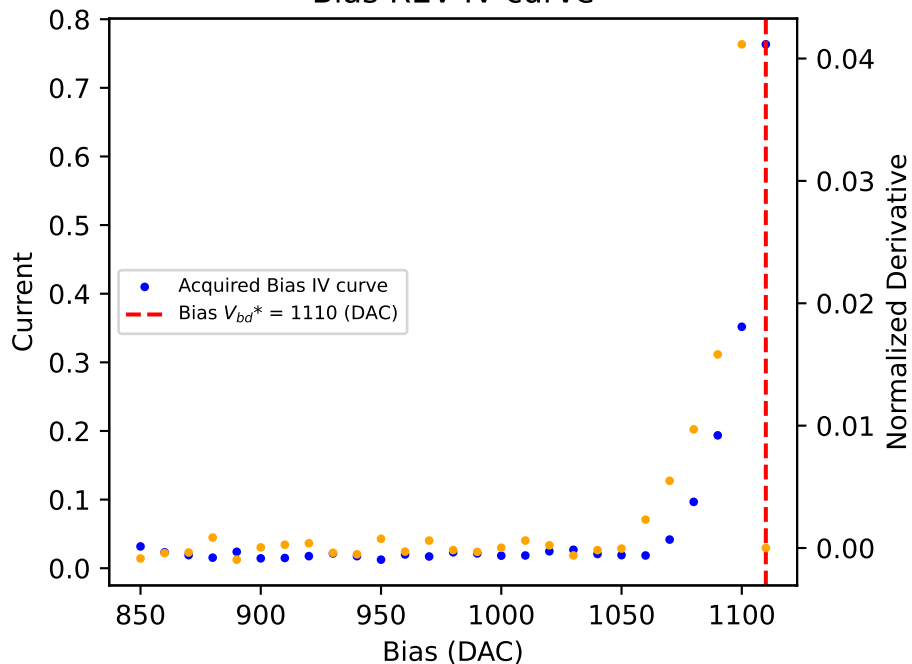


Trim REV IV curve

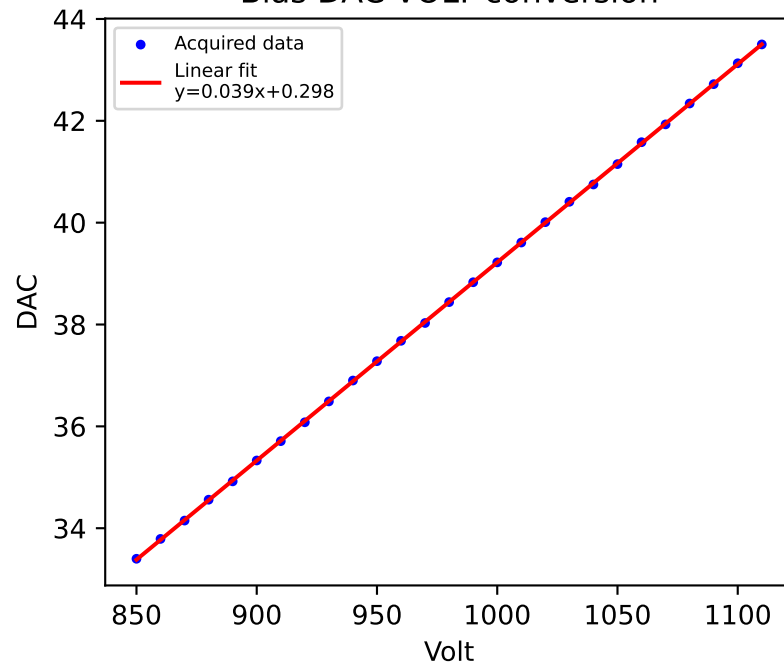




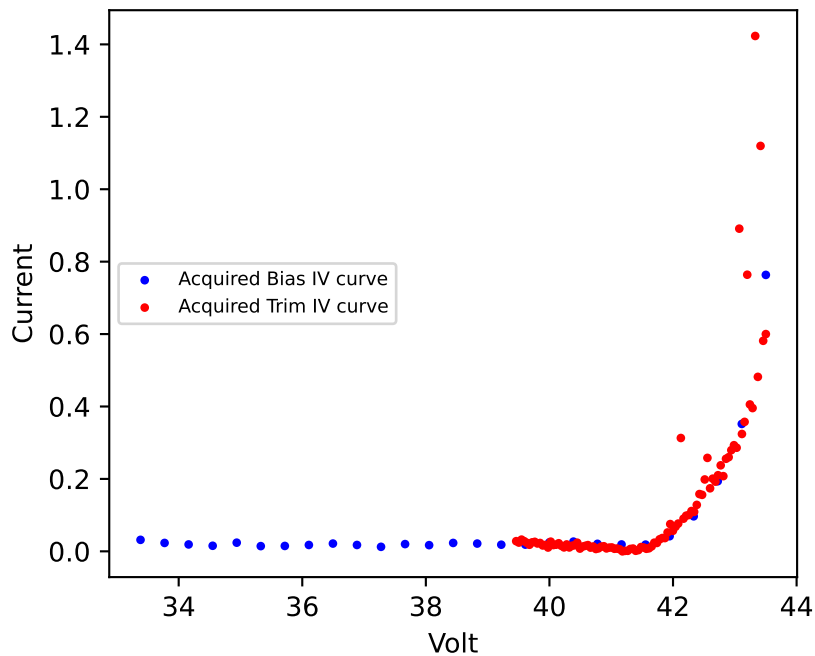
Bias REV IV curve



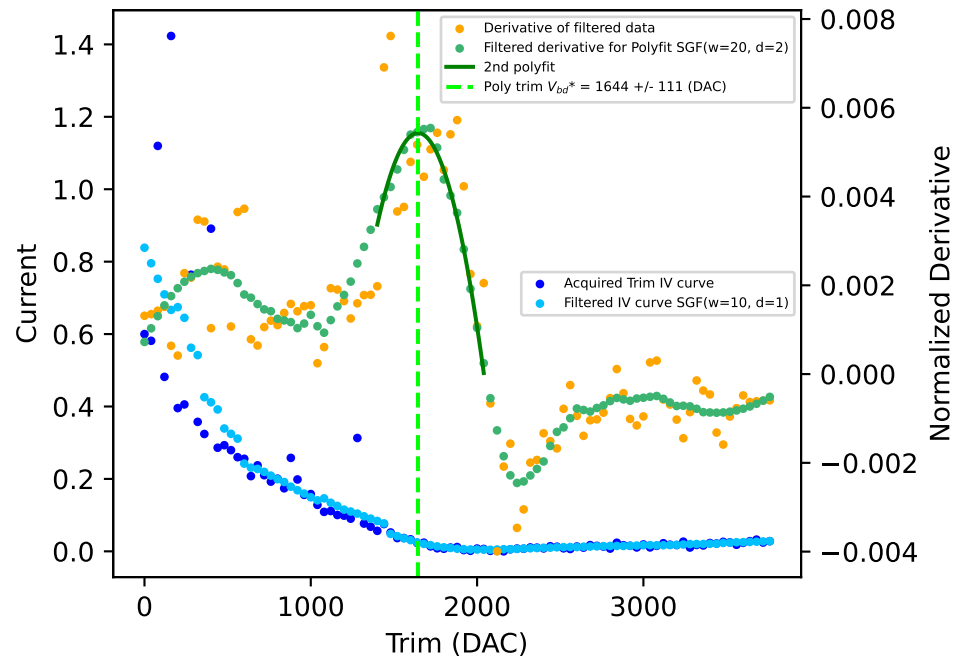
Bias DAC-VOLT conversion



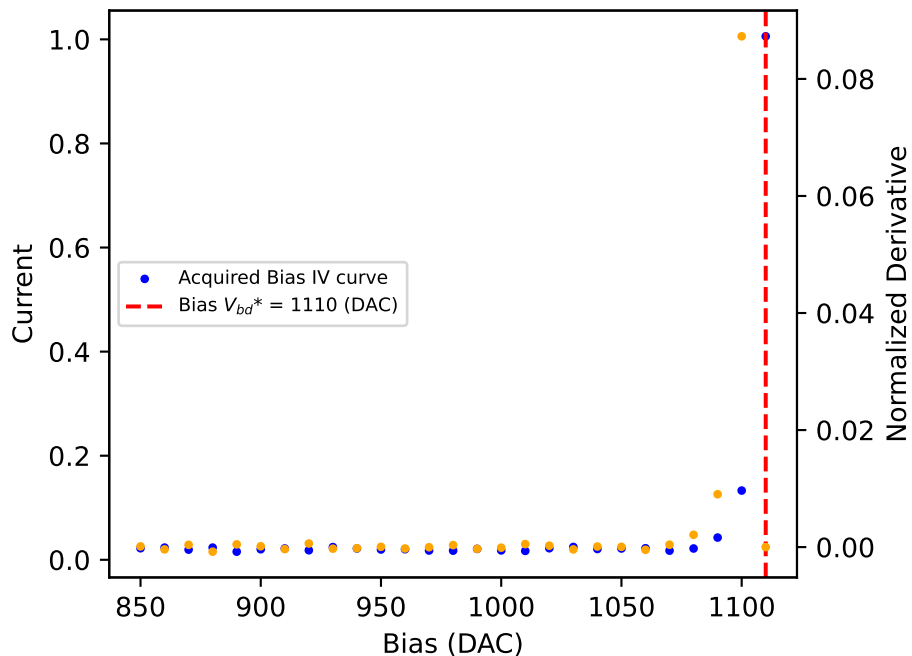
Bias + Trim REV IV curve



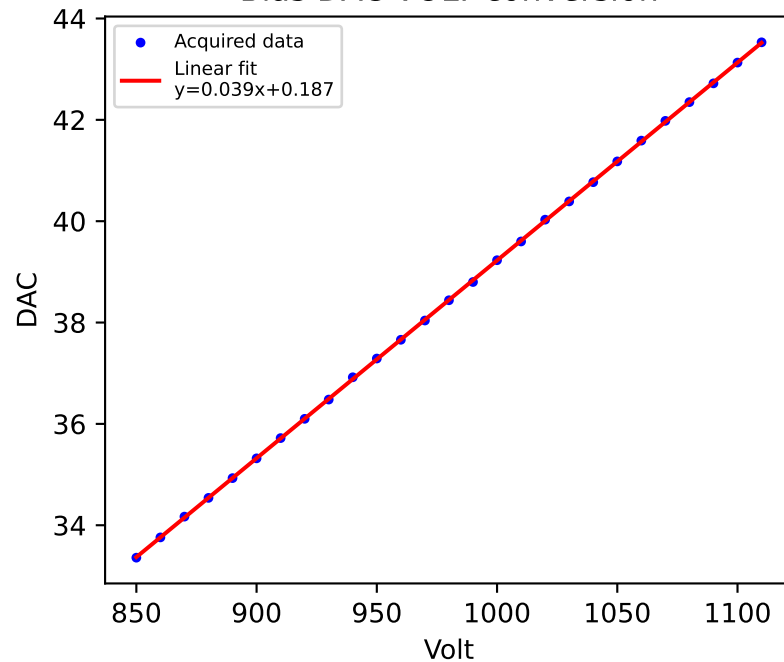
Trim REV IV curve



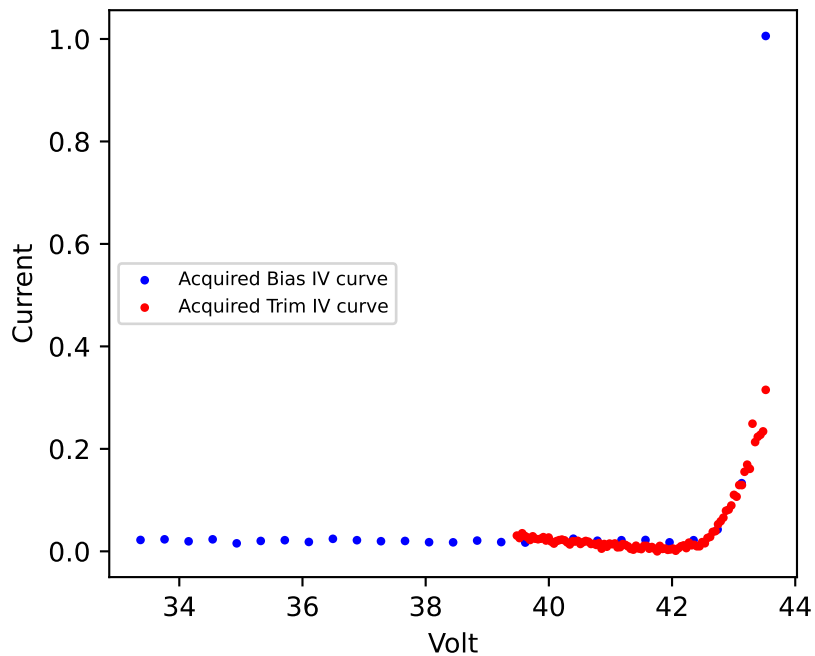
Bias REV IV curve



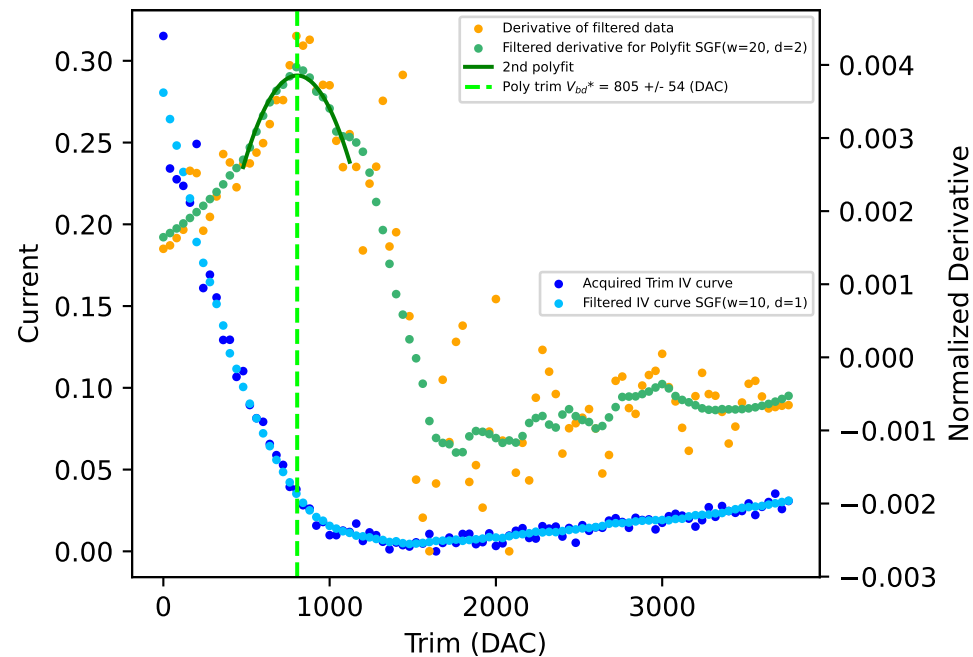
Bias DAC-VOLT conversion



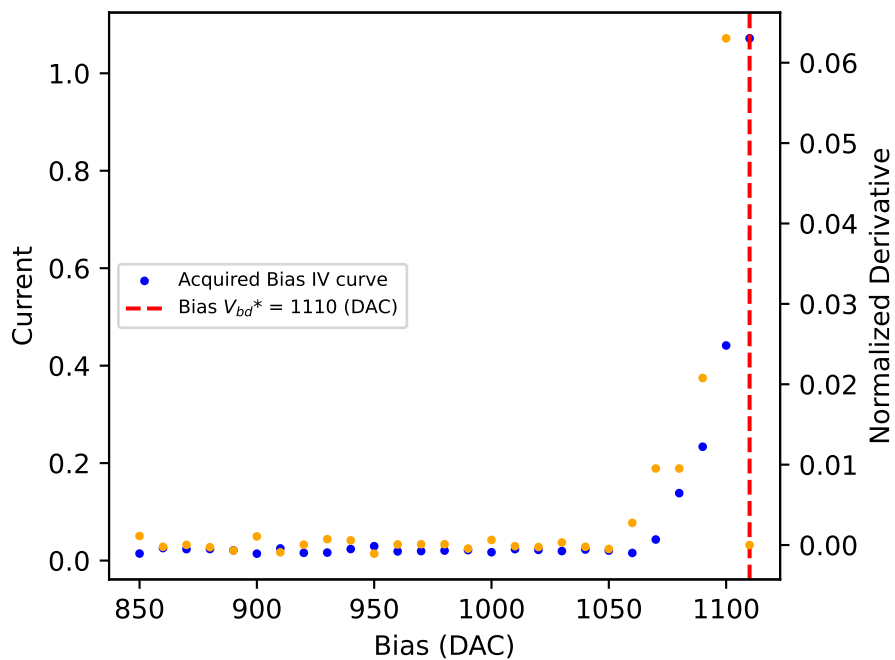
Bias + Trim REV IV curve



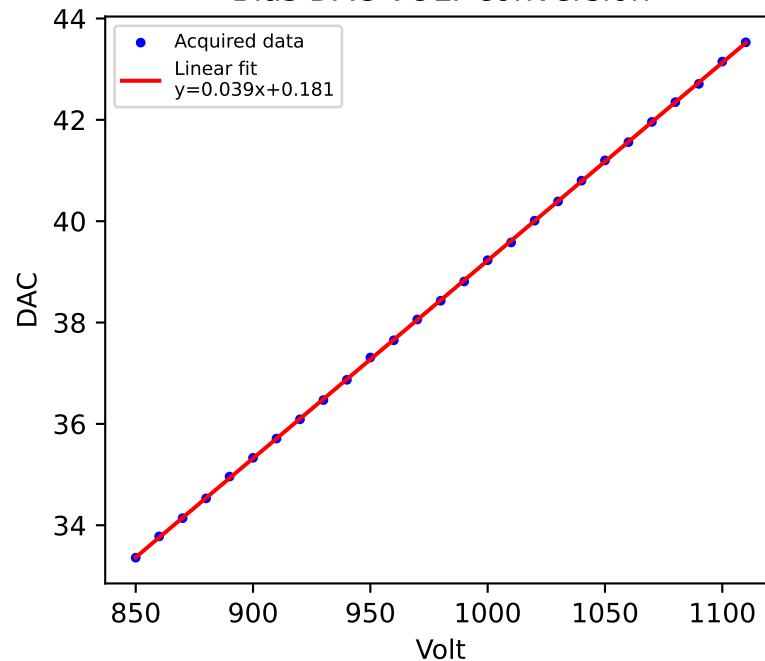
Trim REV IV curve



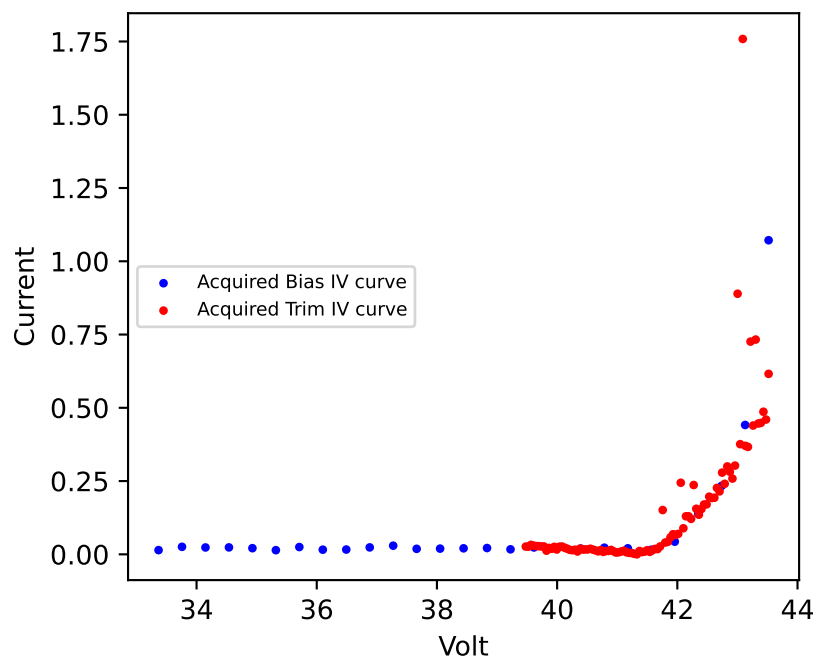
Bias REV IV curve



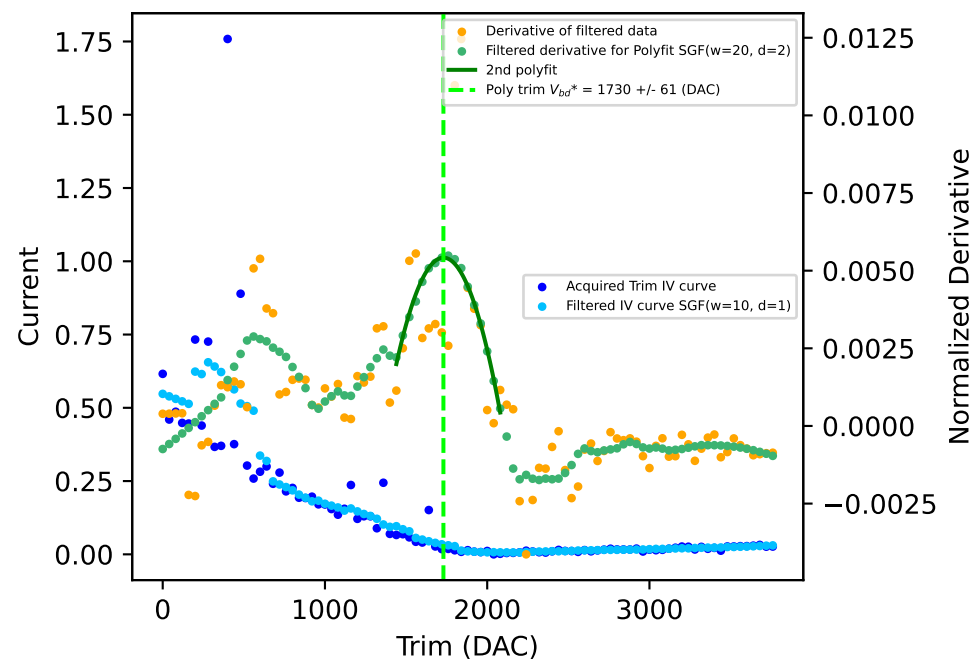
Bias DAC-VOLT conversion



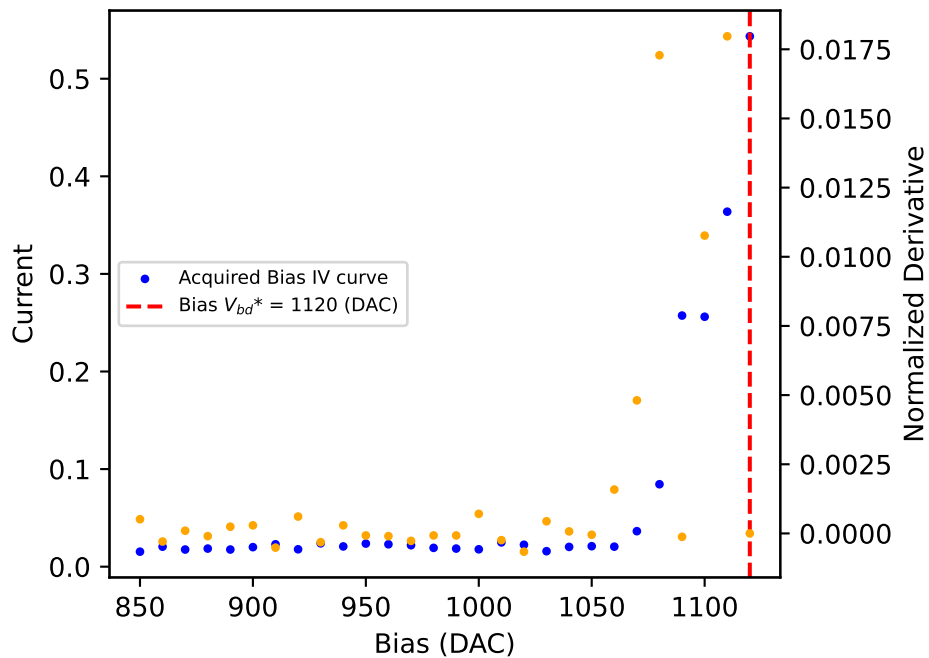
Bias + Trim REV IV curve



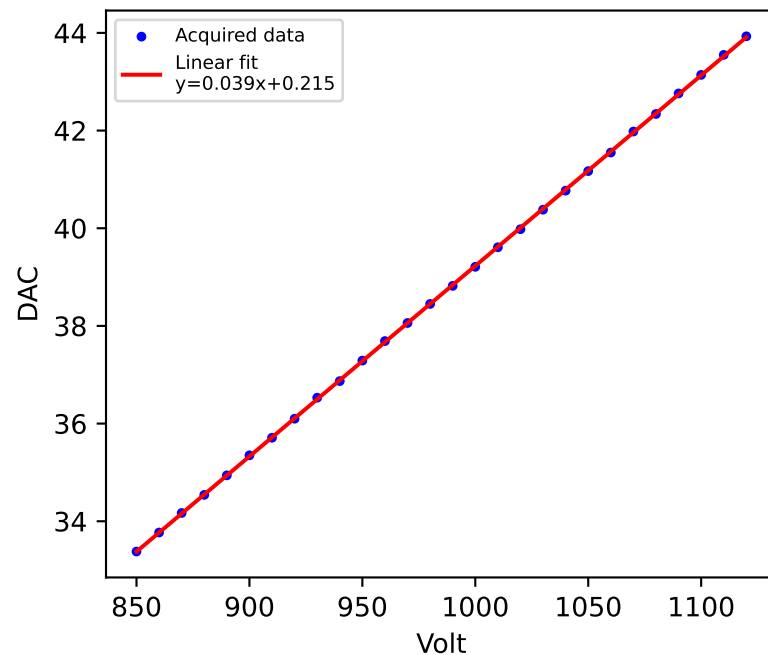
Trim REV IV curve



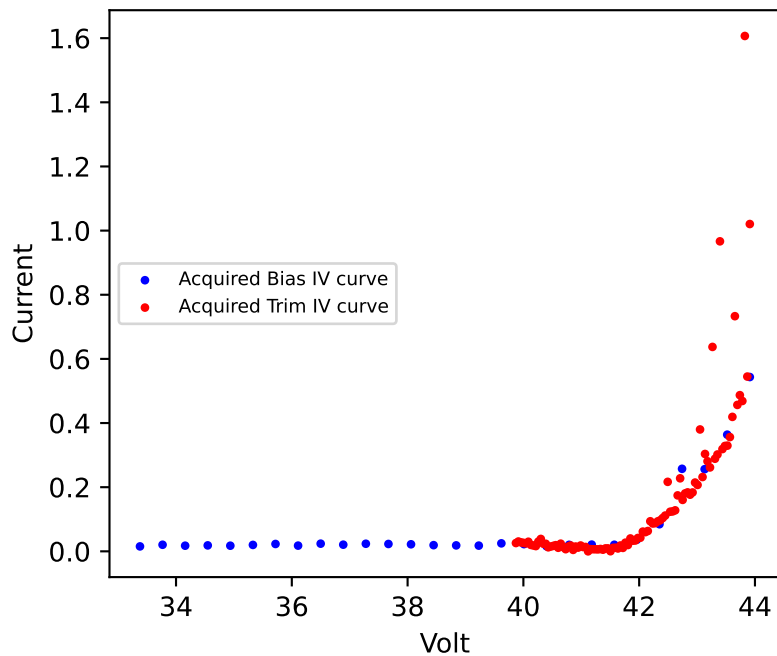
Bias REV IV curve



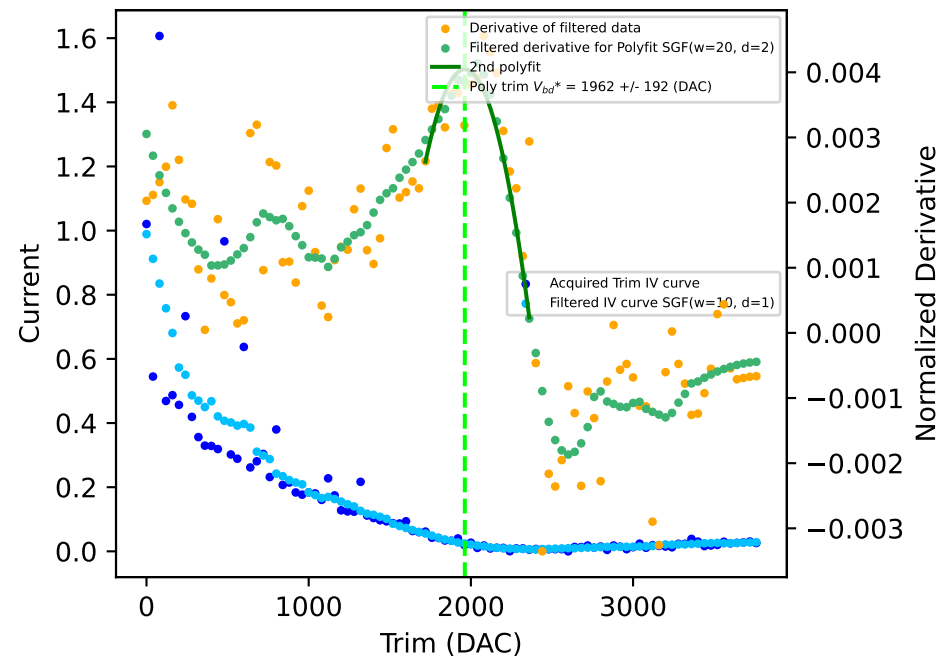
Bias DAC-VOLT conversion



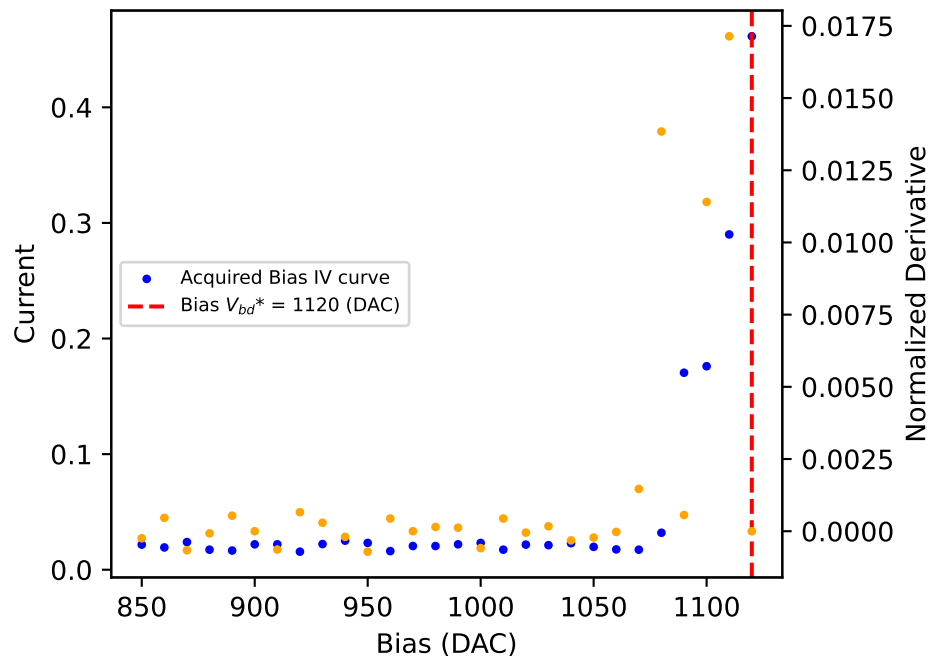
Bias + Trim REV IV curve



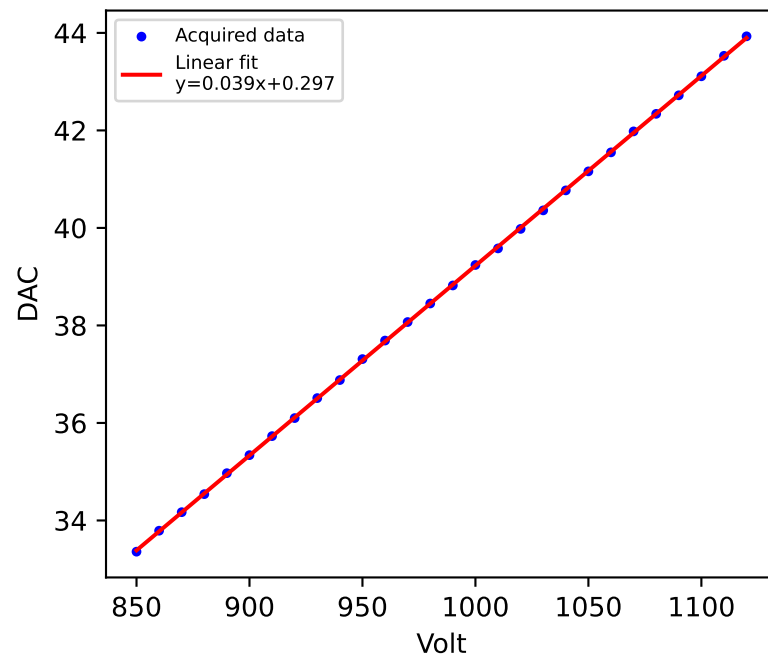
Trim REV IV curve



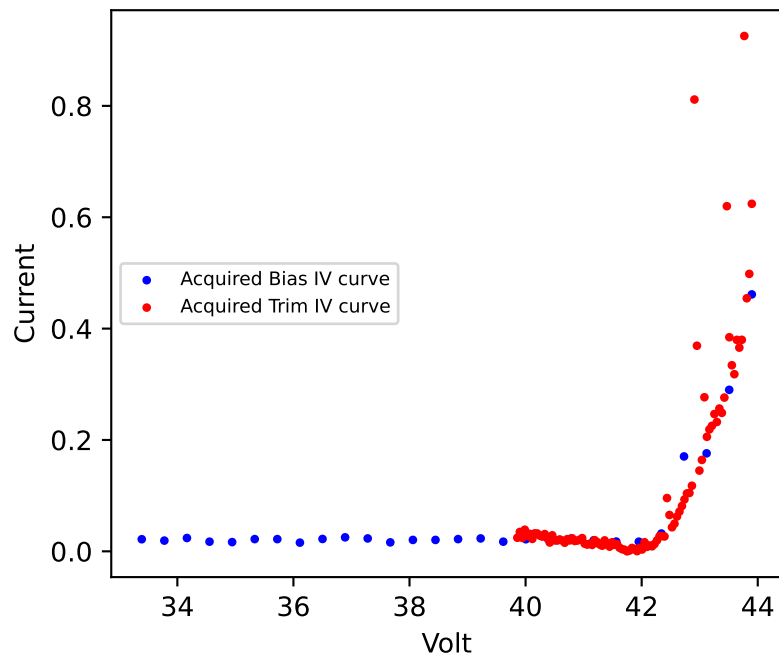
Bias REV IV curve



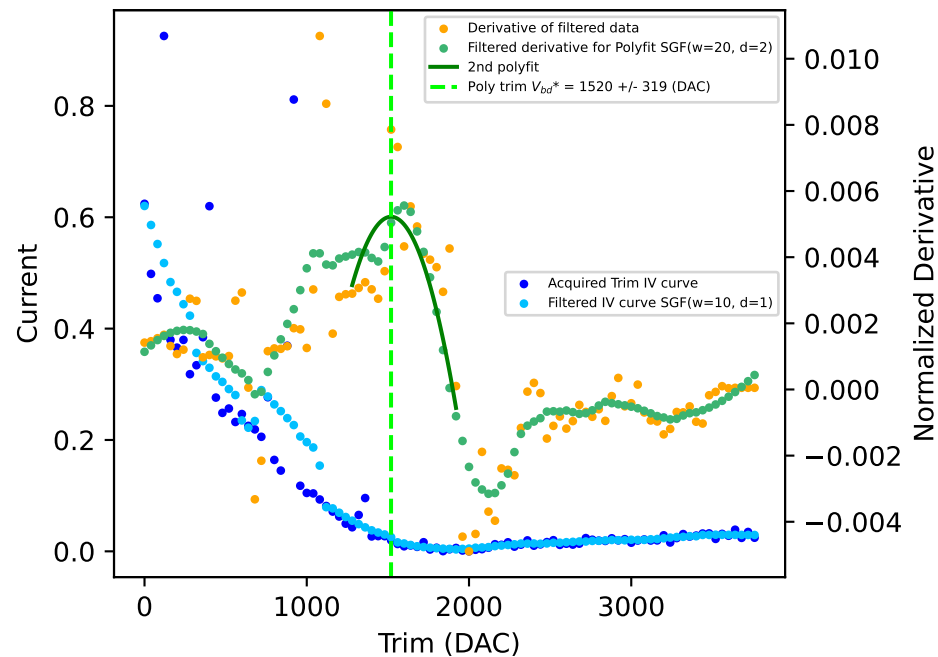
Bias DAC-VOLT conversion



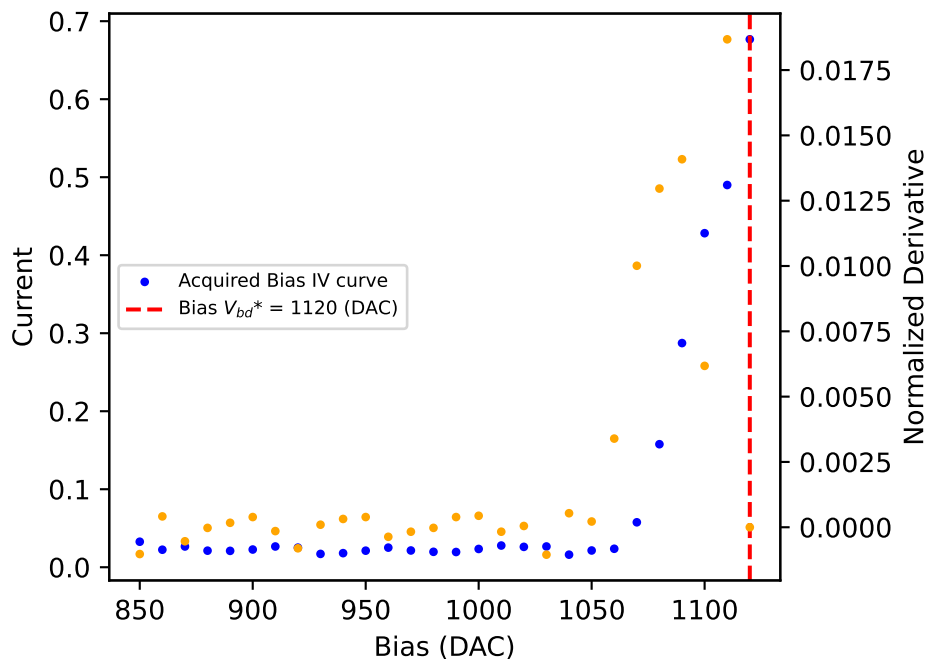
Bias + Trim REV IV curve



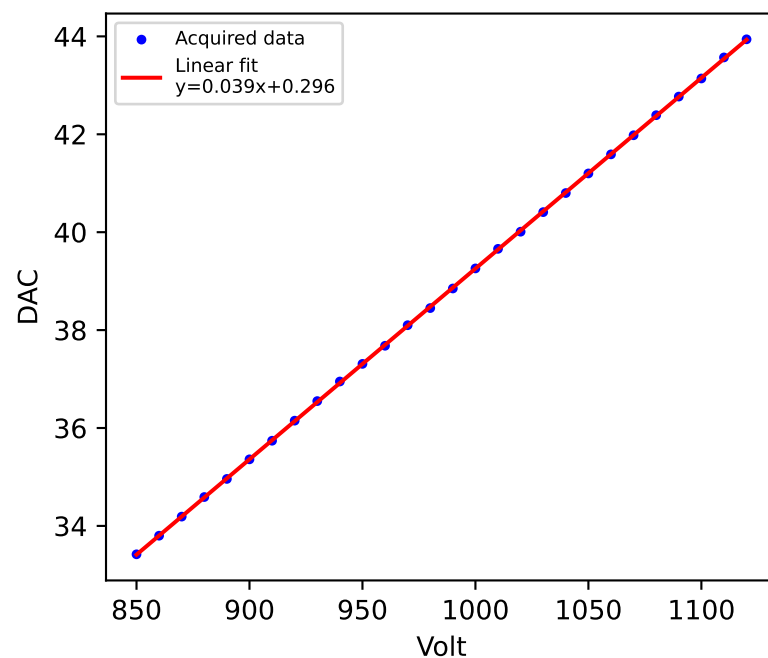
Trim REV IV curve



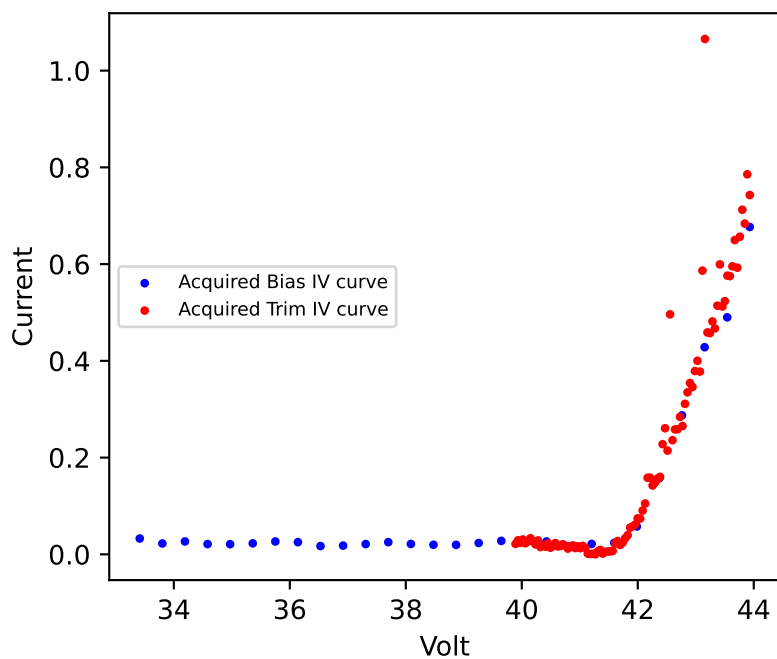
Bias REV IV curve



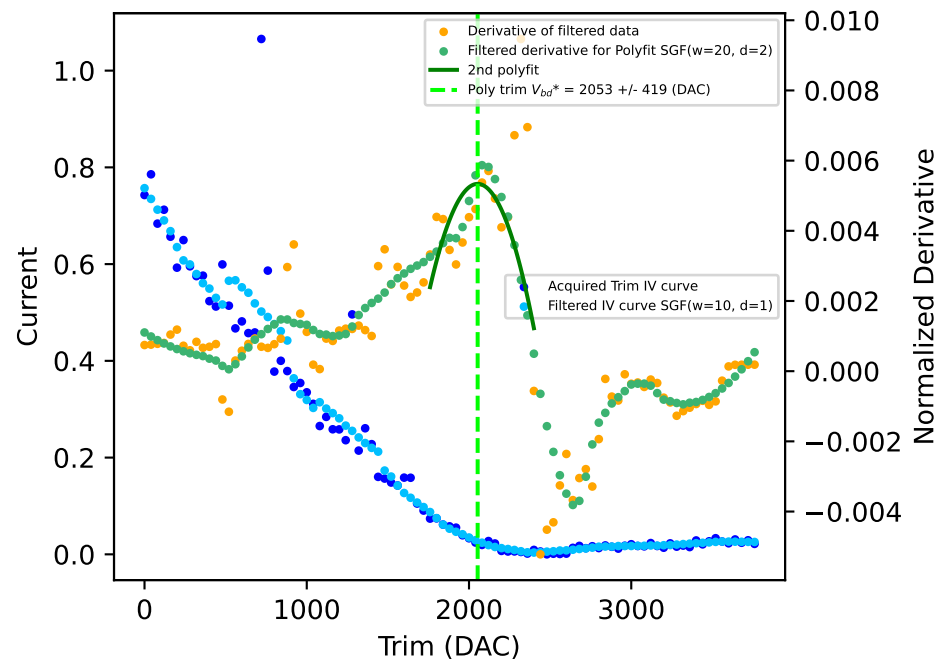
Bias DAC-VOLT conversion



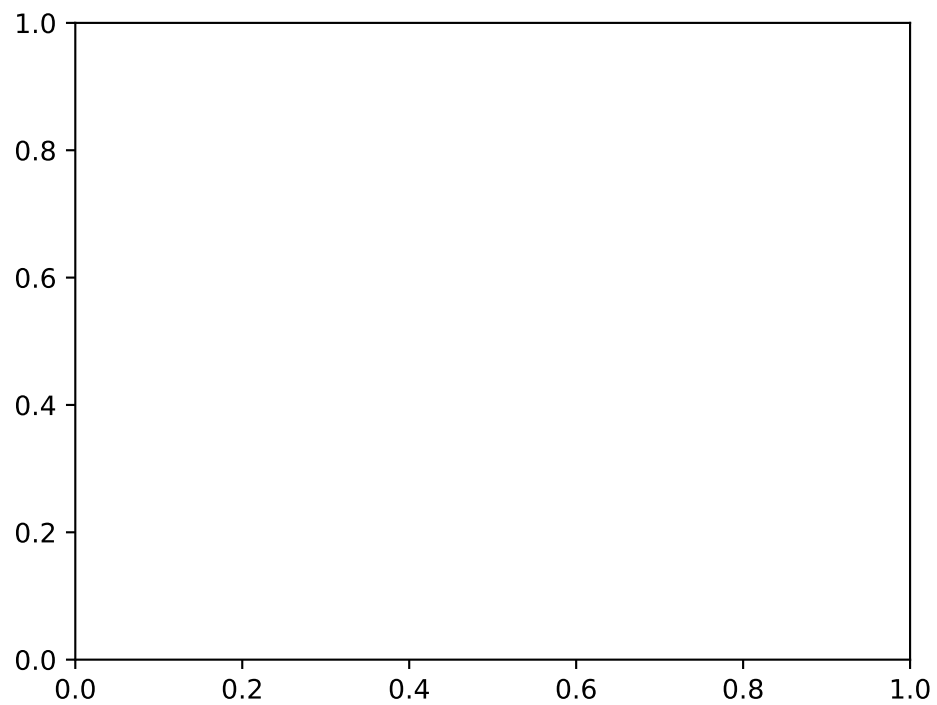
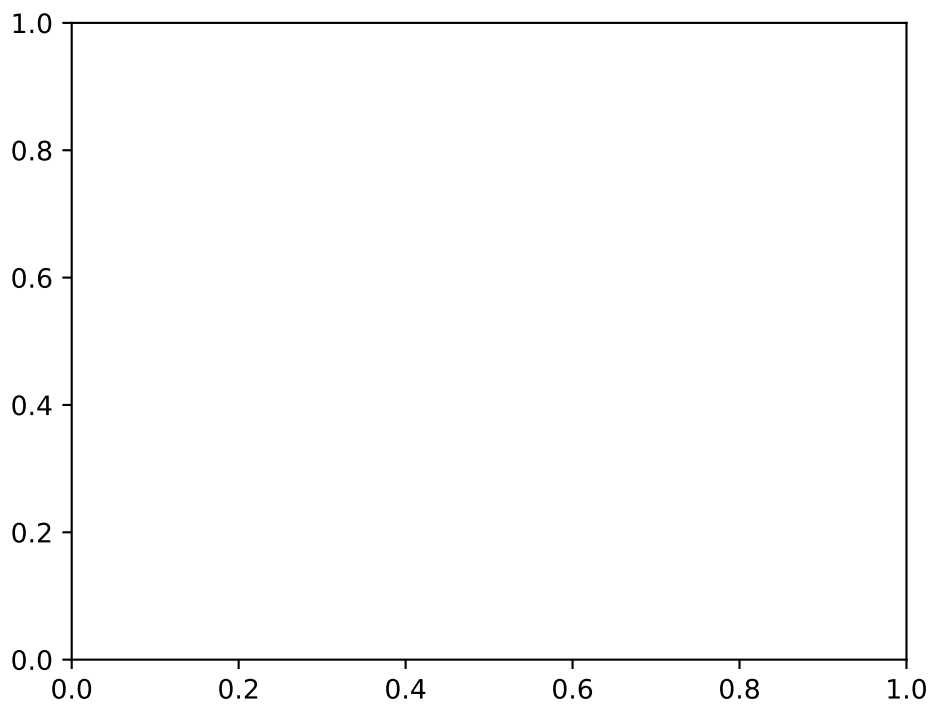
Bias + Trim REV IV curve



Trim REV IV curve



ENDPOINT:109 APA:2 AFE:3 Config\_CH:30 CH\_daq:36 SiPM:HPK --> MISSING FILE

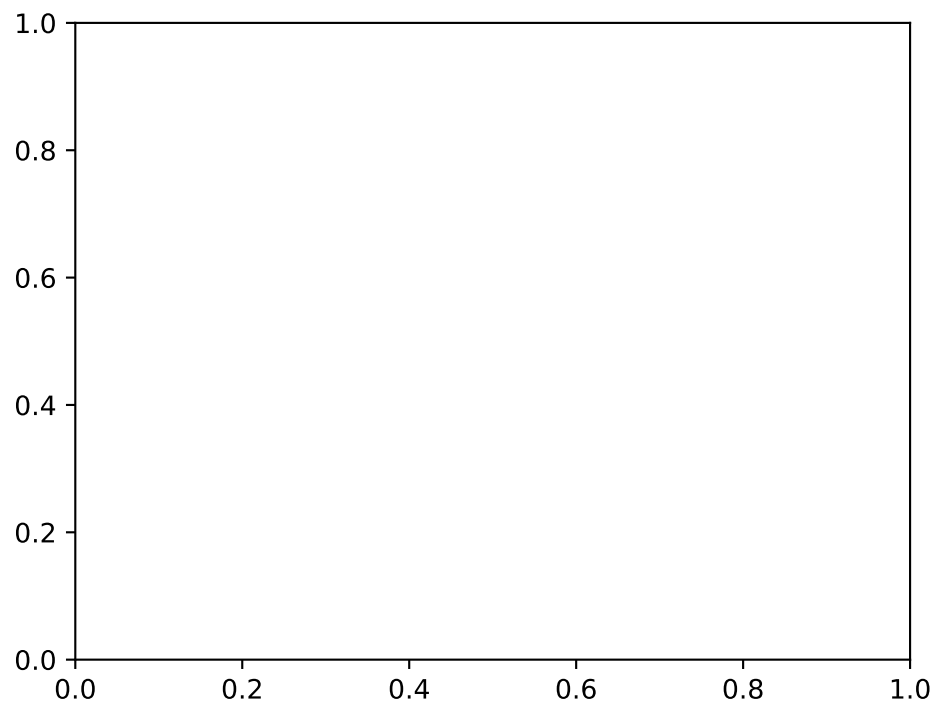
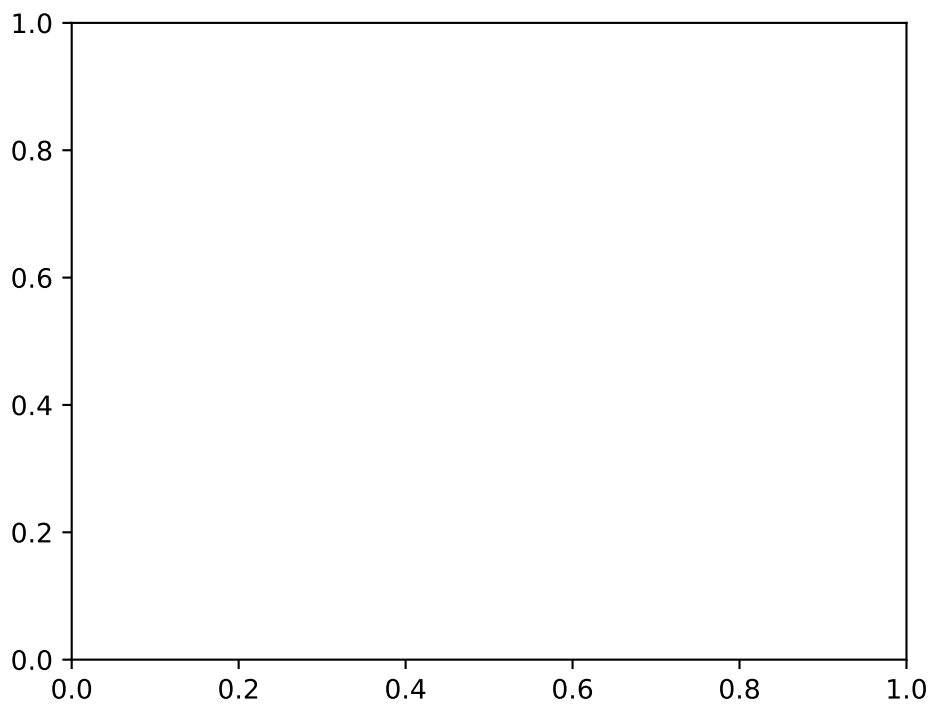
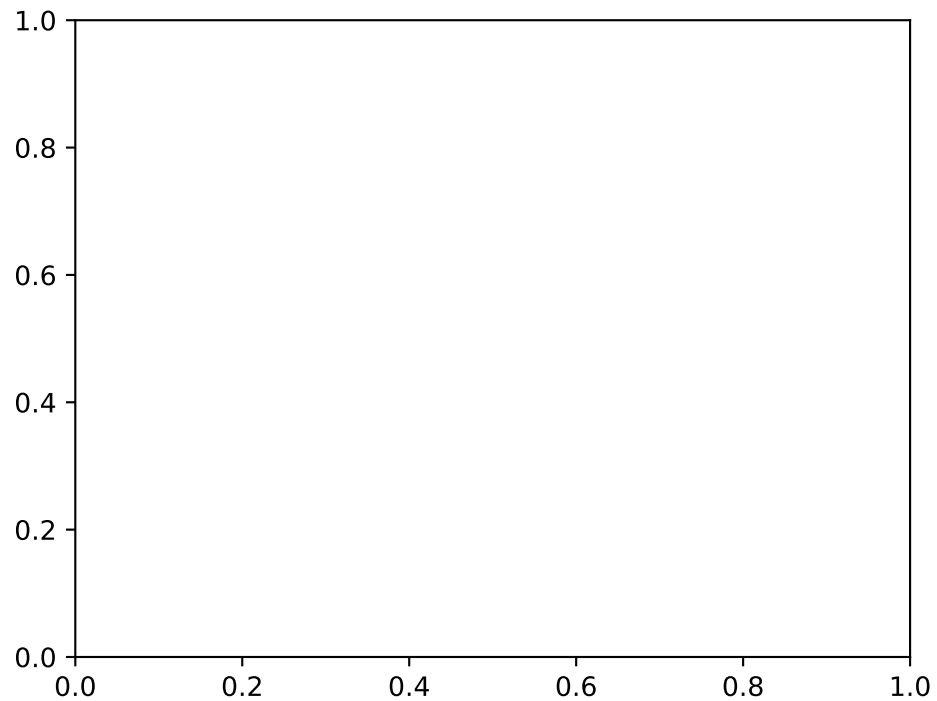
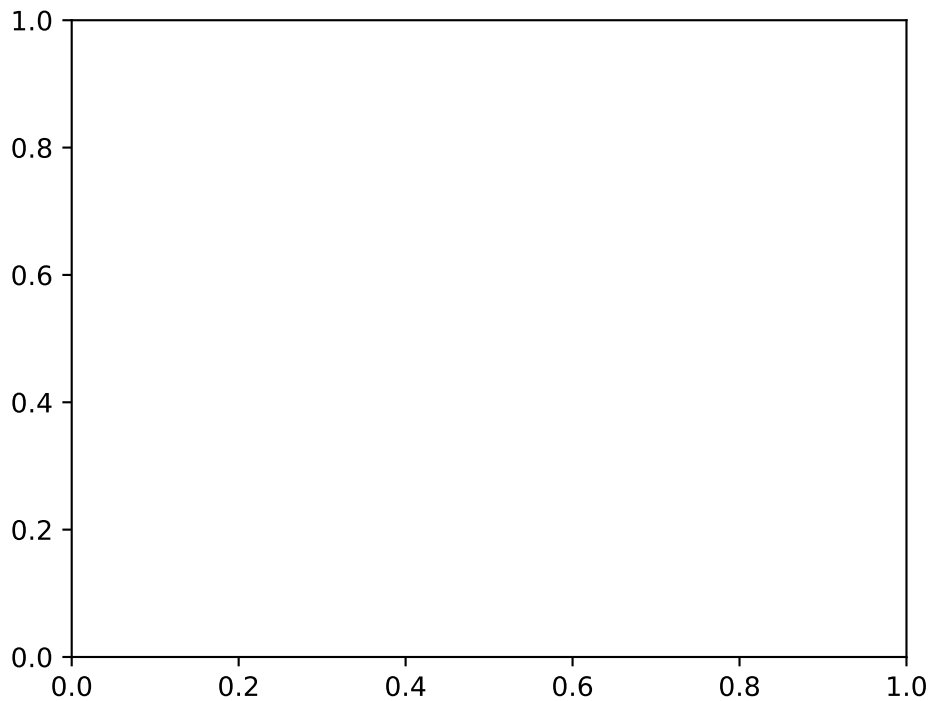


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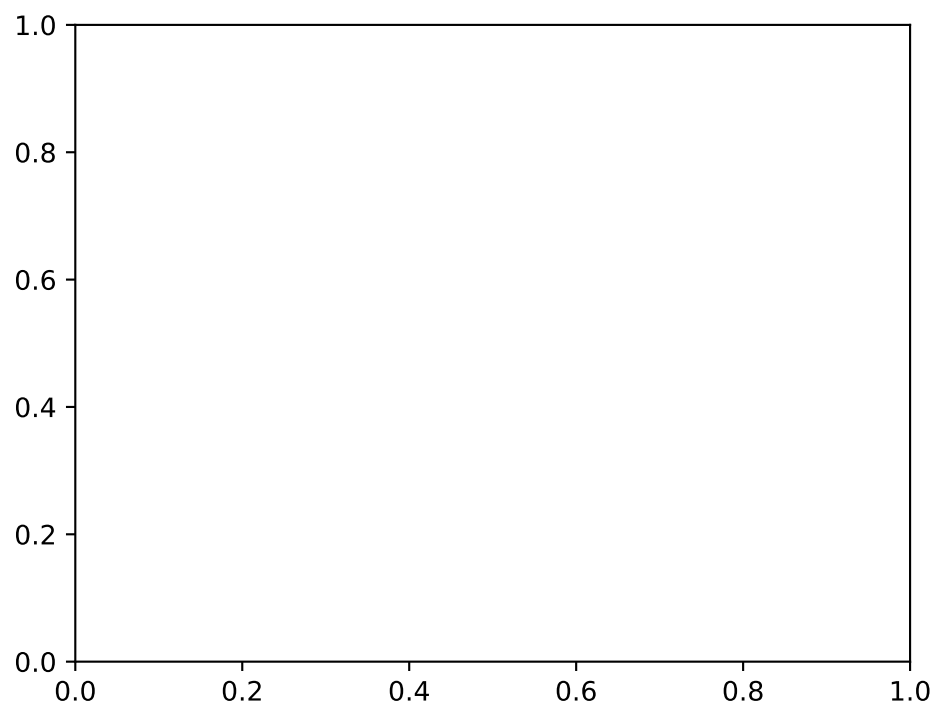
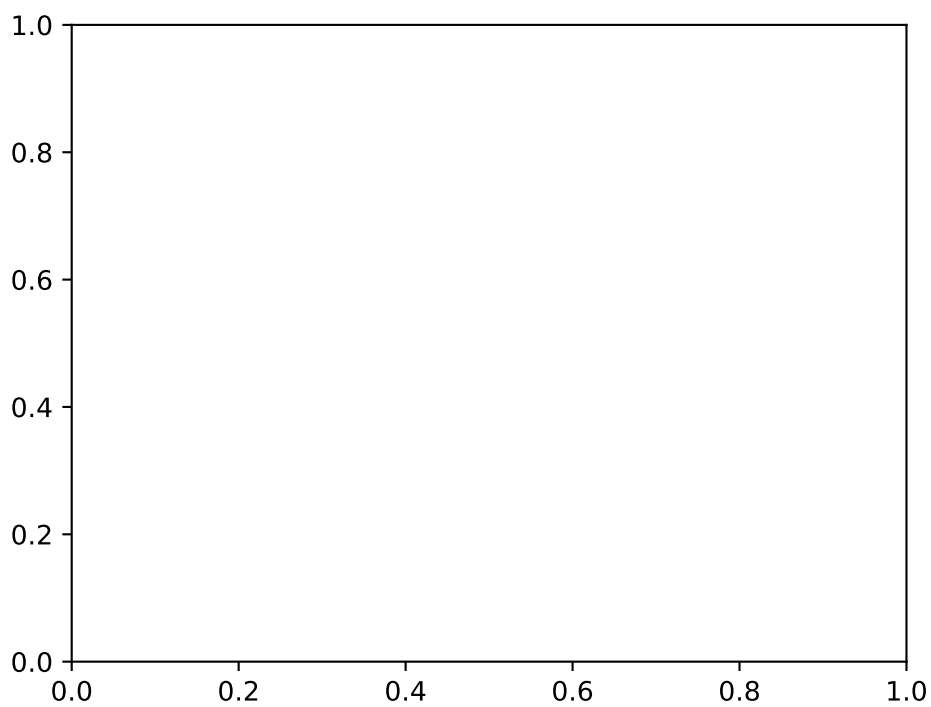
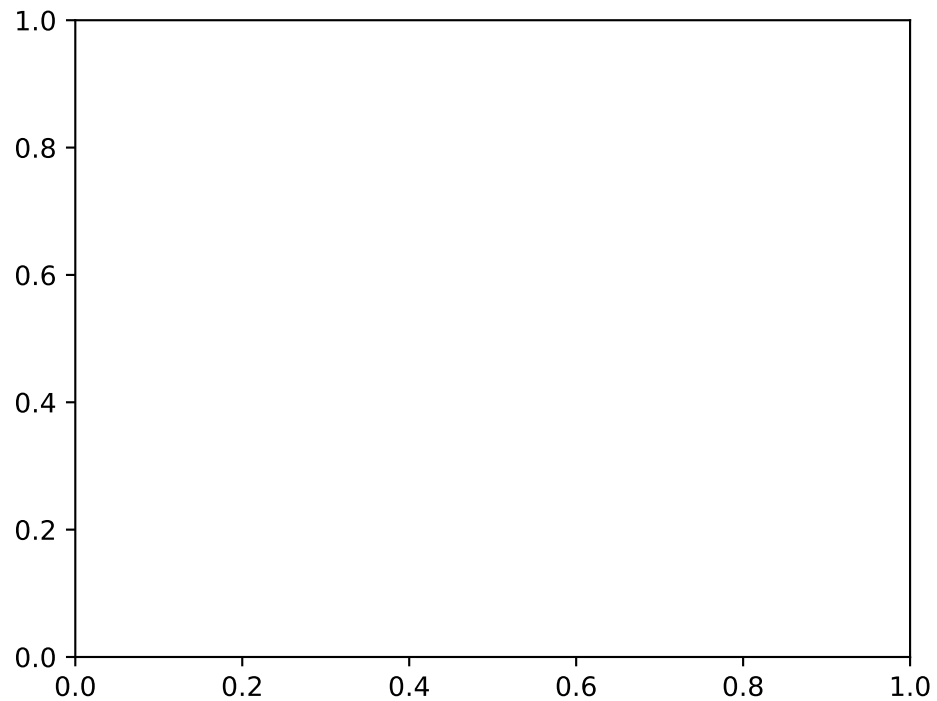




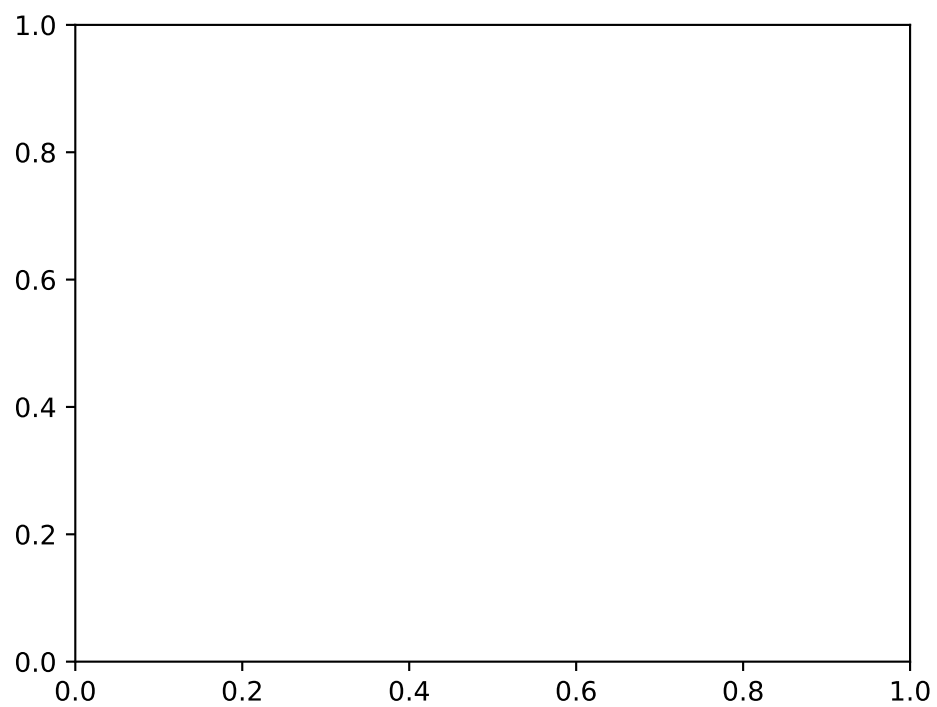
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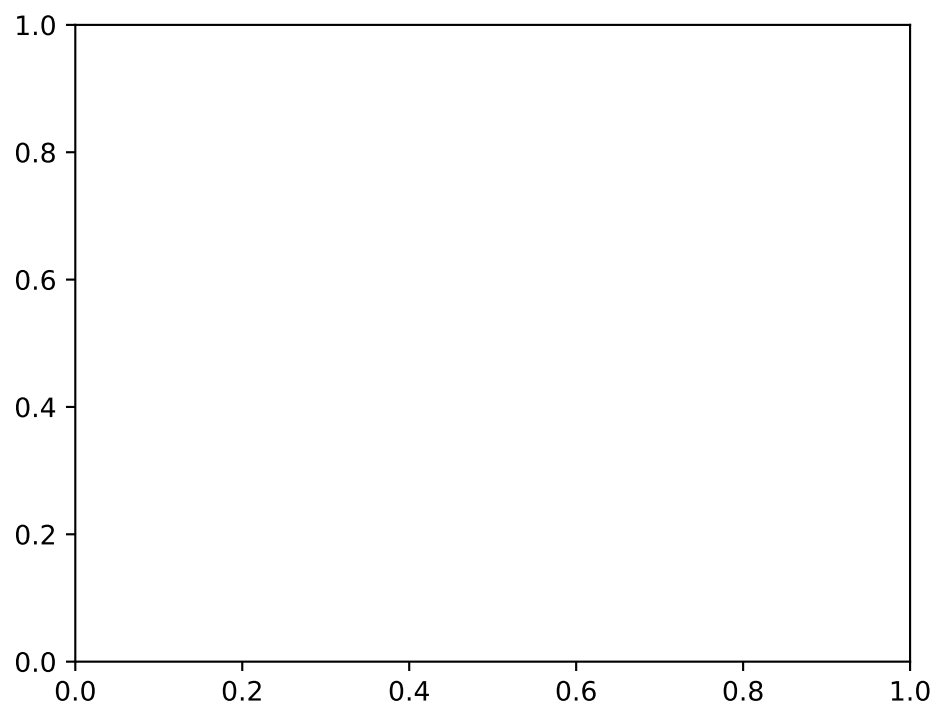
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ENDPOINT:109 APA:2 AFE:4 Config\_CH:34 CH\_daq:42 SiPM:HPK --> MISSING FILE



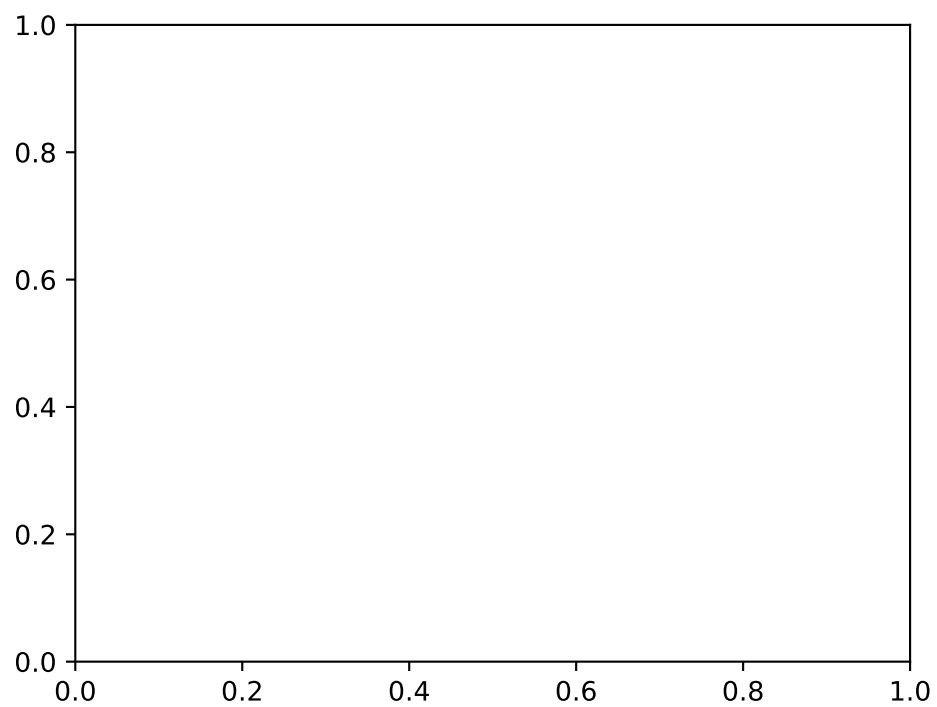
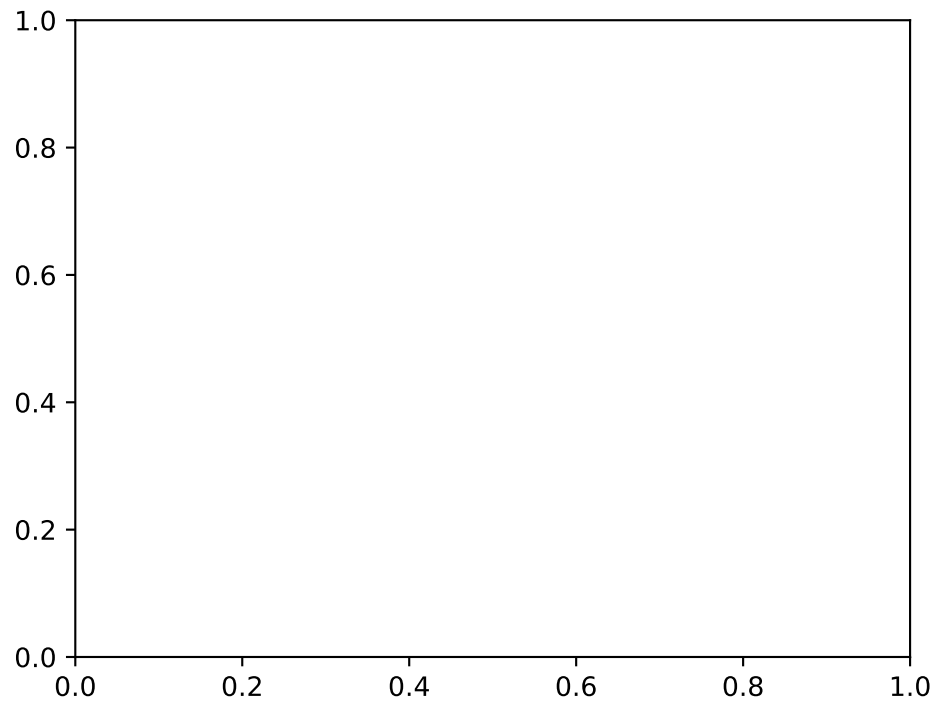
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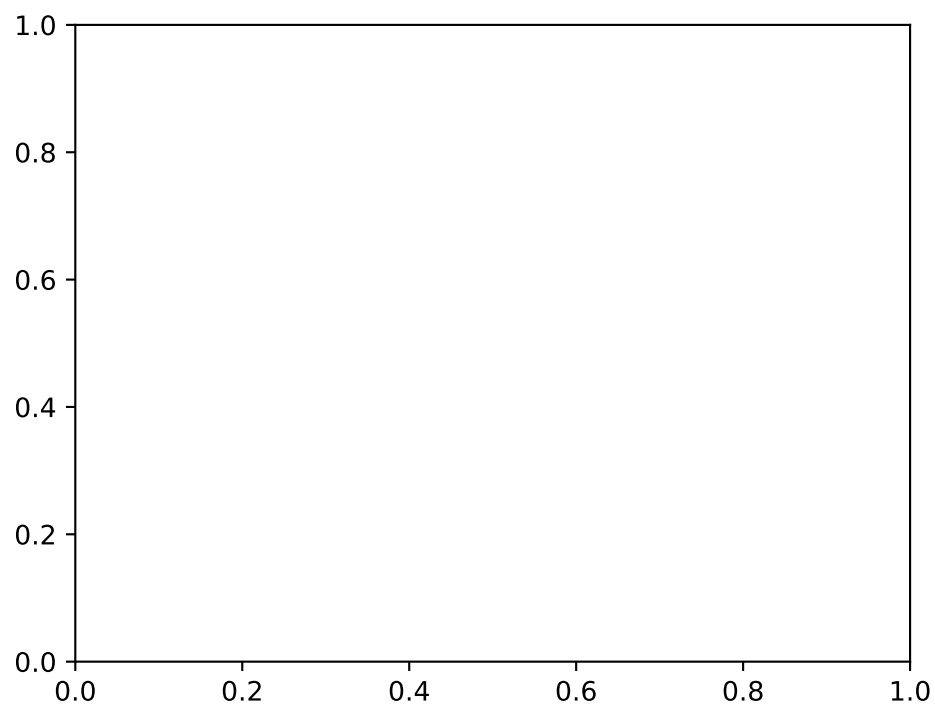
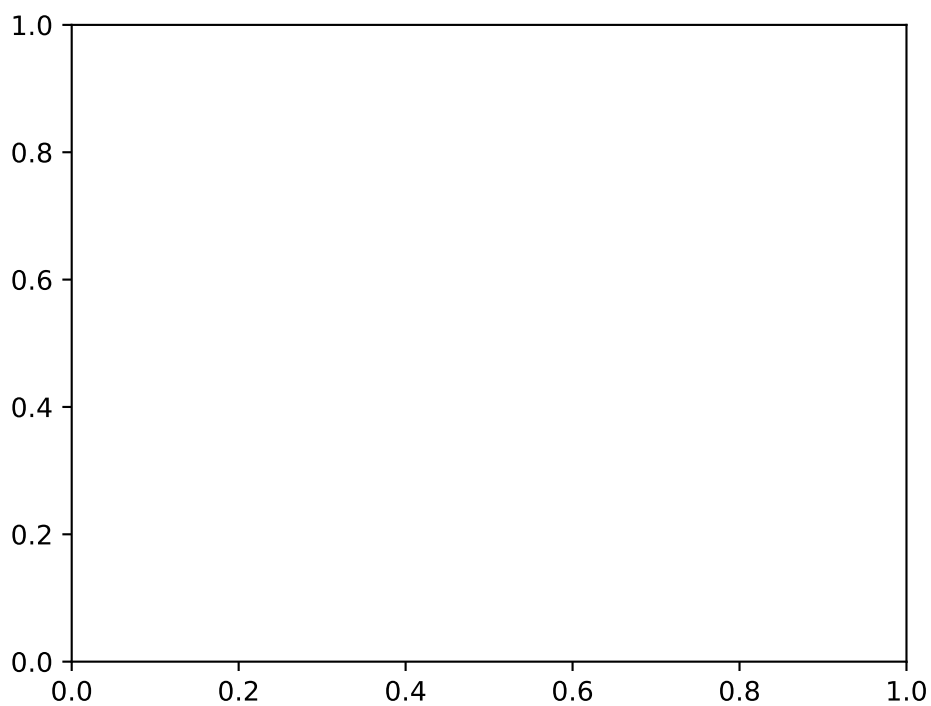
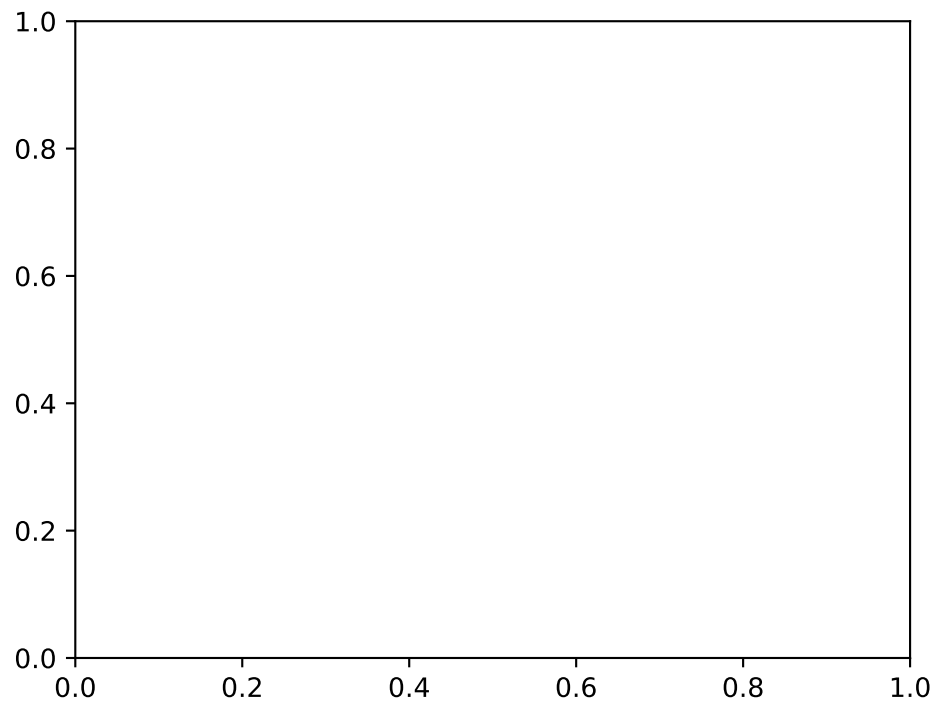
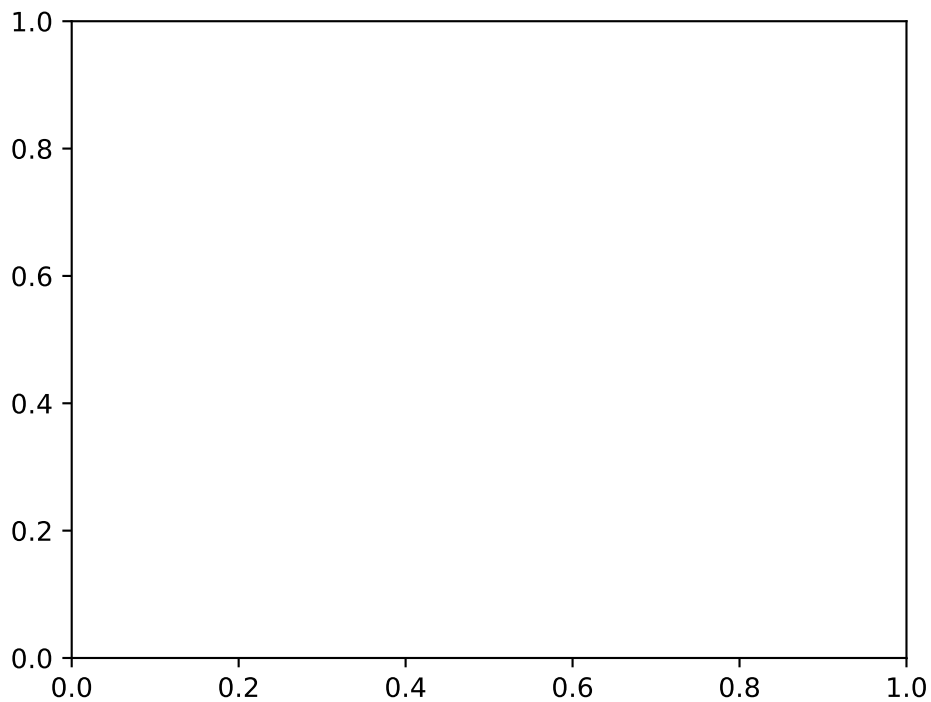
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ENDPOINT:109 APA:2 AFE:4 Config\_CH:37 CH\_daq:45 SiPM:HPK --> MISSING FILE



ENDPOINT:109 APA:2 AFE:4 Config\_CH:38 CH\_daq:46 SiPM:HPK --> MISSING FILE



ENDPOINT:109 APA:2 AFE:4 Config\_CH:39 CH\_daq:47 SiPM:HPK --> MISSING FILE

