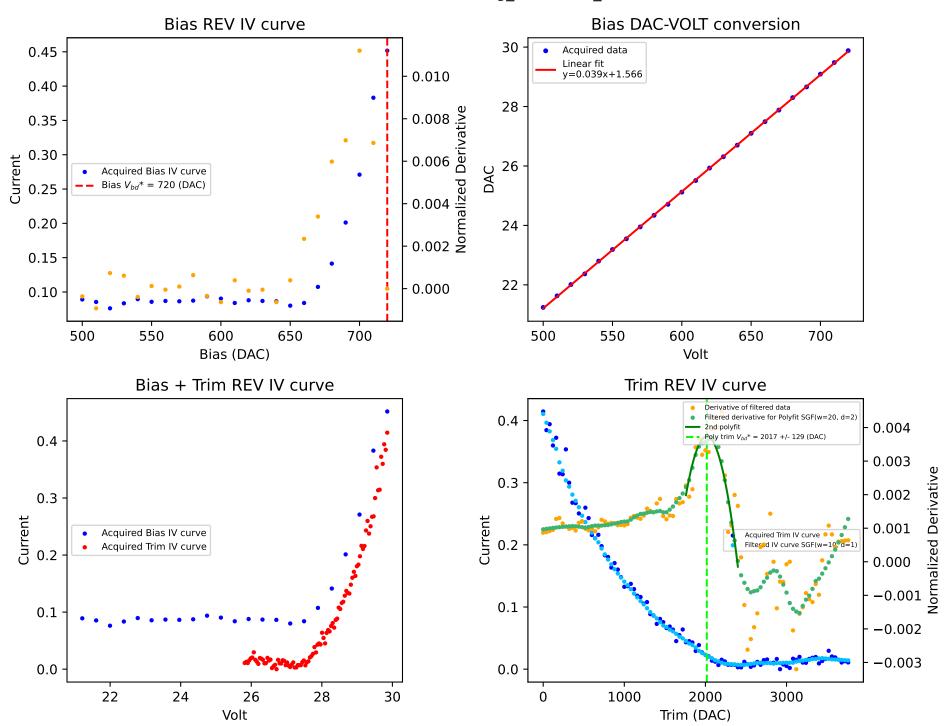
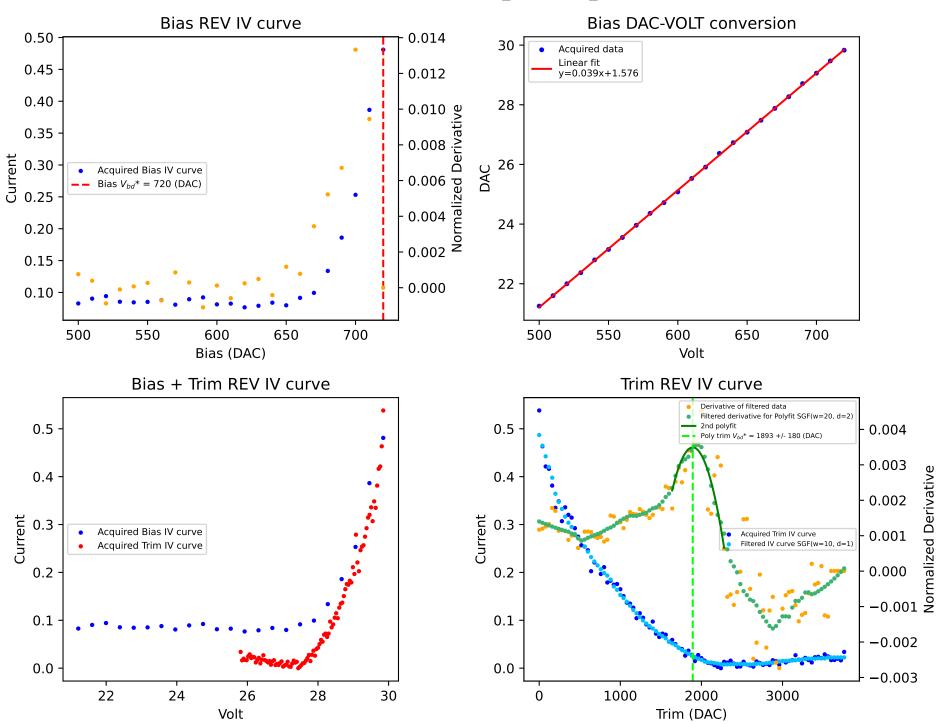
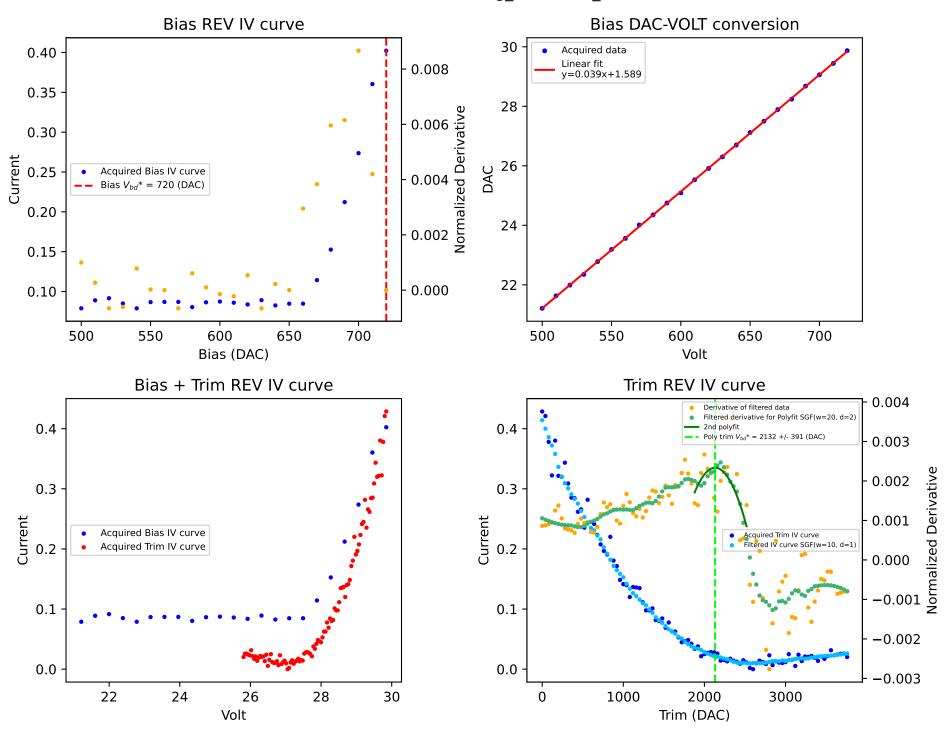
ENDPOINT:104 APA:1 AFE:0 Config CH:0 DAQ CH:0 SiPM:FBK



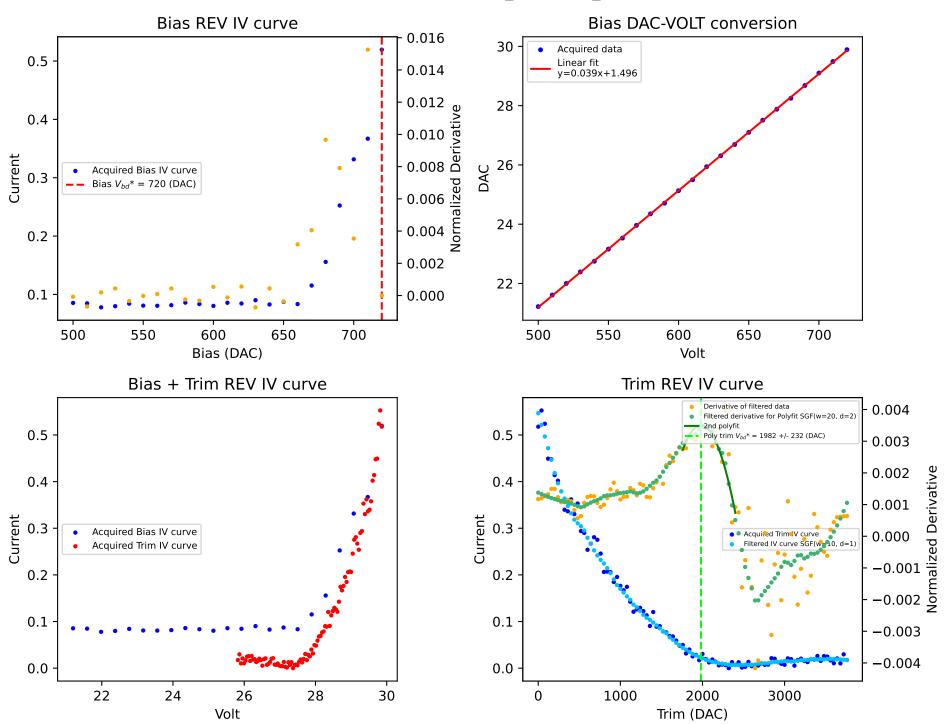
ENDPOINT:104 APA:1 AFE:0 Config_CH:1 DAQ_CH:1 SiPM:FBK

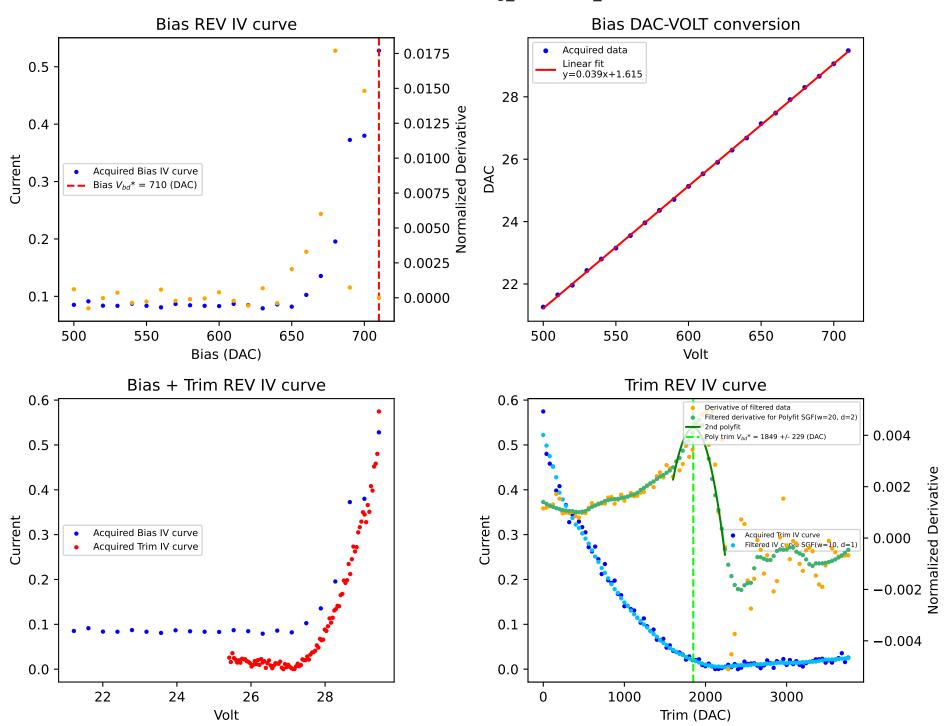


ENDPOINT:104 APA:1 AFE:0 Config CH:2 DAQ CH:2 SiPM:FBK

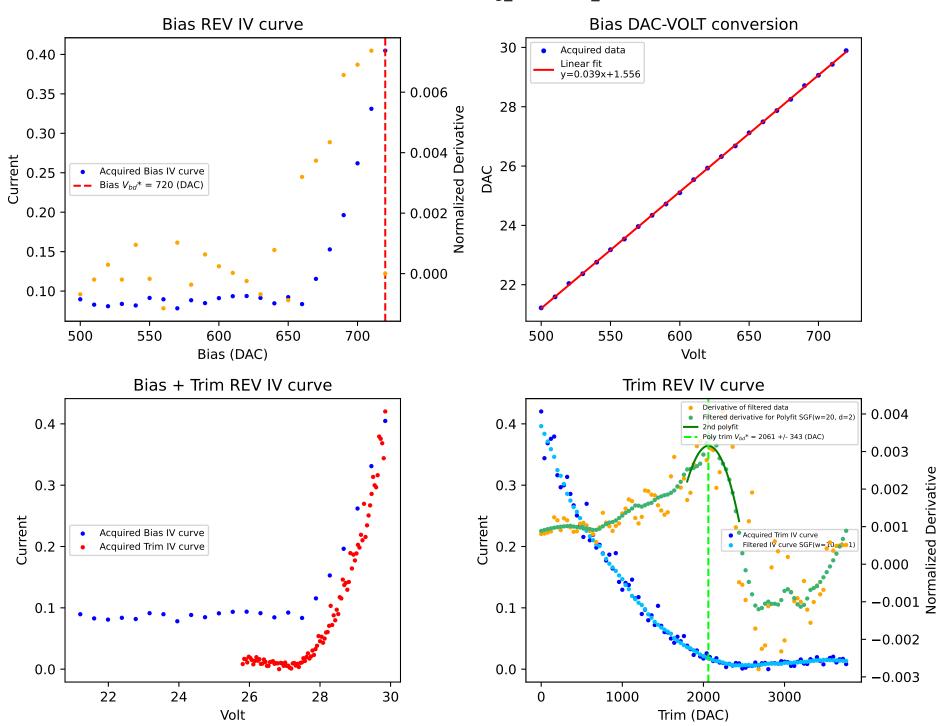


ENDPOINT:104 APA:1 AFE:0 Config_CH:3 DAQ_CH:3 SiPM:FBK

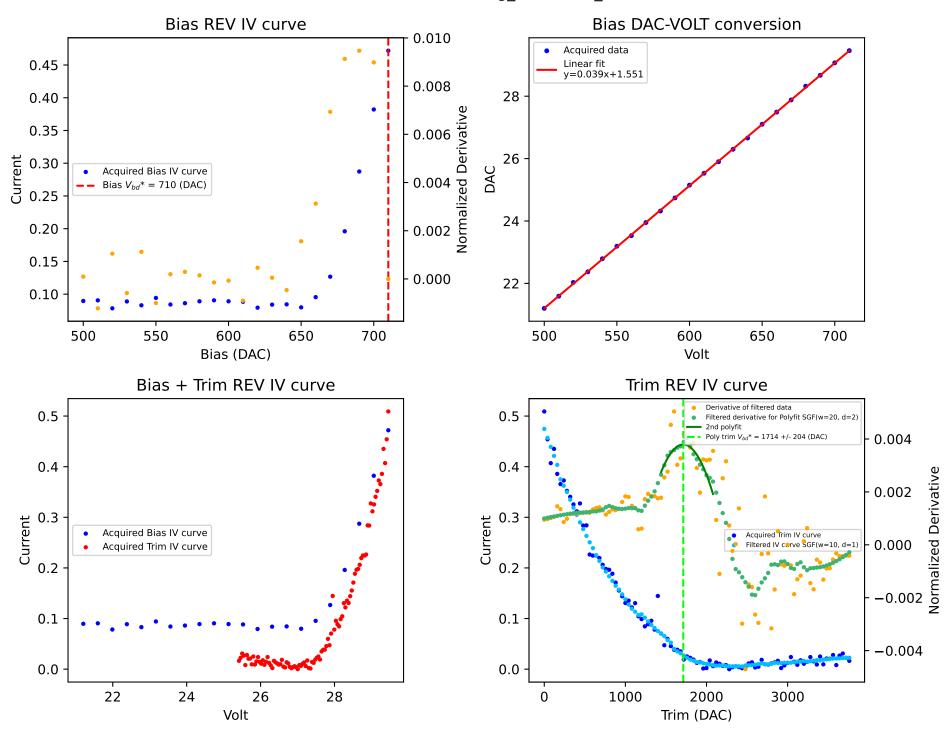




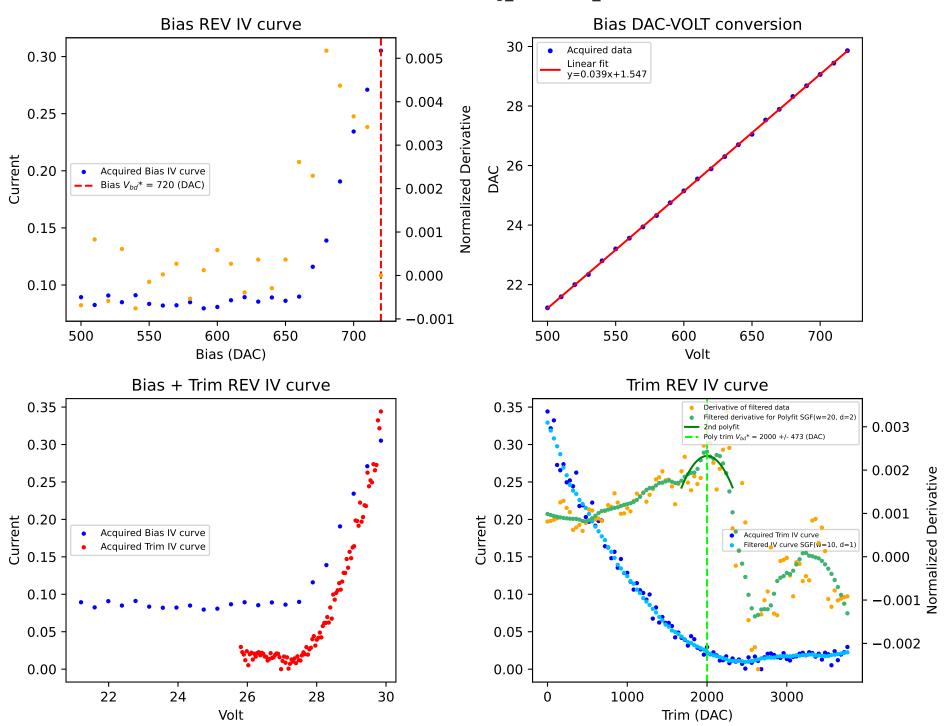
ENDPOINT:104 APA:1 AFE:0 Config CH:5 DAQ CH:5 SiPM:FBK

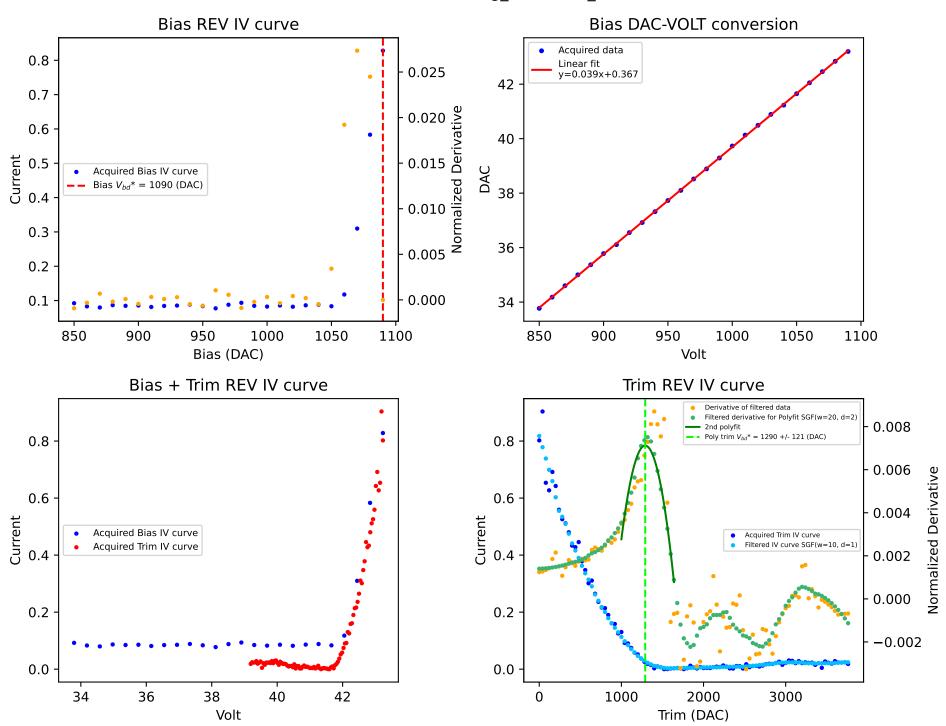


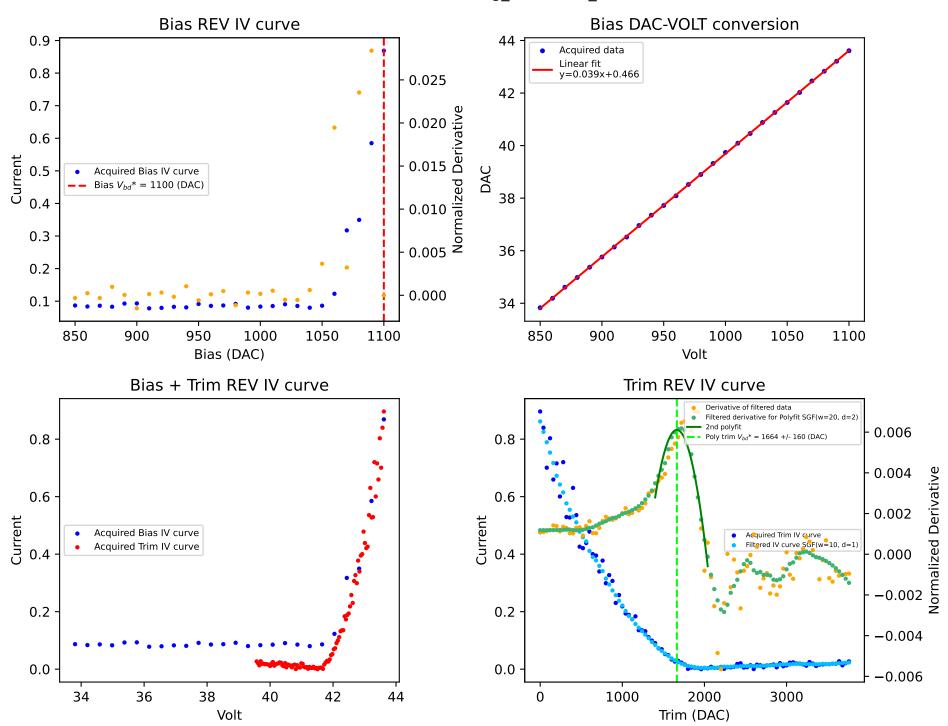
ENDPOINT:104 APA:1 AFE:0 Config CH:6 DAQ CH:6 SiPM:FBK



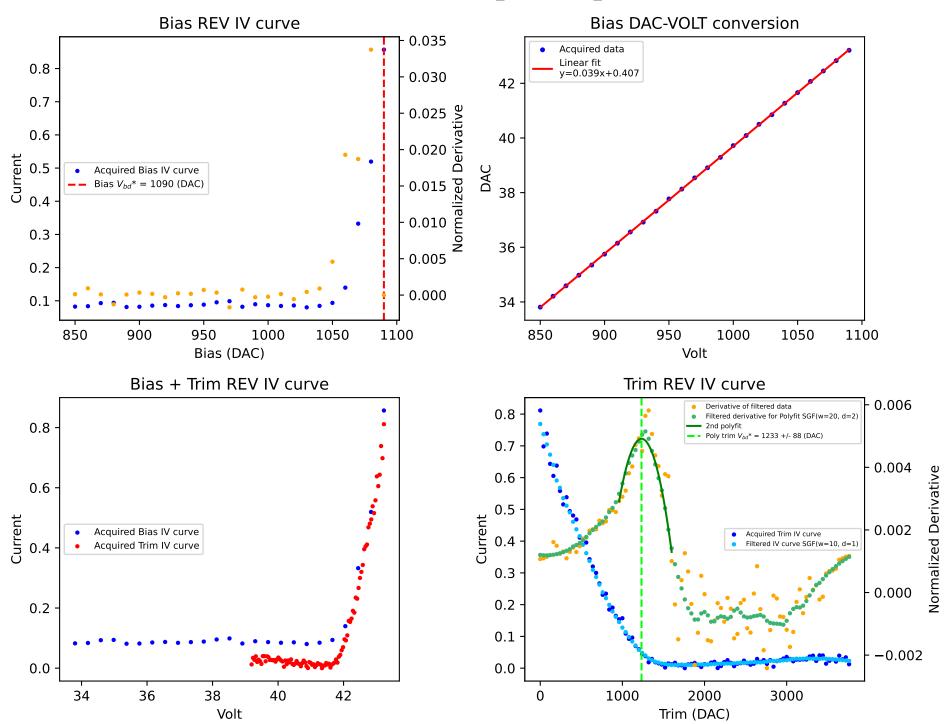
ENDPOINT:104 APA:1 AFE:0 Config CH:7 DAQ CH:7 SiPM:FBK



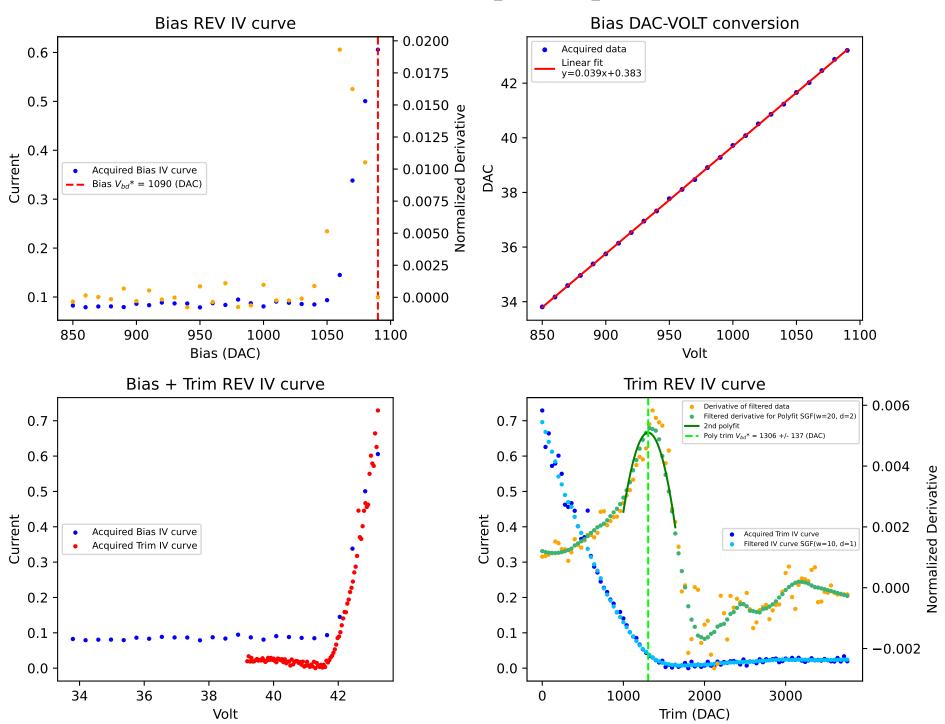


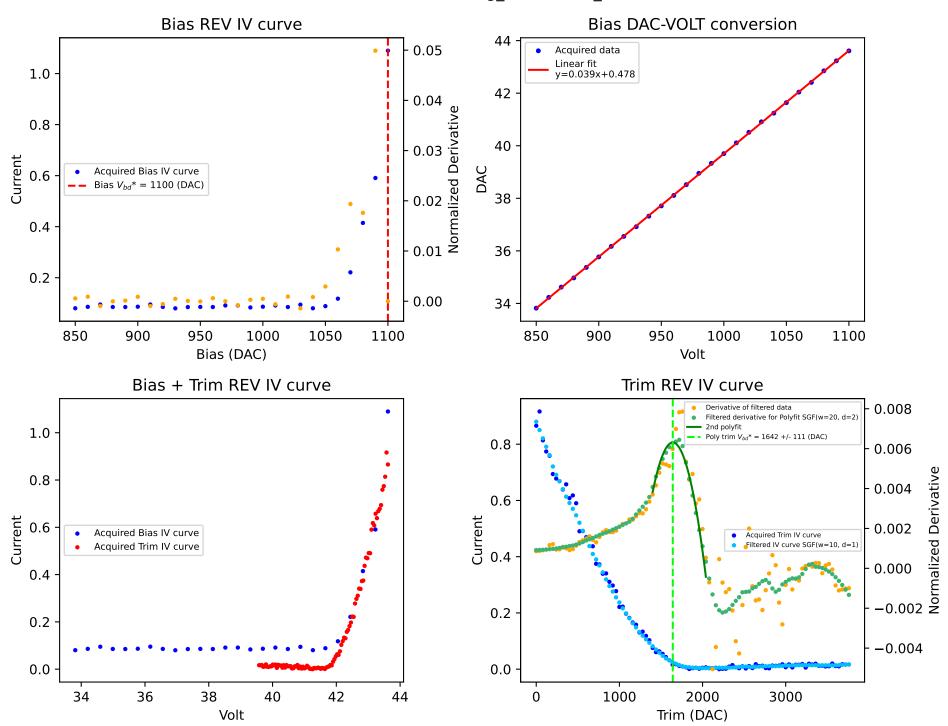


ENDPOINT:104 APA:1 AFE:1 Config_CH:10 DAQ_CH:12 SiPM:HPK

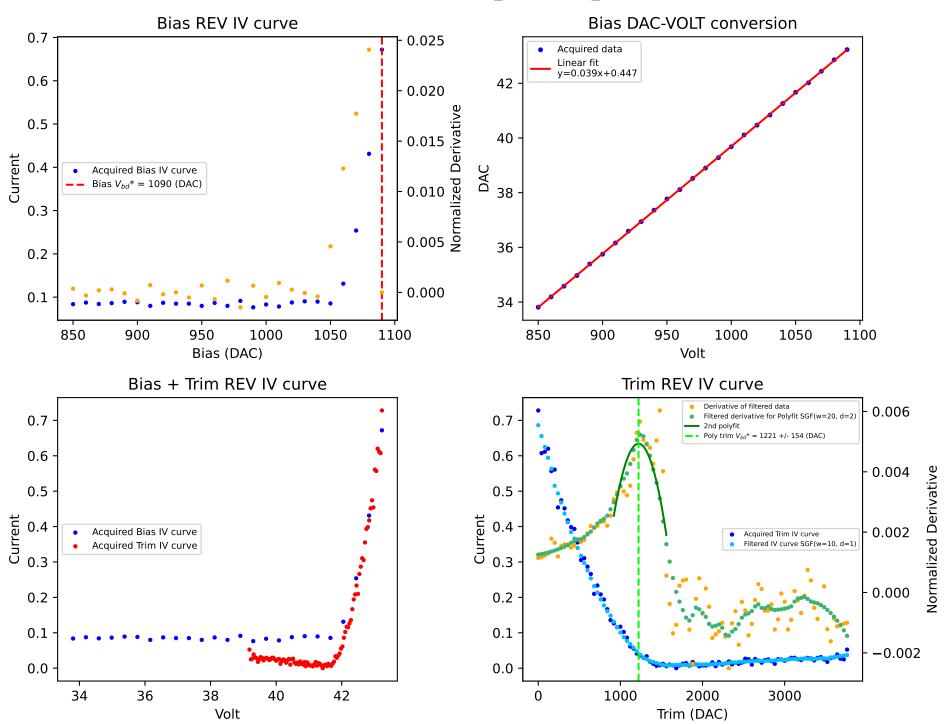


ENDPOINT:104 APA:1 AFE:1 Config_CH:11 DAQ_CH:13 SiPM:HPK

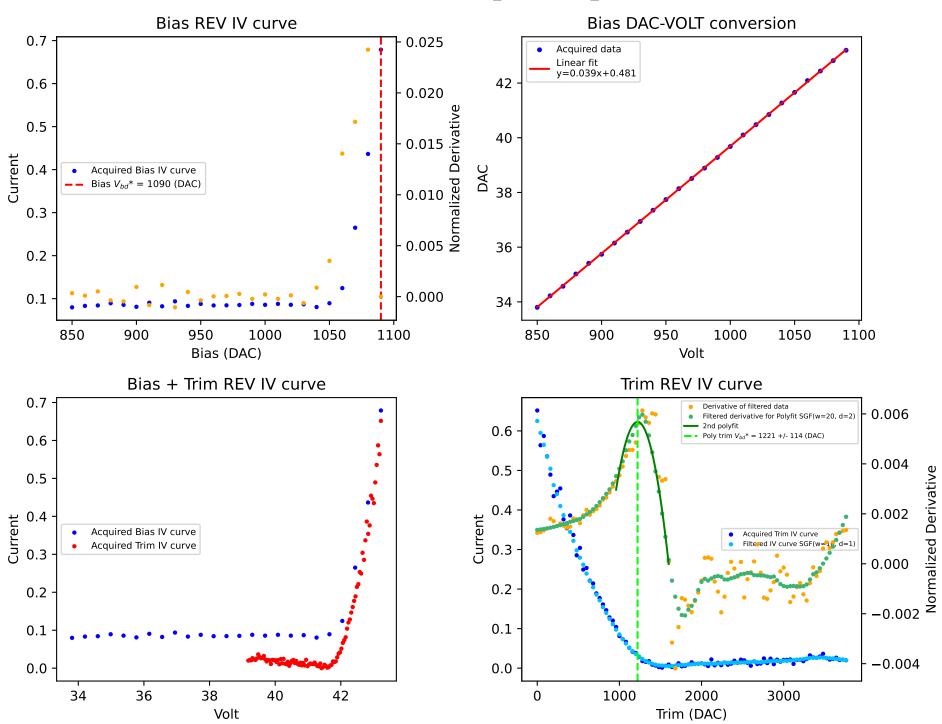




ENDPOINT:104 APA:1 AFE:1 Config_CH:13 DAQ_CH:15 SiPM:HPK



ENDPOINT:104 APA:1 AFE:1 Config_CH:14 DAQ_CH:16 SiPM:HPK



ENDPOINT:104 APA:1 AFE:1 Config CH:15 DAQ CH:17 SiPM:HPK

