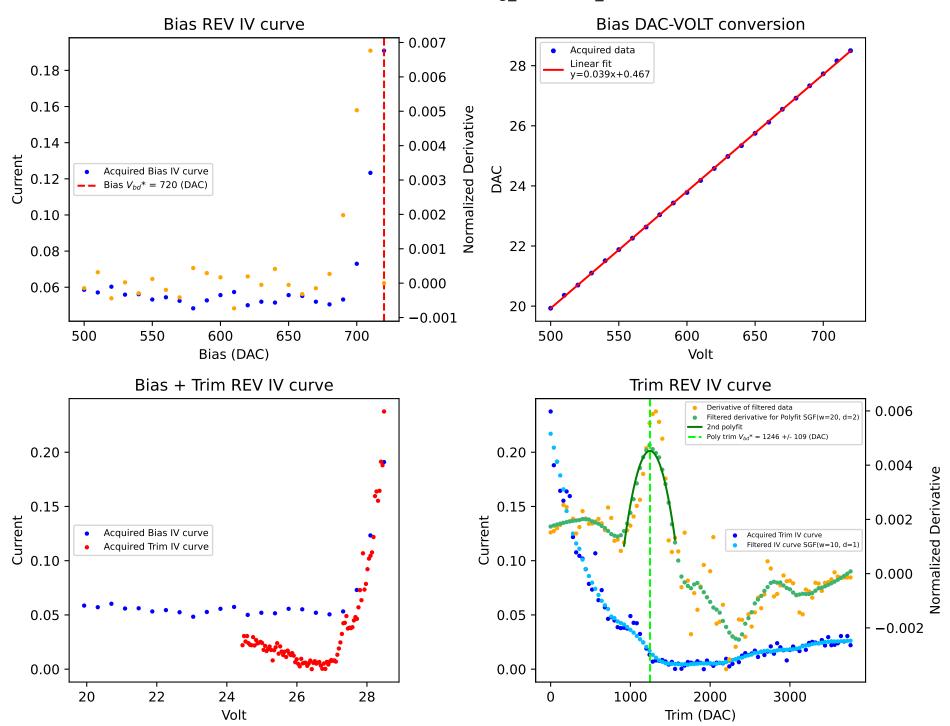
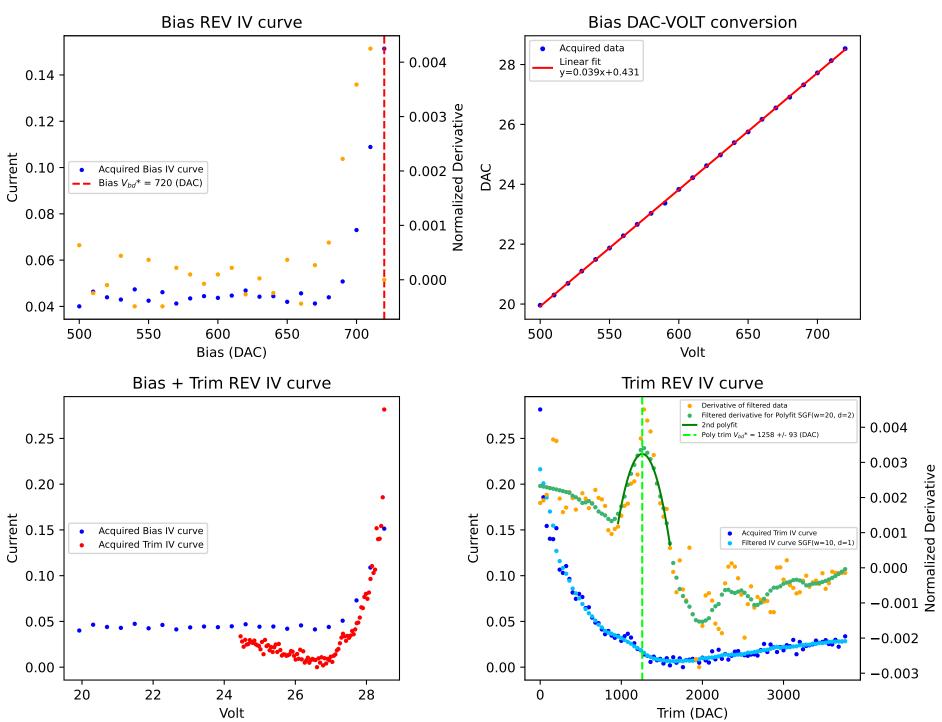
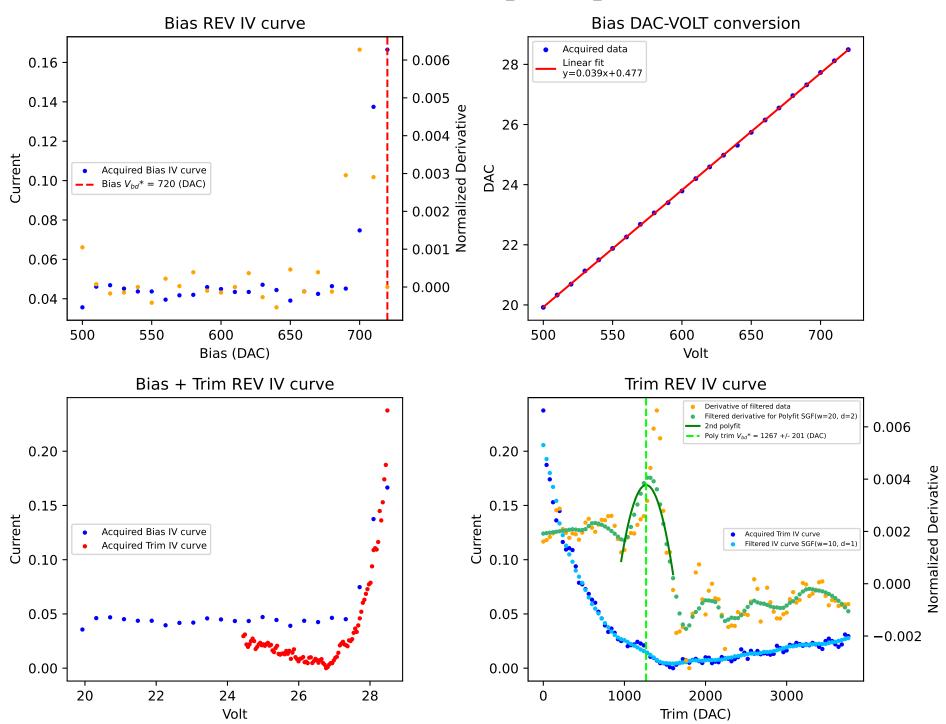
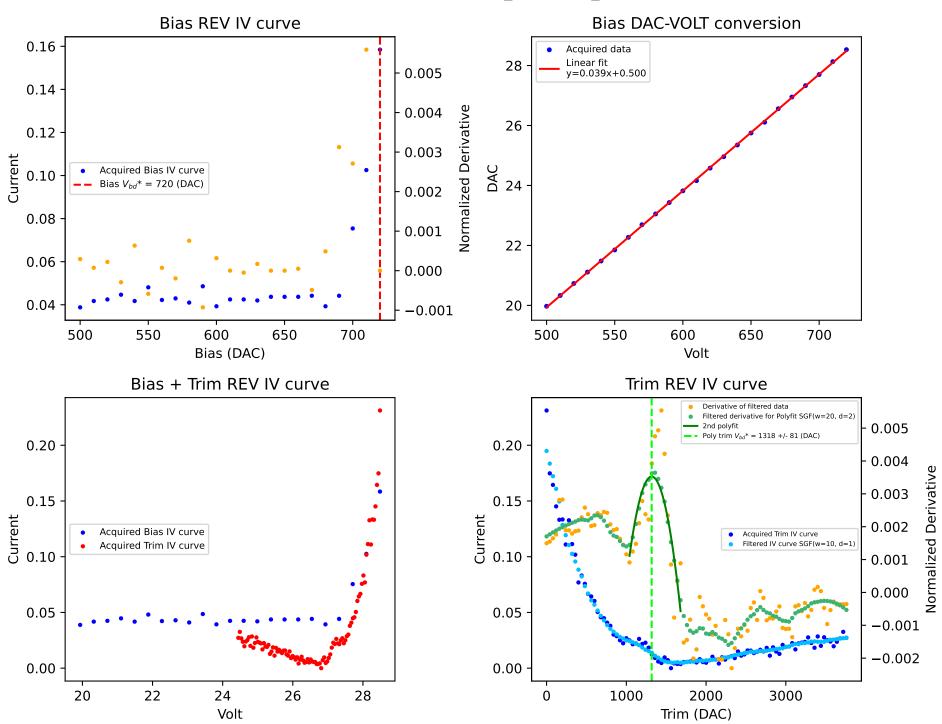
## ENDPOINT:105 APA:1 AFE:0 Config CH:0 DAQ CH:0 SiPM:FBK



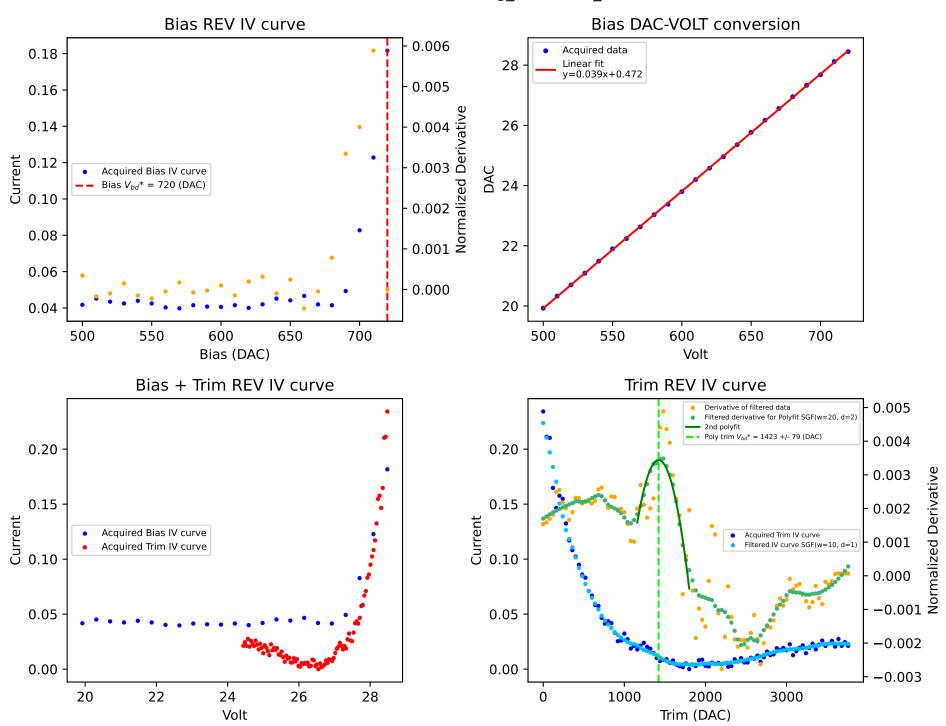




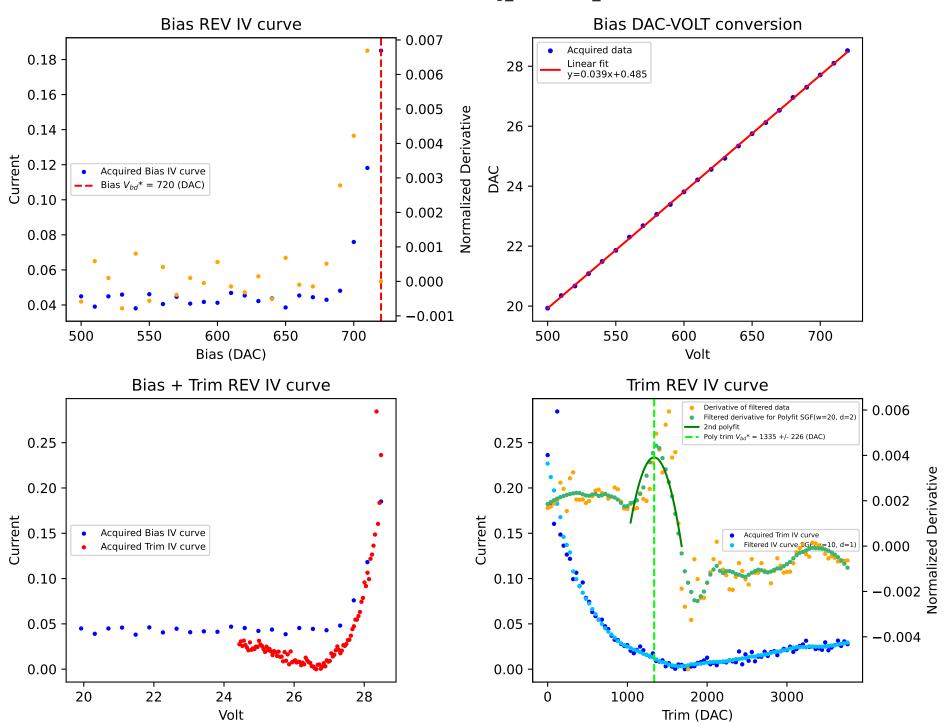
# ENDPOINT:105 APA:1 AFE:0 Config\_CH:3 DAQ\_CH:3 SiPM:FBK



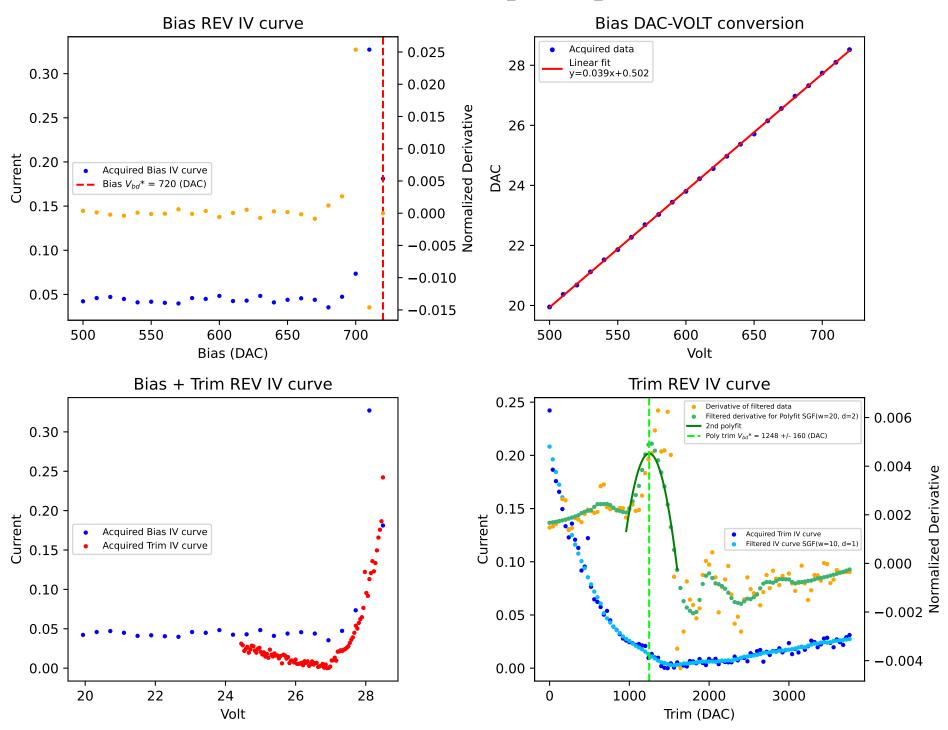
## ENDPOINT:105 APA:1 AFE:0 Config CH:4 DAQ CH:4 SiPM:FBK



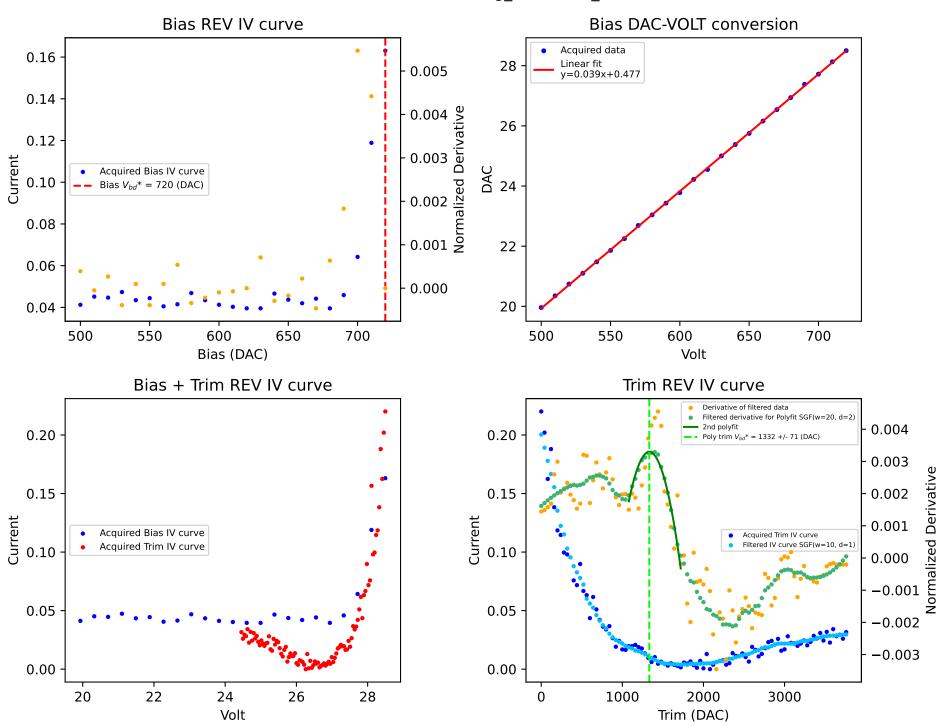
## ENDPOINT:105 APA:1 AFE:0 Config CH:5 DAQ CH:5 SiPM:FBK

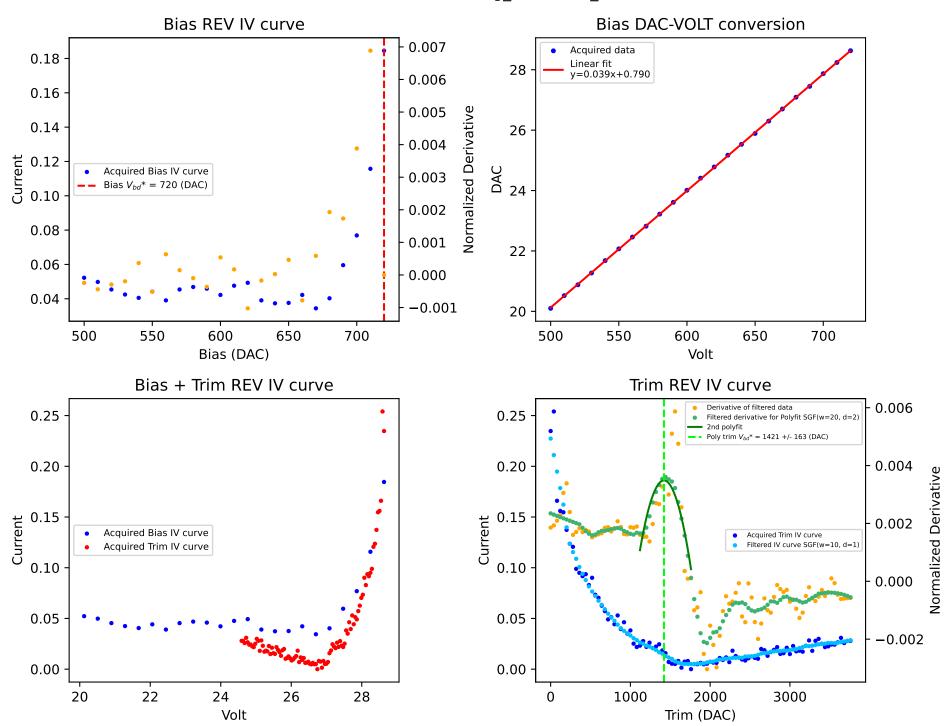


# ENDPOINT:105 APA:1 AFE:0 Config\_CH:6 DAQ\_CH:6 SiPM:FBK

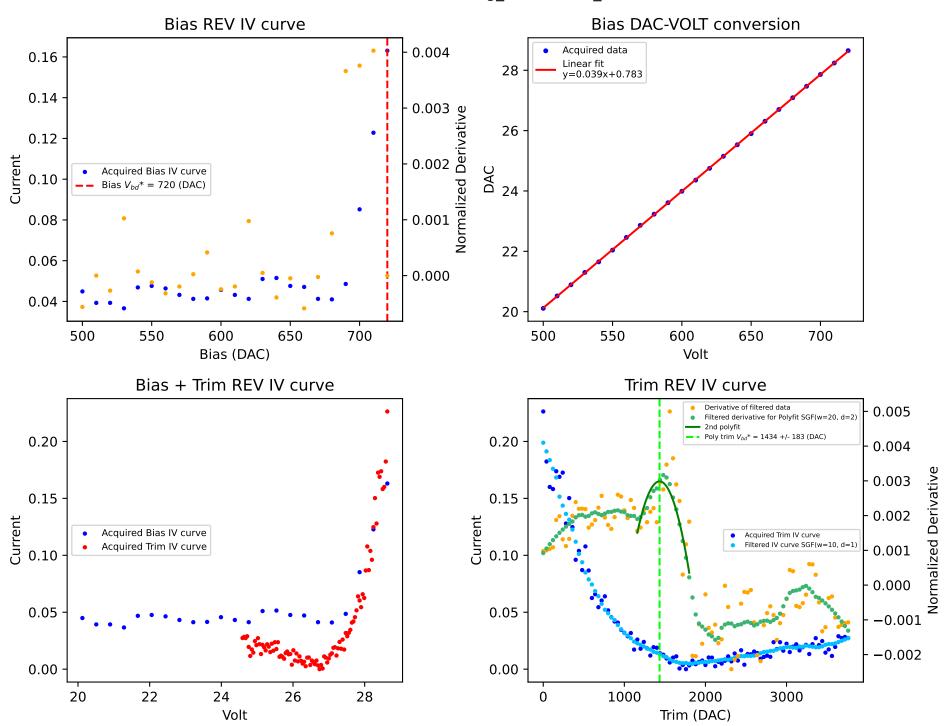


## ENDPOINT:105 APA:1 AFE:0 Config CH:7 DAQ CH:7 SiPM:FBK

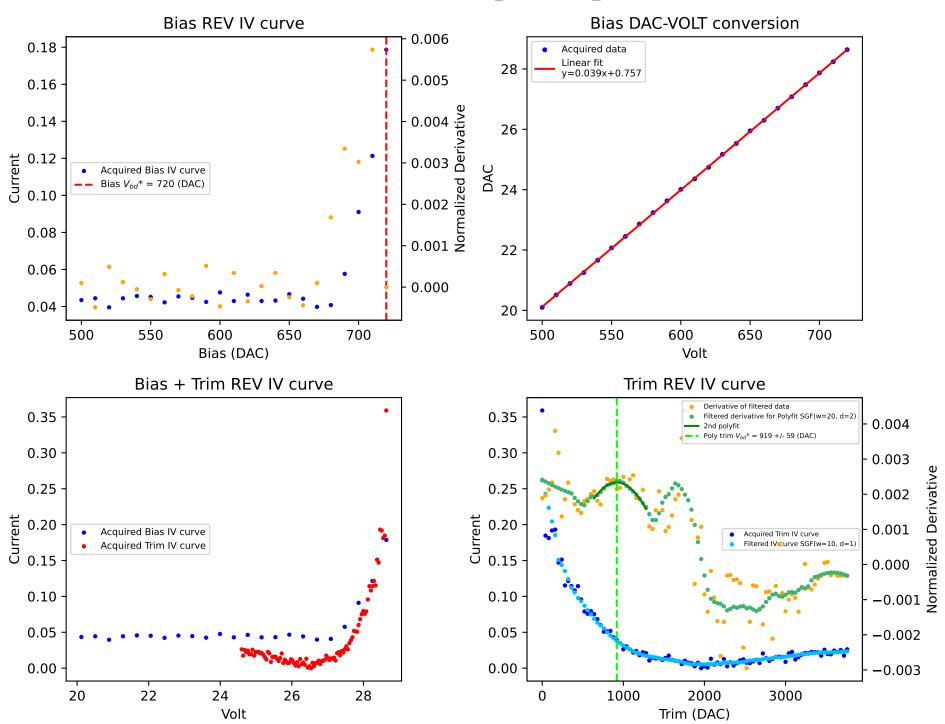




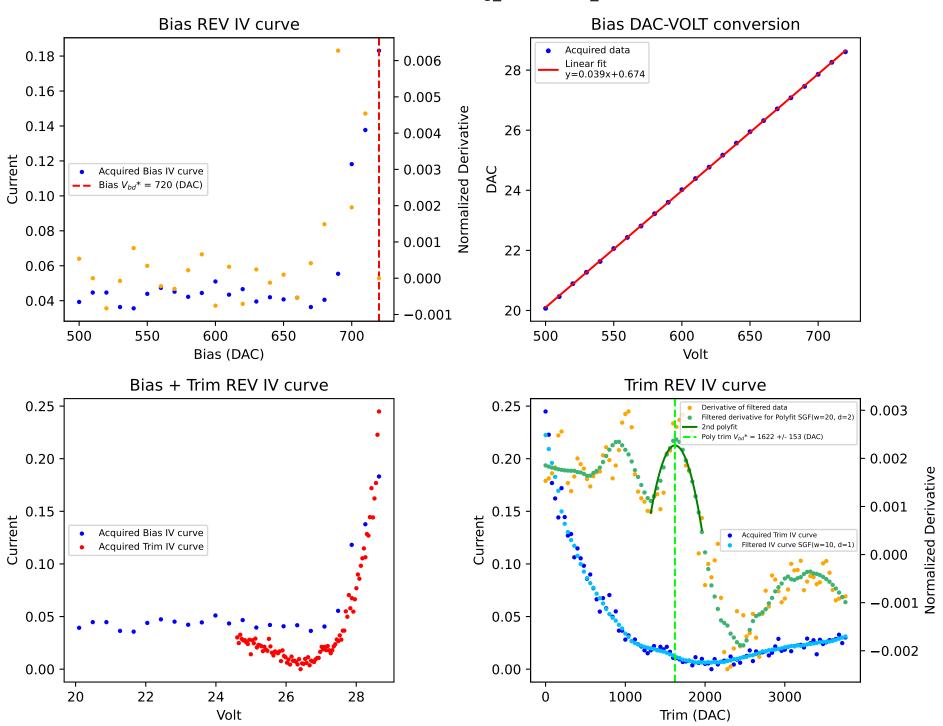
ENDPOINT:105 APA:1 AFE:1 Config CH:10 DAQ CH:12 SiPM:FBK

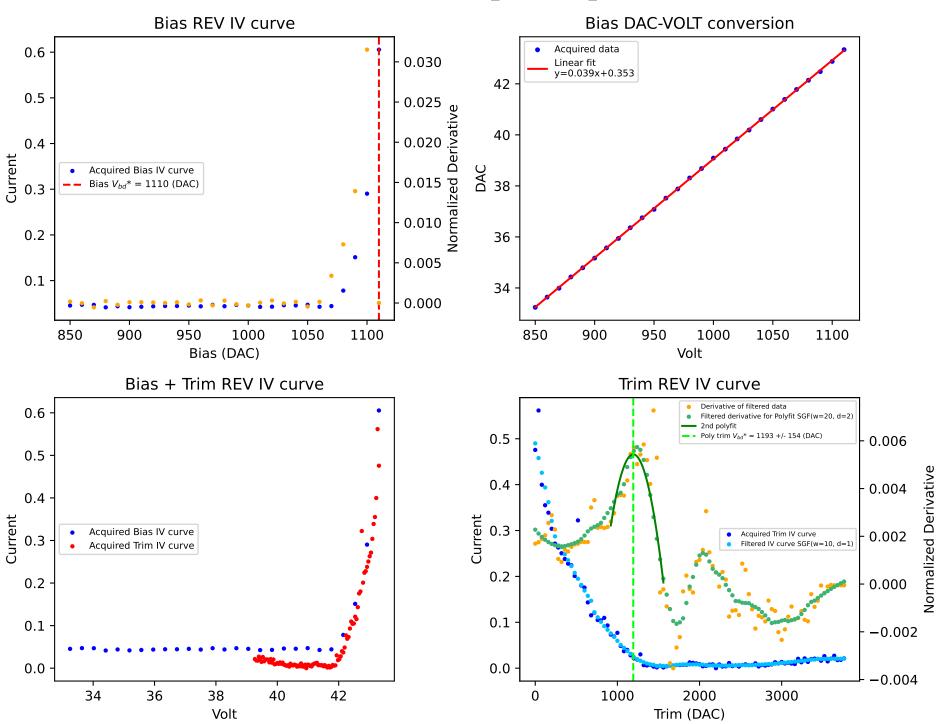


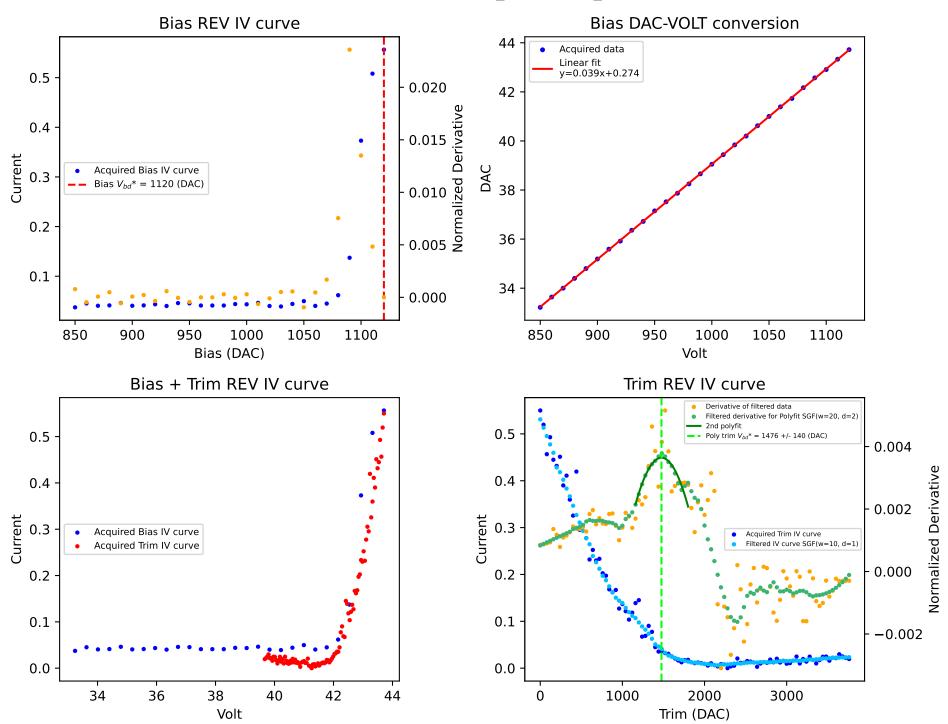
# ENDPOINT:105 APA:1 AFE:1 Config\_CH:13 DAQ\_CH:15 SiPM:FBK



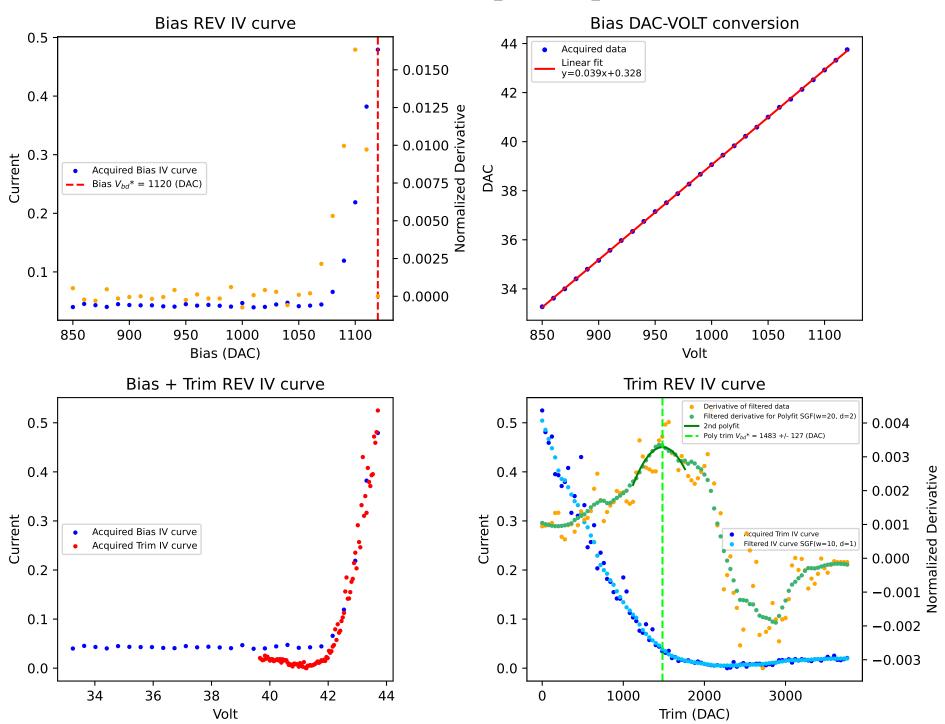
ENDPOINT:105 APA:1 AFE:1 Config CH:15 DAQ CH:17 SiPM:FBK







# ENDPOINT:105 APA:1 AFE:2 Config\_CH:20 DAQ\_CH:24 SiPM:HPK



# ENDPOINT:105 APA:1 AFE:2 Config\_CH:22 DAQ\_CH:26 SiPM:HPK

