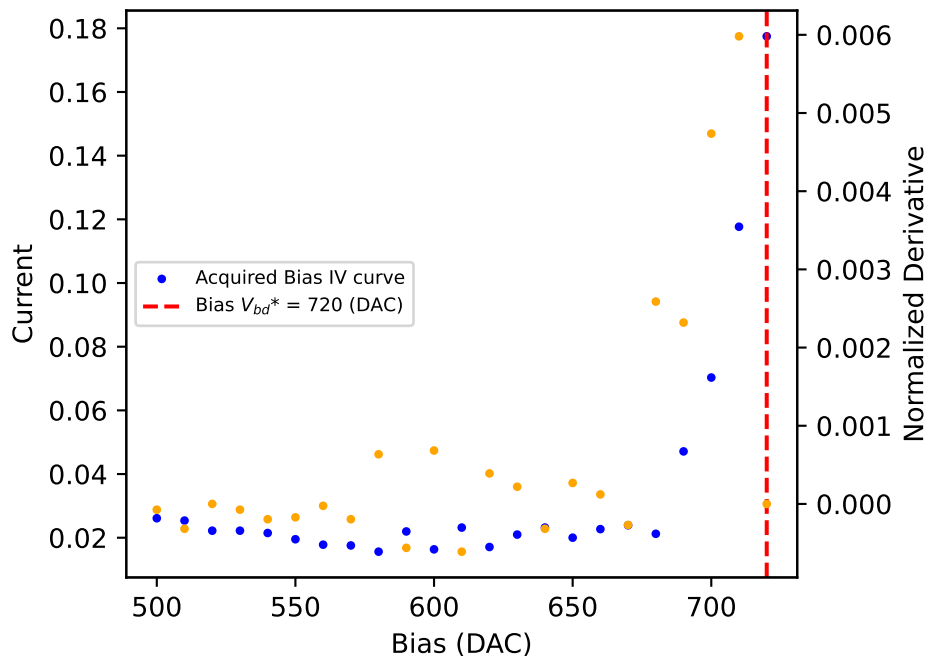
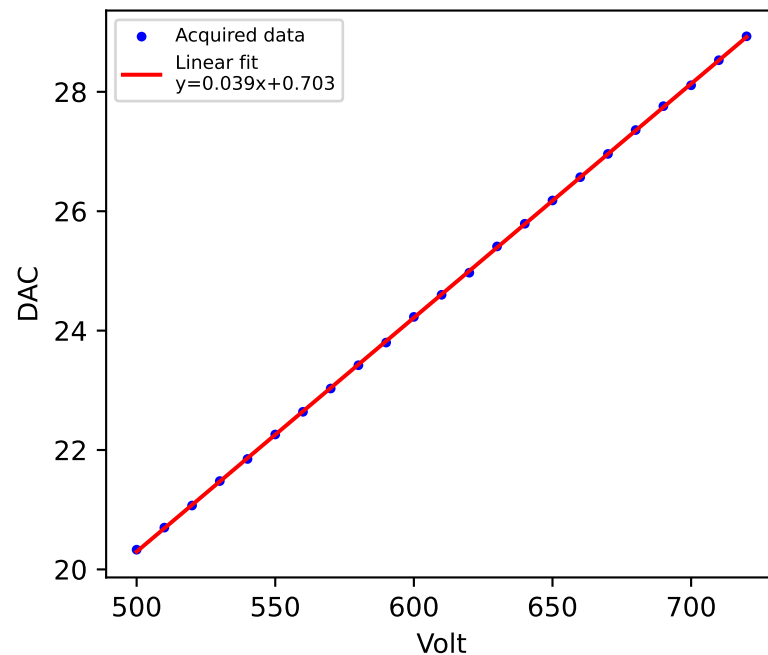


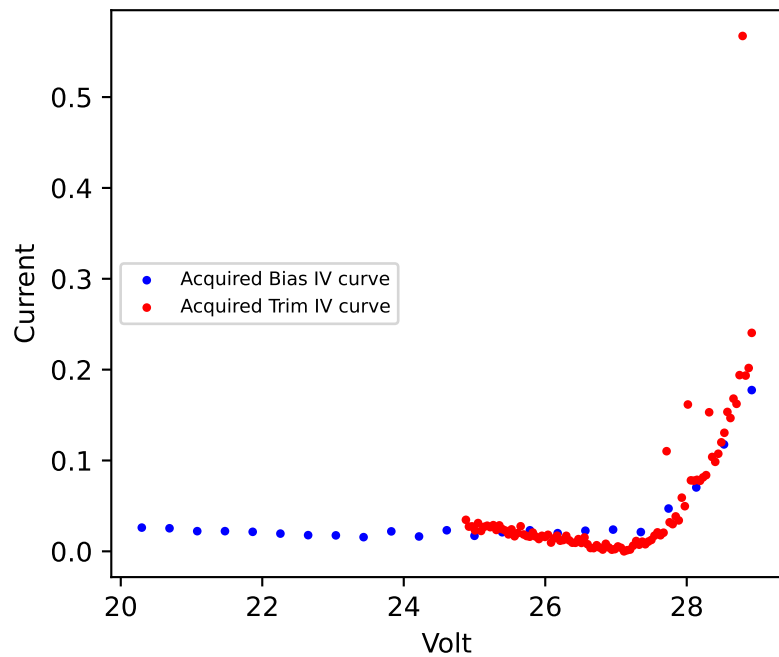
Bias REV IV curve



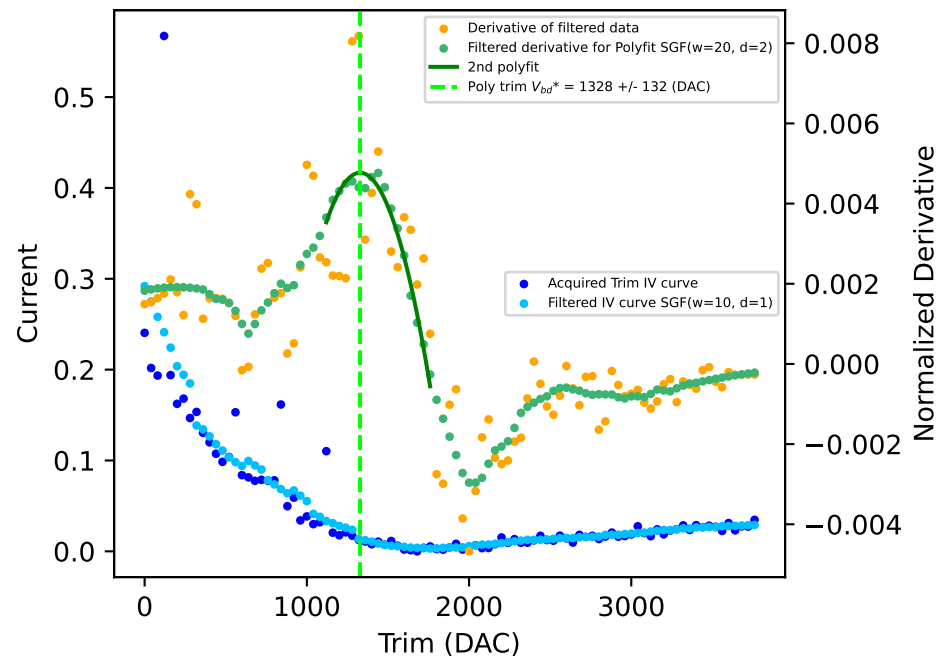
Bias DAC-VOLT conversion



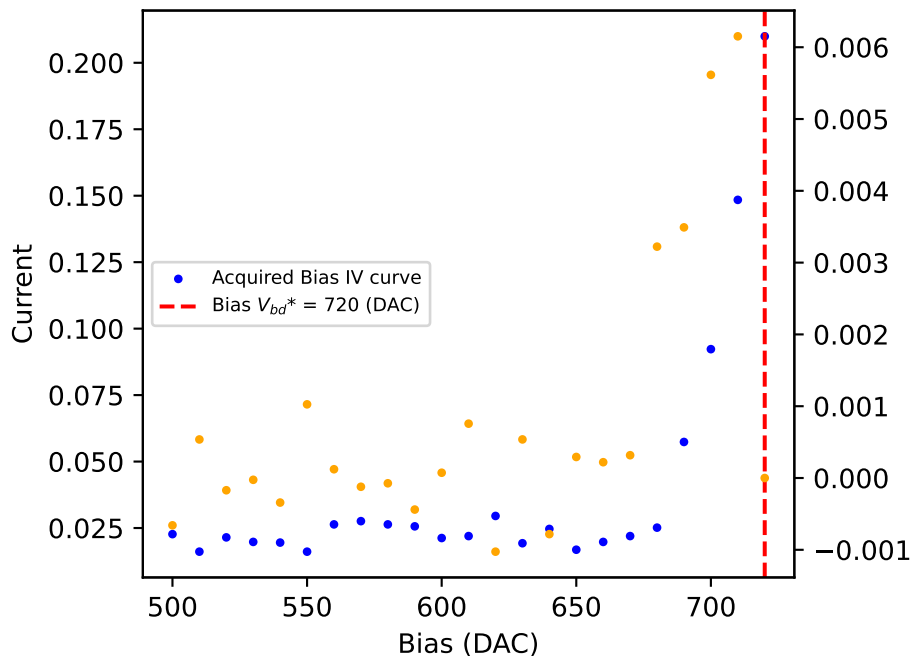
Bias + Trim REV IV curve



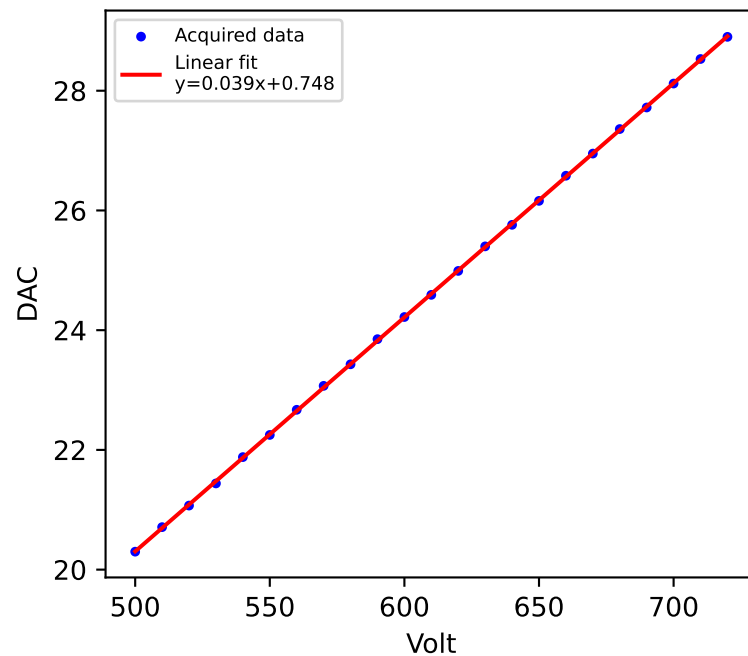
Trim REV IV curve



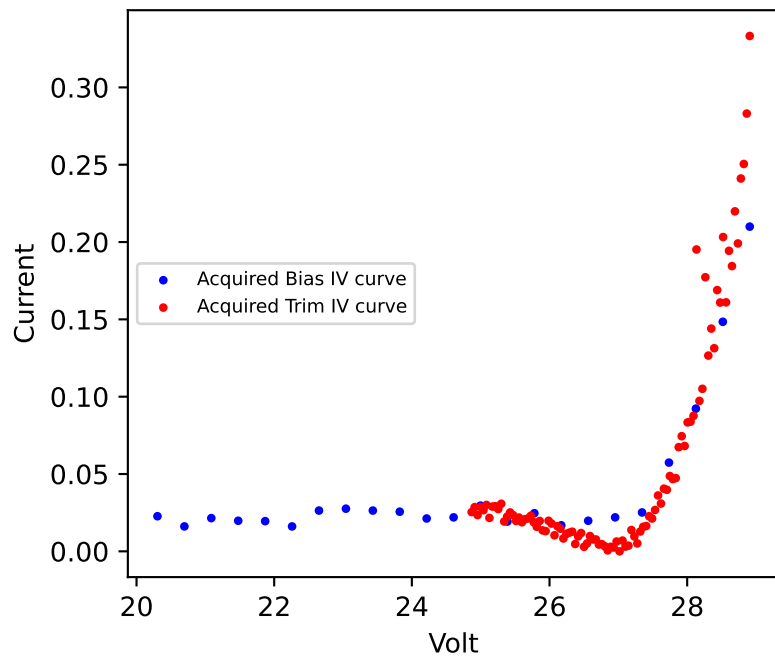
Bias REV IV curve



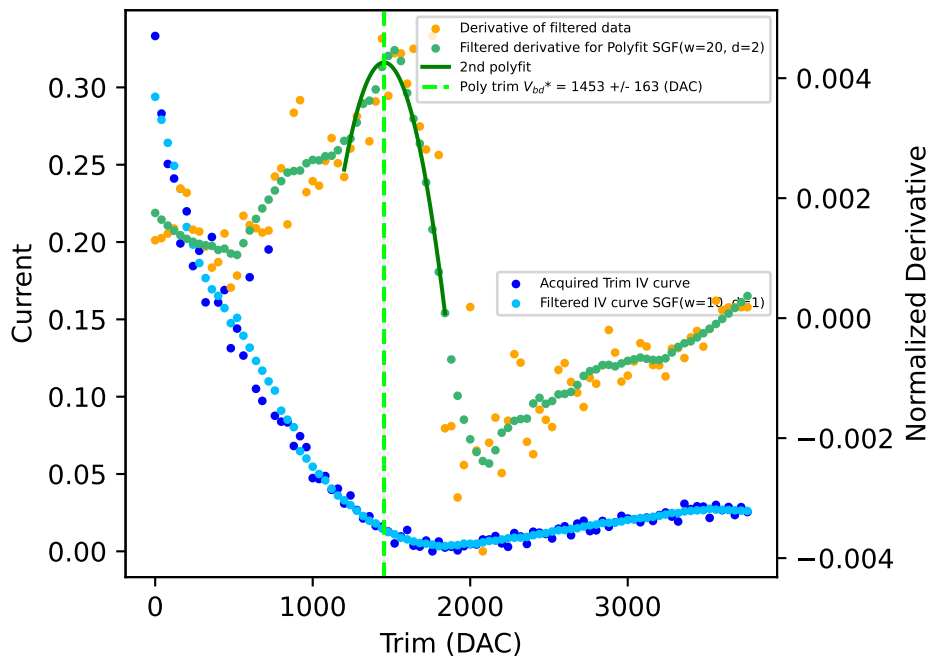
Bias DAC-VOLT conversion



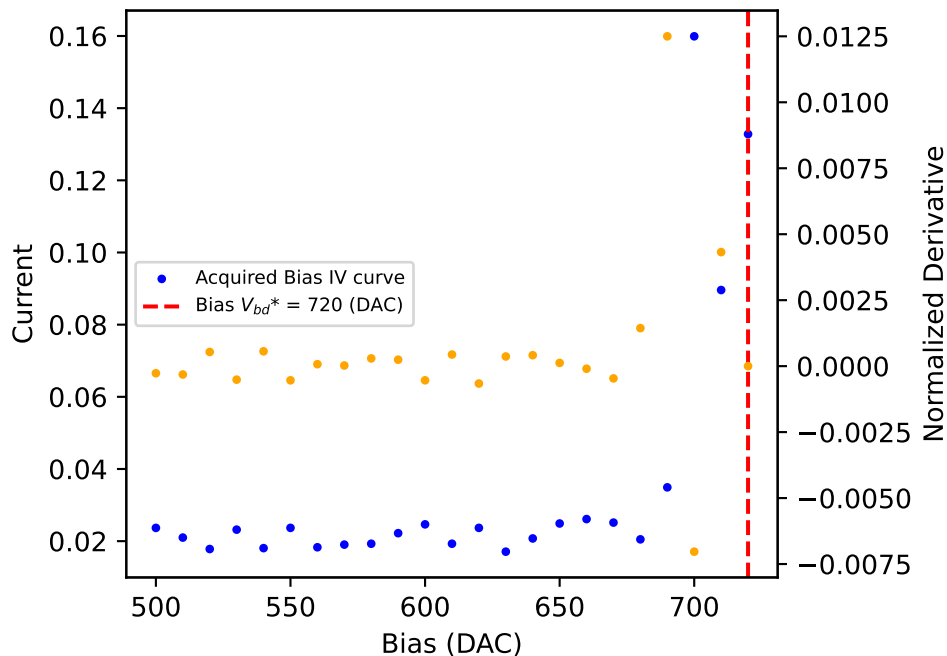
Bias + Trim REV IV curve



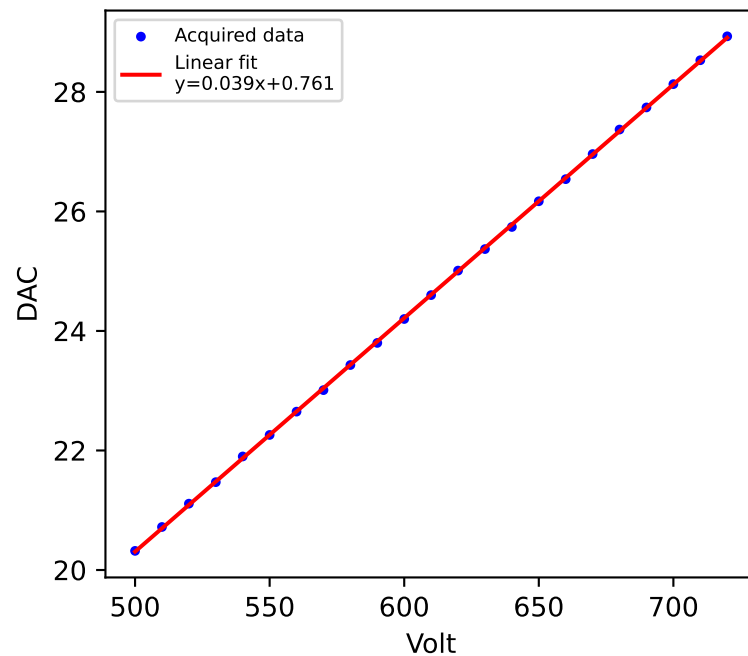
Trim REV IV curve



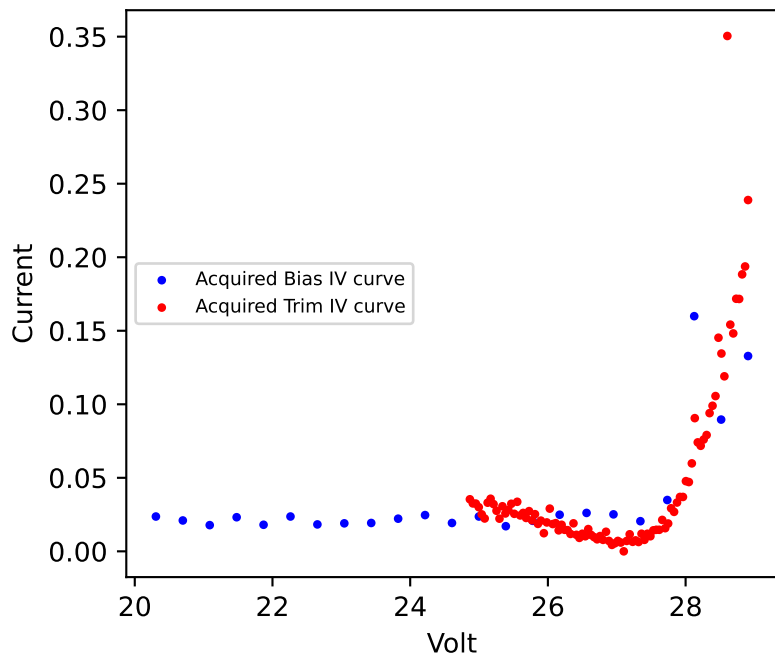
Bias REV IV curve



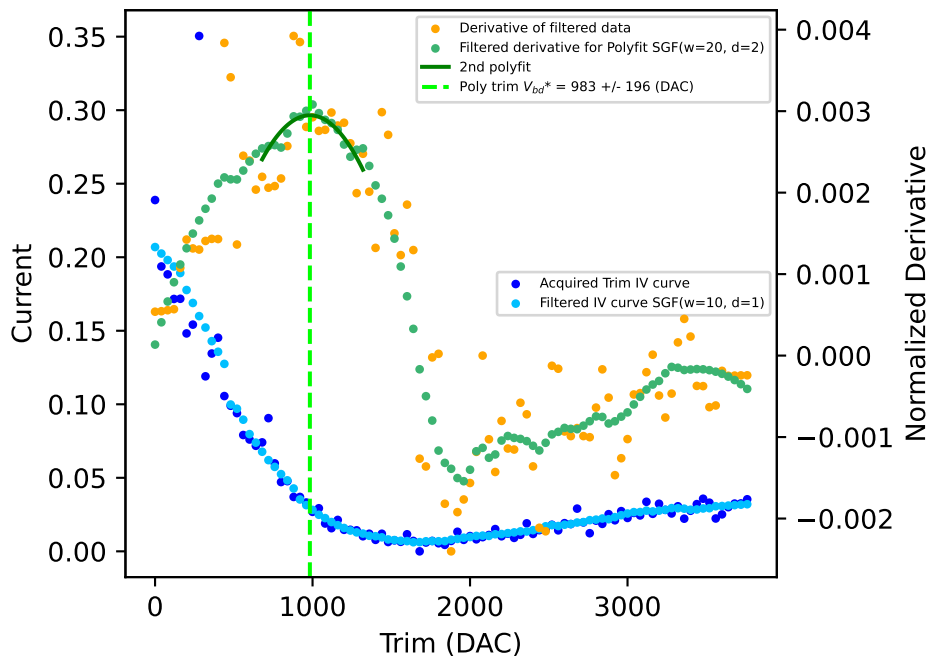
Bias DAC-VOLT conversion



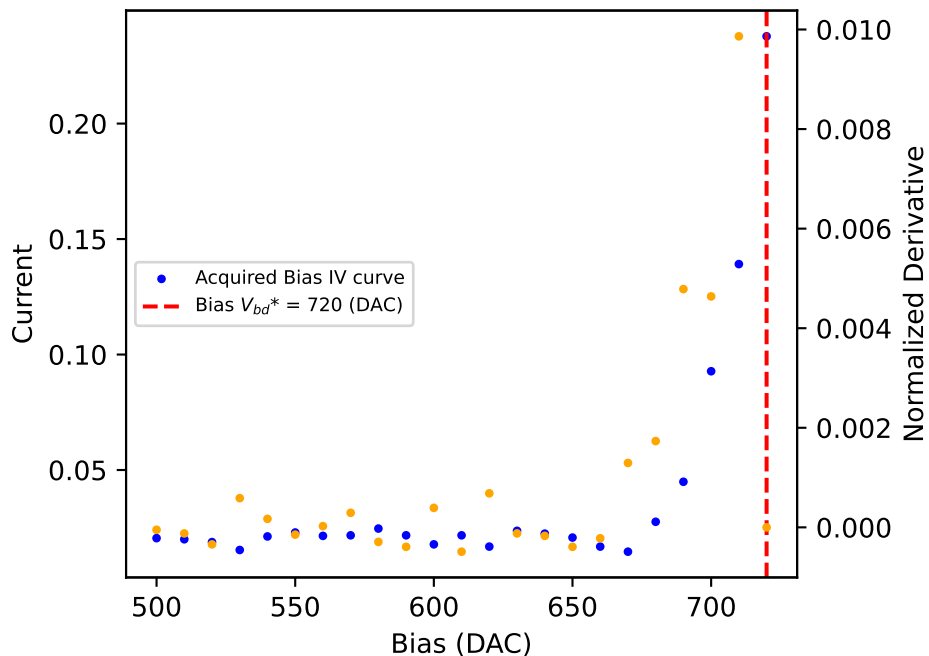
Bias + Trim REV IV curve



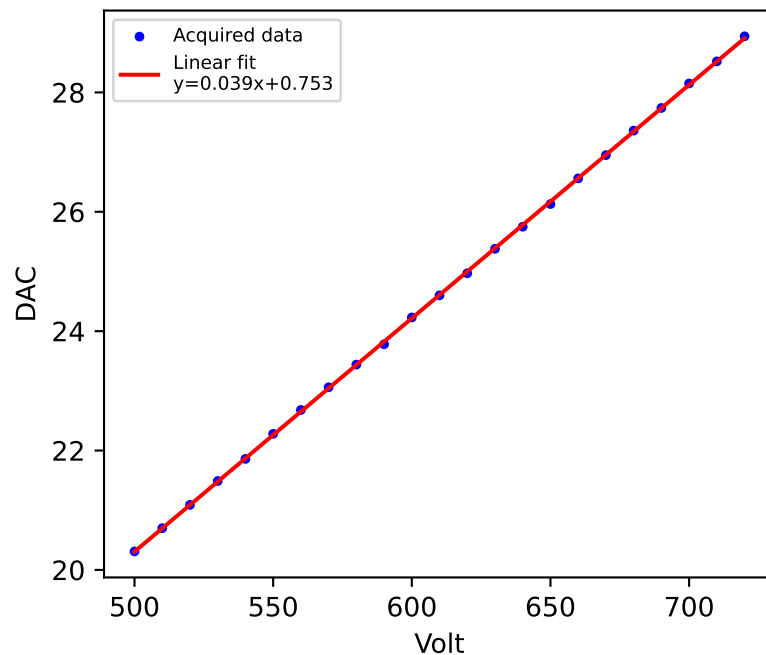
Trim REV IV curve



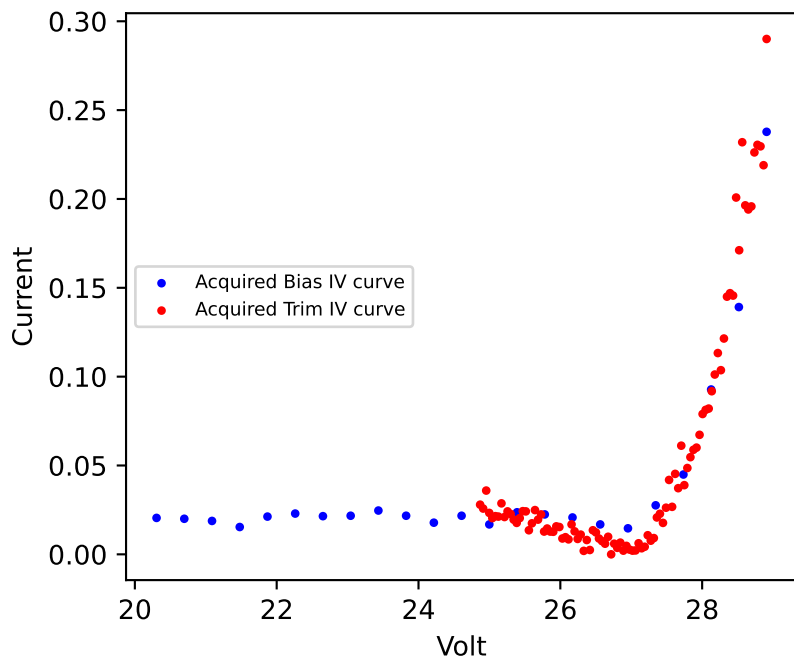
Bias REV IV curve



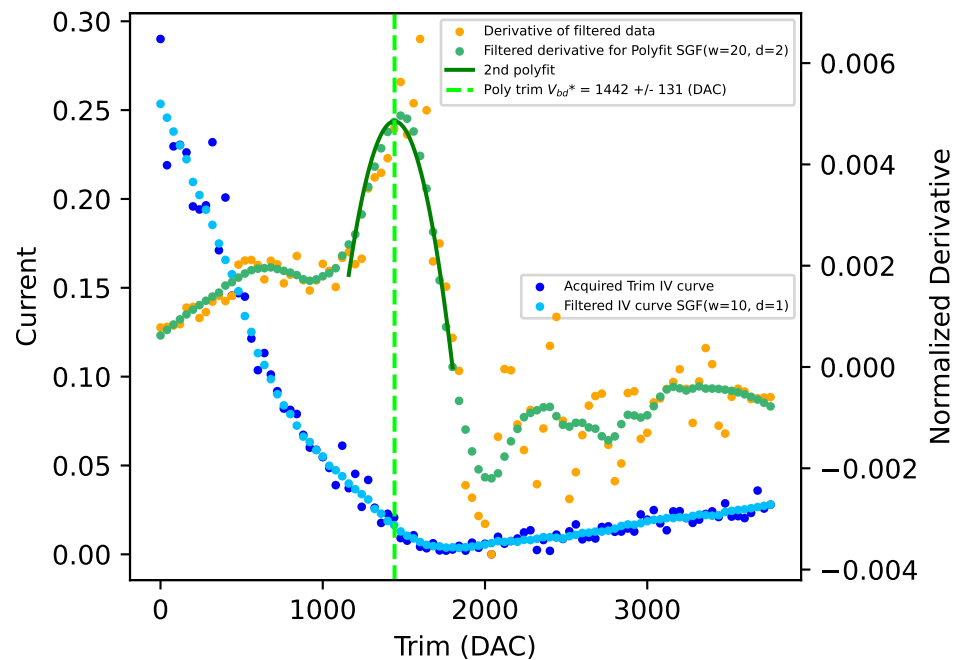
Bias DAC-VOLT conversion



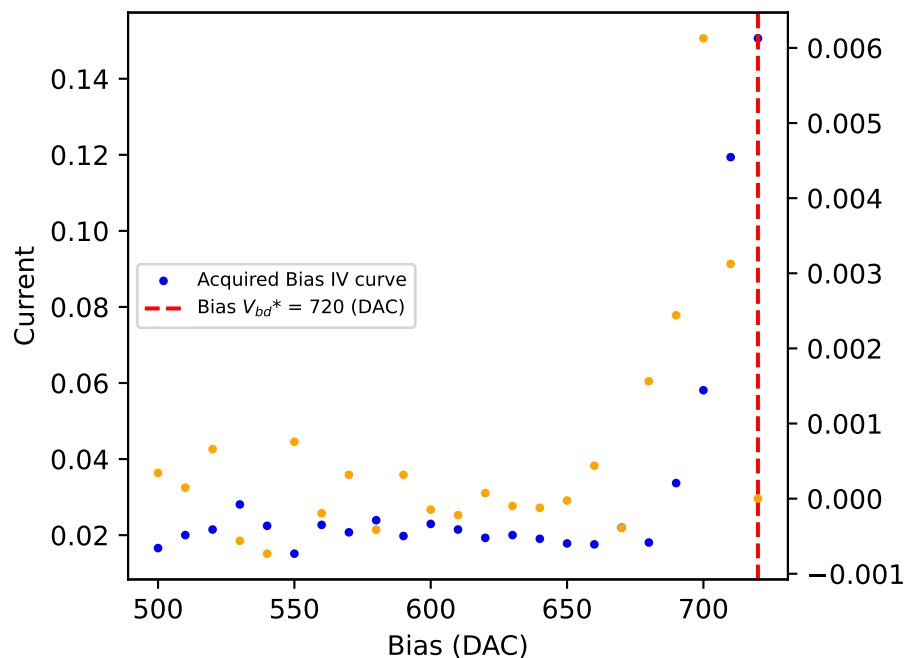
Bias + Trim REV IV curve



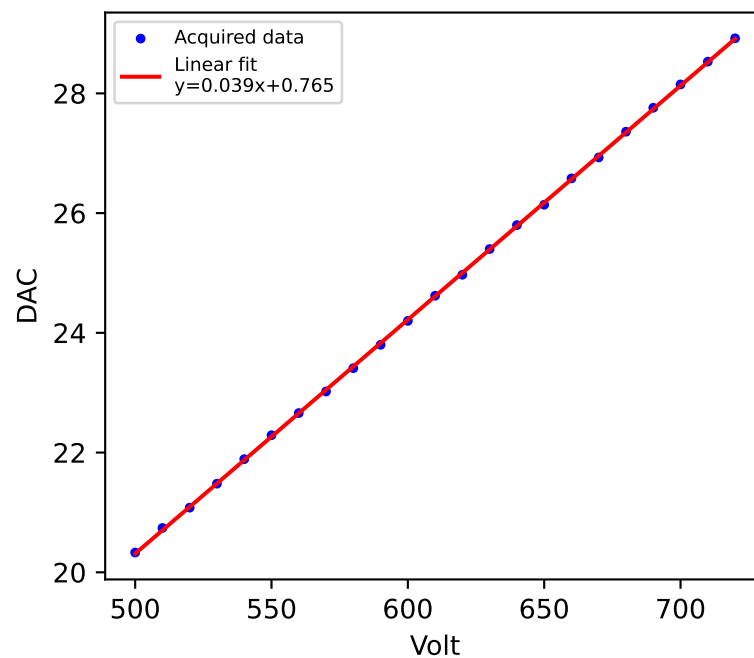
Trim REV IV curve



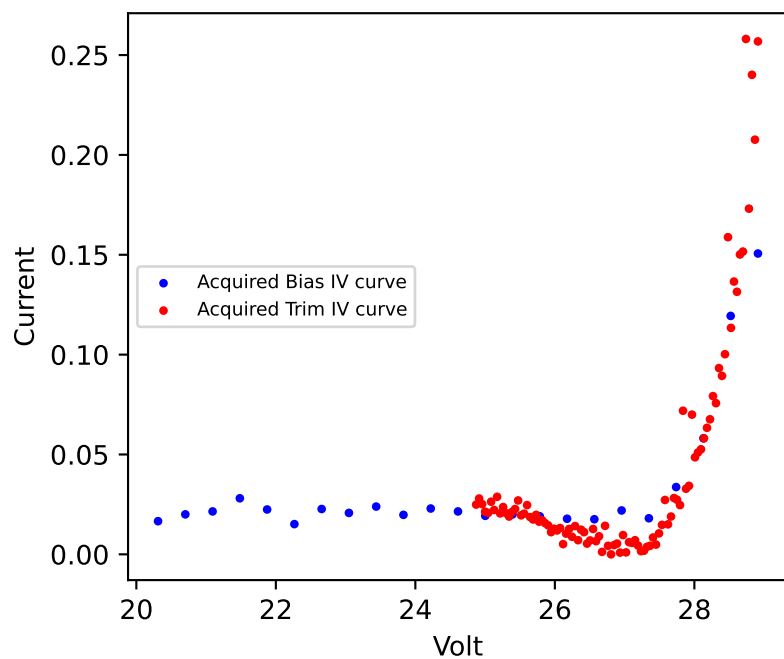
Bias REV IV curve



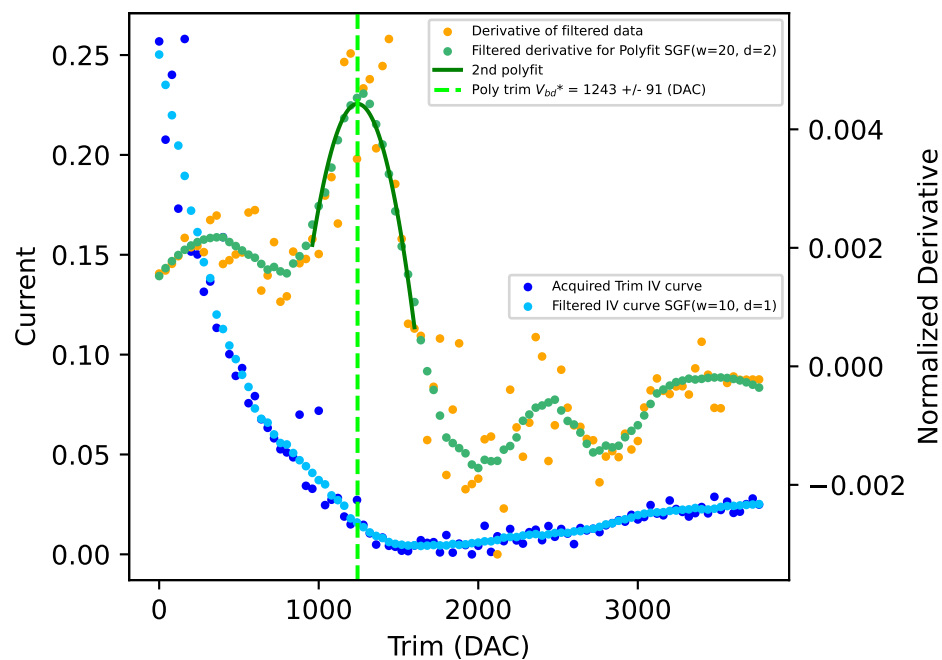
Bias DAC-VOLT conversion



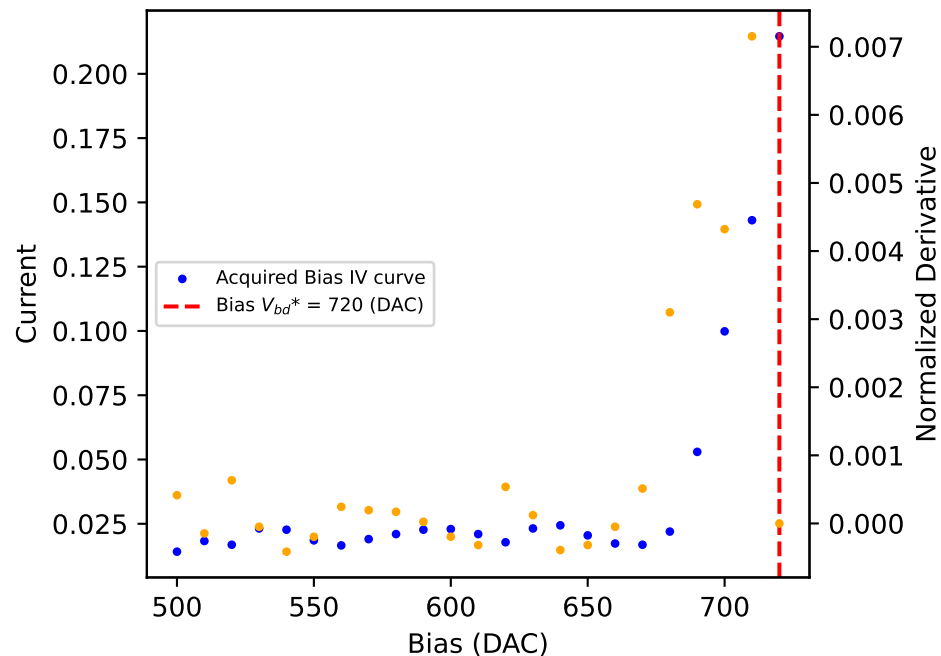
Bias + Trim REV IV curve



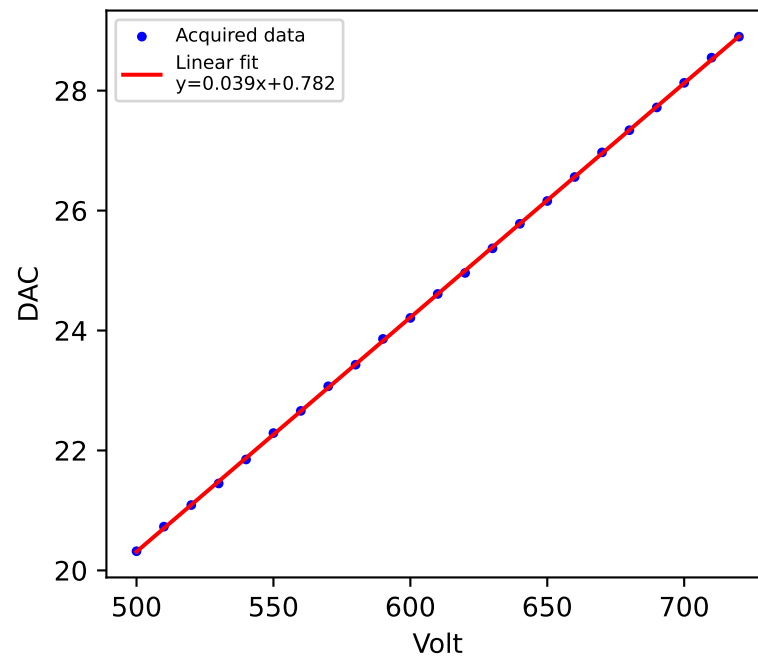
Trim REV IV curve



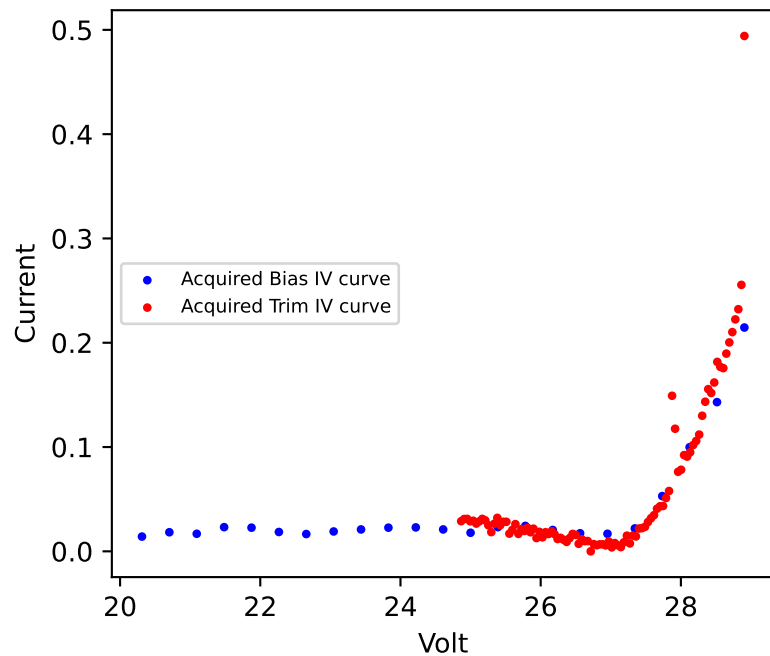
Bias REV IV curve



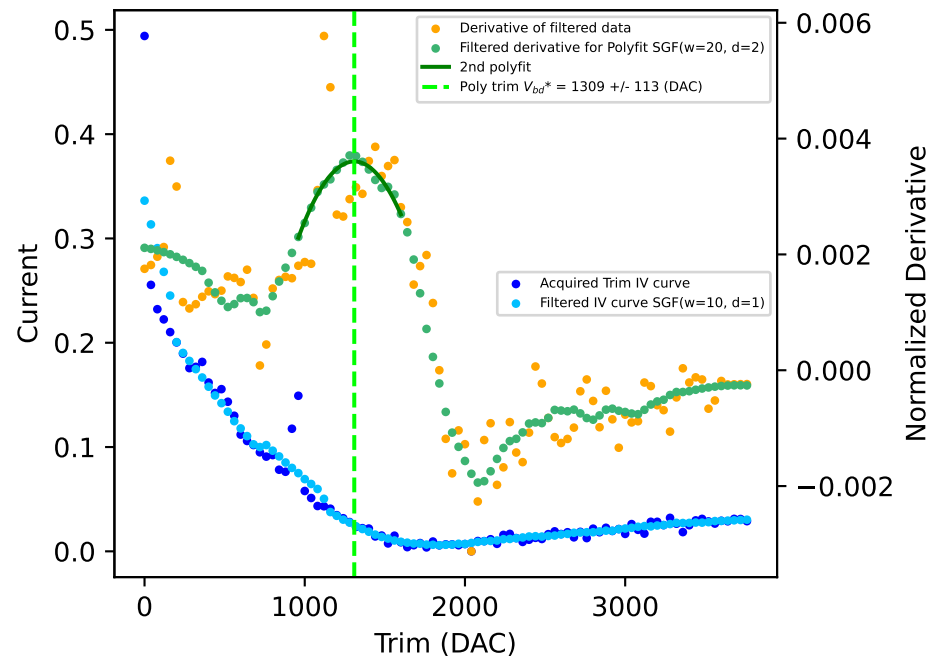
Bias DAC-VOLT conversion



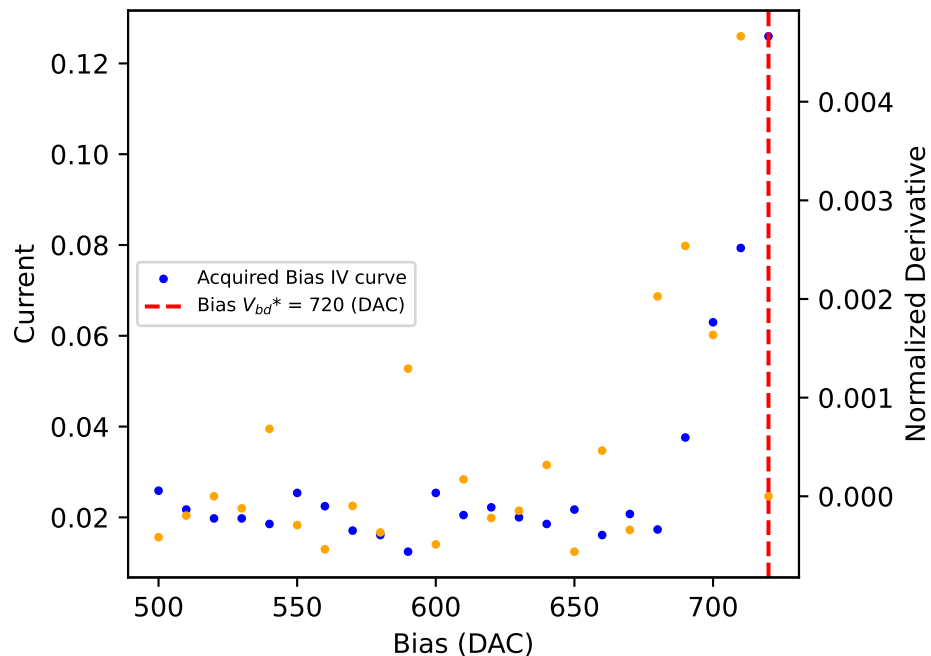
Bias + Trim REV IV curve



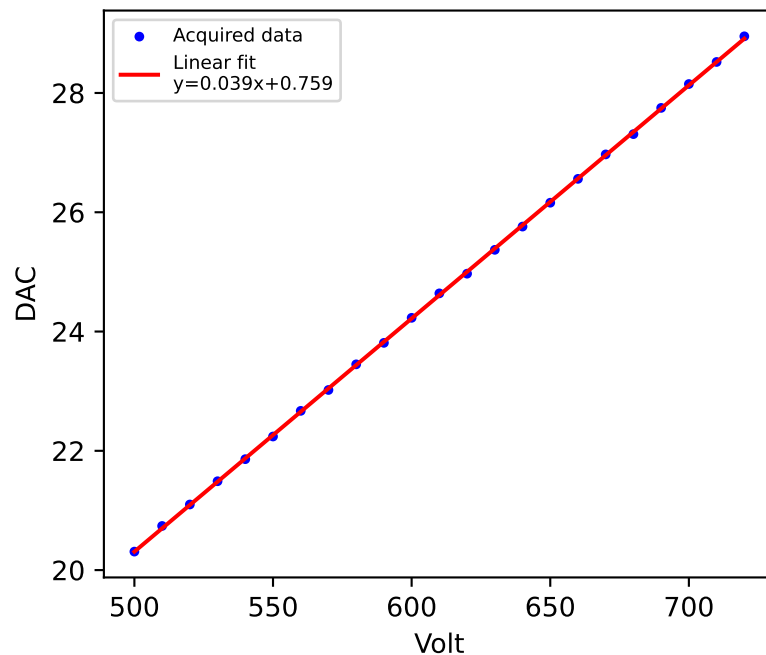
Trim REV IV curve



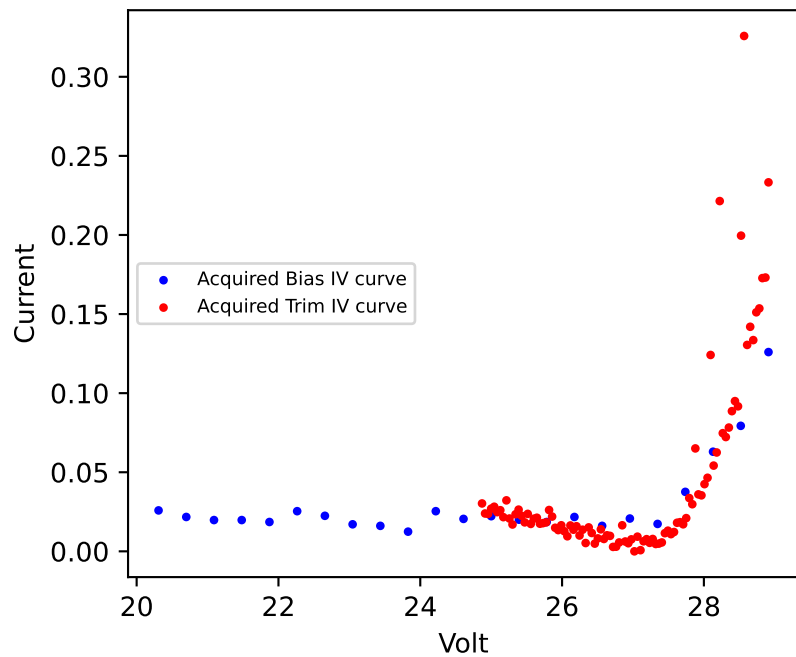
Bias REV IV curve



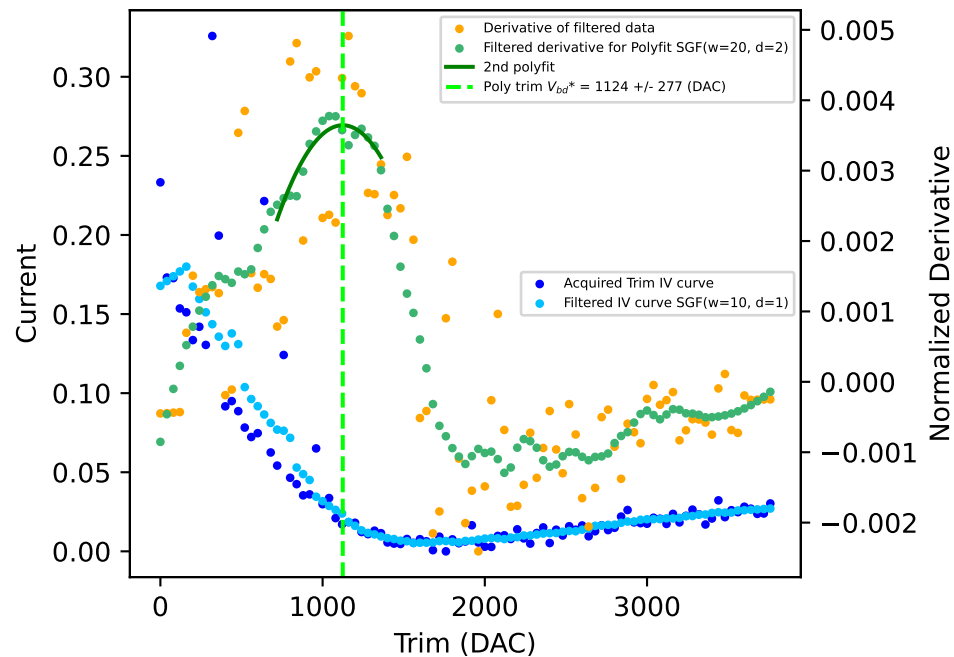
Bias DAC-VOLT conversion



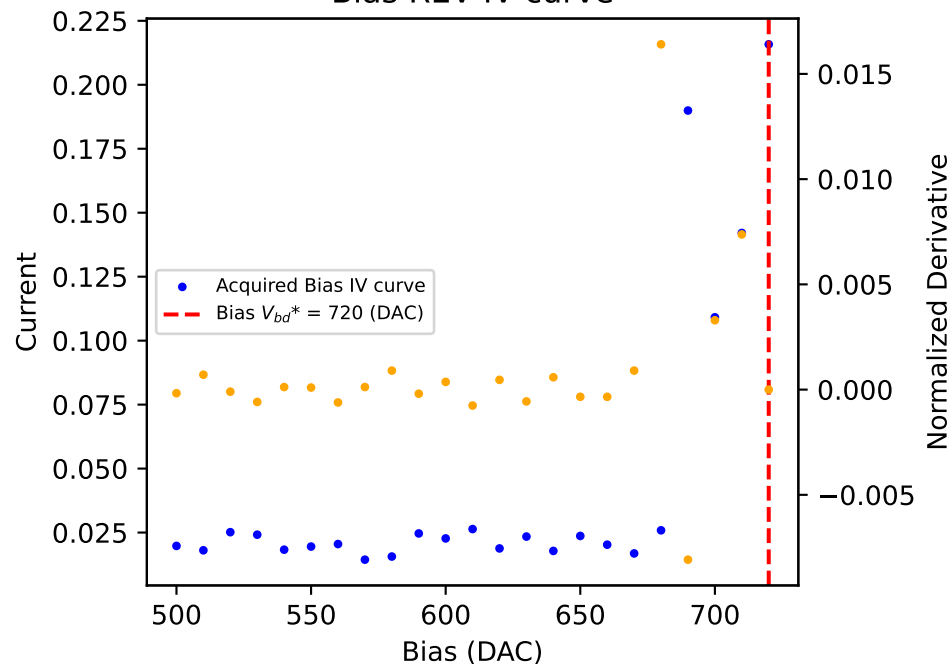
Bias + Trim REV IV curve



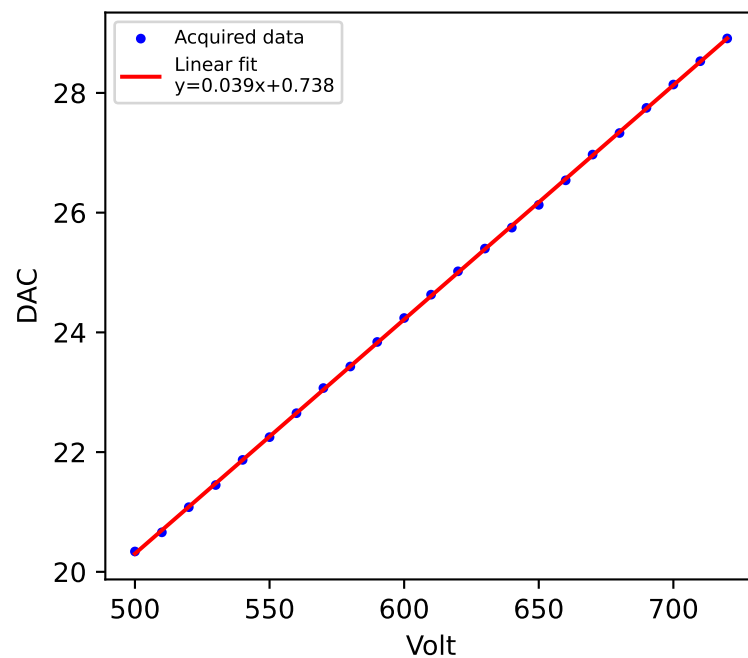
Trim REV IV curve



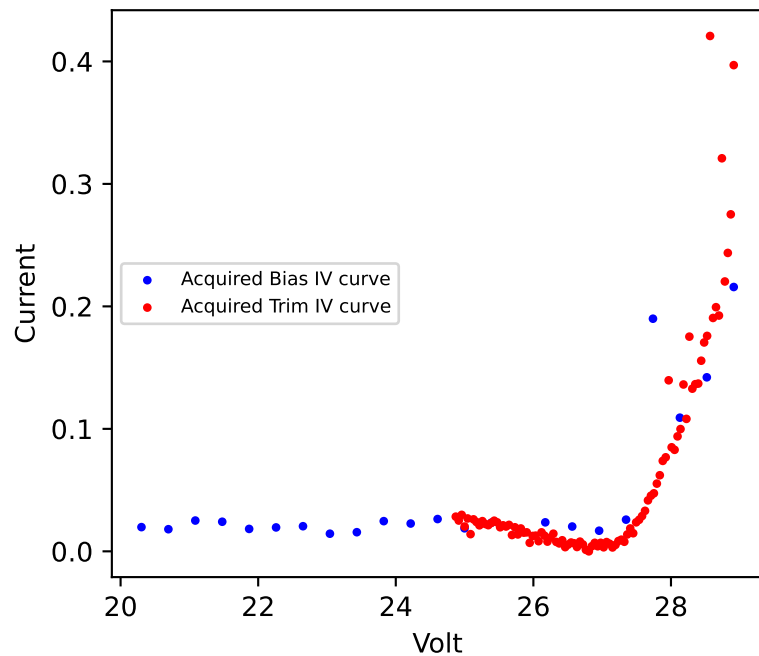
Bias REV IV curve



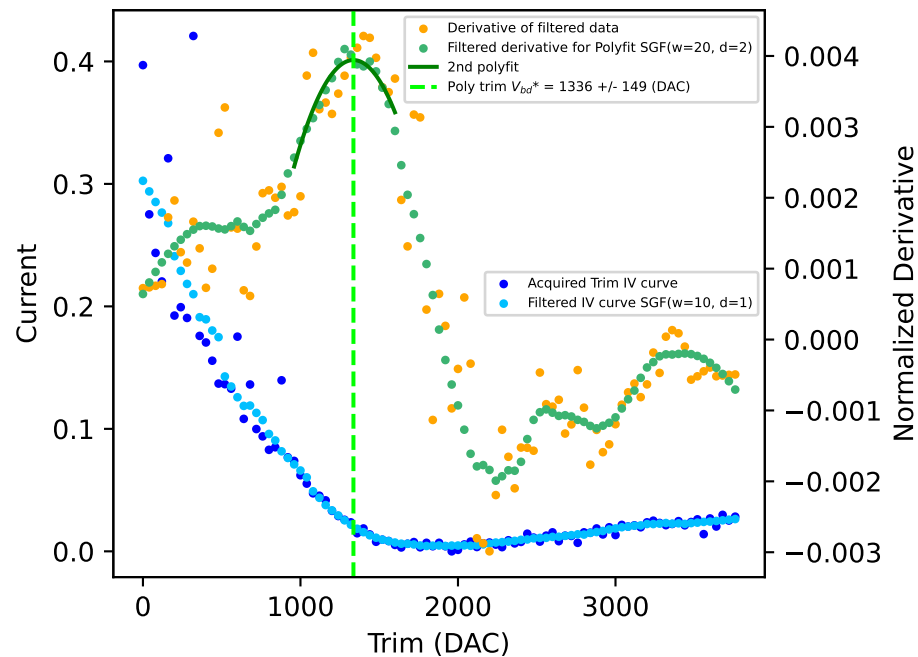
Bias DAC-VOLT conversion



Bias + Trim REV IV curve

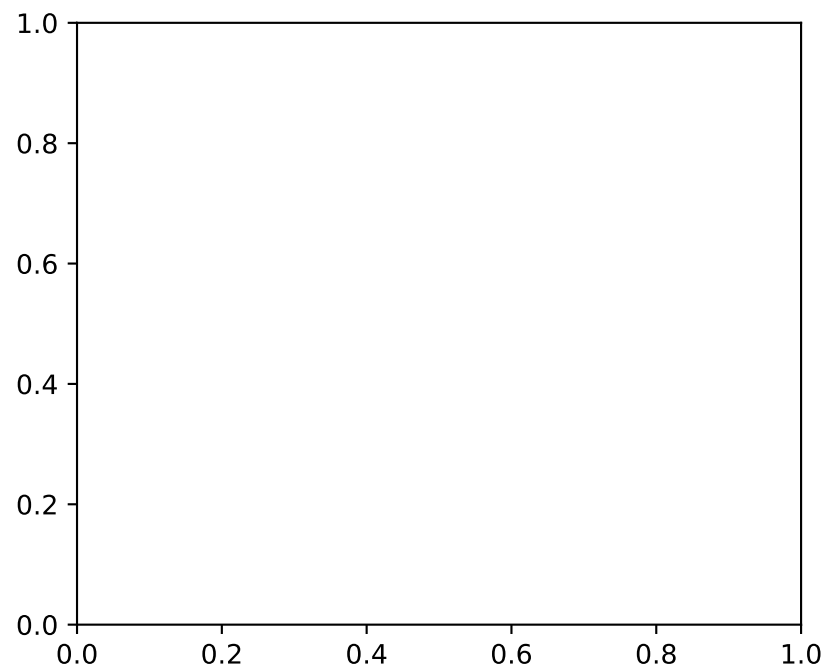
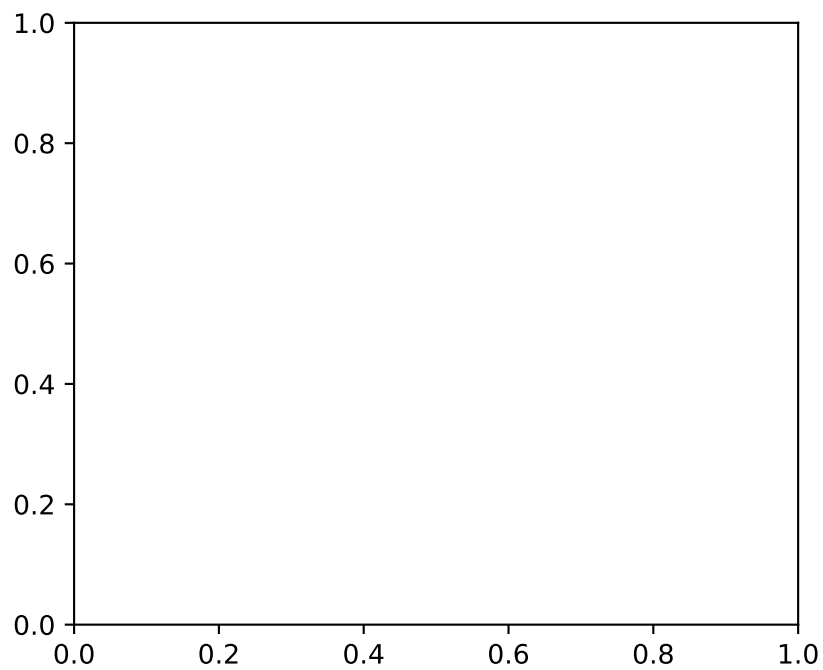
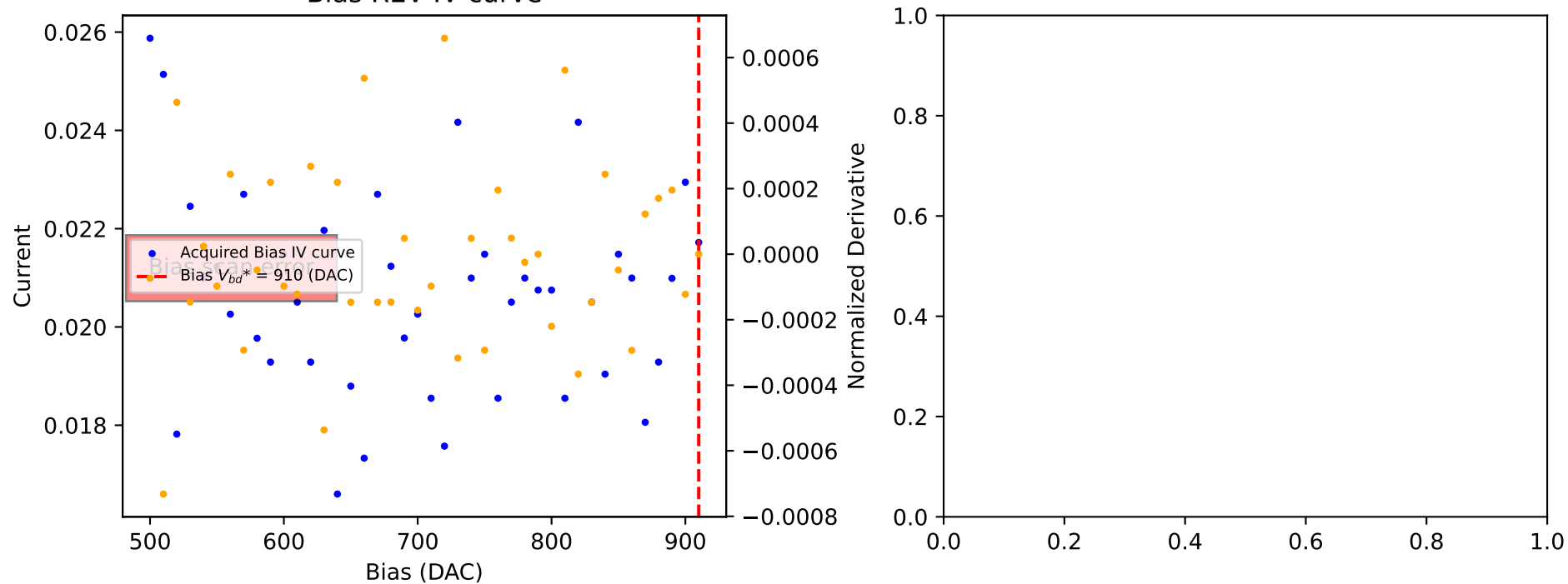


Trim REV IV curve

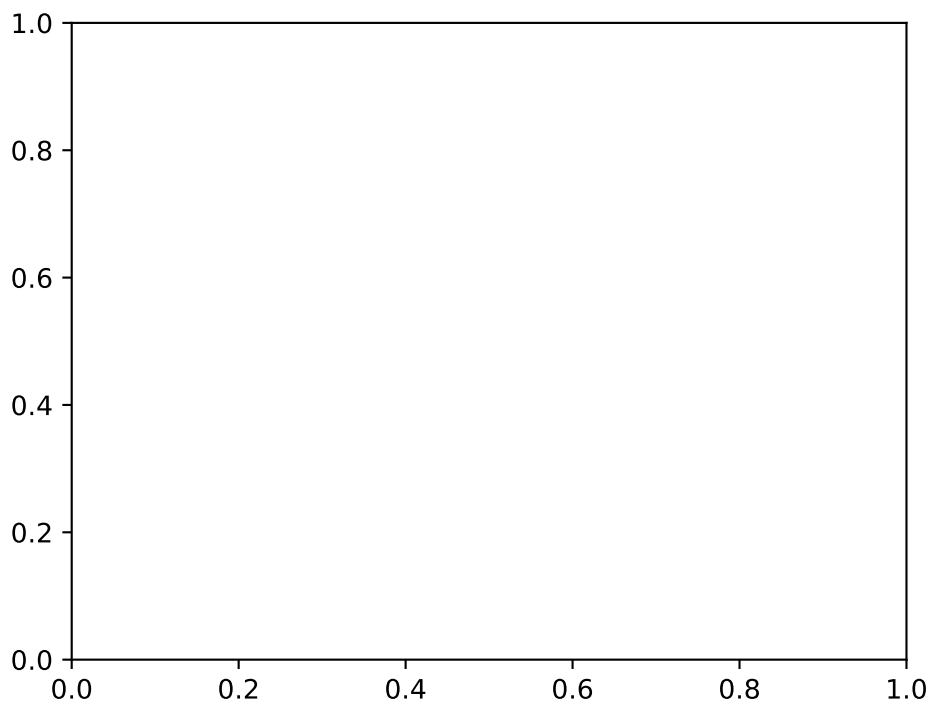




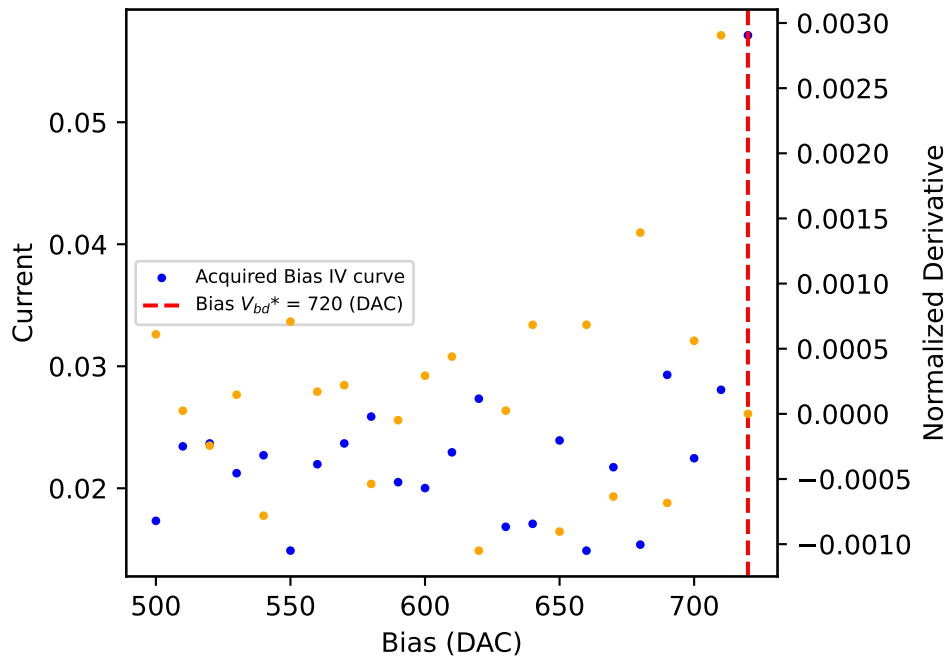
Bias REV IV curve



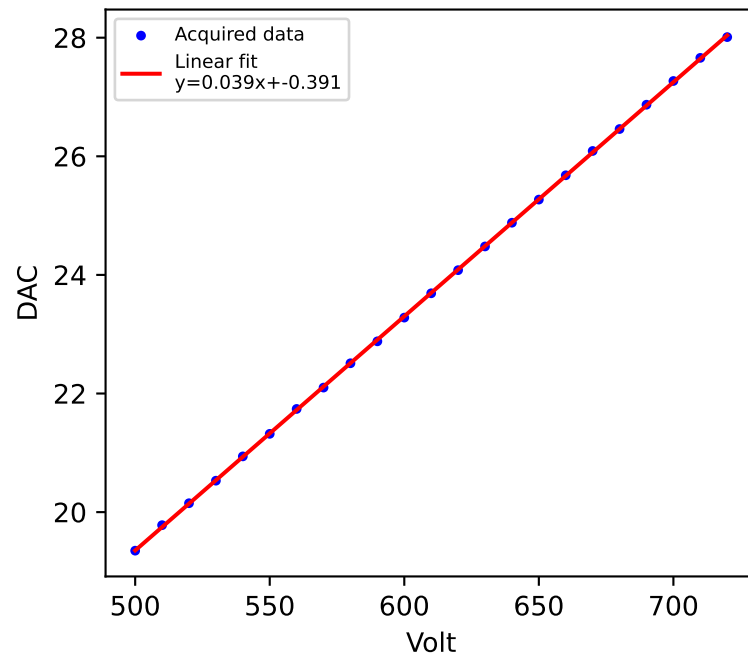
ENDPOINT:109 APA:2 AFE:1 Config\_CH:9 CH\_daq:11 SiPM:FBK --> MISSING FILE



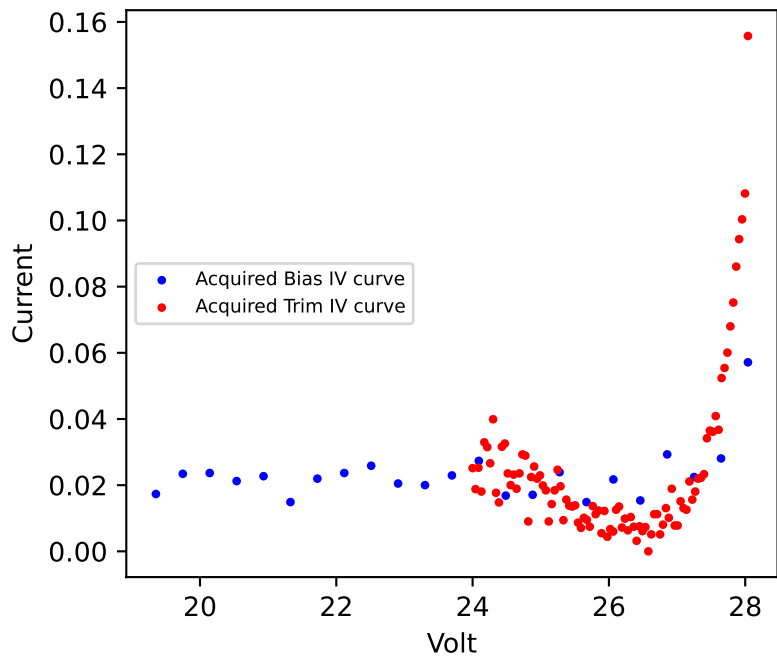
Bias REV IV curve



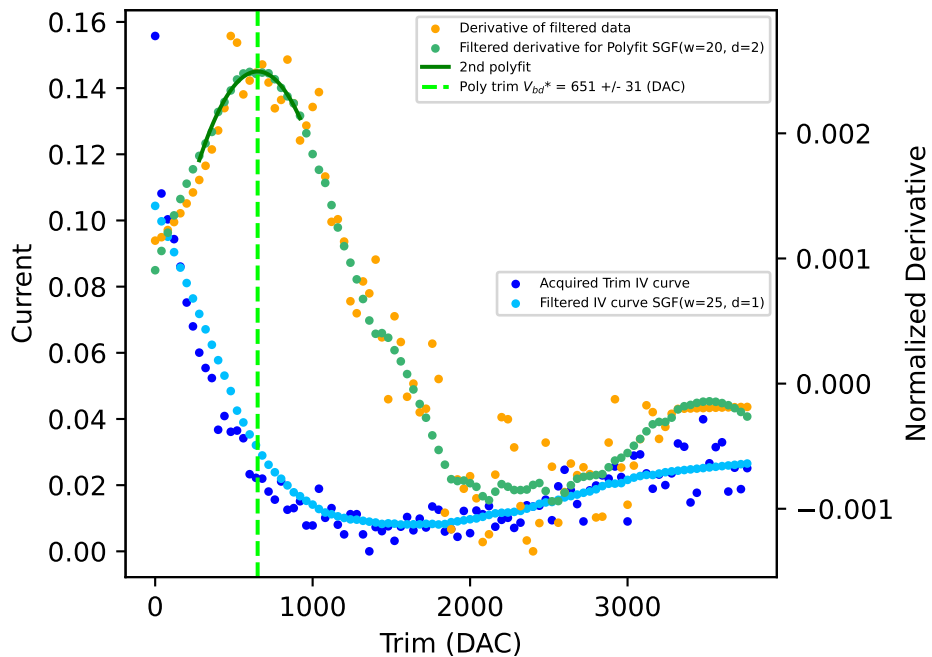
Bias DAC-VOLT conversion



Bias + Trim REV IV curve



Trim REV IV curve



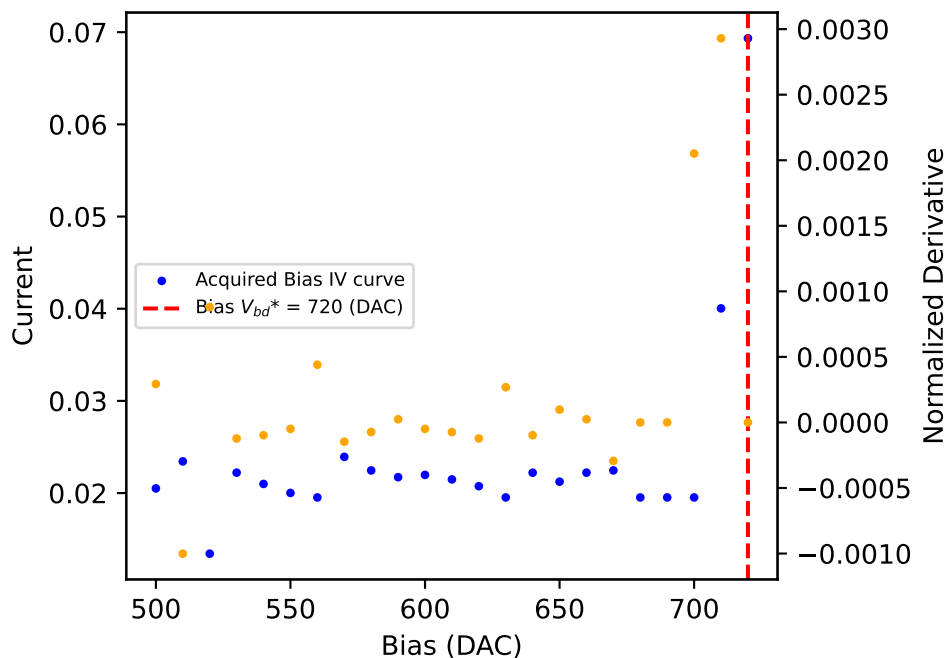
ENDPOINT:109 APA:2 AFE:1 Config\_CH:11 CH\_daq:13 SiPM:FBK --> MISSING FILE



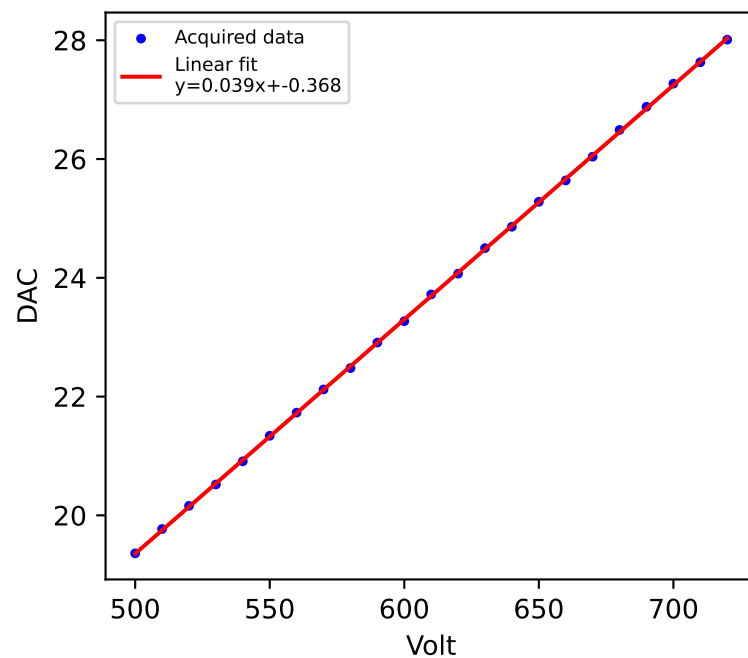
ENDPOINT:109 APA:2 AFE:1 Config\_CH:12 CH\_daq:14 SiPM:FBK --> MISSING FILE



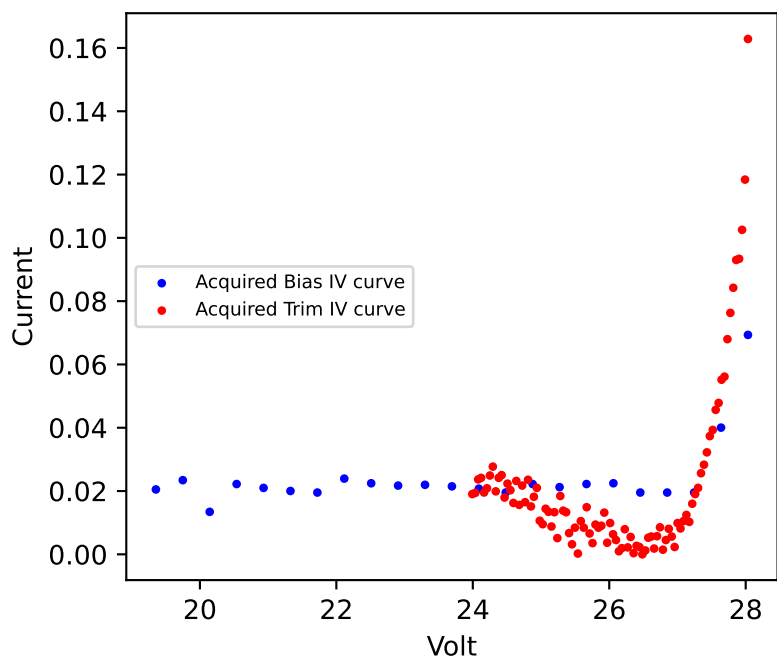
Bias REV IV curve



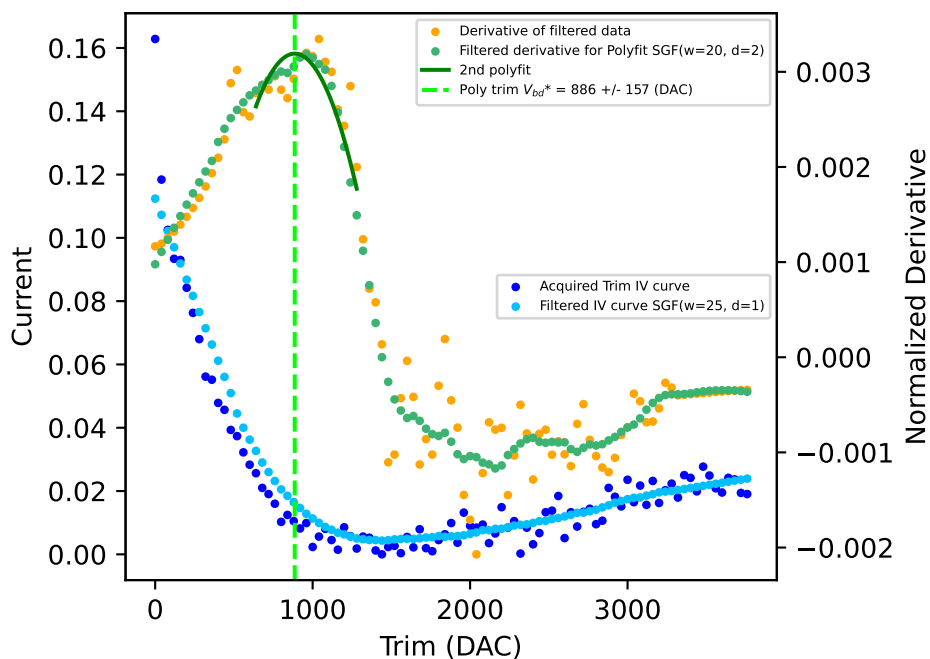
Bias DAC-VOLT conversion



Bias + Trim REV IV curve



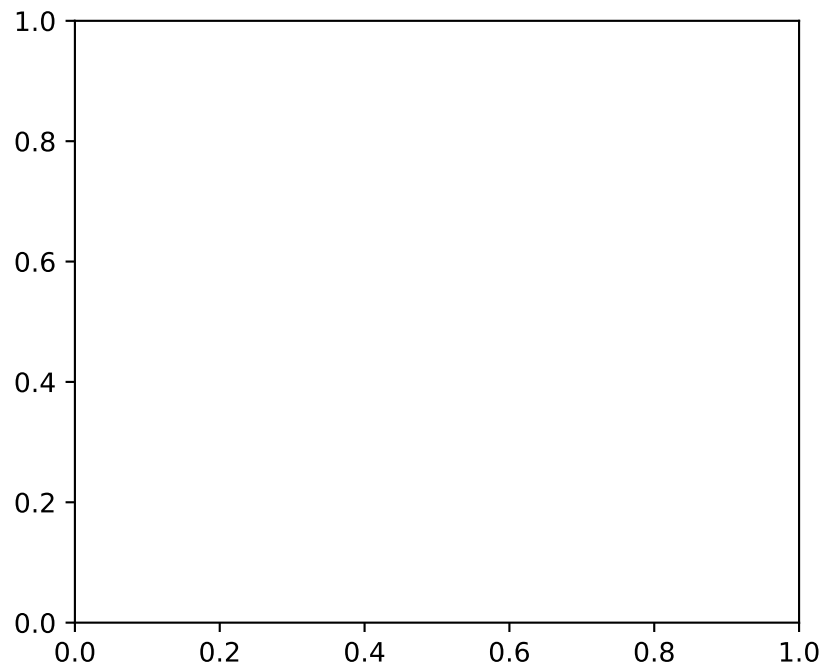
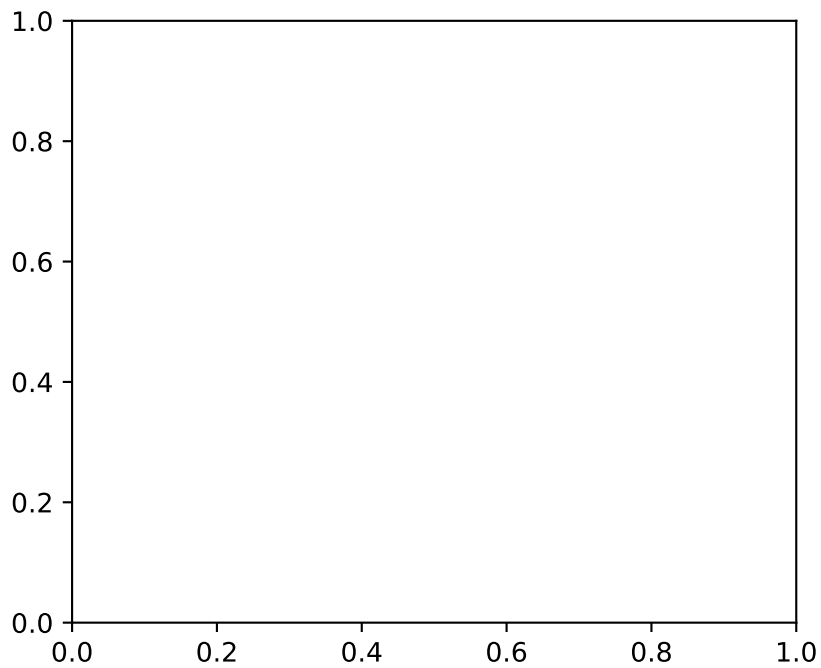
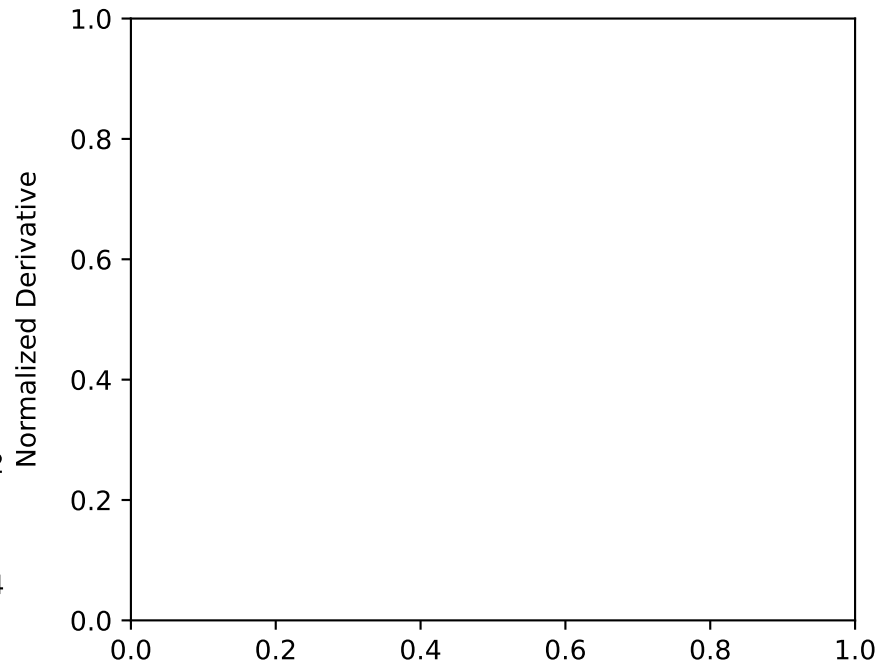
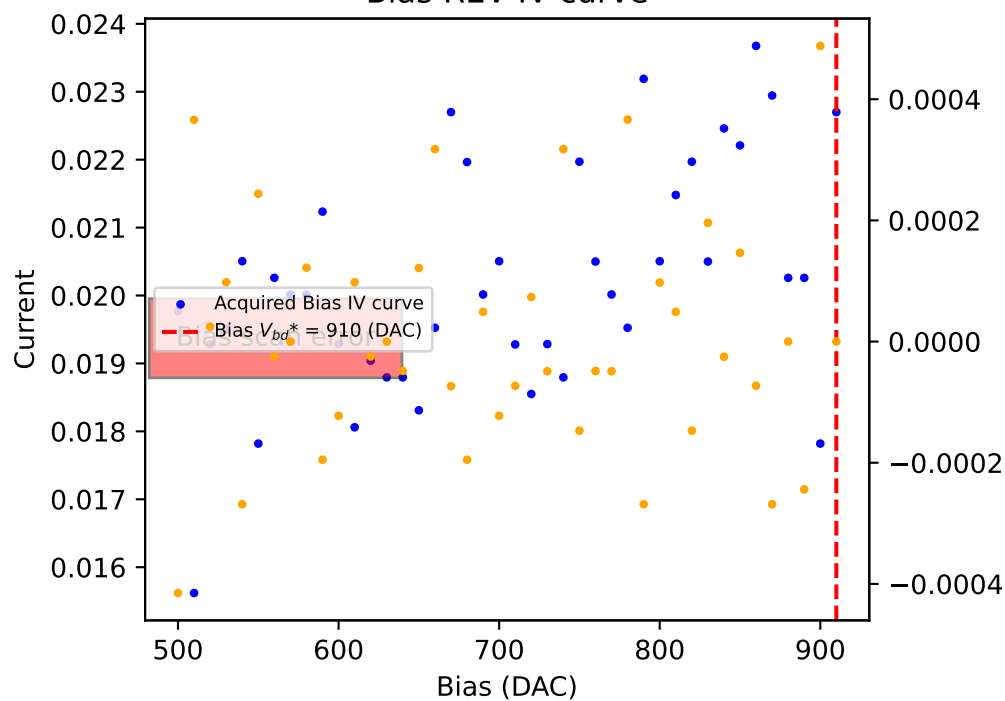
Trim REV IV curve



ENDPOINT:109 APA:2 AFE:1 Config\_CH:14 CH\_daq:16 SiPM:FBK --> MISSING FILE

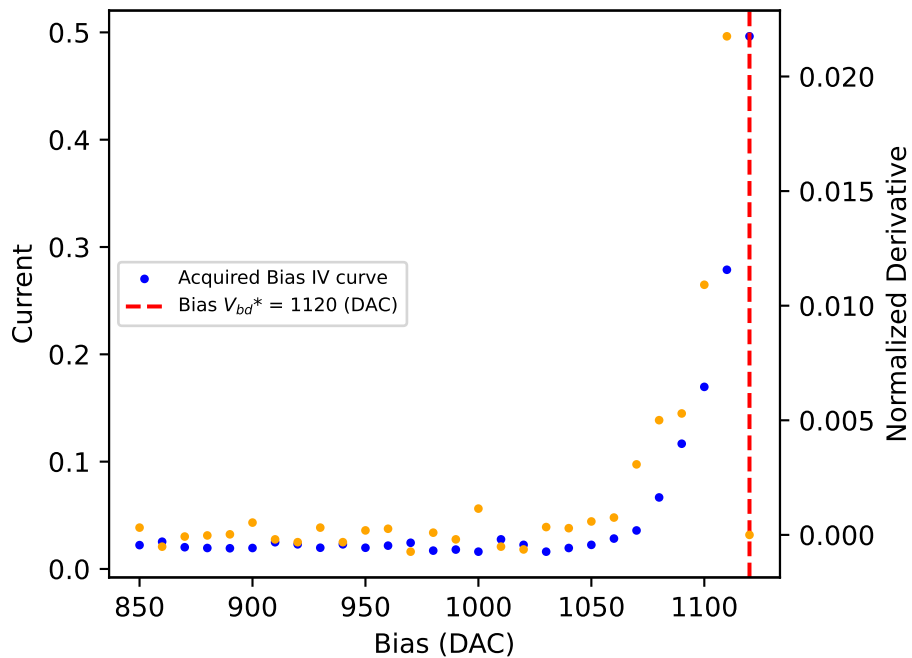


Bias REV IV curve

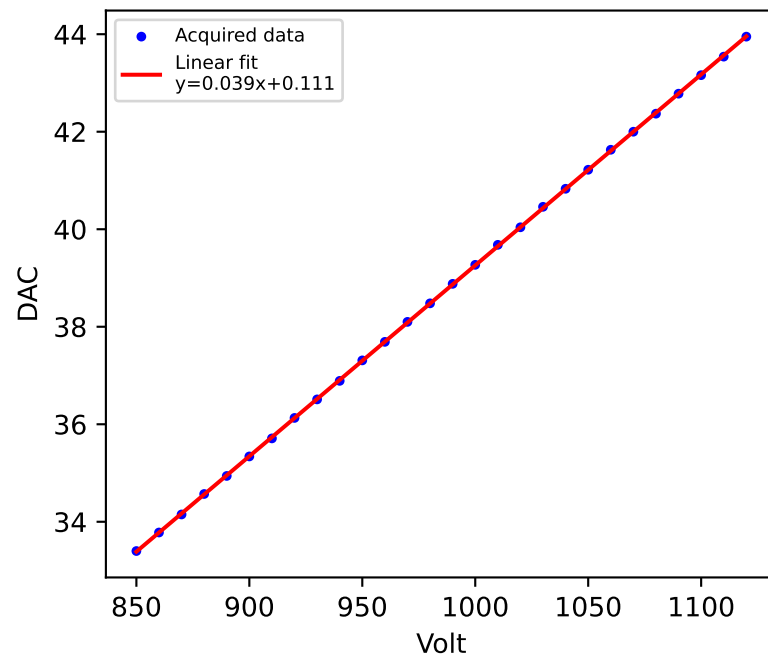




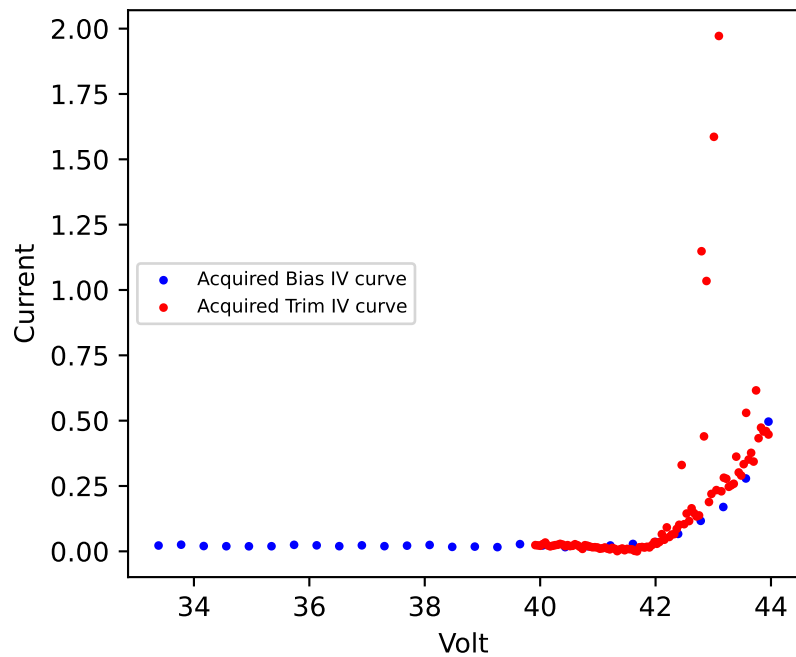
Bias REV IV curve



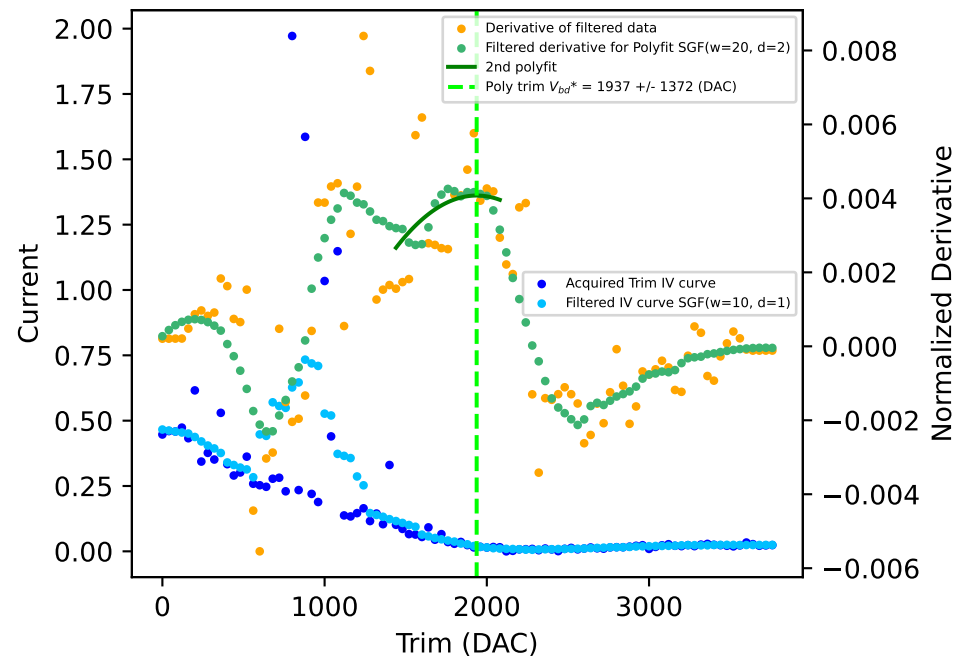
Bias DAC-VOLT conversion



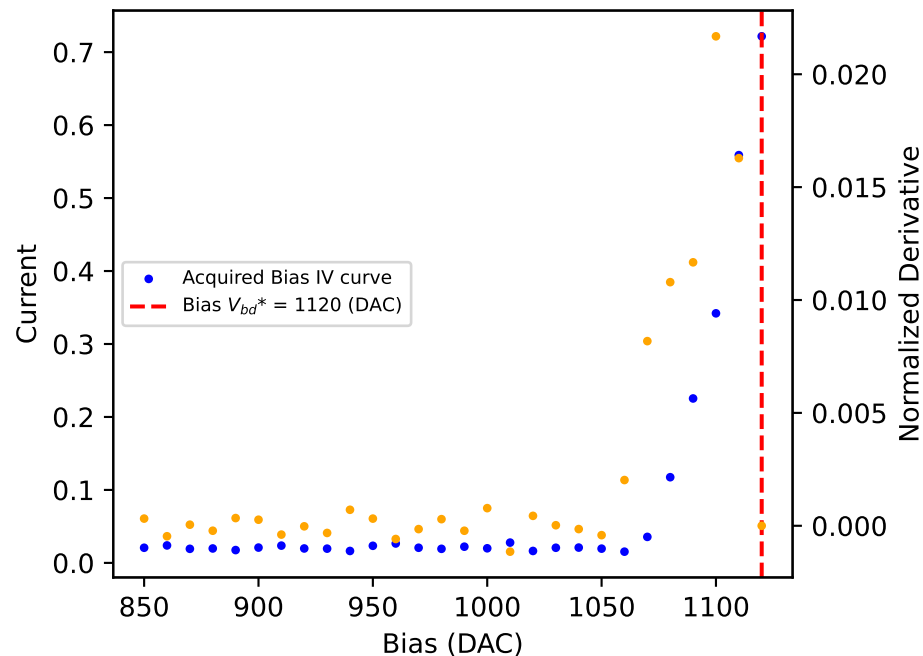
Bias + Trim REV IV curve



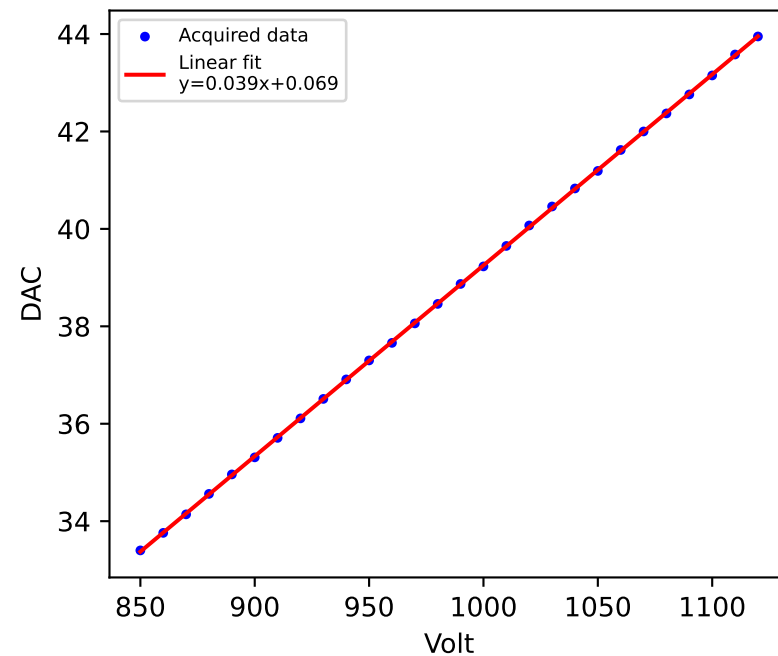
Trim REV IV curve



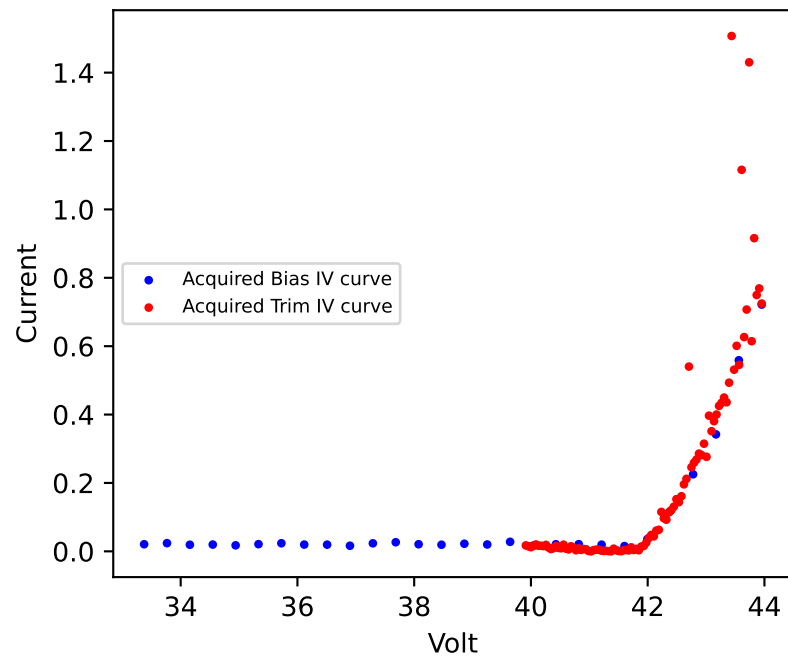
Bias REV IV curve



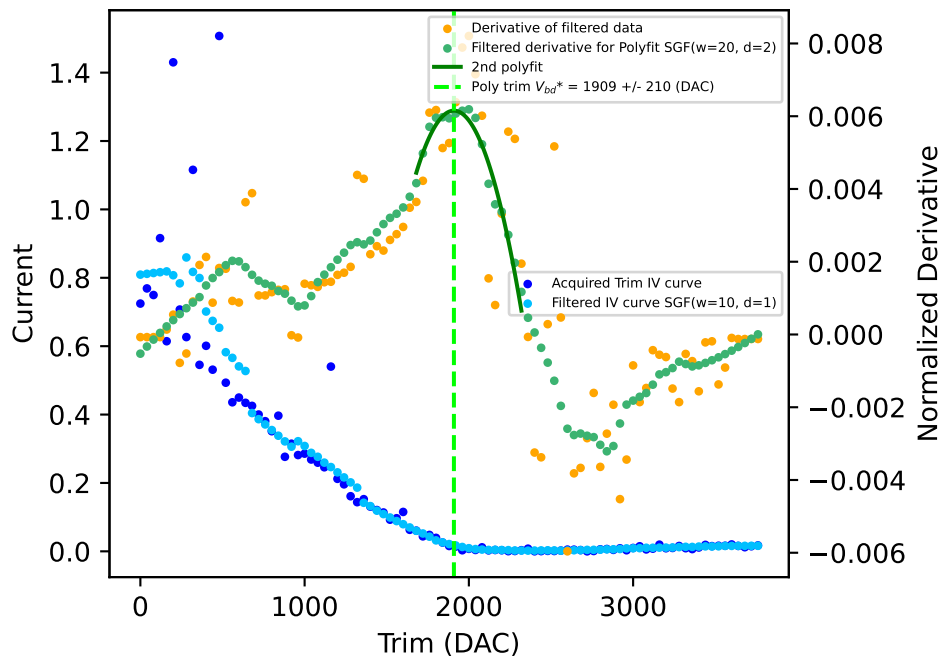
Bias DAC-VOLT conversion



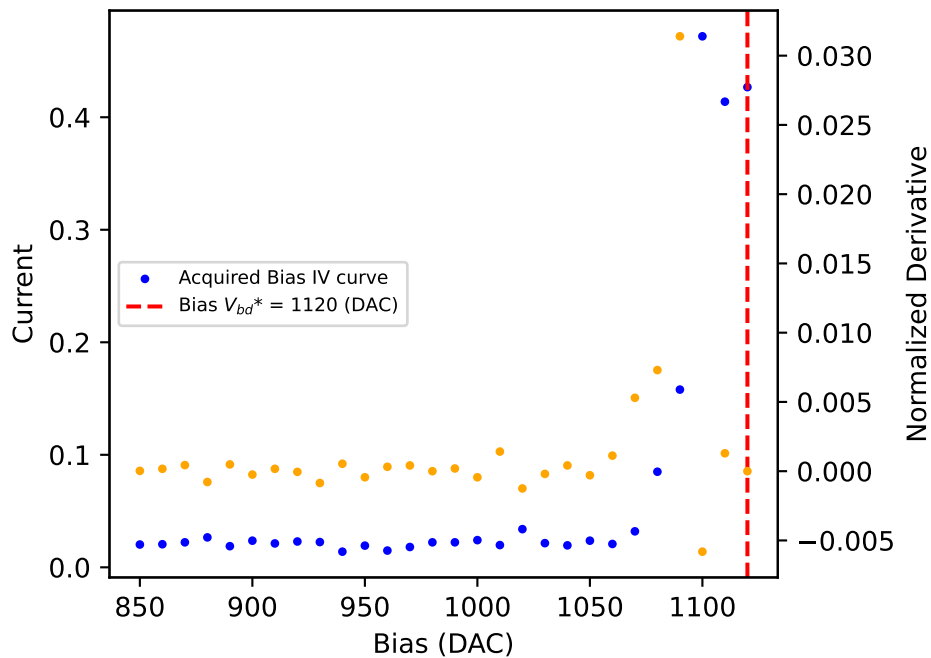
Bias + Trim REV IV curve



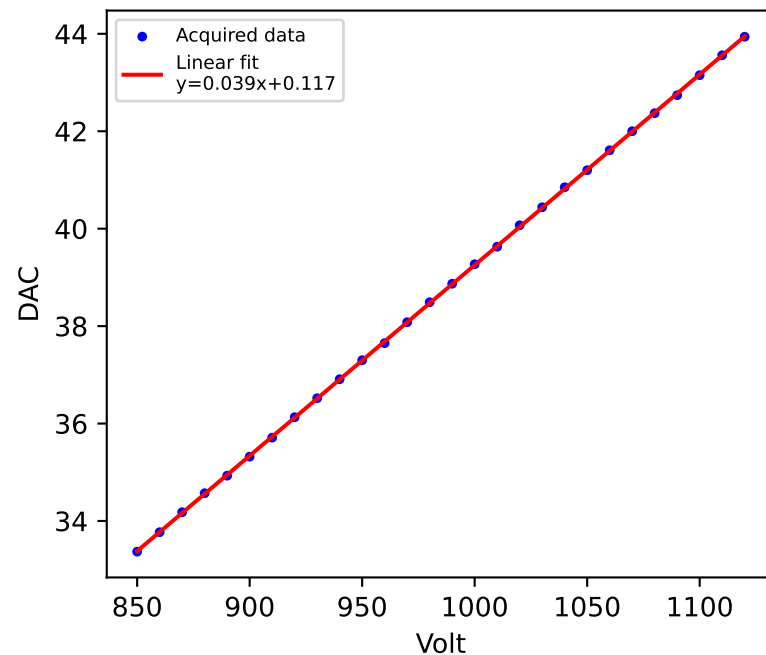
Trim REV IV curve



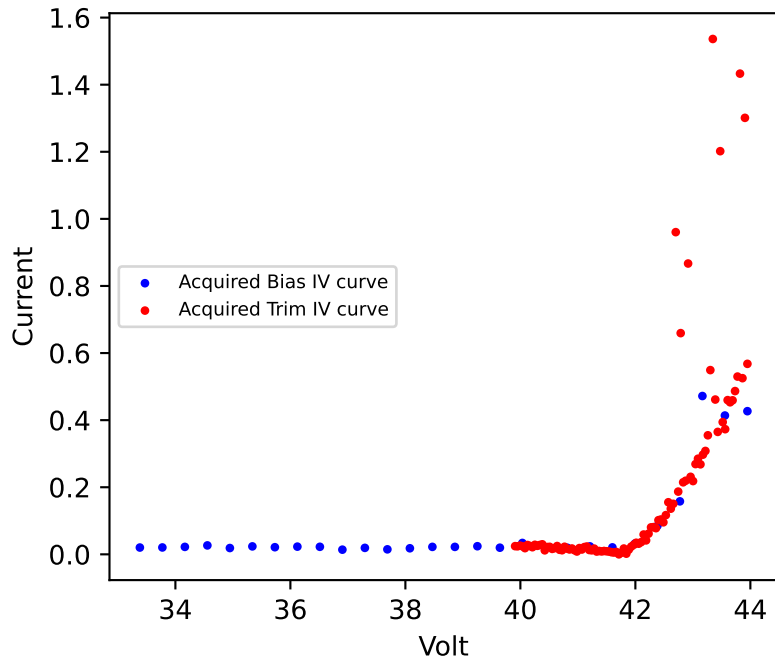
Bias REV IV curve



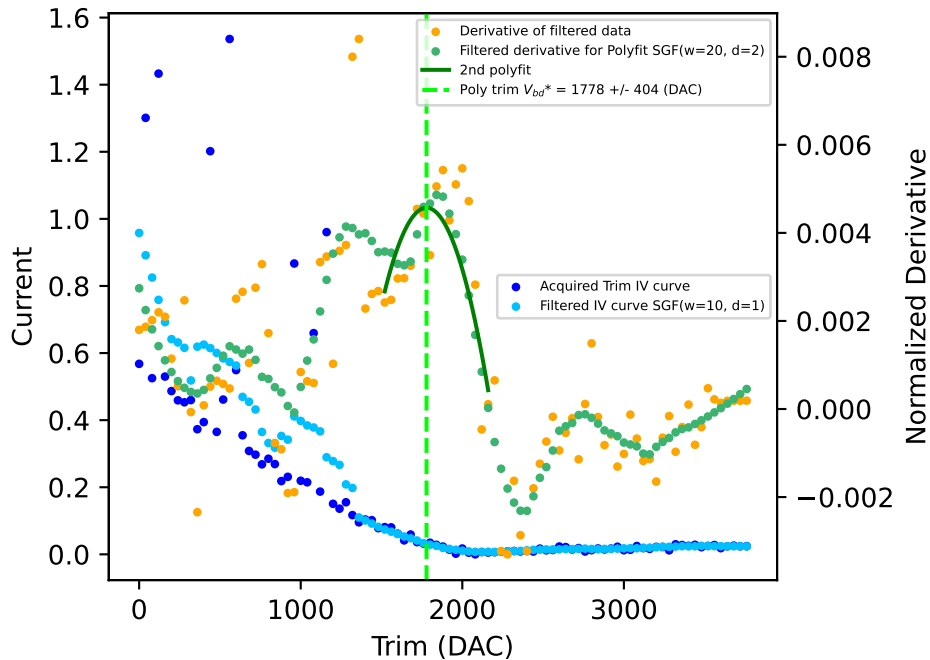
Bias DAC-VOLT conversion



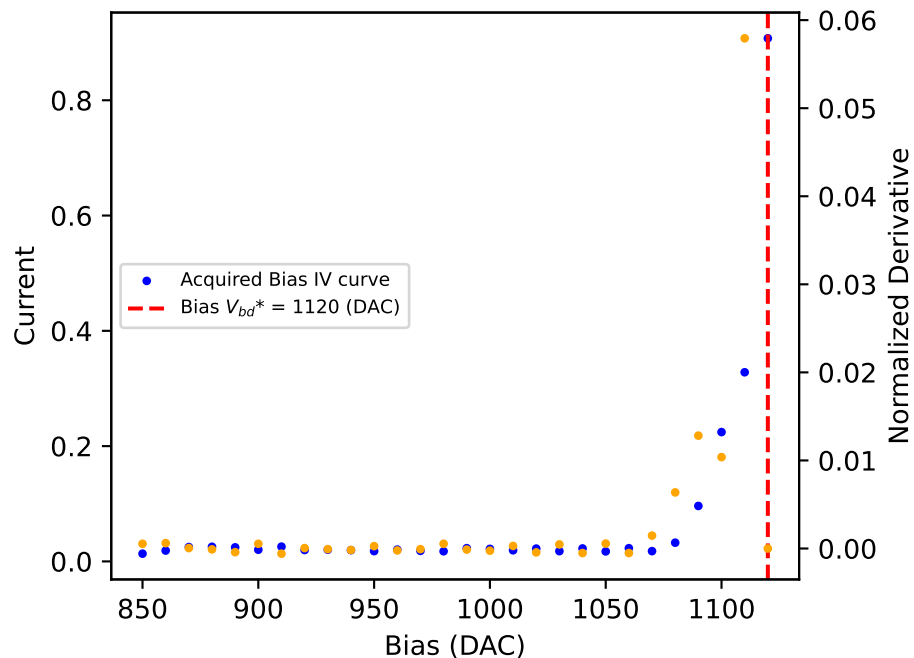
Bias + Trim REV IV curve



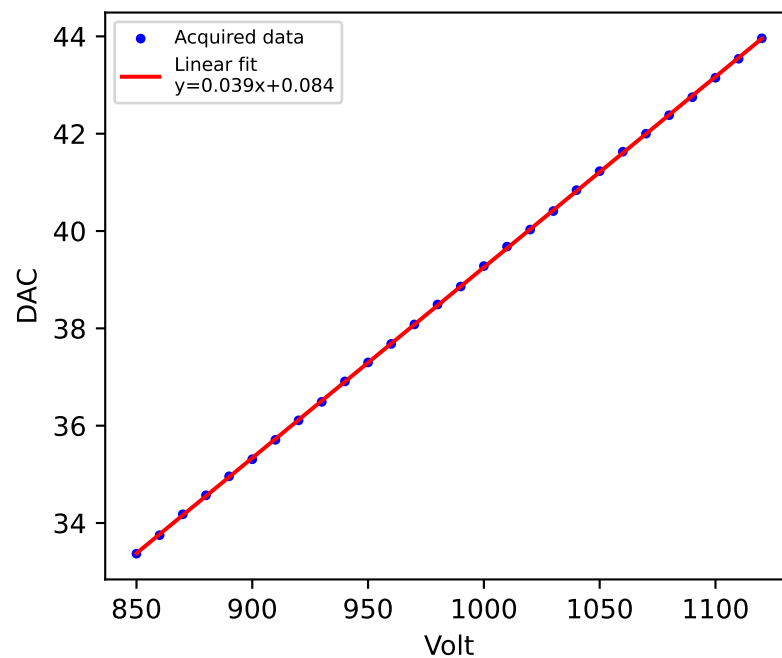
Trim REV IV curve



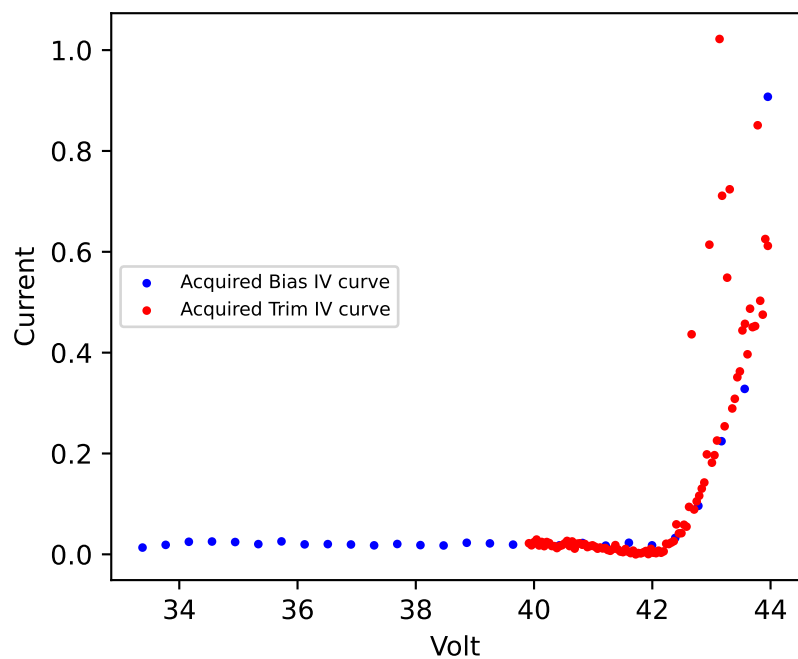
Bias REV IV curve



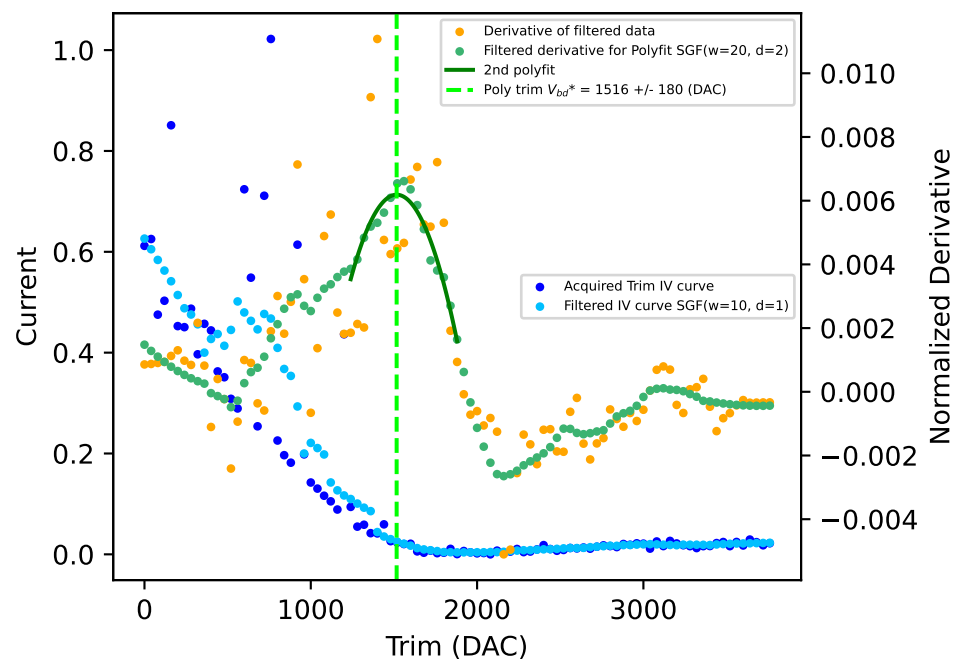
Bias DAC-VOLT conversion



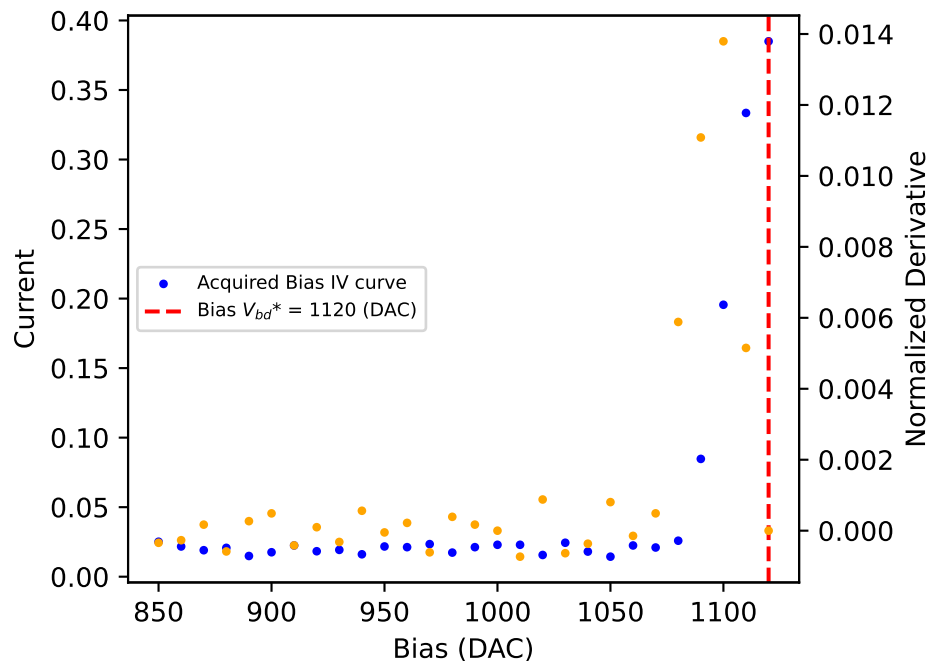
Bias + Trim REV IV curve



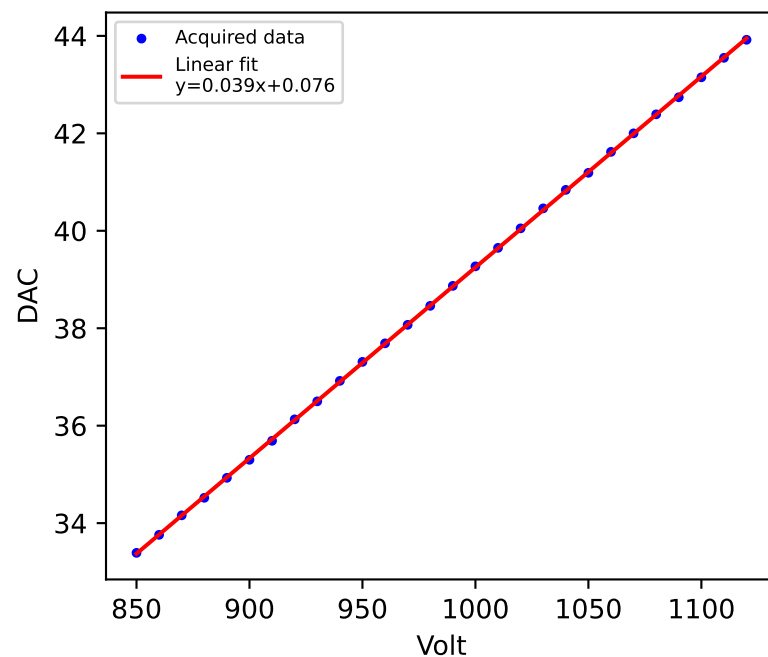
Trim REV IV curve



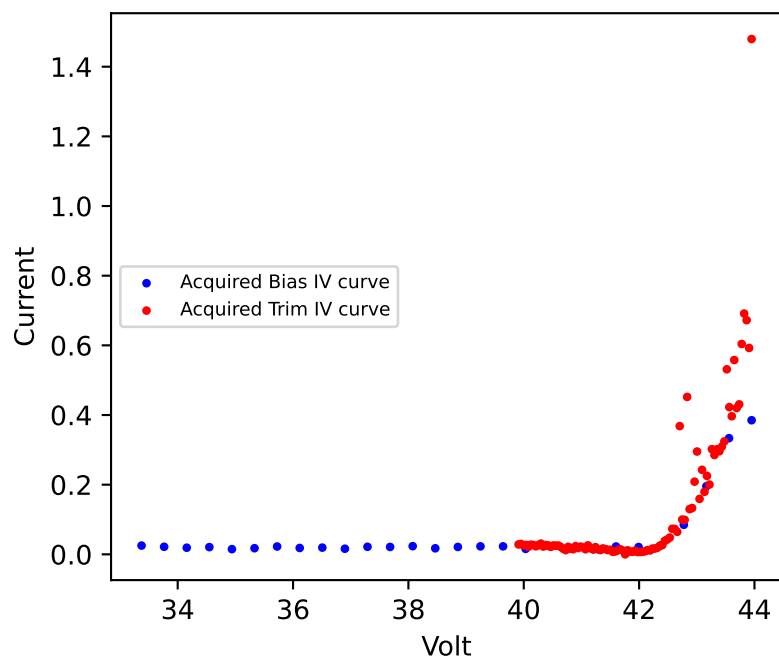
Bias REV IV curve



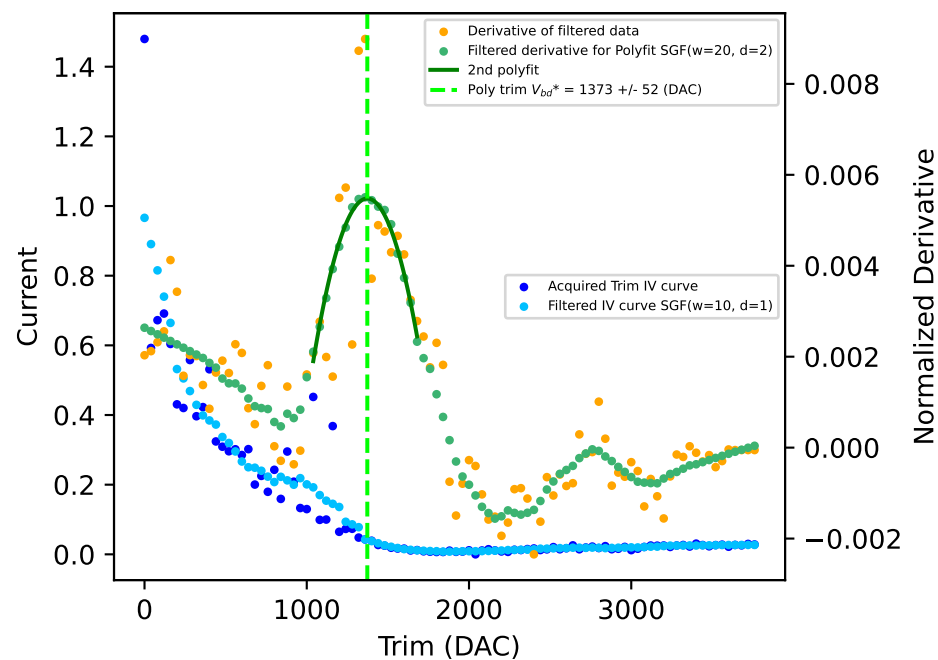
Bias DAC-VOLT conversion



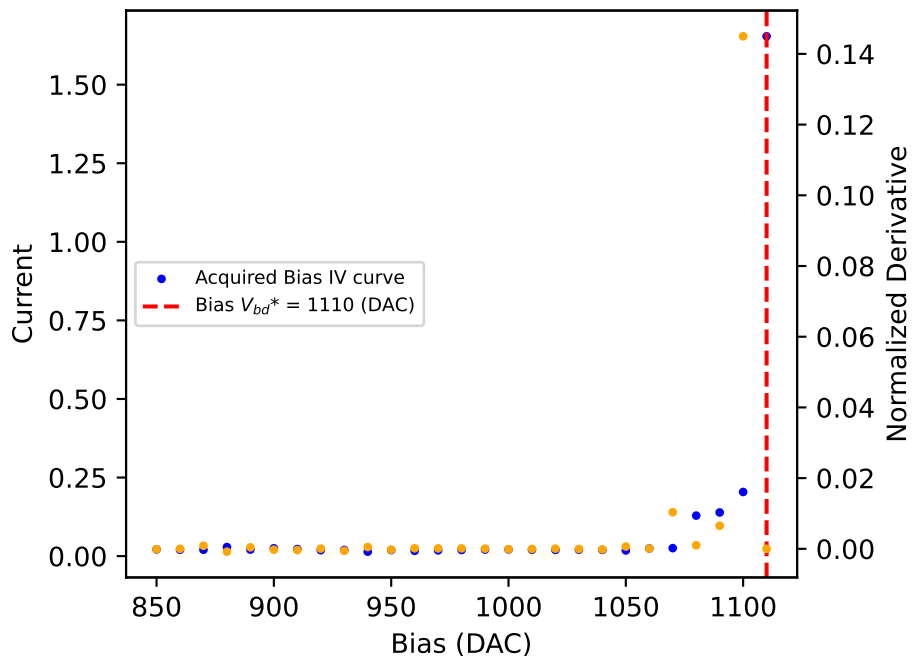
Bias + Trim REV IV curve



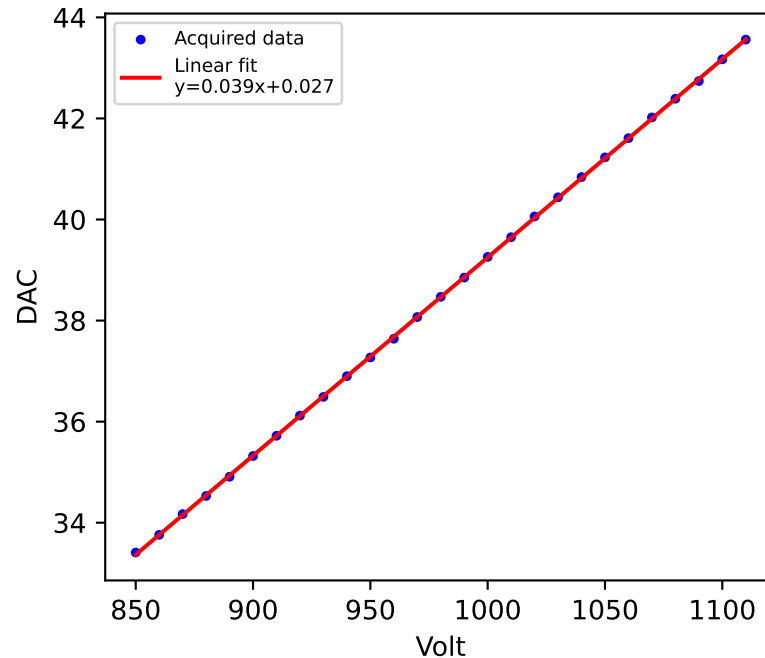
Trim REV IV curve



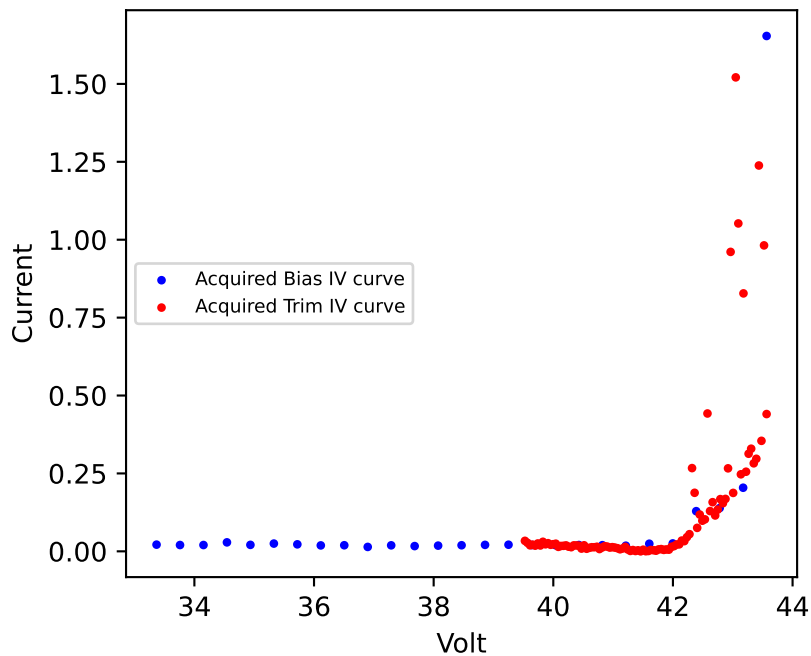
Bias REV IV curve



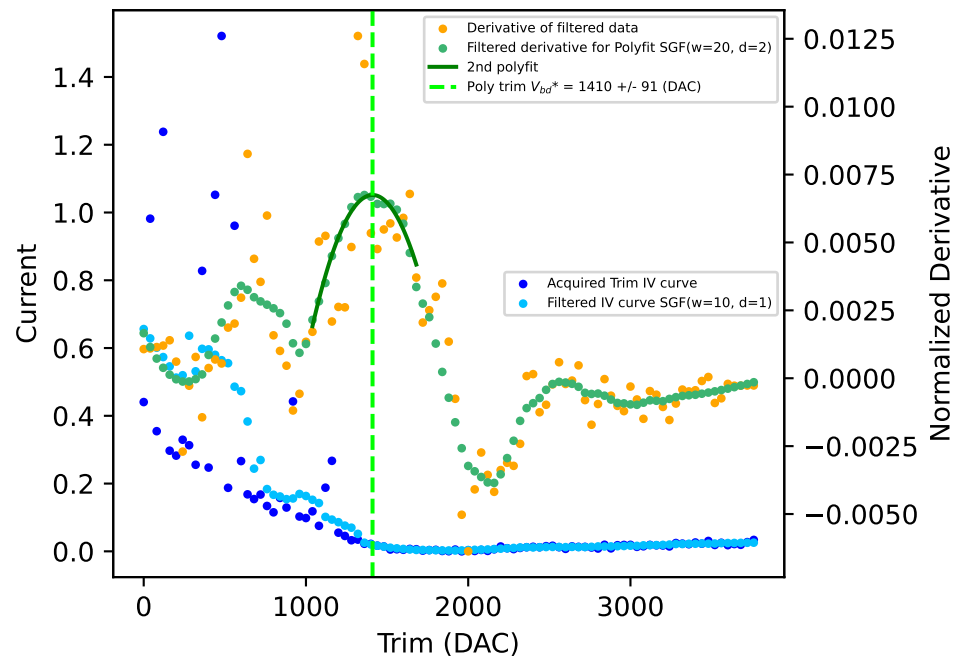
Bias DAC-VOLT conversion



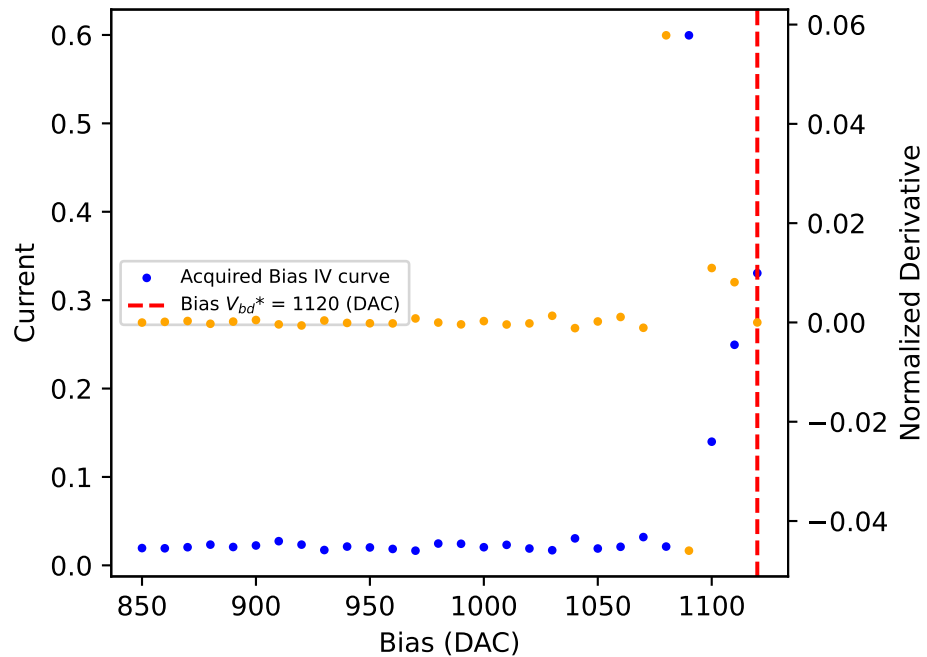
Bias + Trim REV IV curve



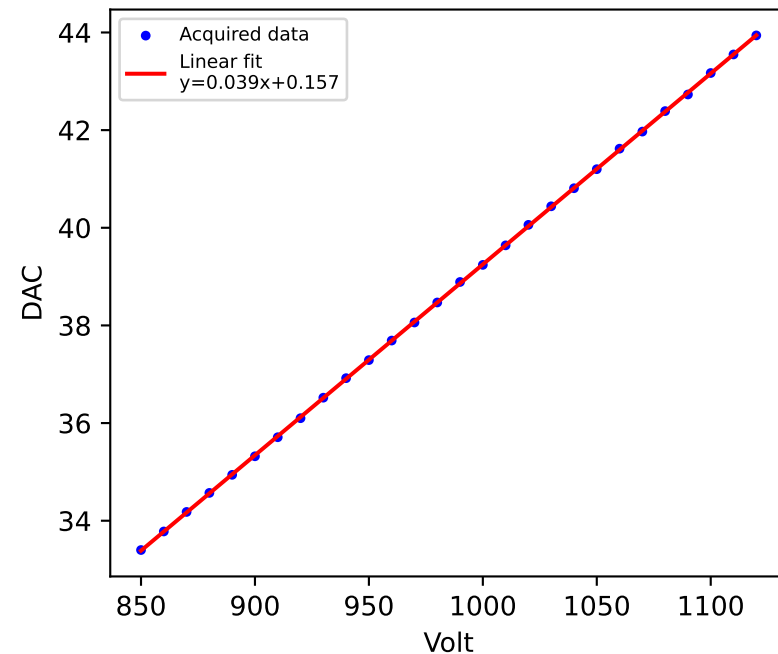
Trim REV IV curve



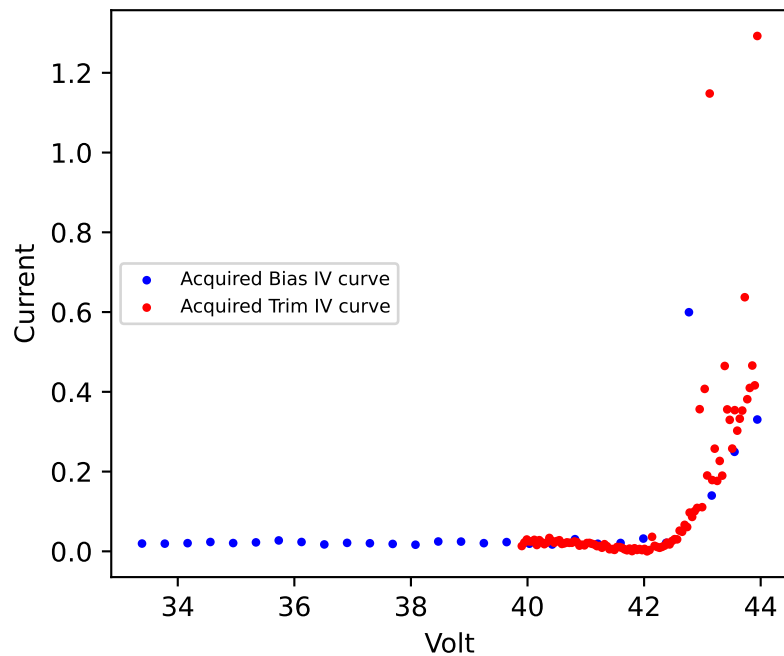
Bias REV IV curve



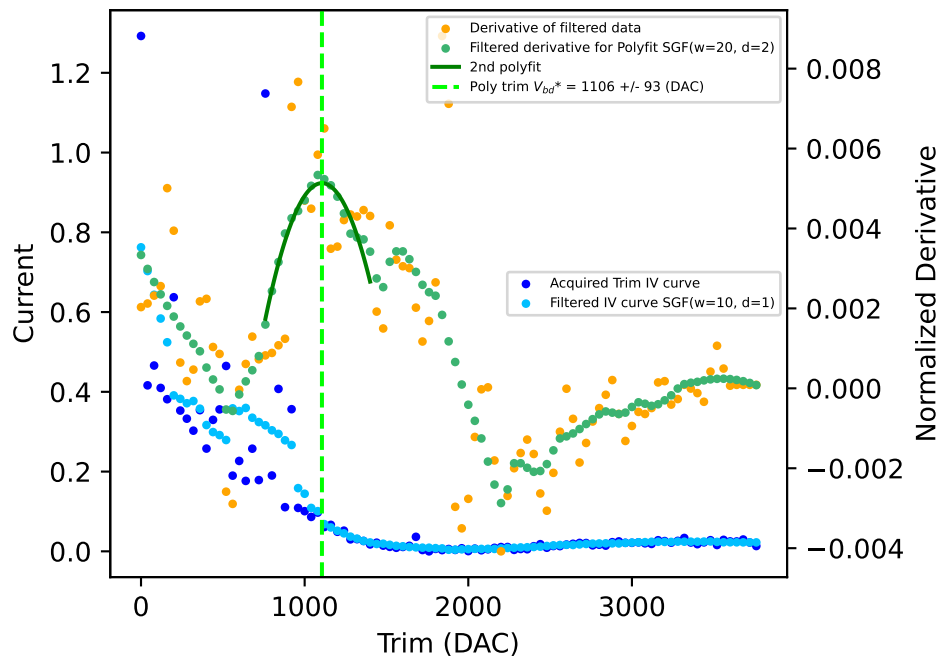
Bias DAC-VOLT conversion



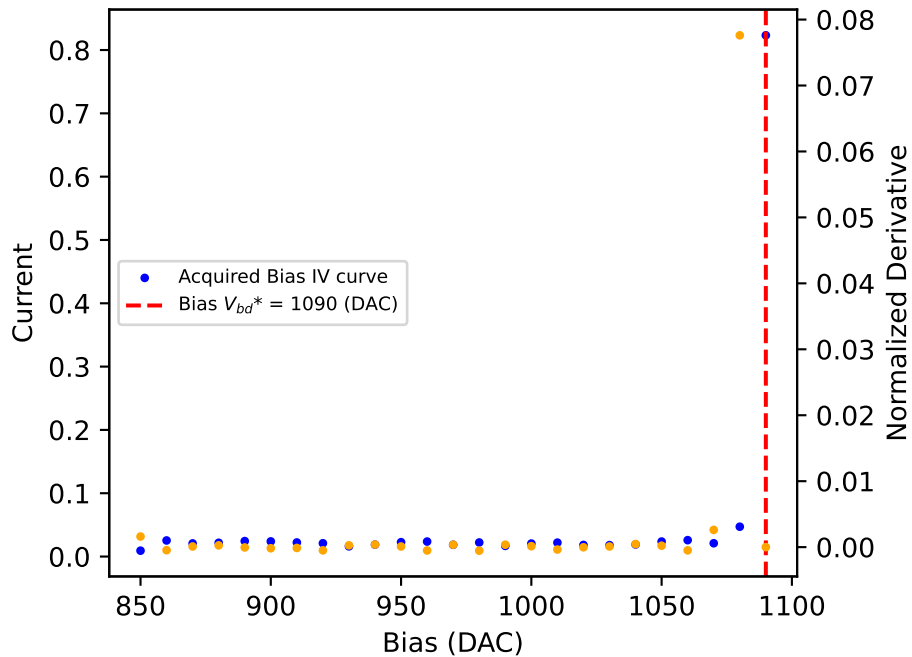
Bias + Trim REV IV curve



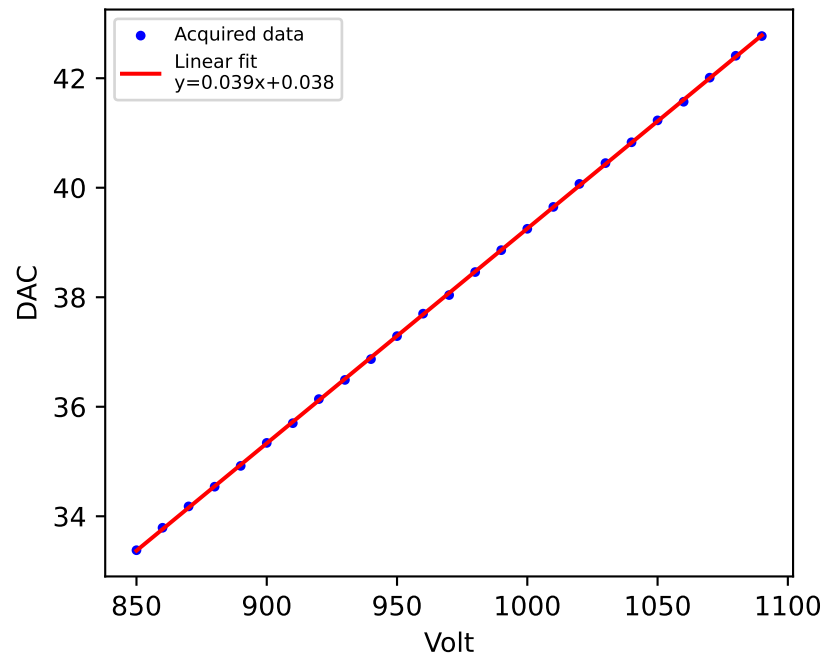
Trim REV IV curve



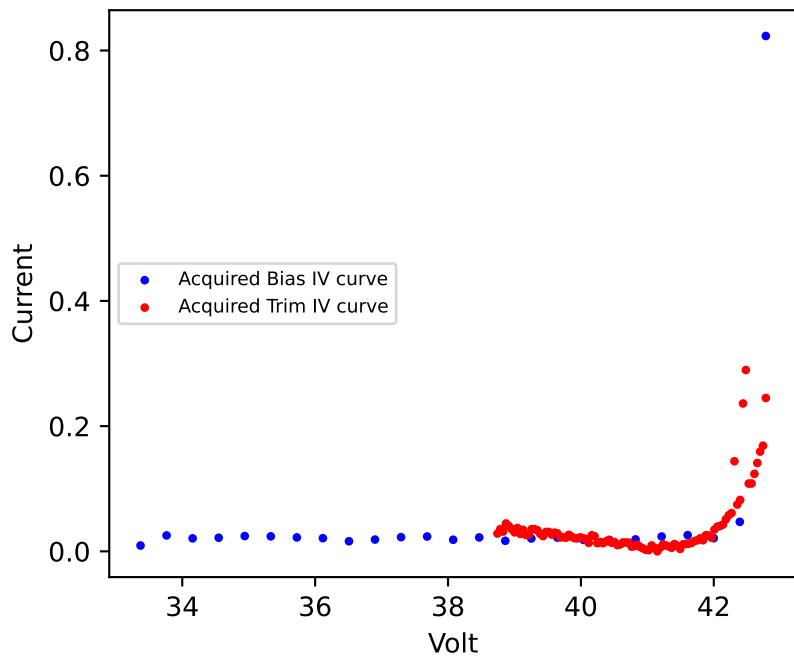
Bias REV IV curve



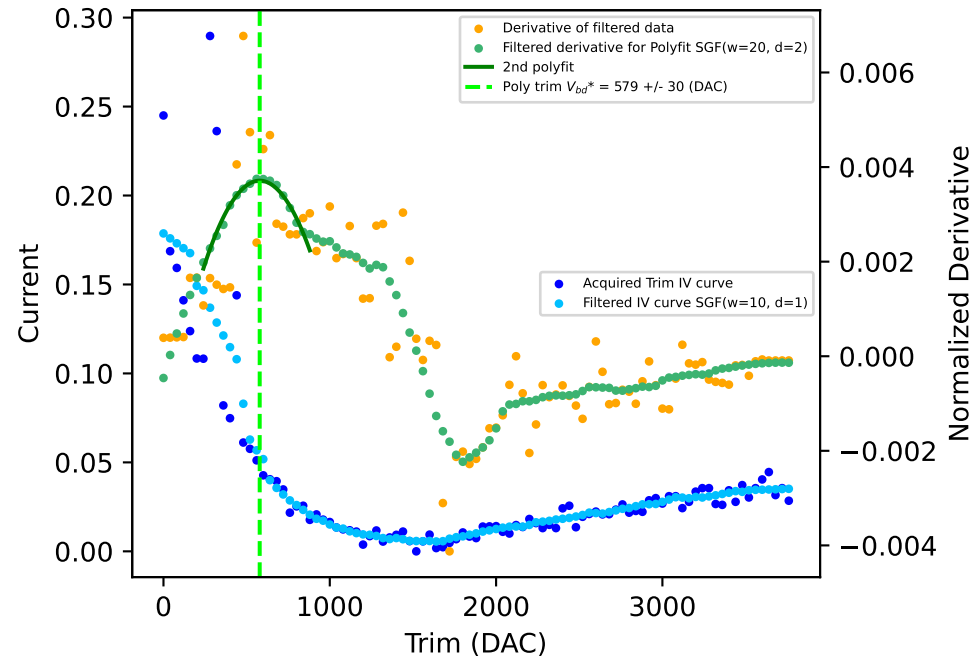
Bias DAC-VOLT conversion



Bias + Trim REV IV curve

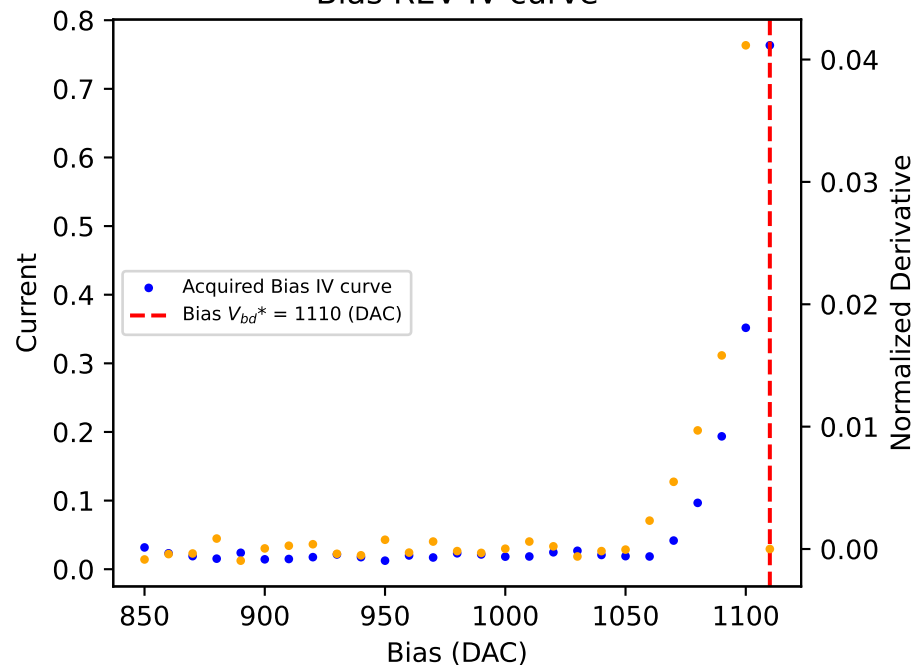


Trim REV IV curve

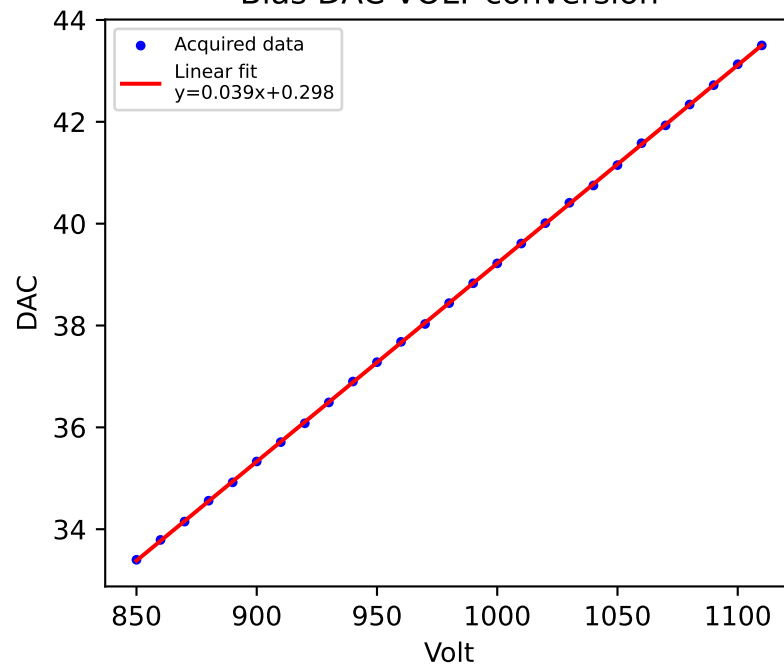




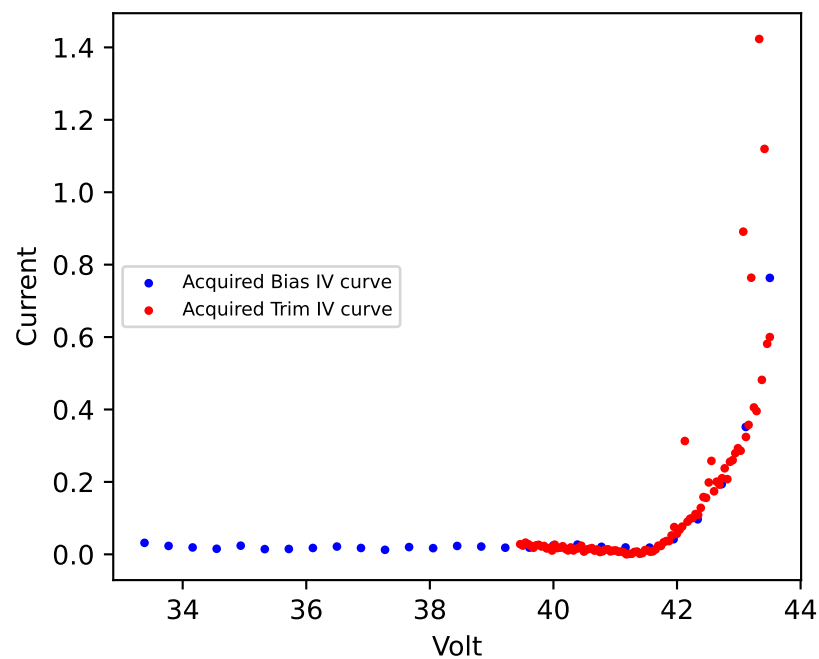
Bias REV IV curve



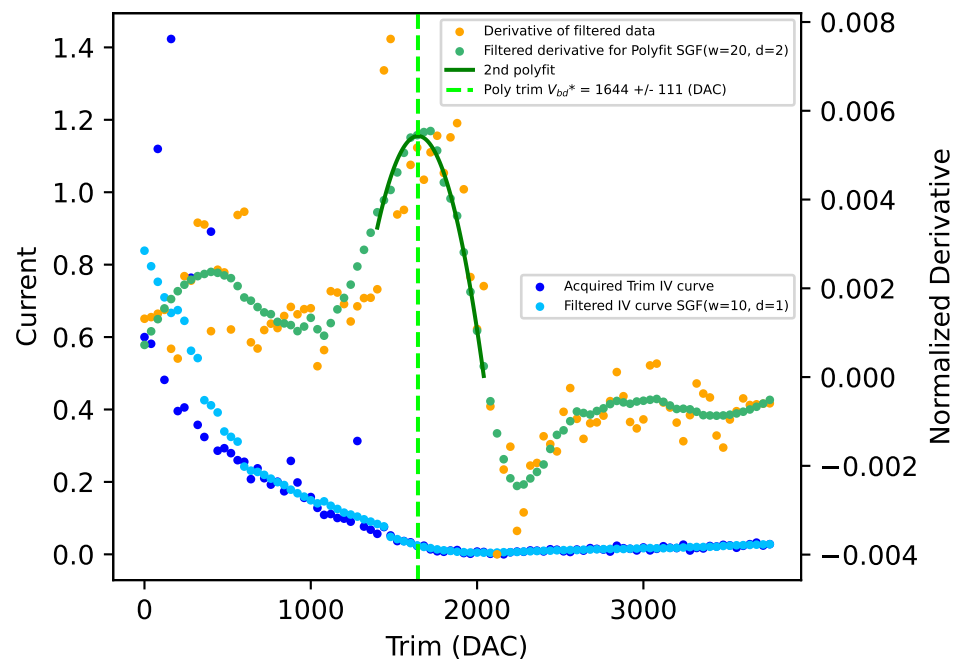
Bias DAC-VOLT conversion



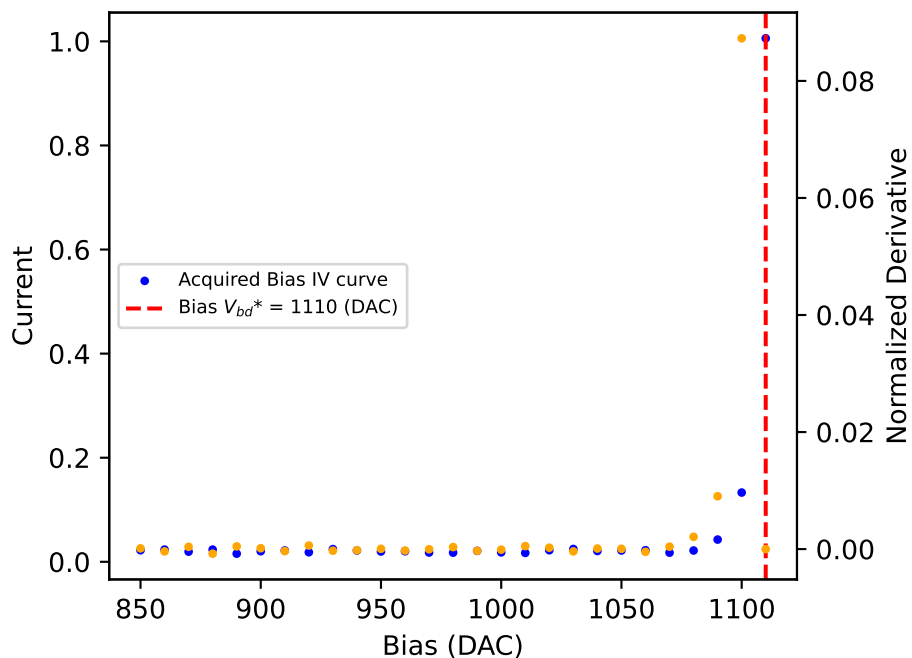
Bias + Trim REV IV curve



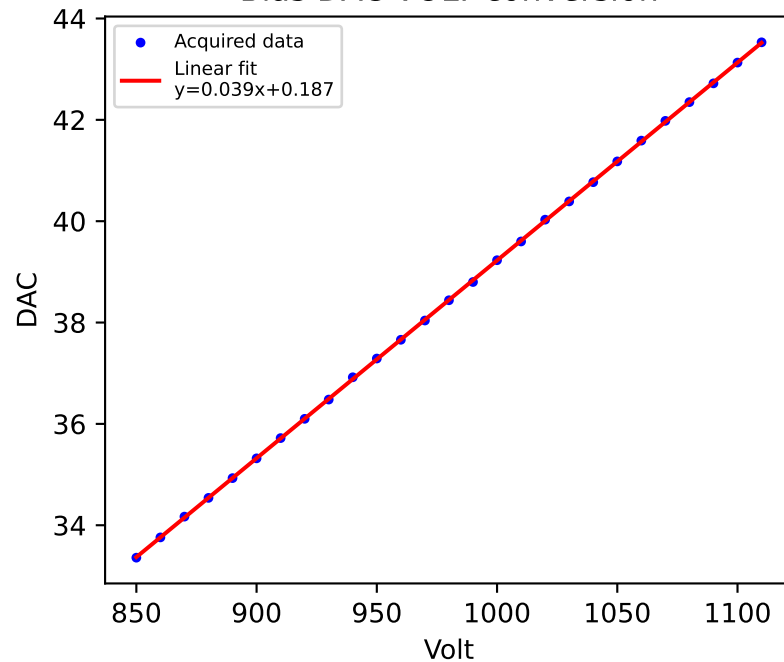
Trim REV IV curve



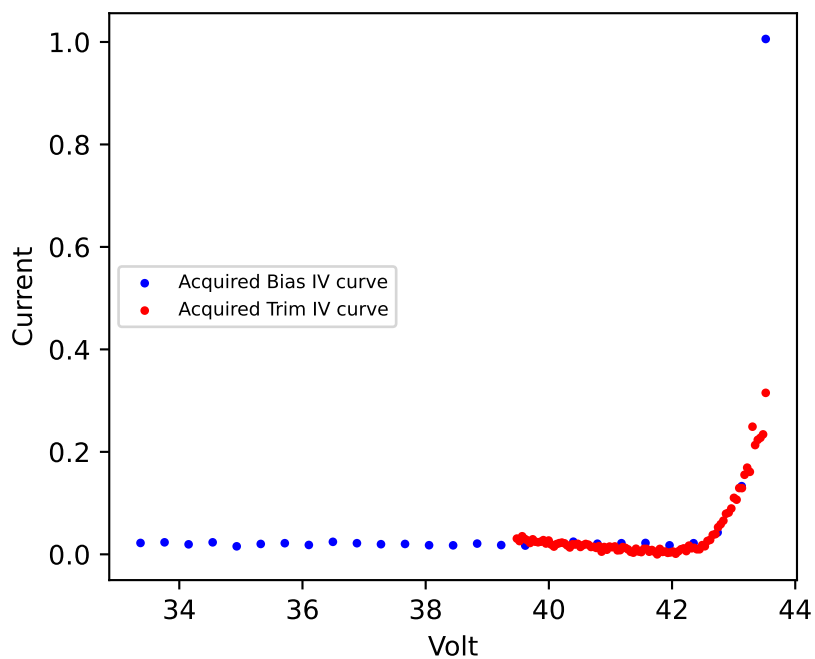
Bias REV IV curve



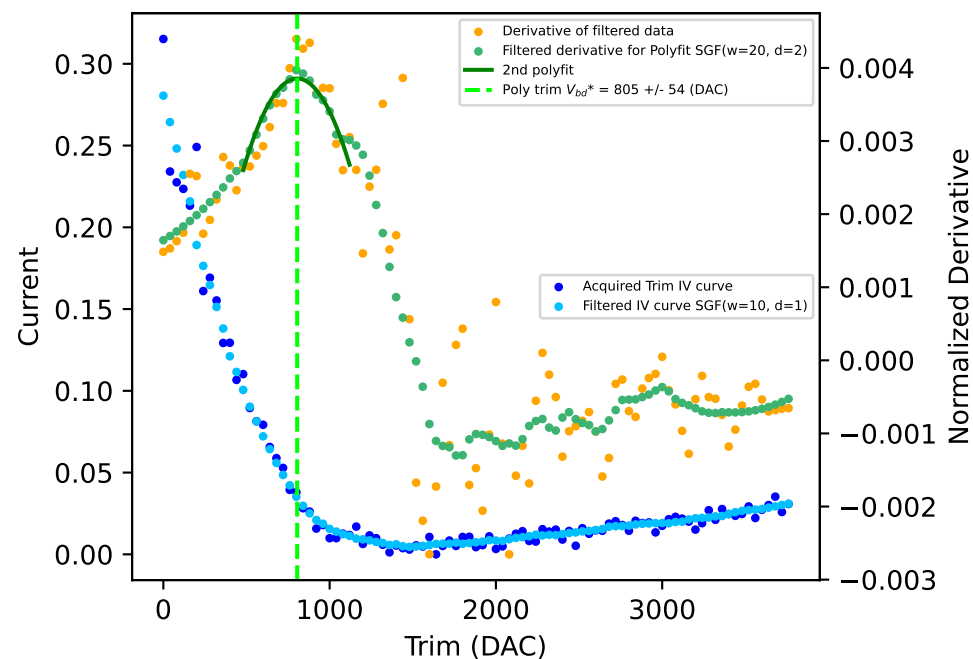
Bias DAC-VOLT conversion



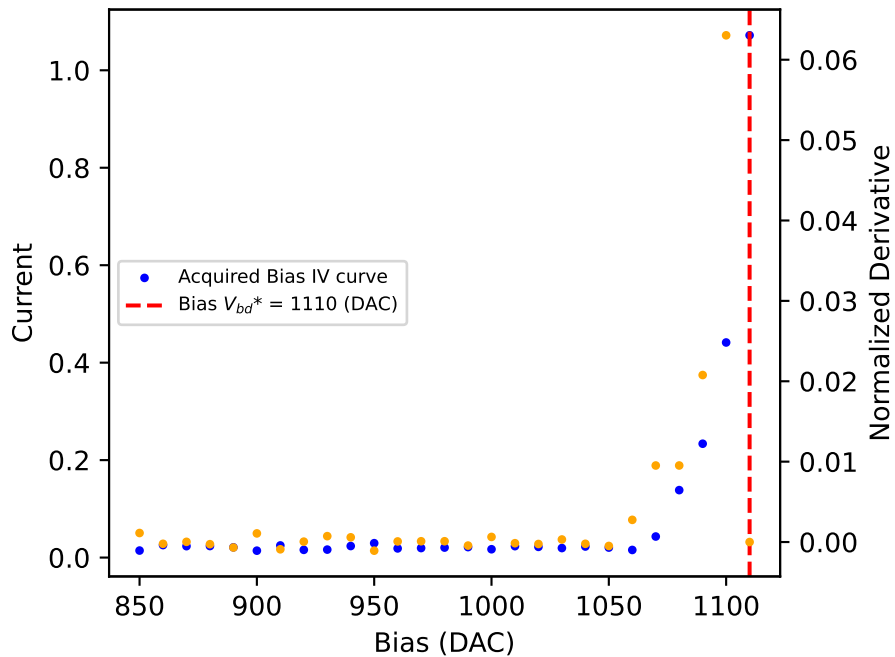
Bias + Trim REV IV curve



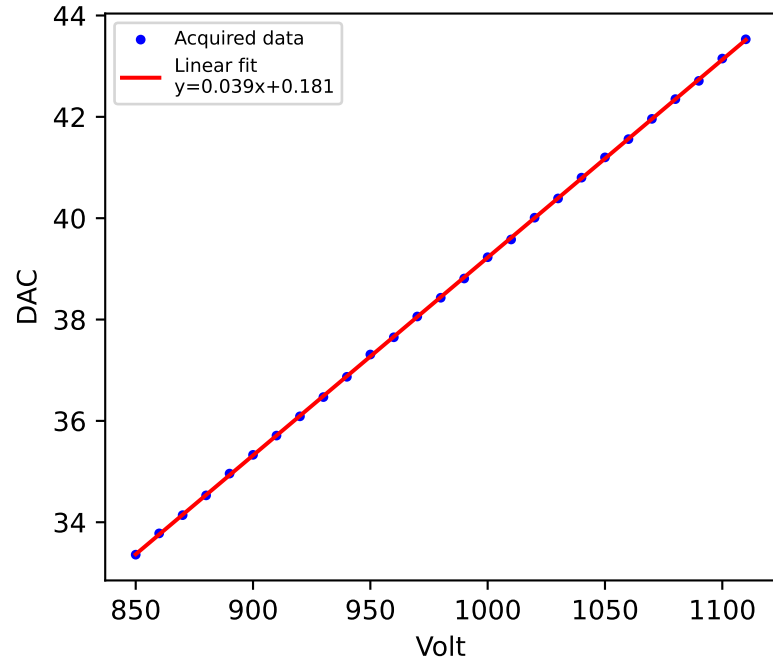
Trim REV IV curve



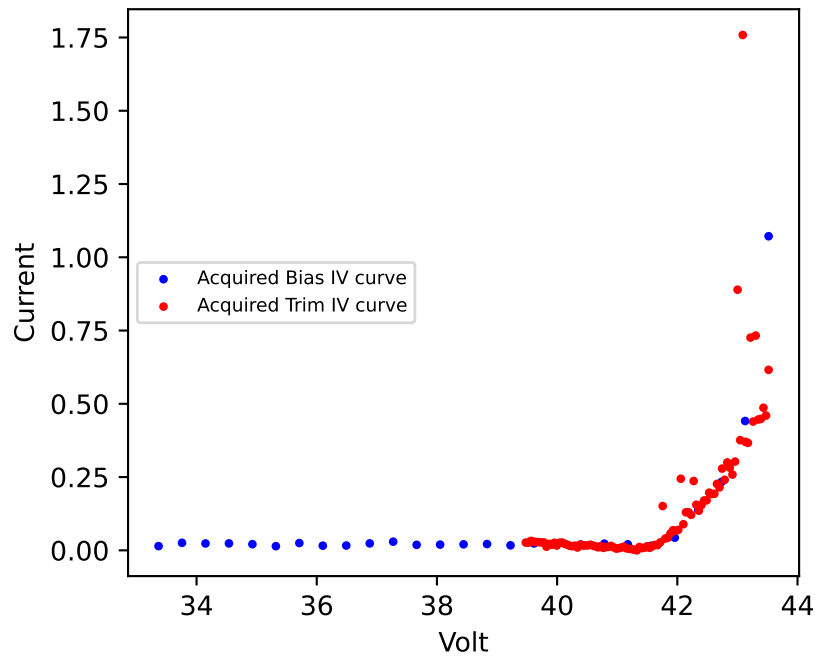
Bias REV IV curve



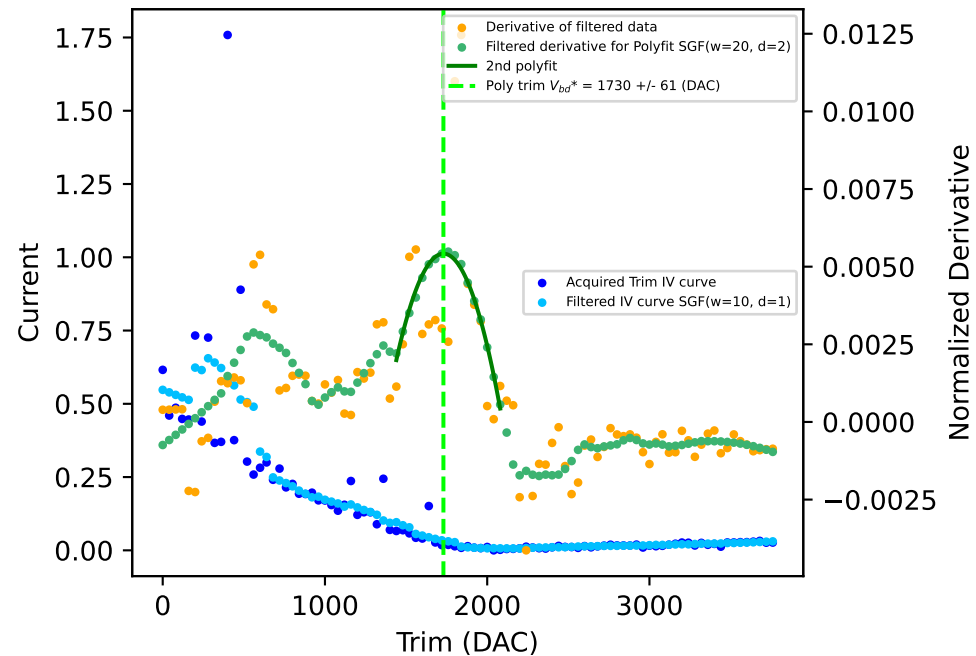
Bias DAC-VOLT conversion



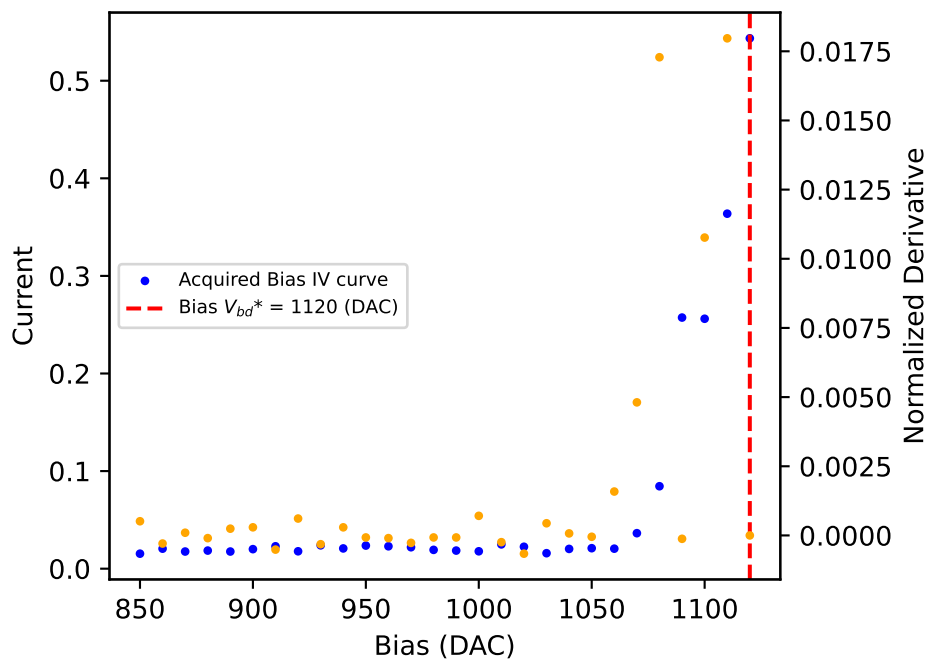
Bias + Trim REV IV curve



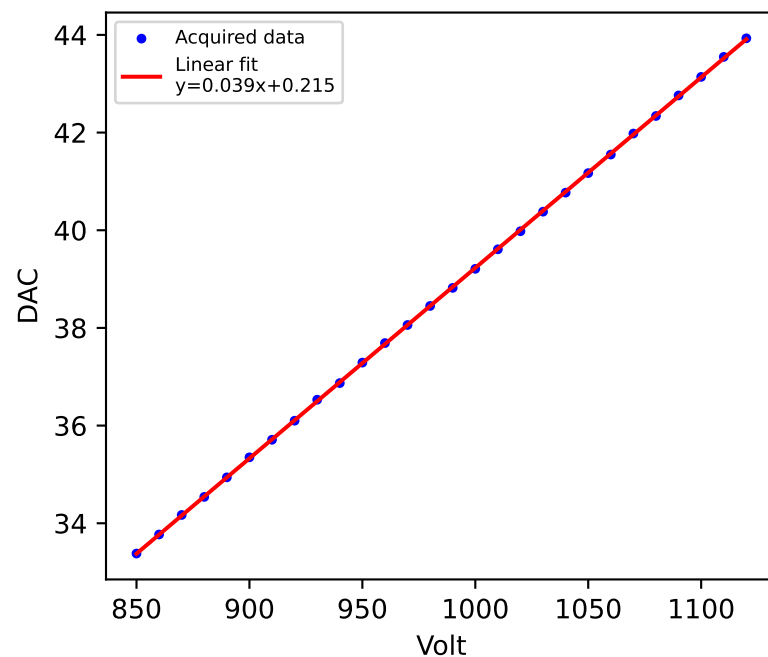
Trim REV IV curve



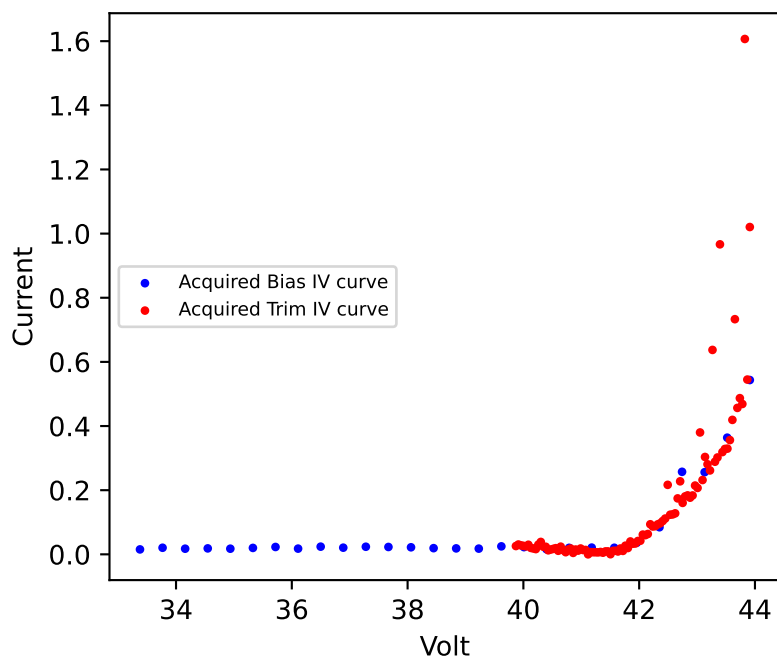
Bias REV IV curve



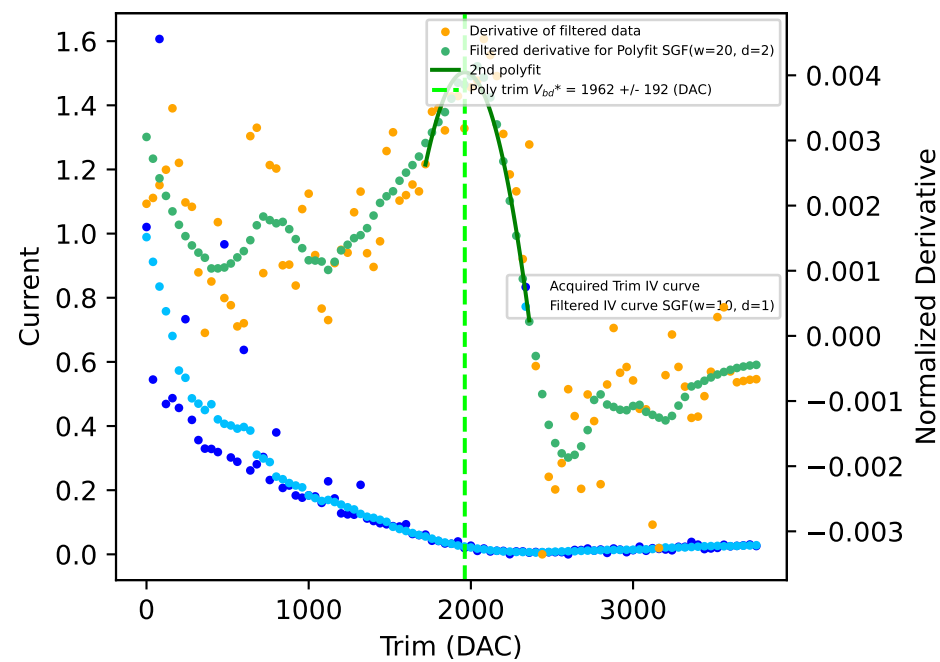
Bias DAC-VOLT conversion



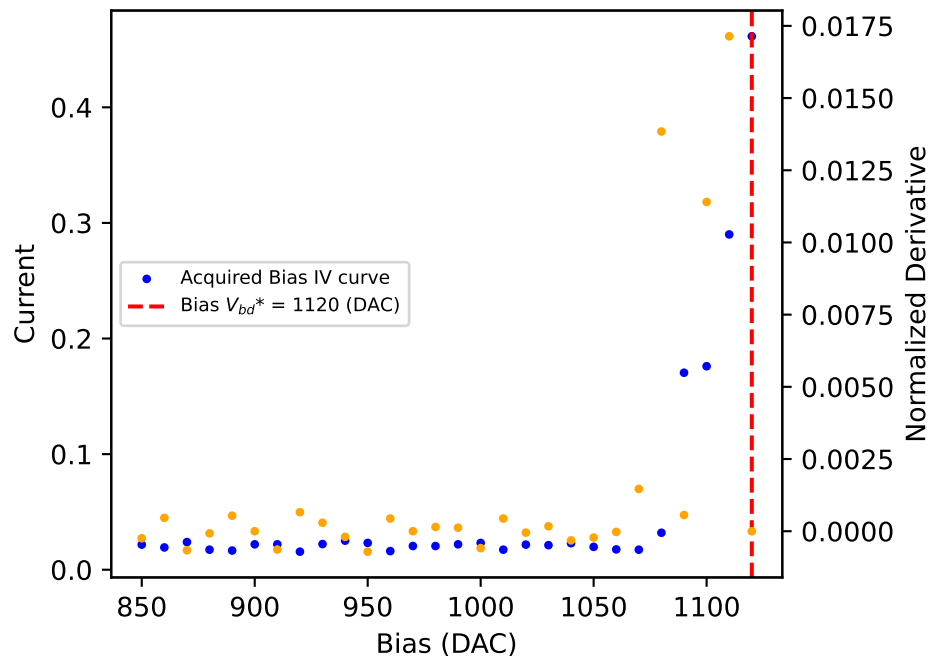
Bias + Trim REV IV curve



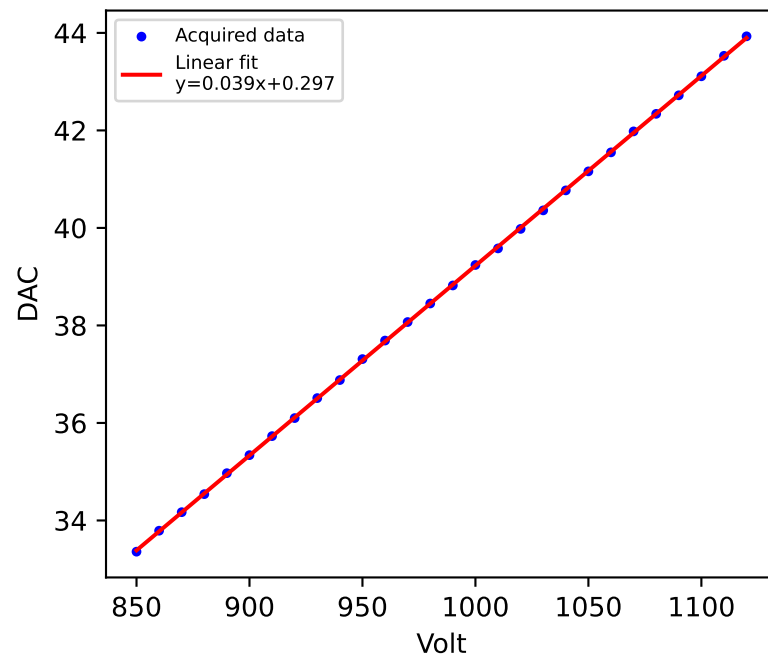
Trim REV IV curve



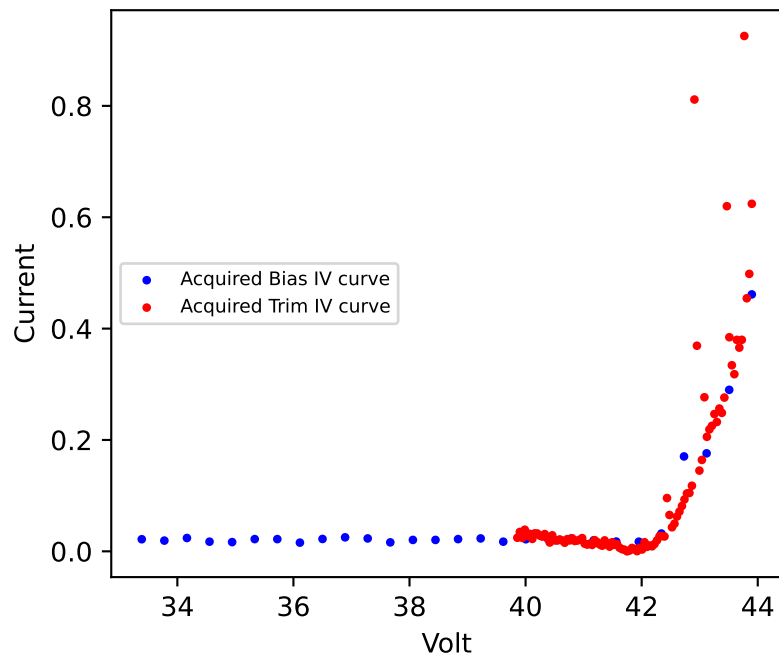
Bias REV IV curve



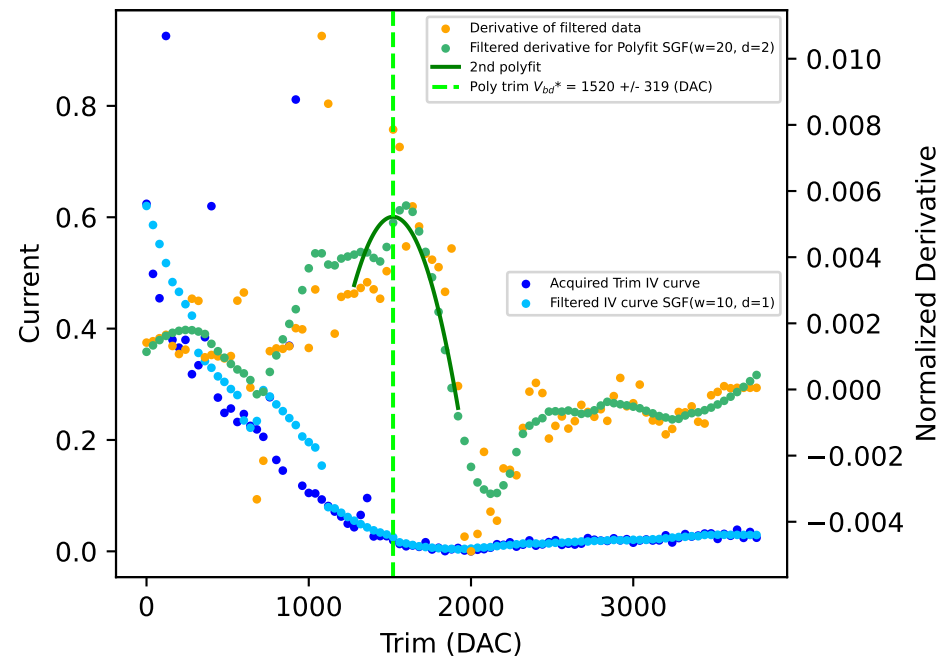
Bias DAC-VOLT conversion



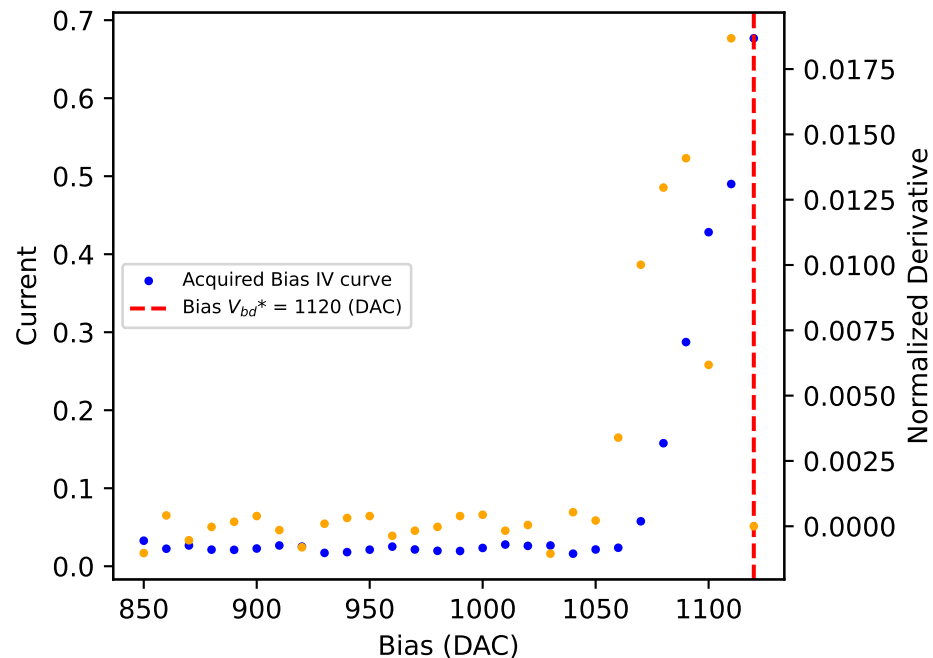
Bias + Trim REV IV curve



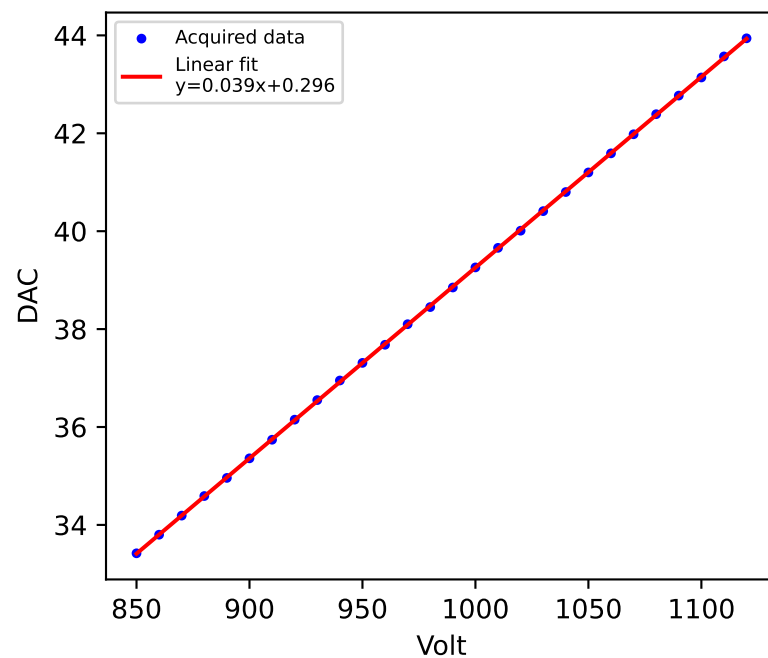
Trim REV IV curve



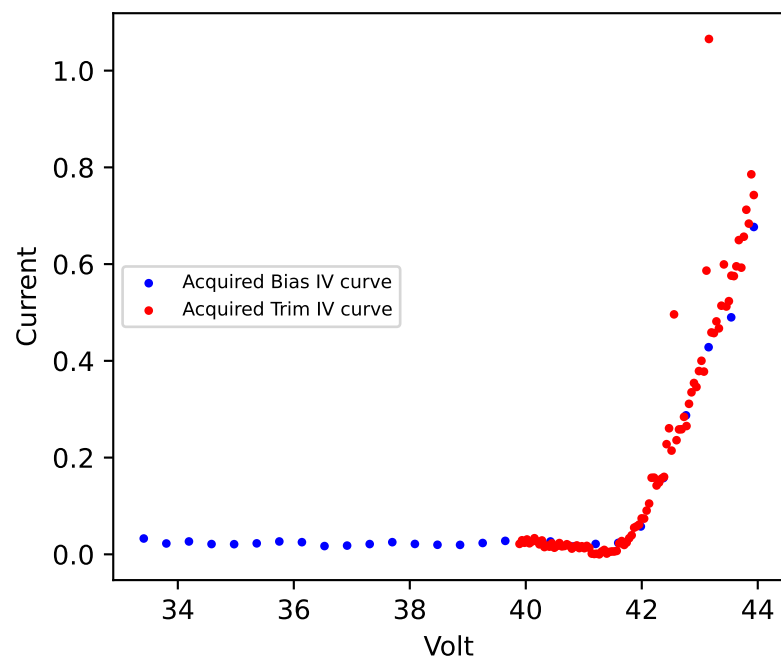
Bias REV IV curve



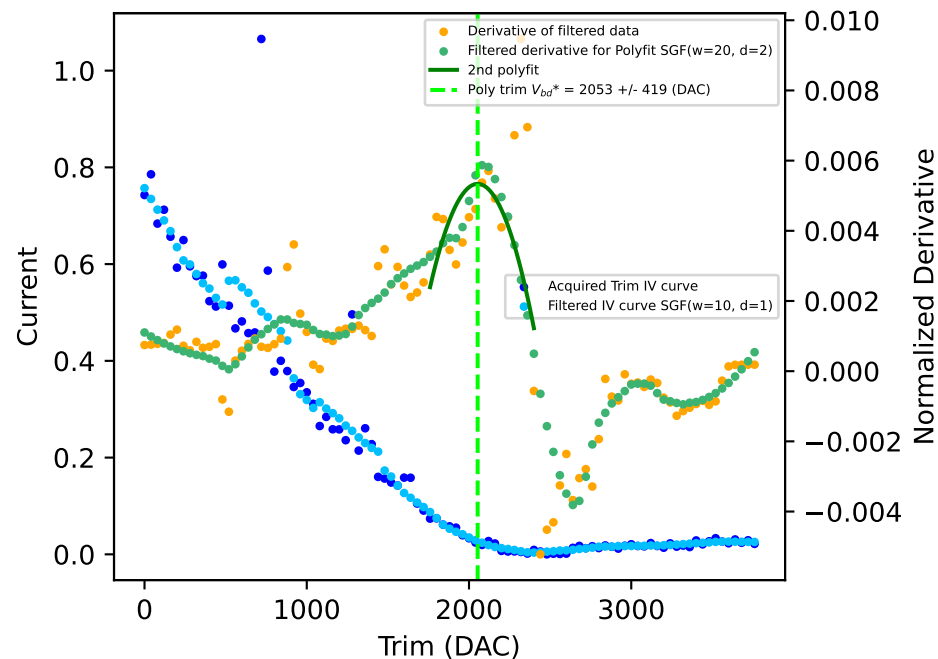
Bias DAC-VOLT conversion



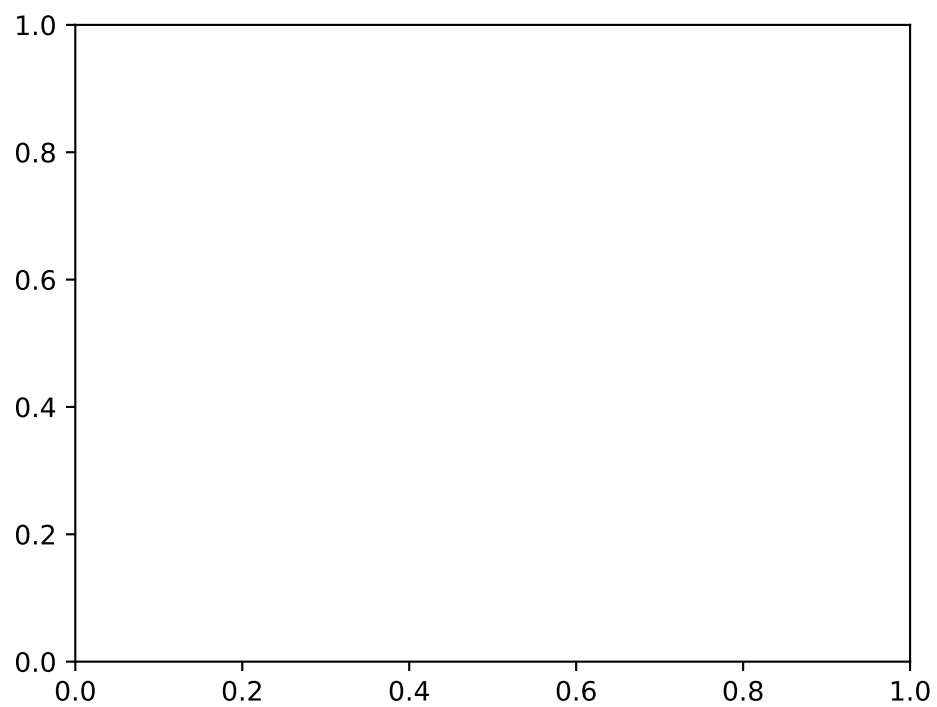
Bias + Trim REV IV curve



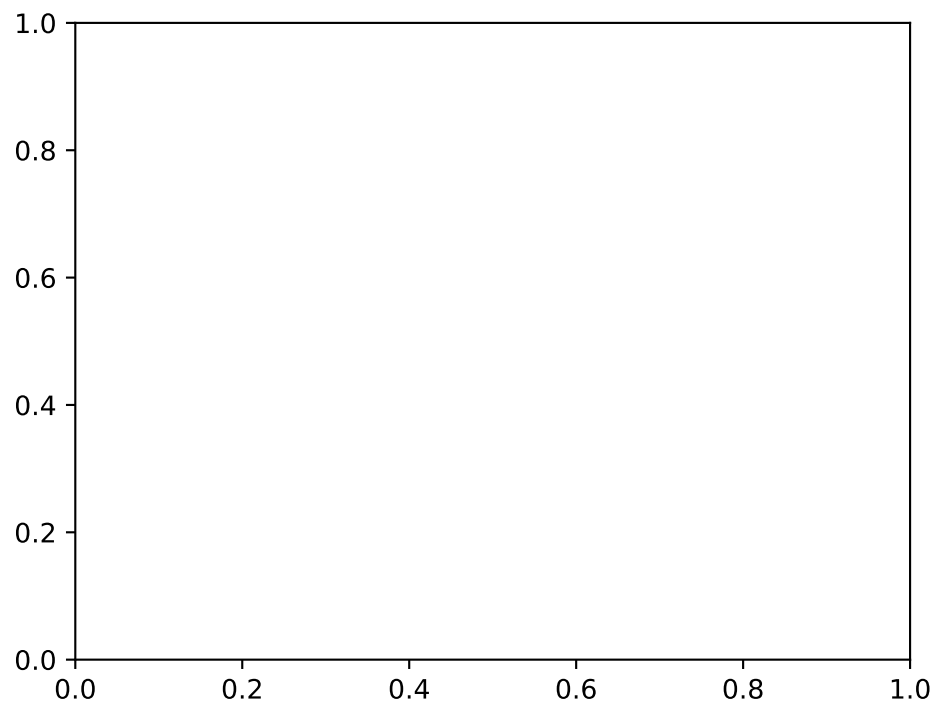
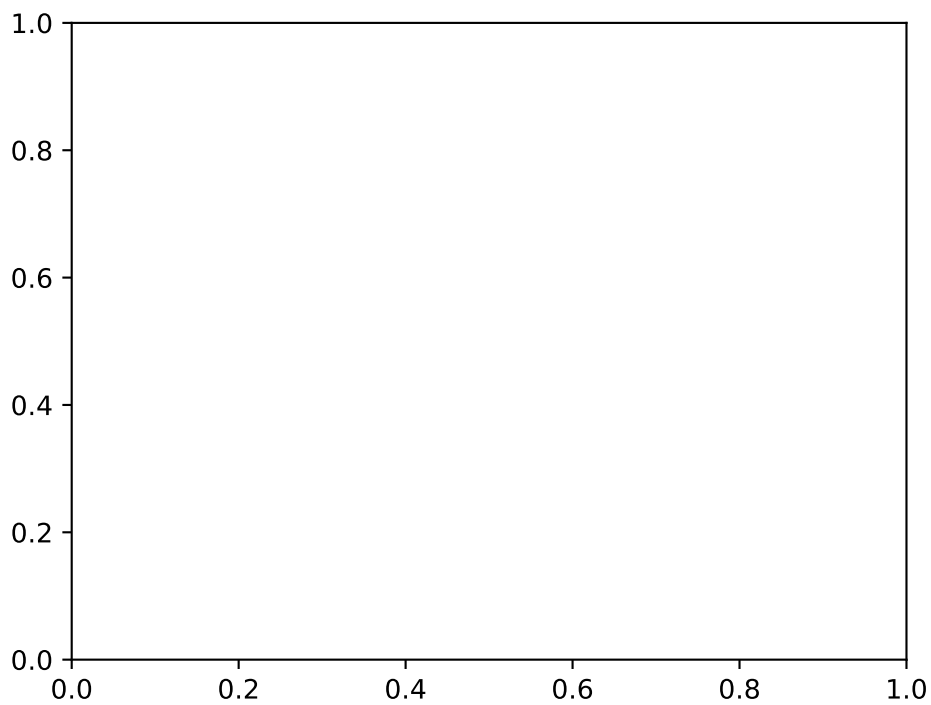
Trim REV IV curve



ENDPOINT:109 APA:2 AFE:3 Config\_CH:30 CH\_daq:36 SiPM:HPK --> MISSING FILE

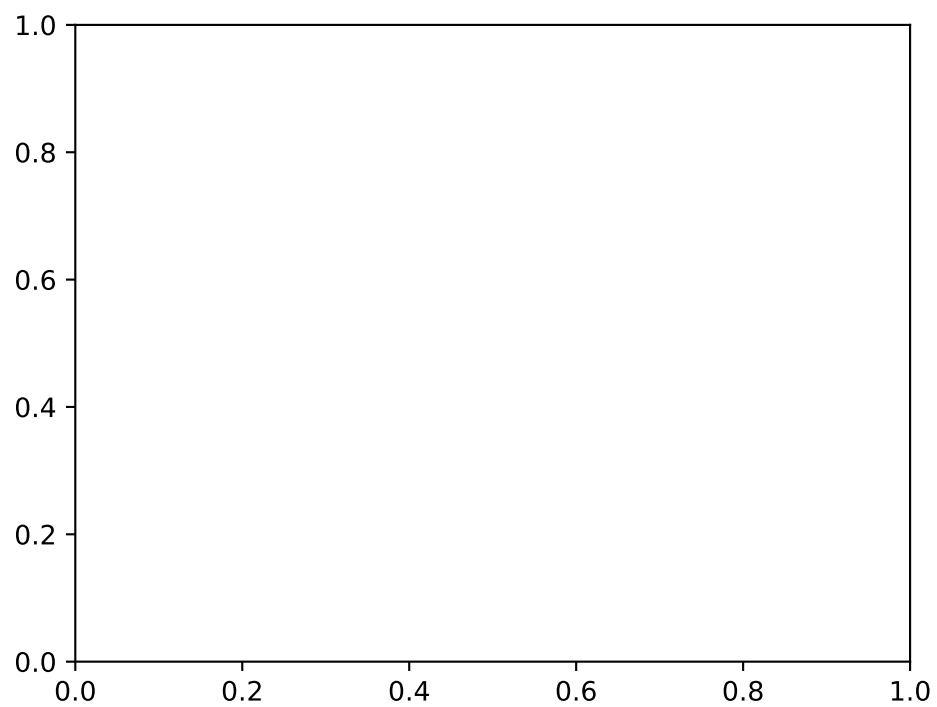
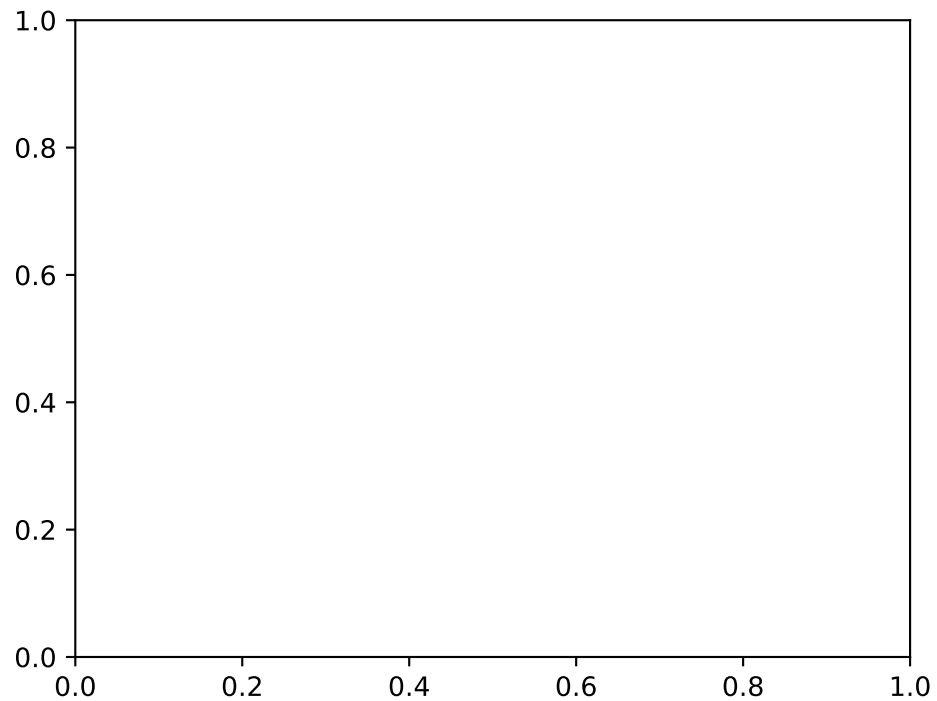
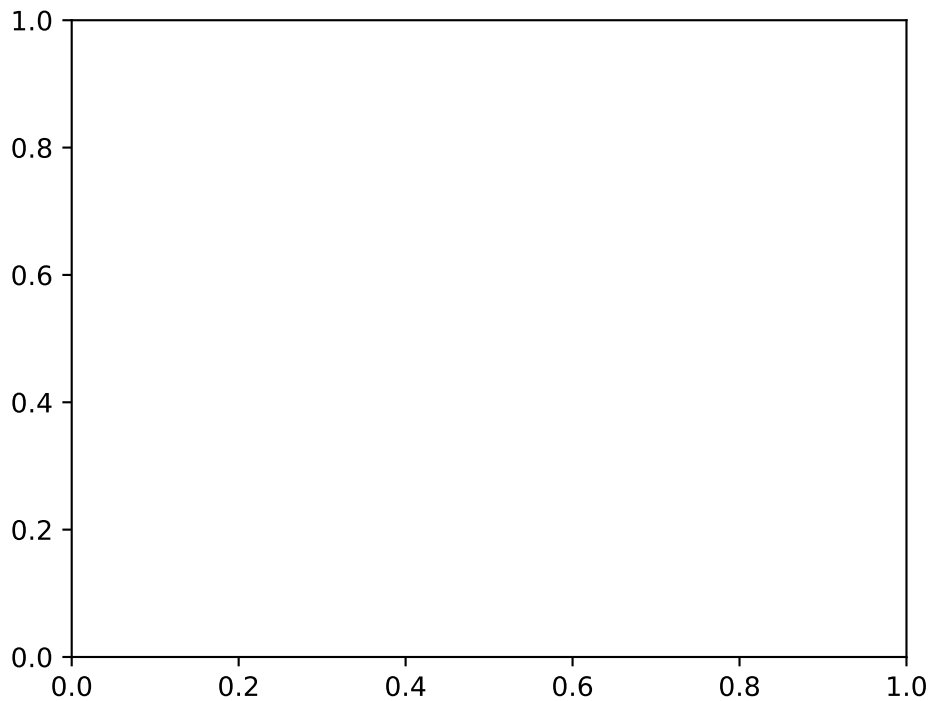


ENDPOINT:109 APA:2 AFE:3 Config\_CH:31 CH\_daq:37 SiPM:HPK --> MISSING FILE

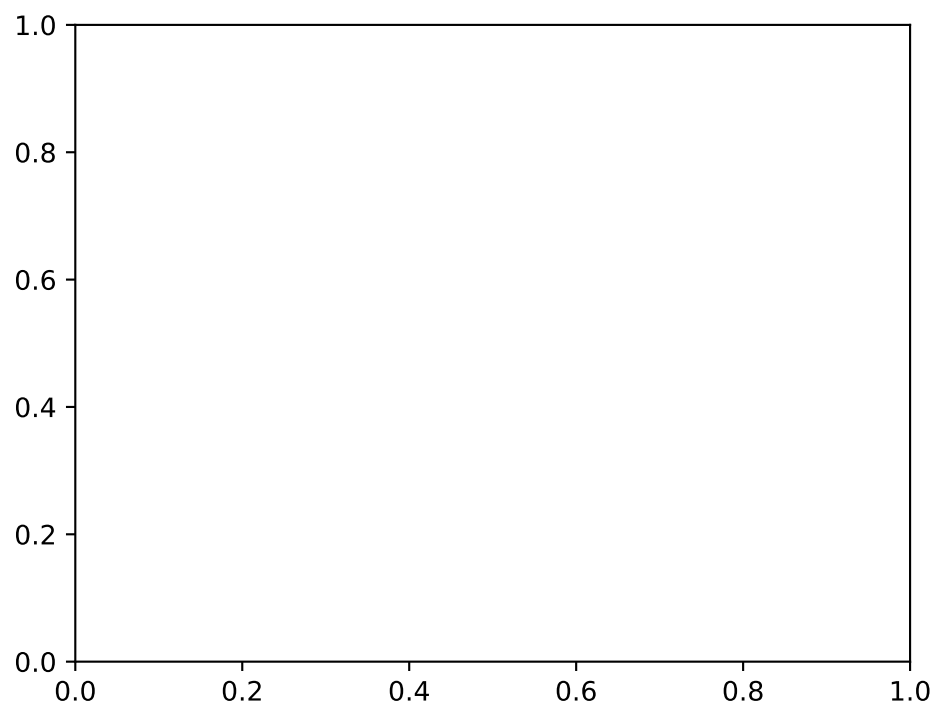
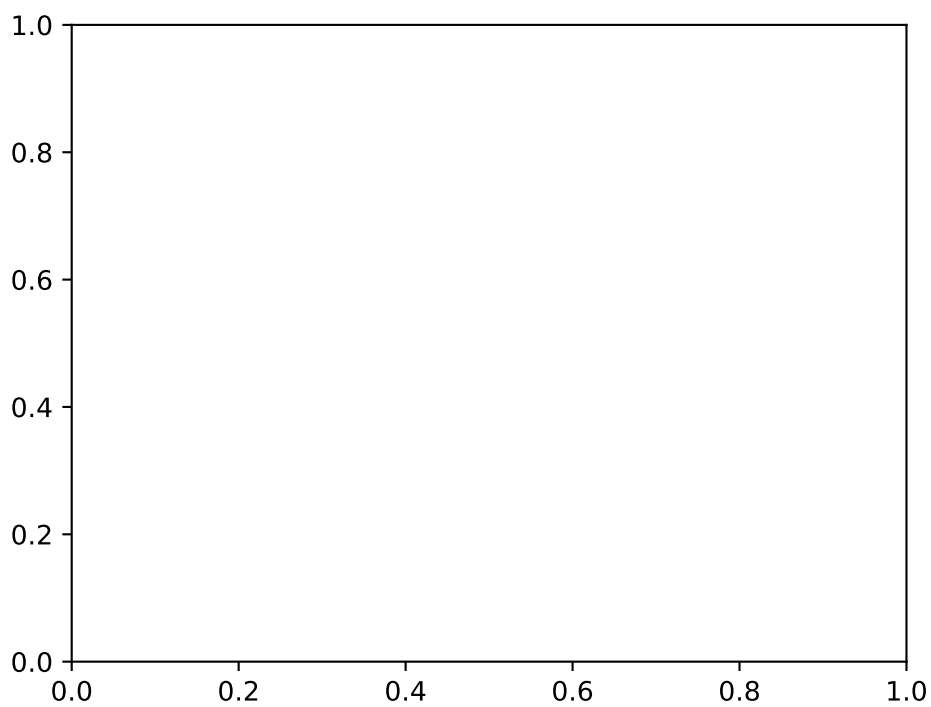
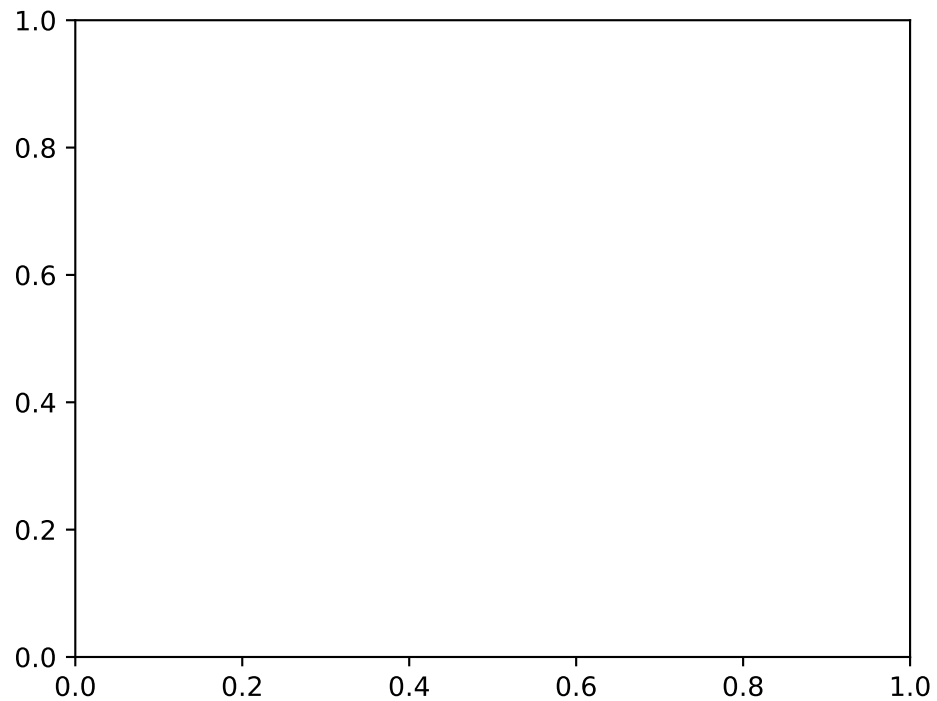




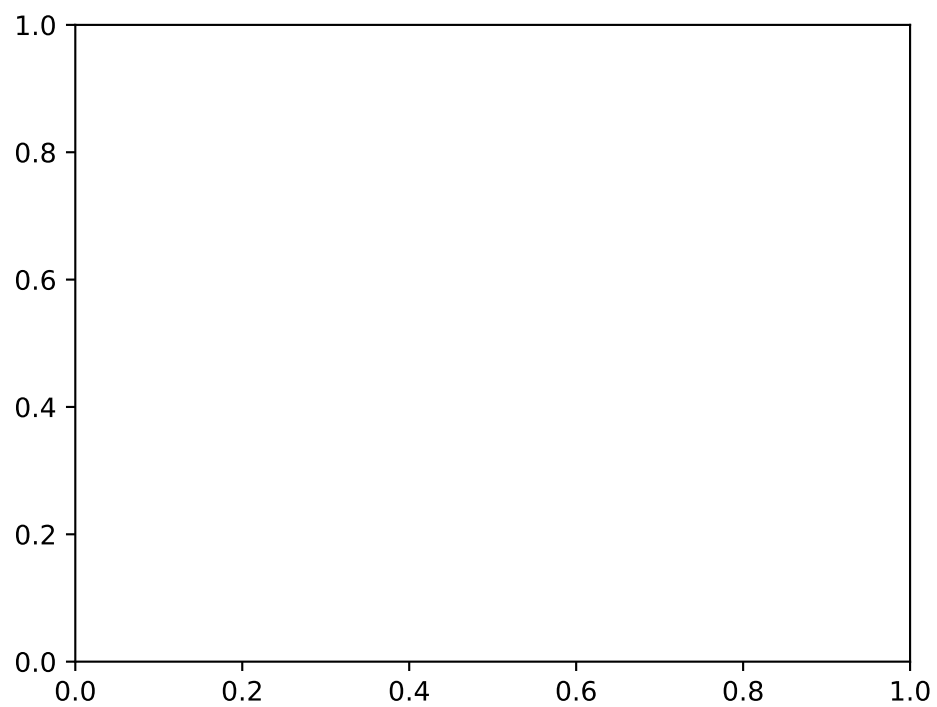
ENDPOINT:109 APA:2 AFE:4 Config\_CH:32 CH\_daq:40 SiPM:HPK --> MISSING FILE



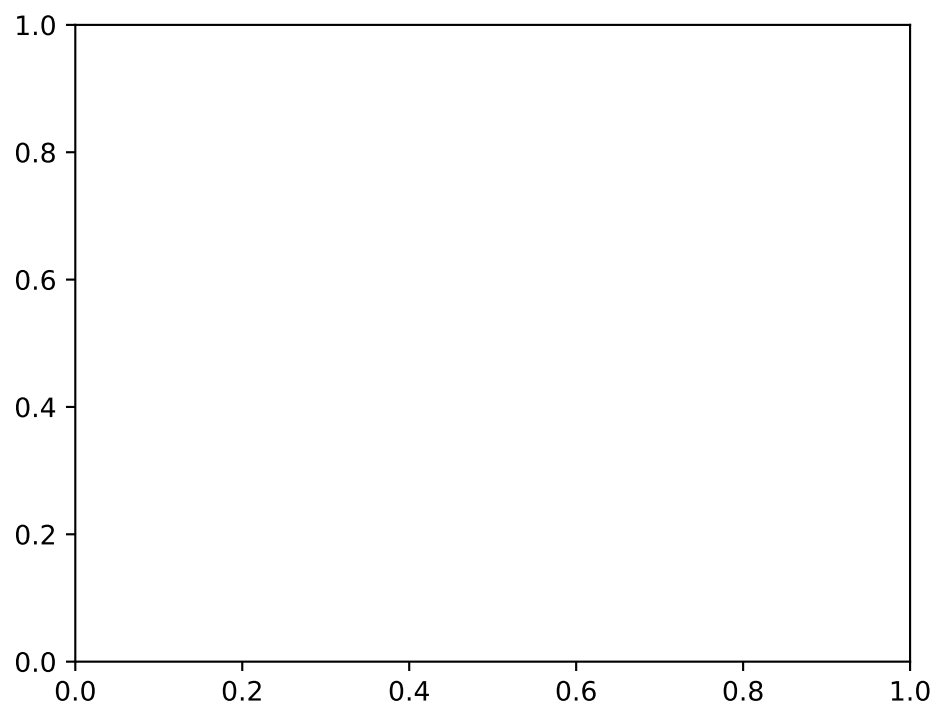
ENDPOINT:109 APA:2 AFE:4 Config\_CH:33 CH\_daq:41 SiPM:HPK --> MISSING FILE



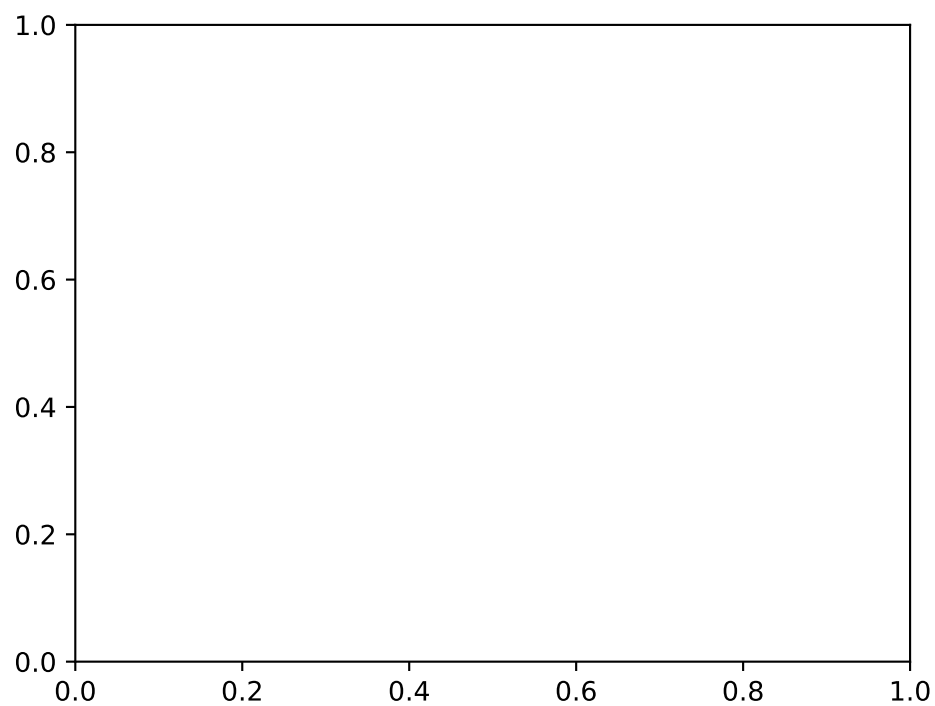
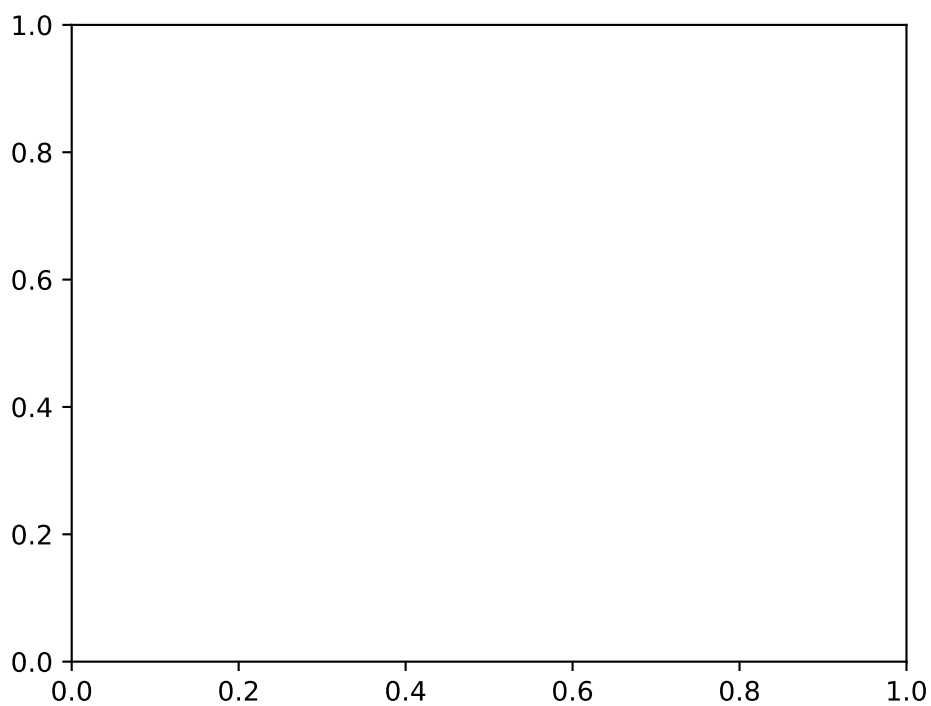
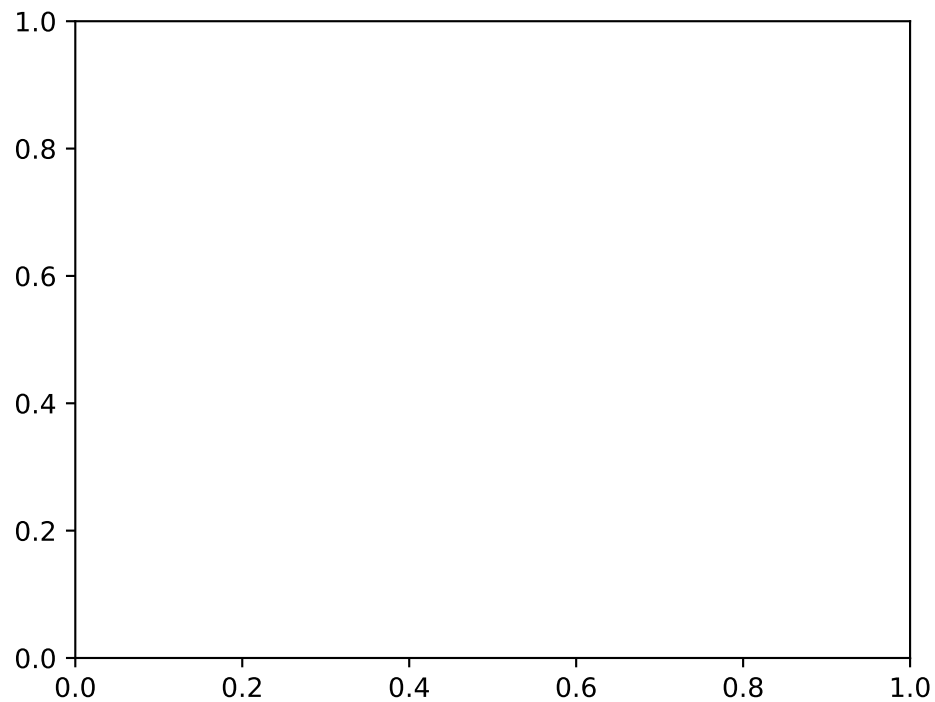
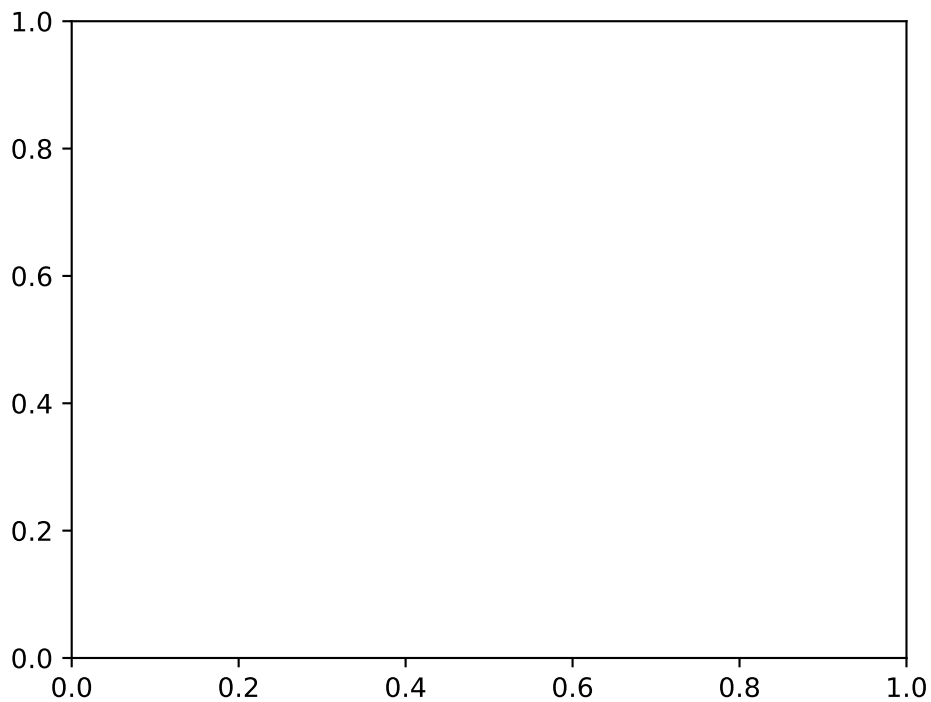
ENDPOINT:109 APA:2 AFE:4 Config\_CH:34 CH\_daq:42 SiPM:HPK --> MISSING FILE



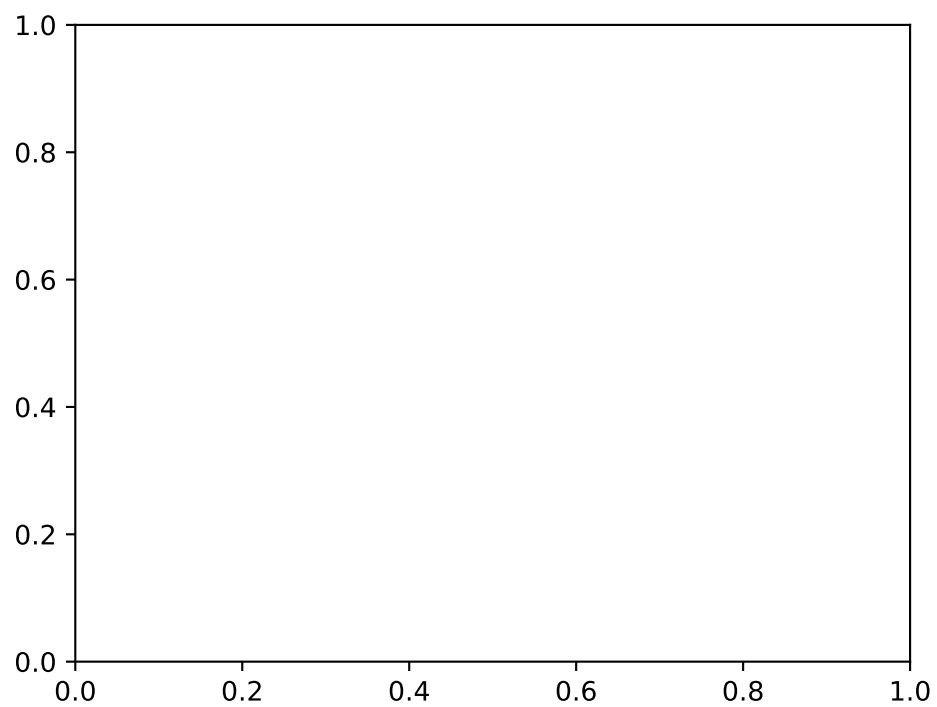
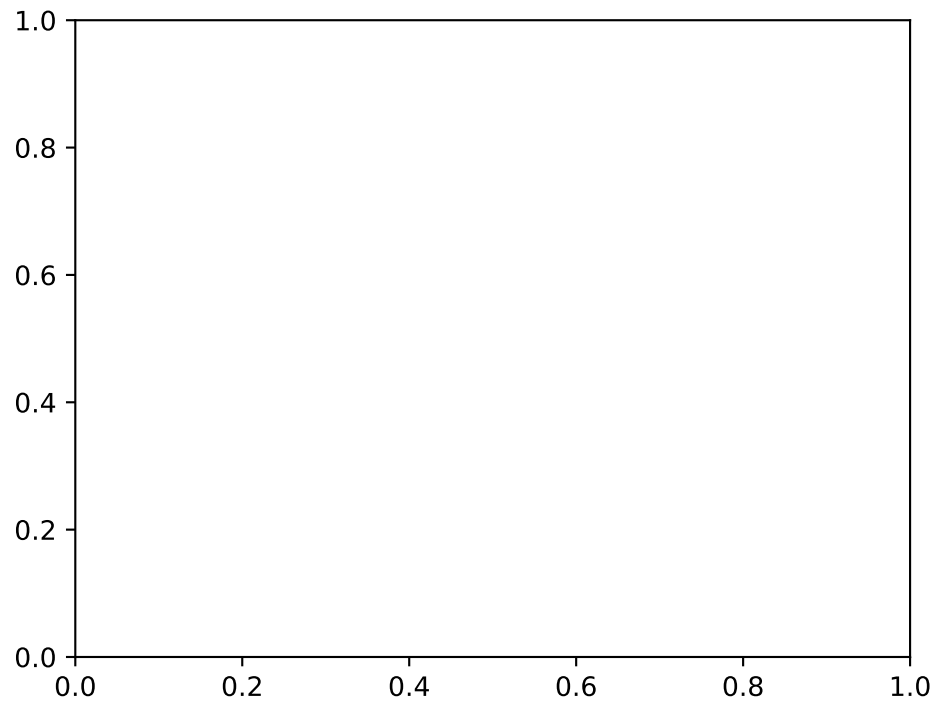
ENDPOINT:109 APA:2 AFE:4 Config\_CH:35 CH\_daq:43 SiPM:HPK --> MISSING FILE



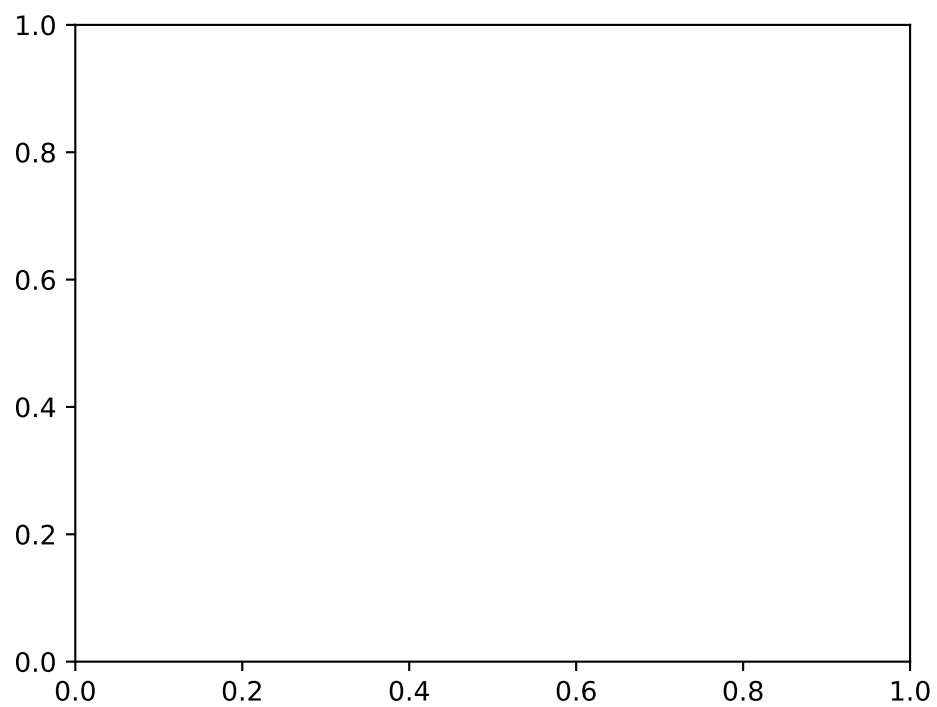
ENDPOINT:109 APA:2 AFE:4 Config\_CH:36 CH\_daq:44 SiPM:HPK --> MISSING FILE



ENDPOINT:109 APA:2 AFE:4 Config\_CH:37 CH\_daq:45 SiPM:HPK --> MISSING FILE



ENDPOINT:109 APA:2 AFE:4 Config\_CH:38 CH\_daq:46 SiPM:HPK --> MISSING FILE



ENDPOINT:109 APA:2 AFE:4 Config\_CH:39 CH\_daq:47 SiPM:HPK --> MISSING FILE

