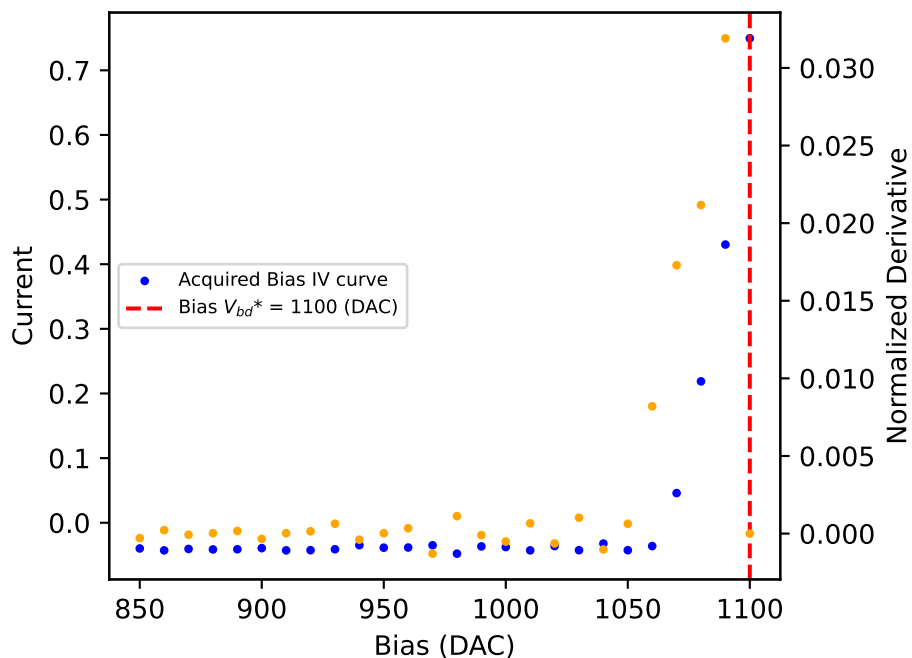
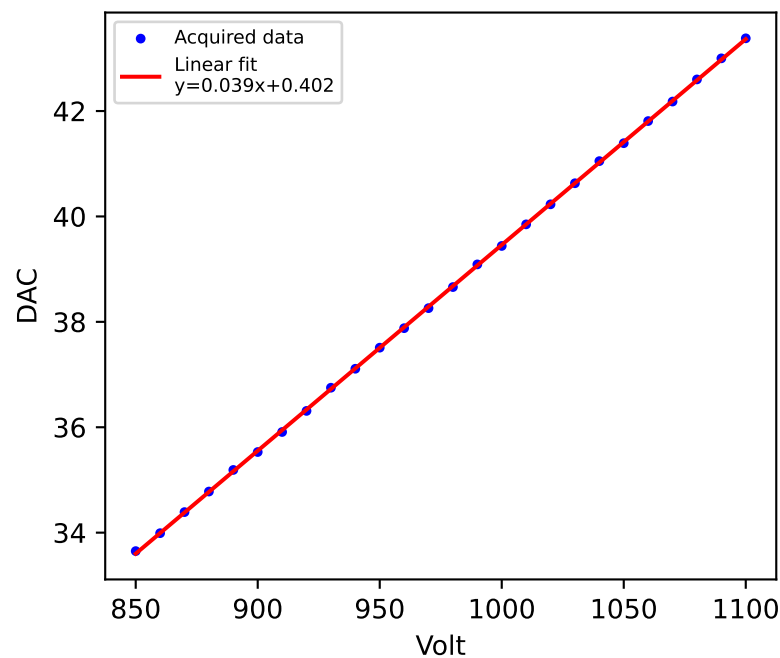


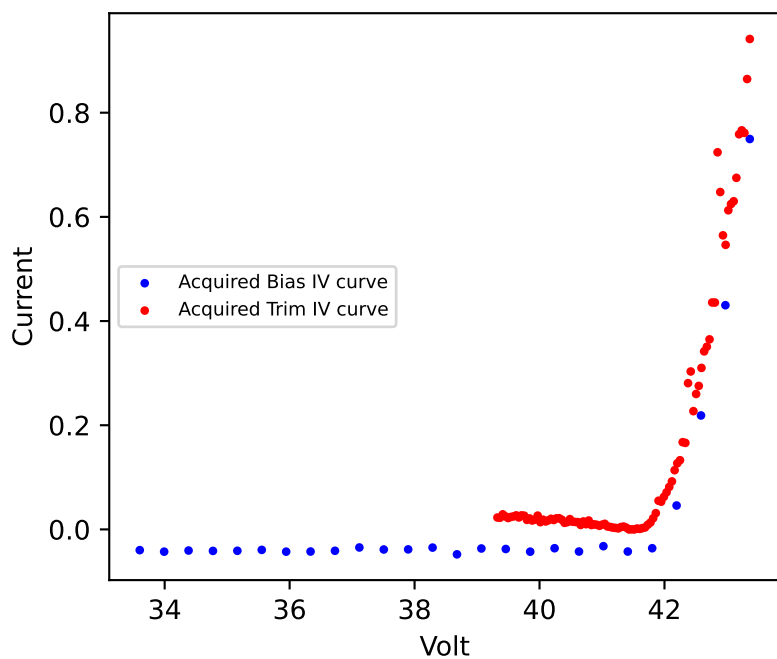
Bias REV IV curve



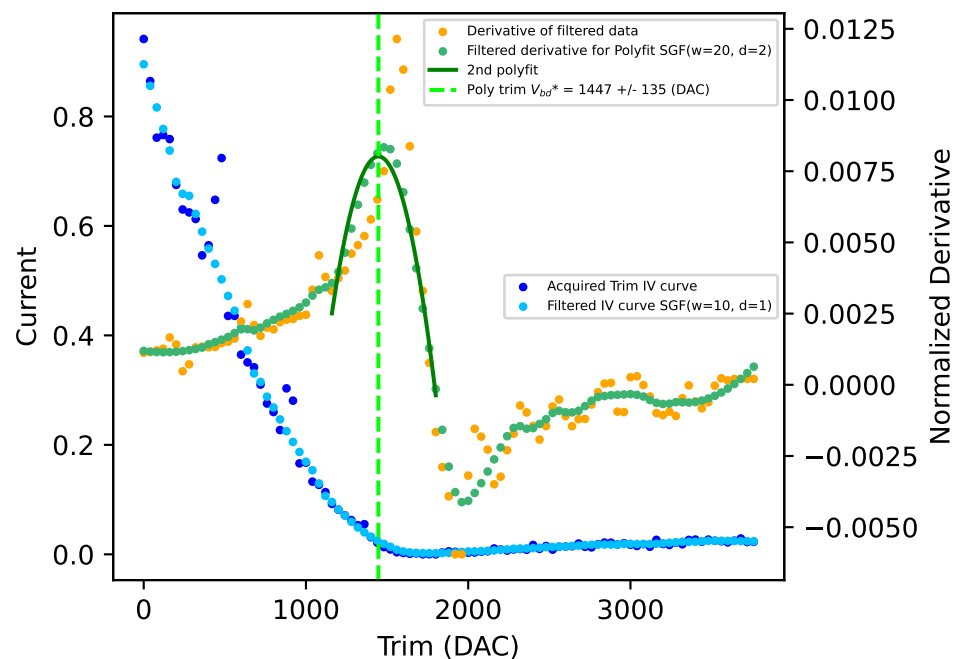
Bias DAC-VOLT conversion



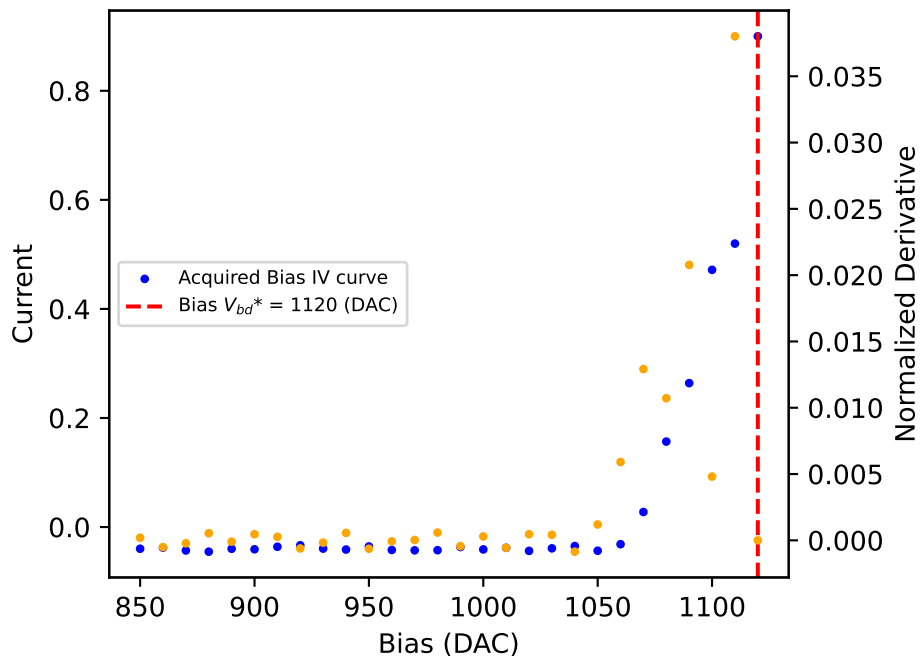
Bias + Trim REV IV curve



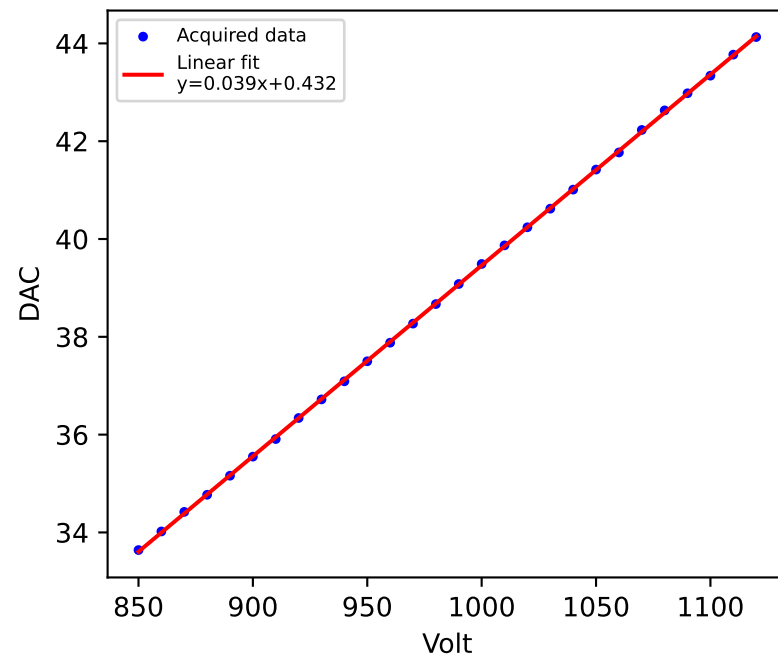
Trim REV IV curve



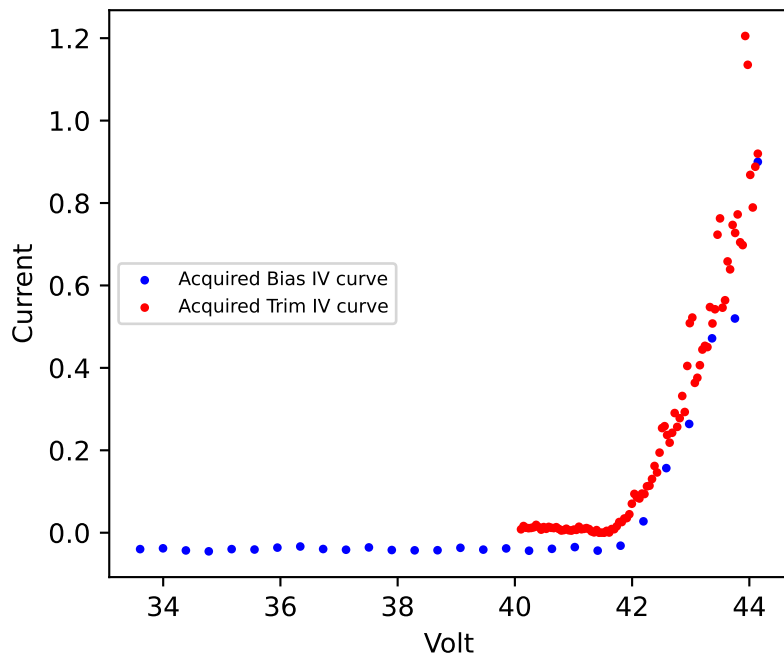
Bias REV IV curve



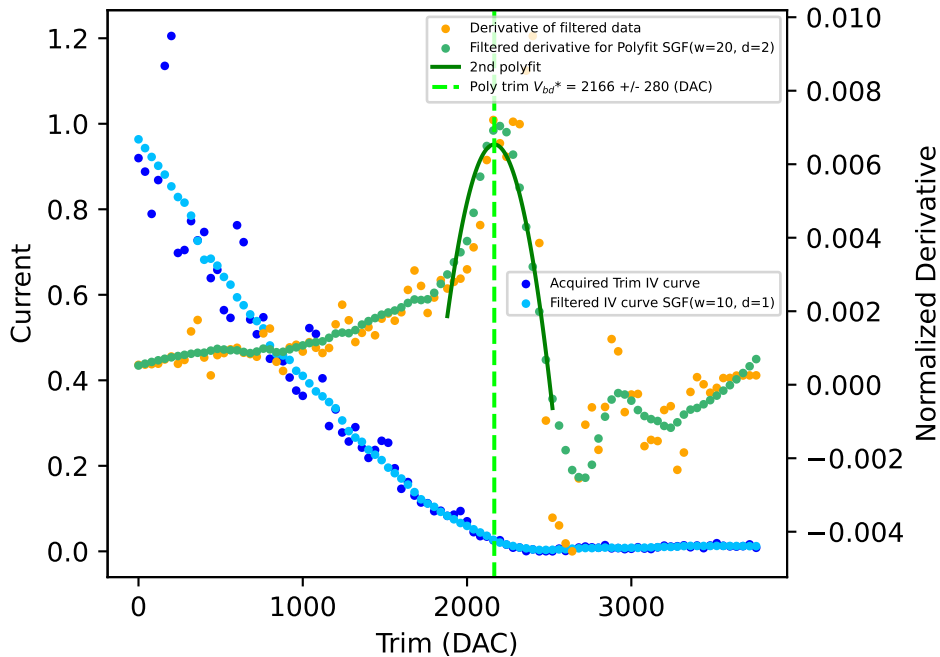
Bias DAC-VOLT conversion



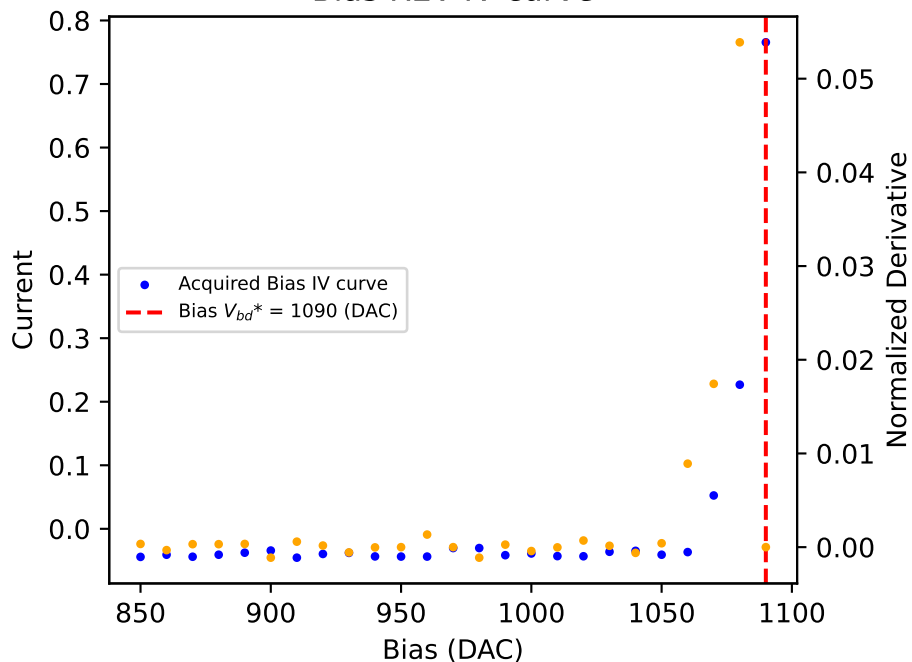
Bias + Trim REV IV curve



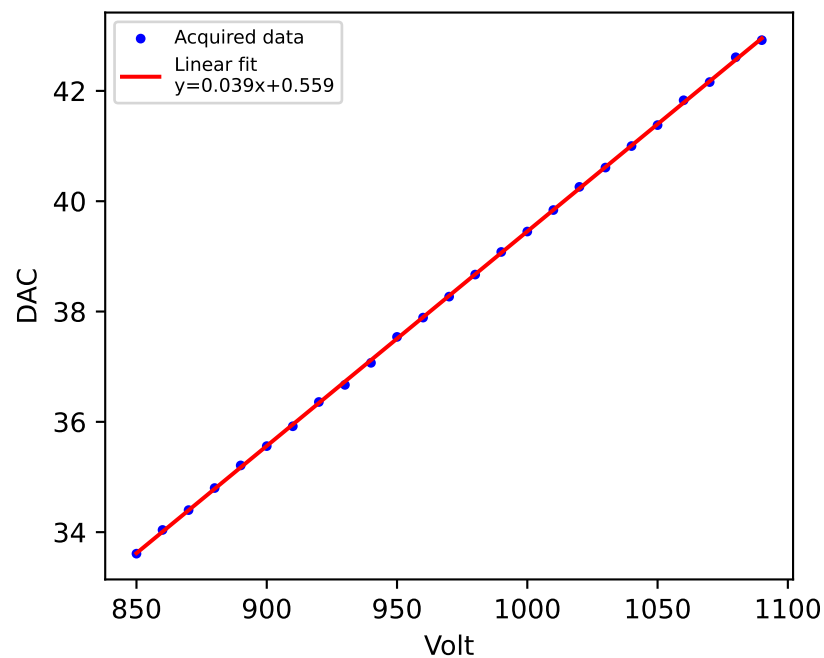
Trim REV IV curve



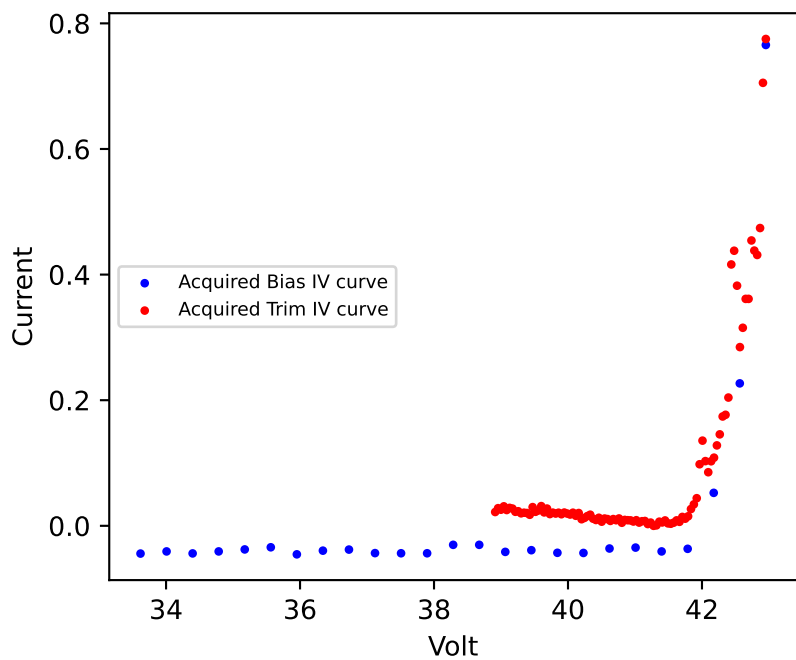
Bias REV IV curve



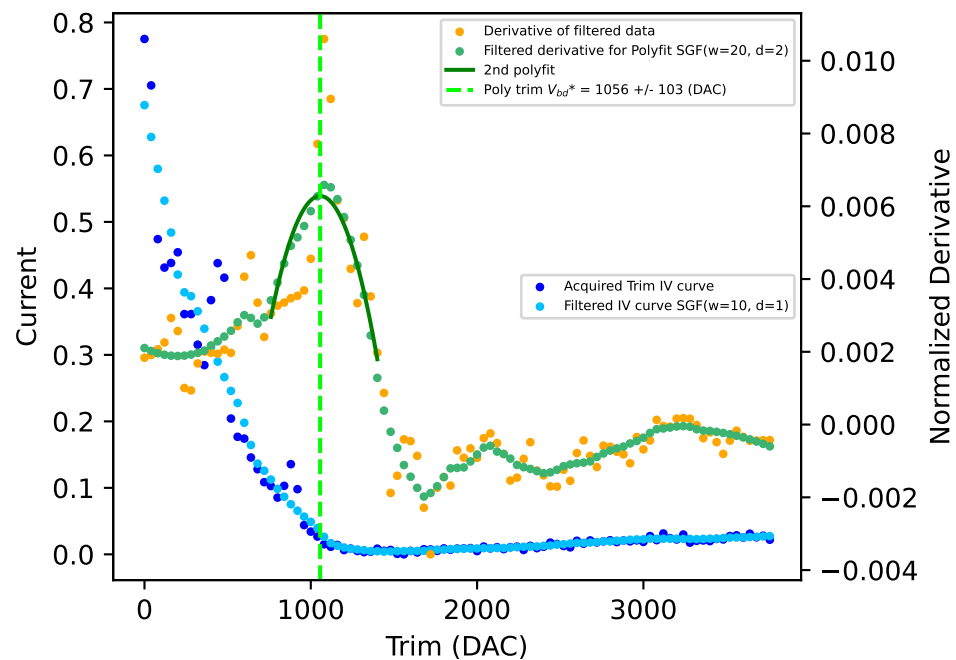
Bias DAC-VOLT conversion



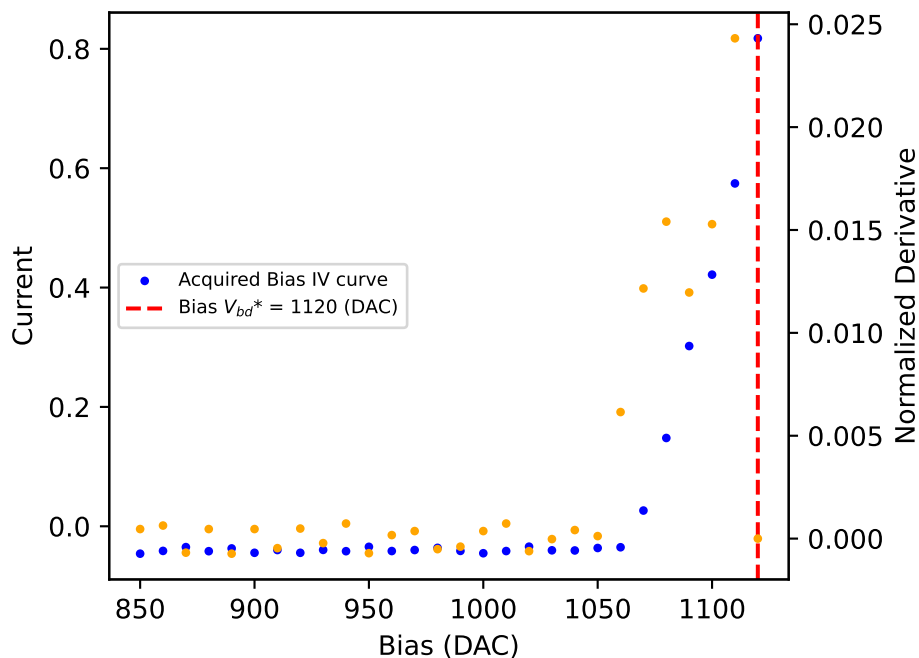
Bias + Trim REV IV curve



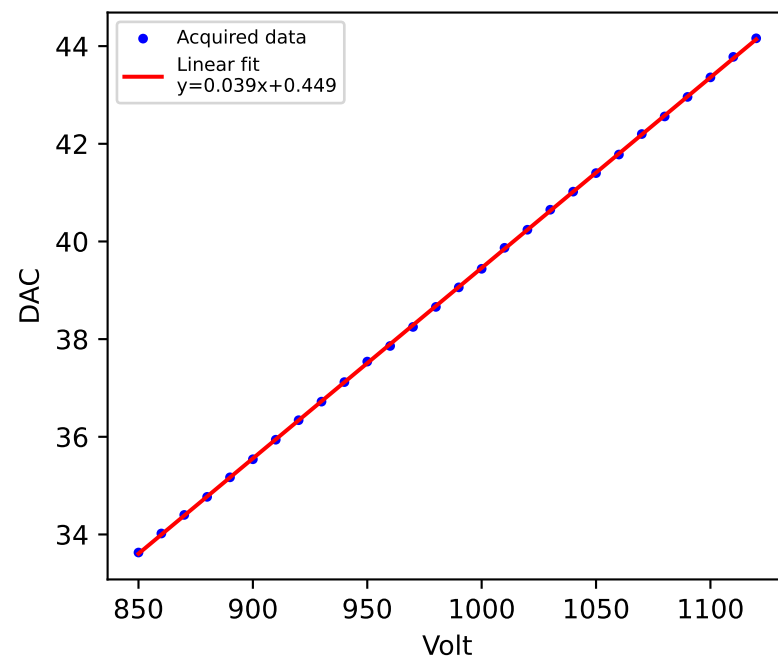
Trim REV IV curve



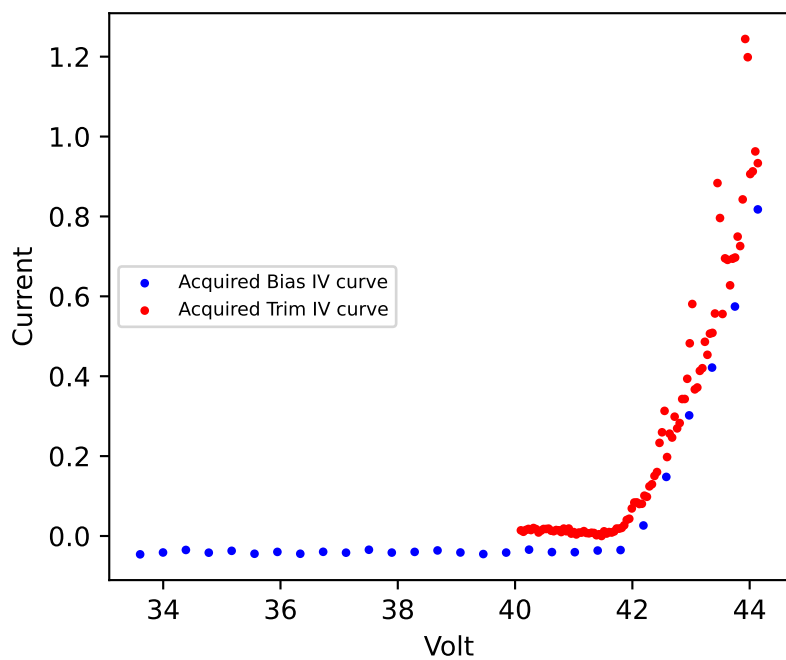
Bias REV IV curve



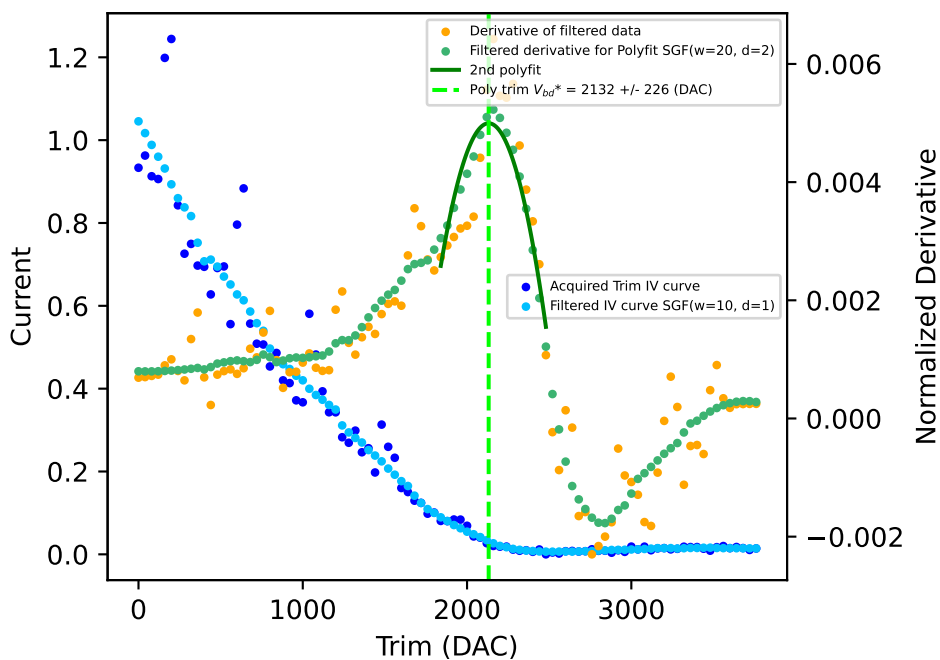
Bias DAC-VOLT conversion



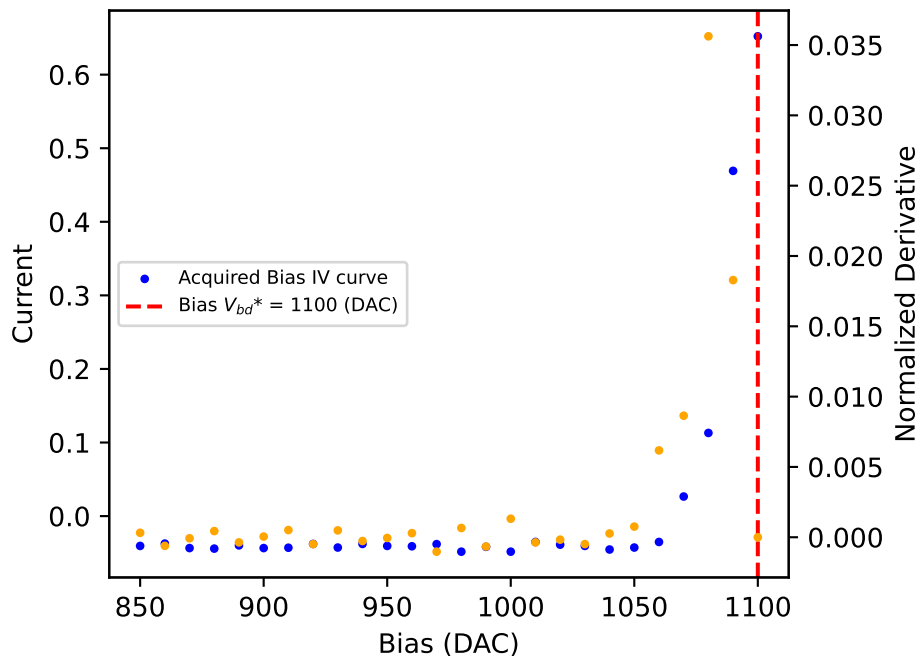
Bias + Trim REV IV curve



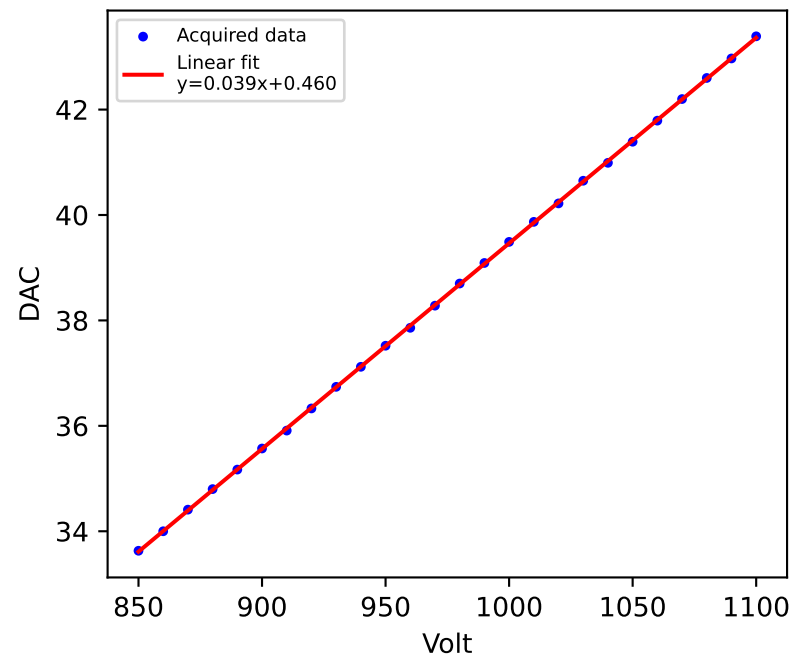
Trim REV IV curve



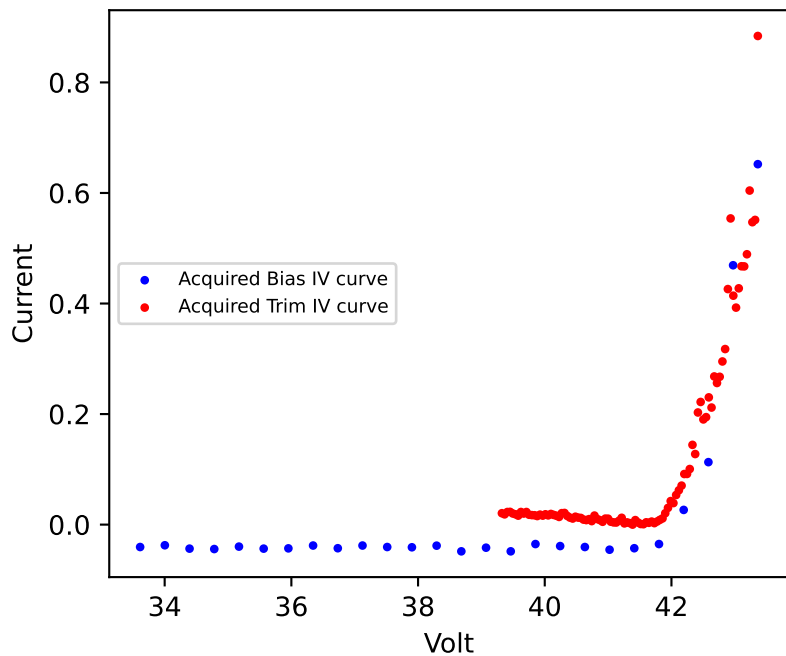
Bias REV IV curve



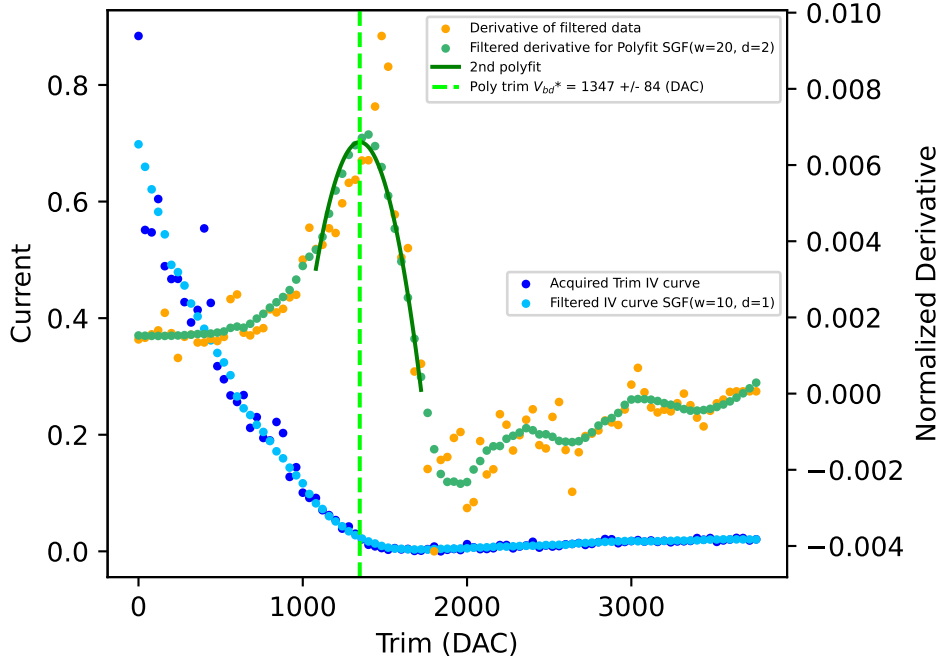
Bias DAC-VOLT conversion



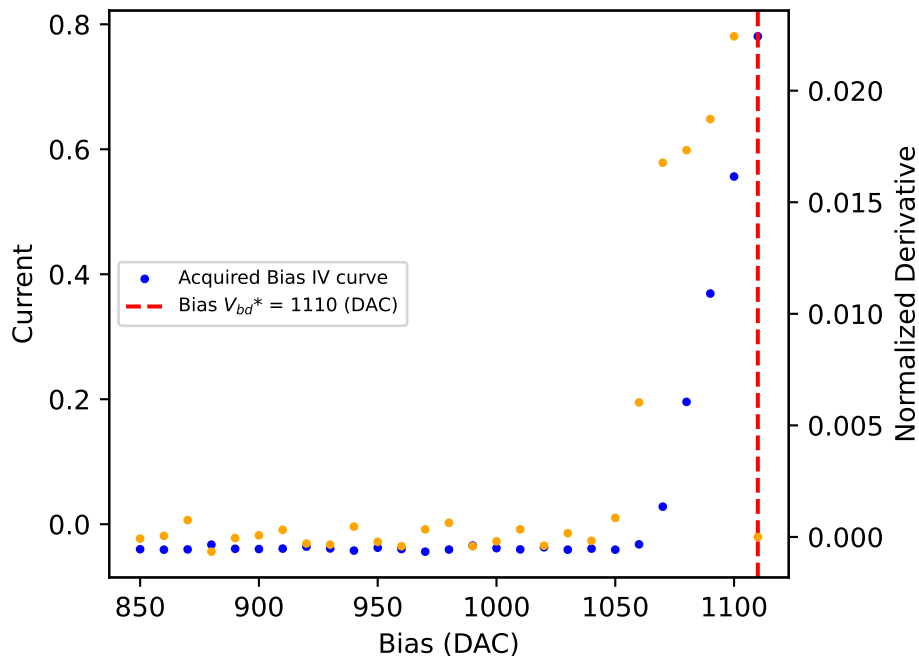
Bias + Trim REV IV curve



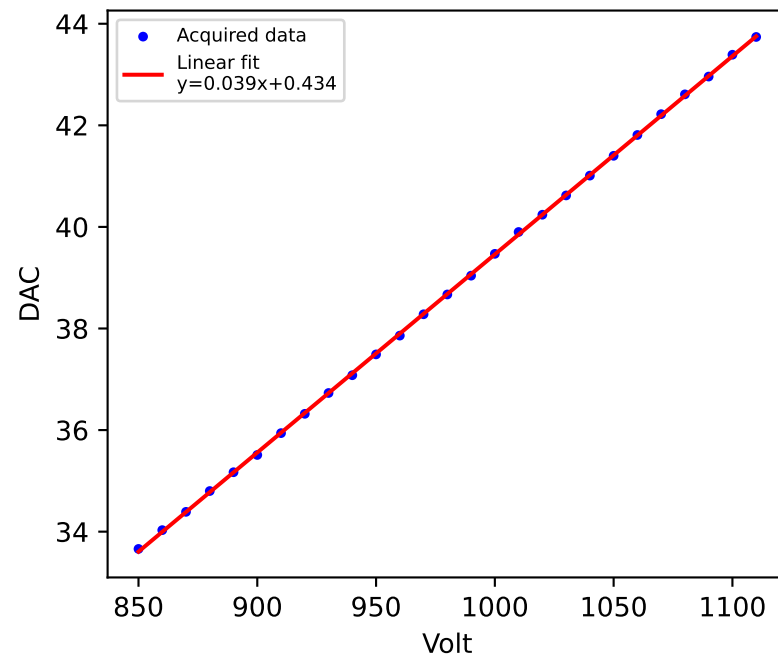
Trim REV IV curve



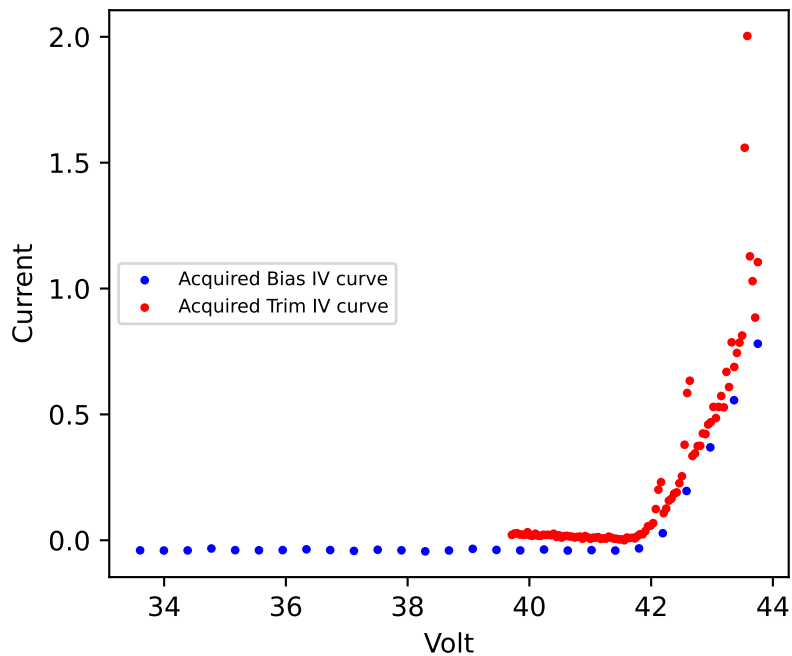
Bias REV IV curve



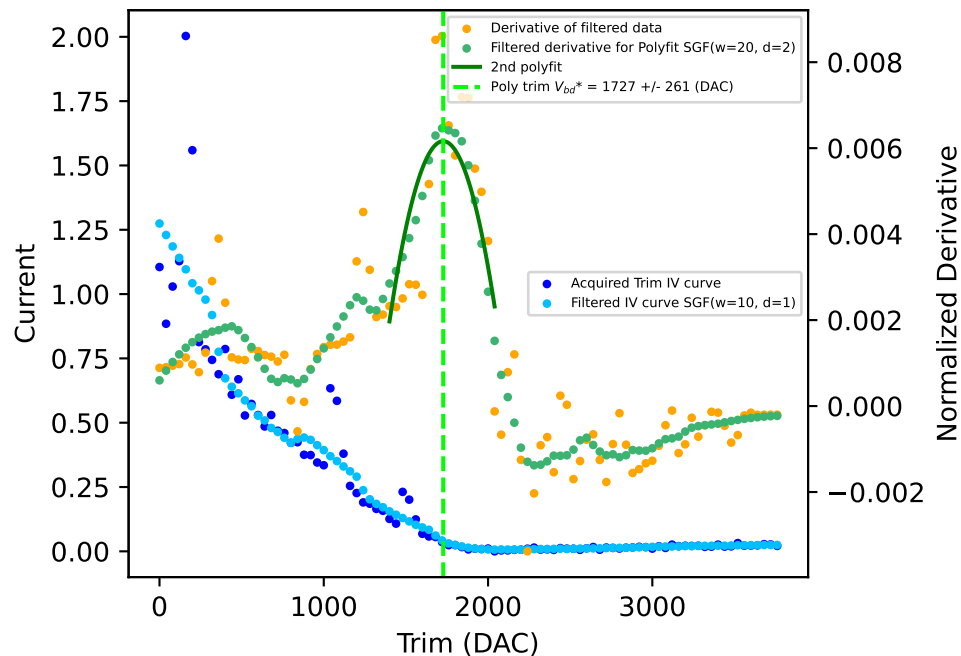
Bias DAC-VOLT conversion



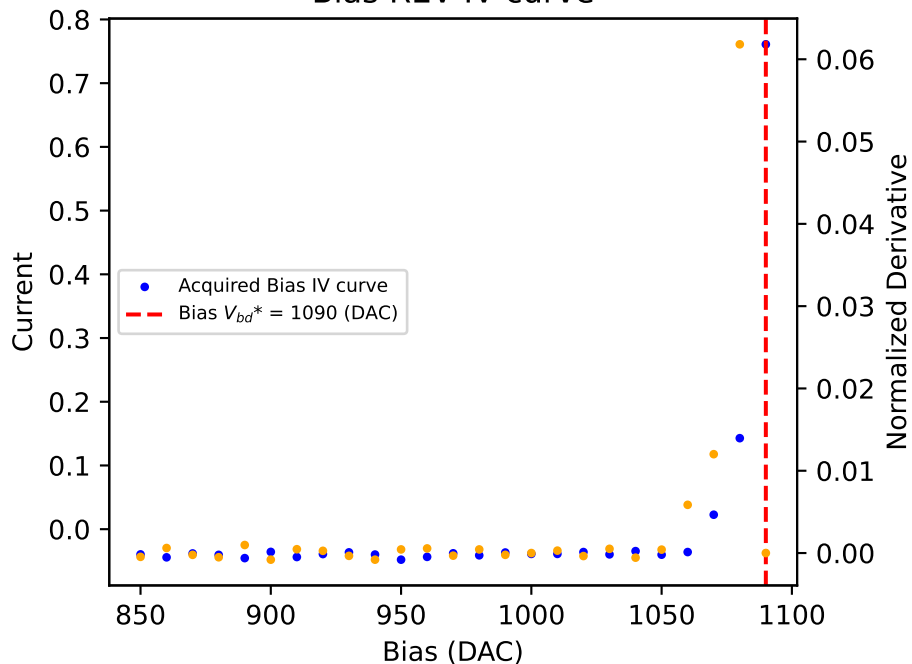
Bias + Trim REV IV curve



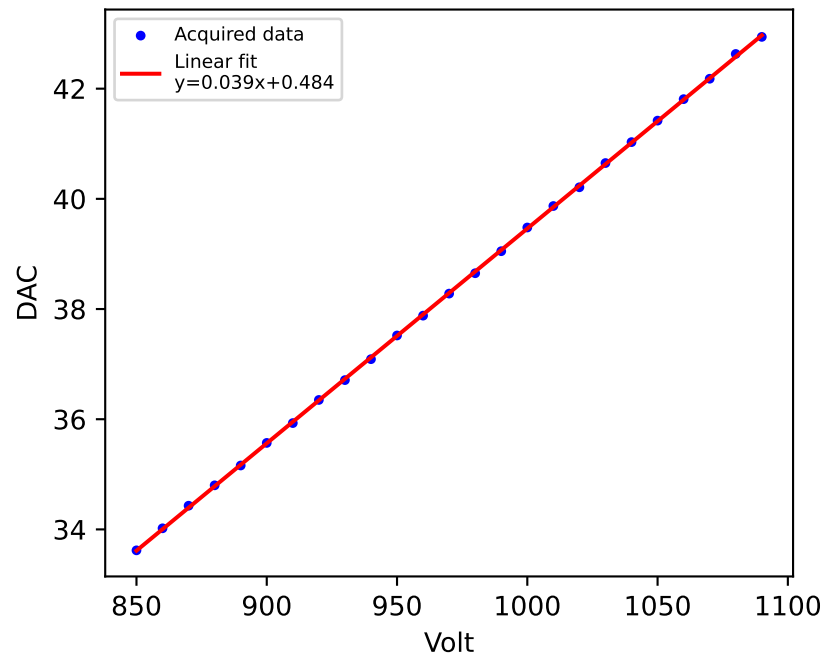
Trim REV IV curve



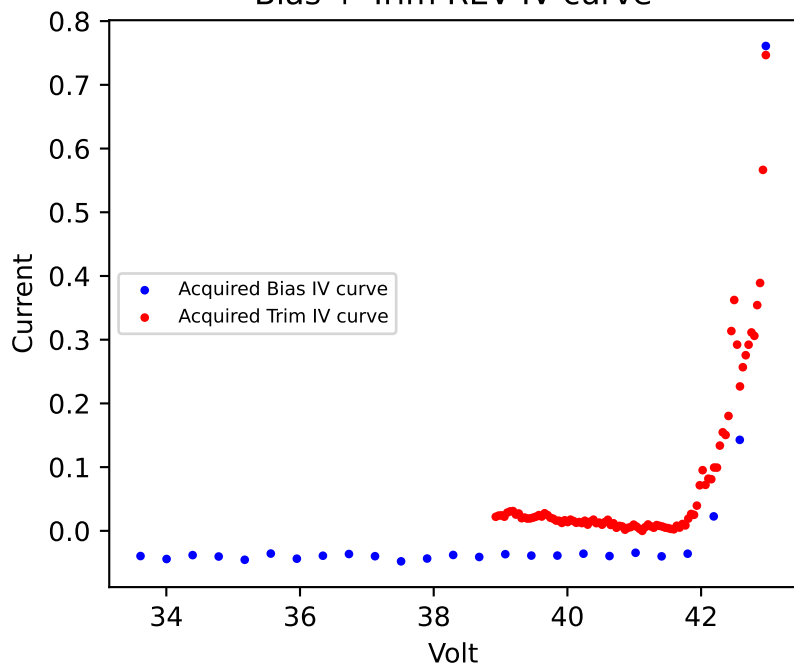
Bias REV IV curve



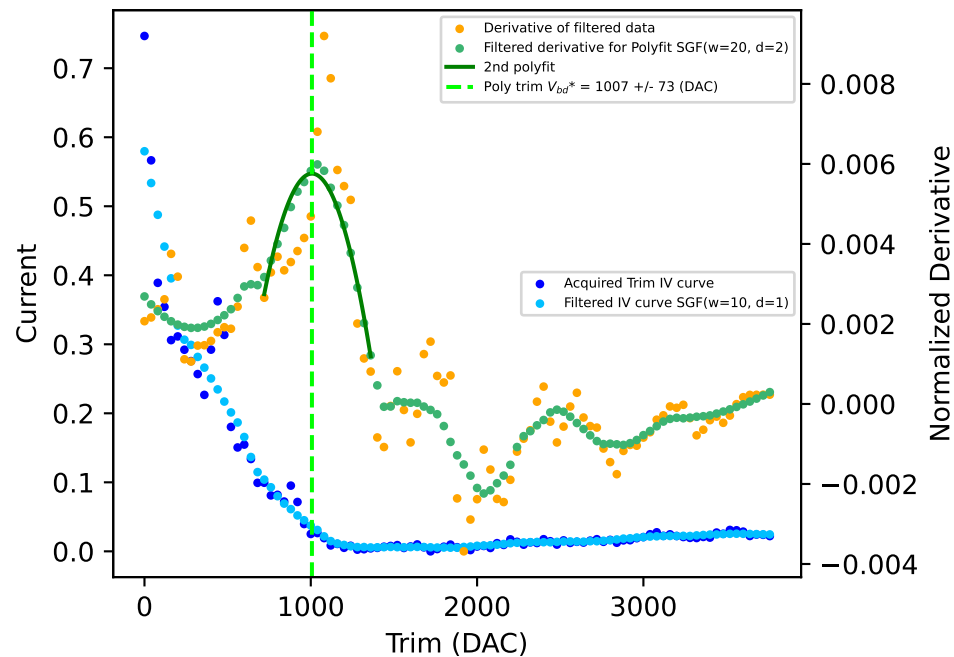
Bias DAC-VOLT conversion



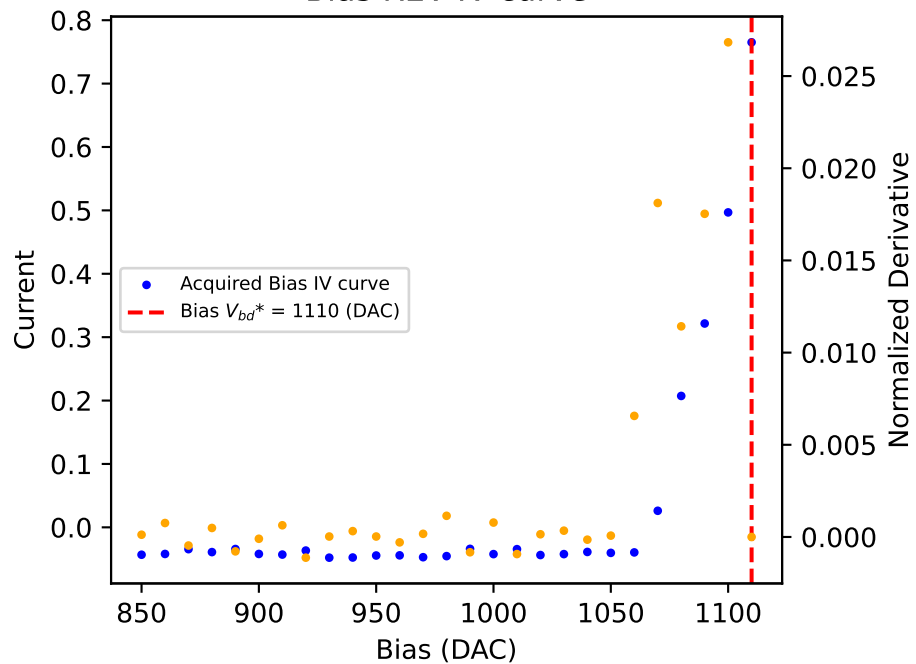
Bias + Trim REV IV curve



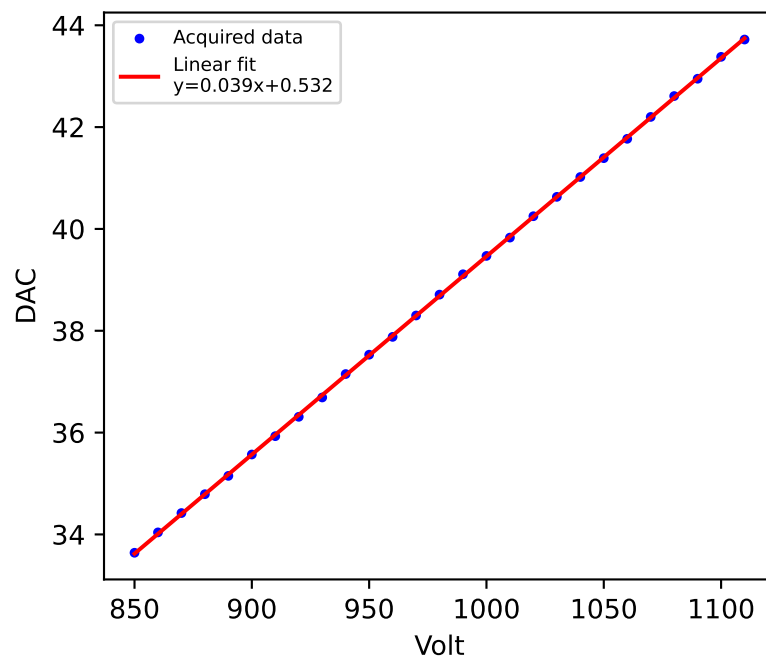
Trim REV IV curve



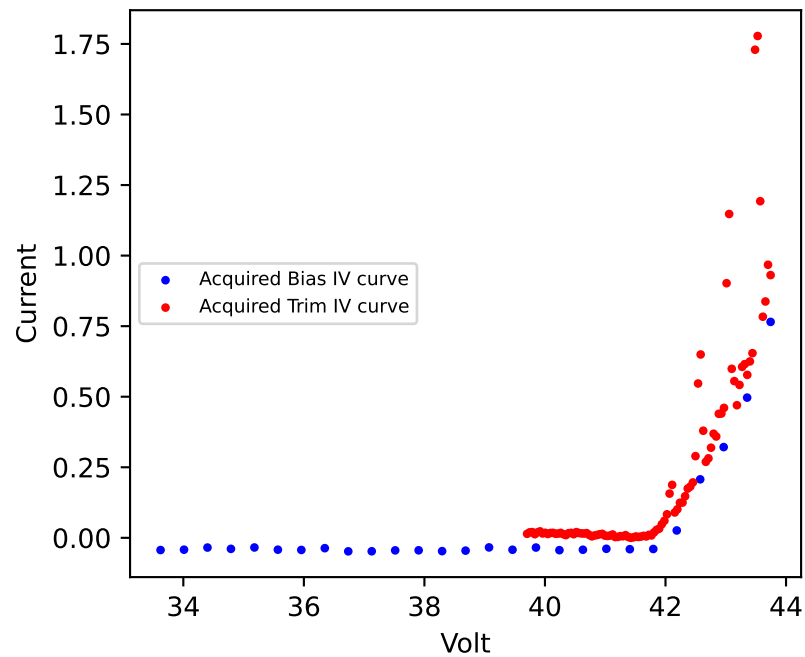
Bias REV IV curve



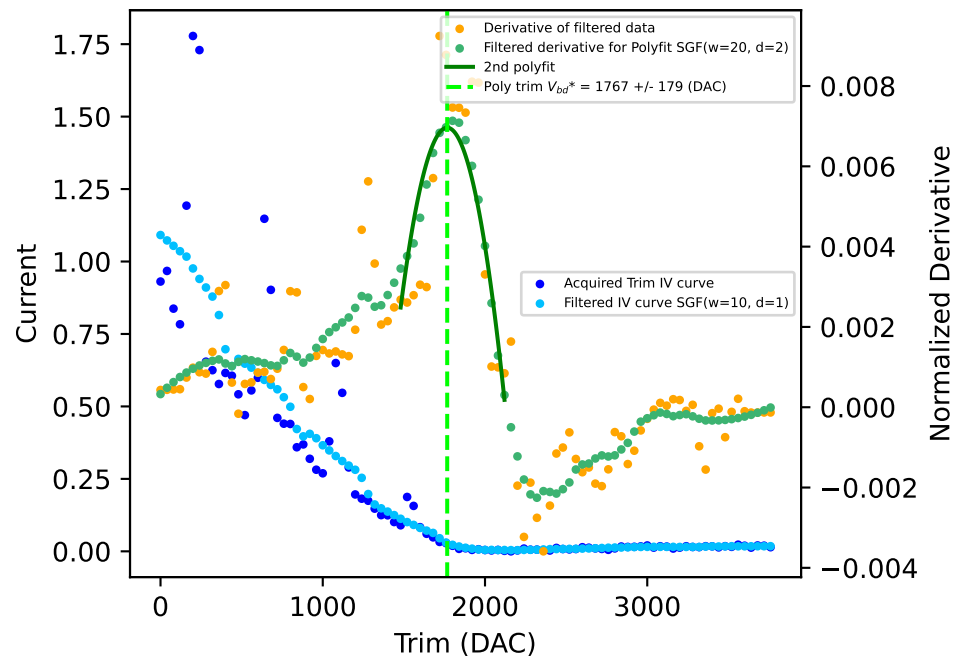
Bias DAC-VOLT conversion



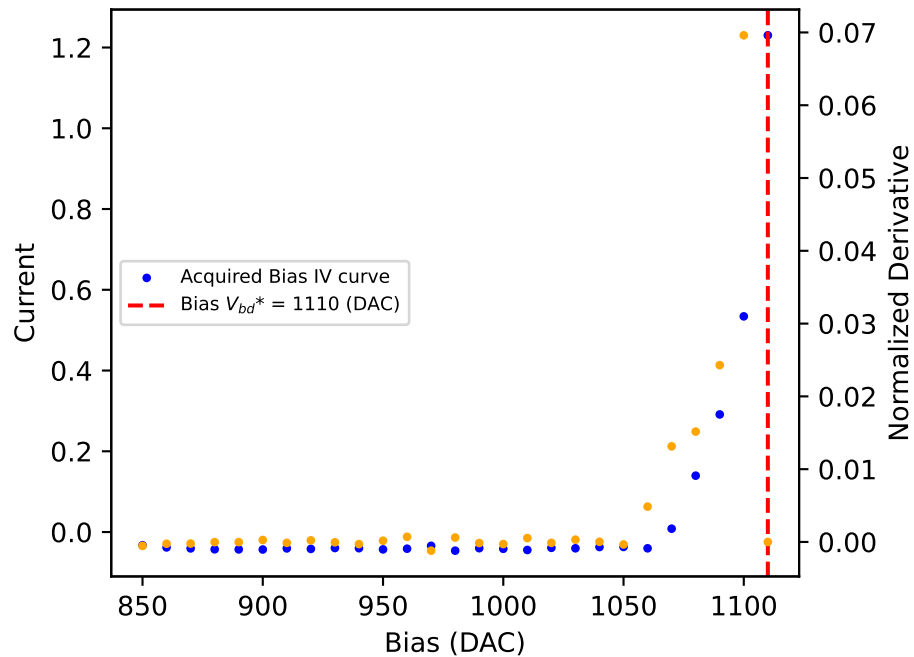
Bias + Trim REV IV curve



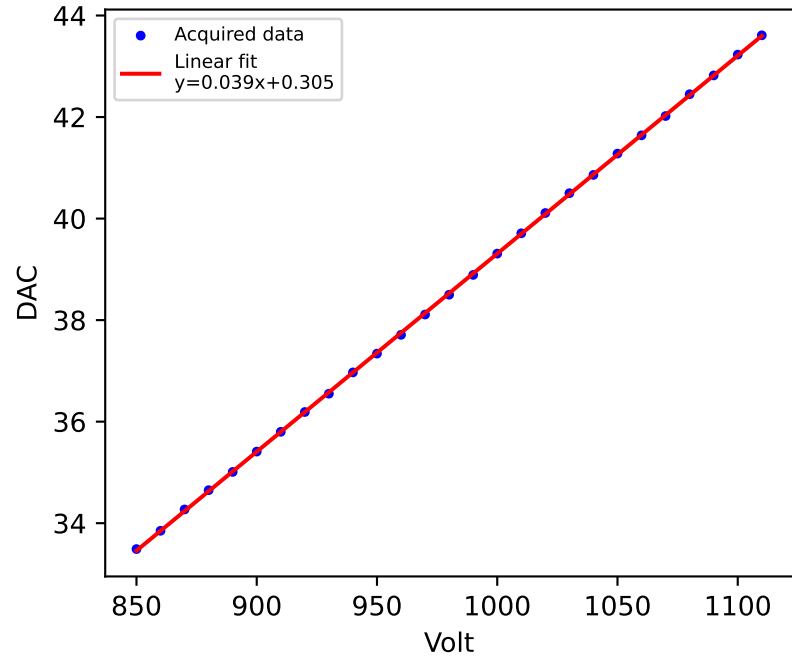
Trim REV IV curve



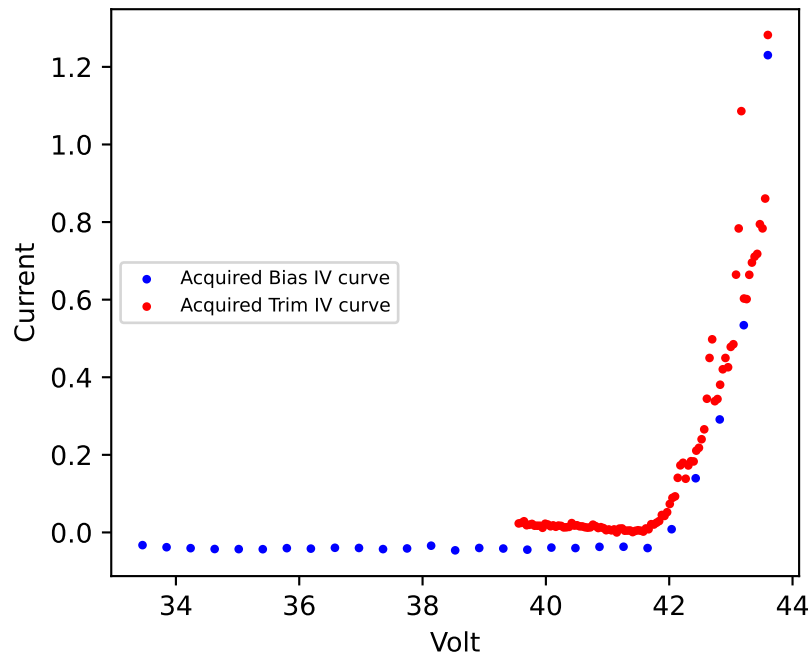
Bias REV IV curve



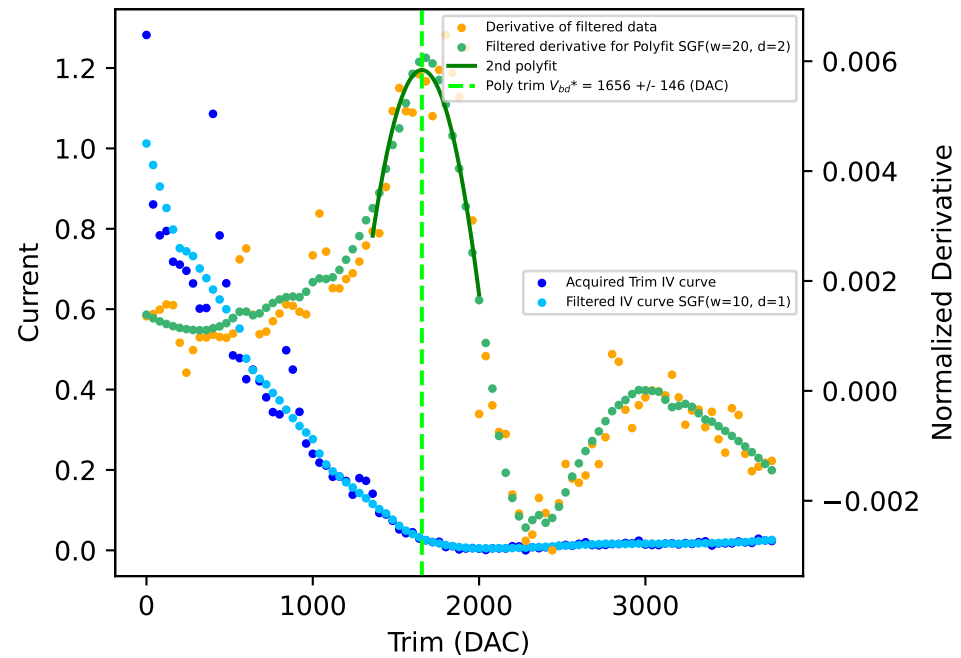
Bias DAC-VOLT conversion



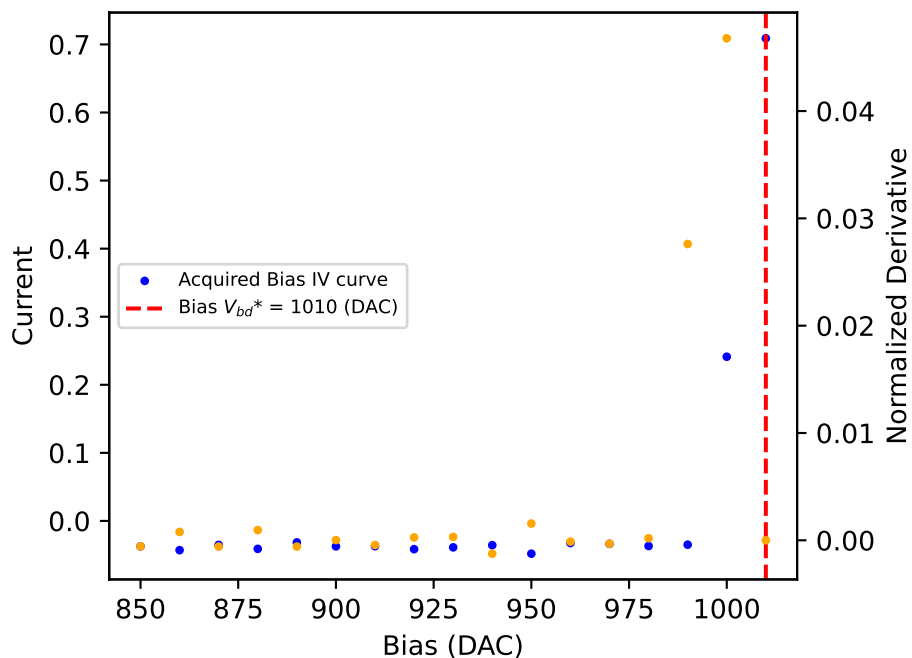
Bias + Trim REV IV curve



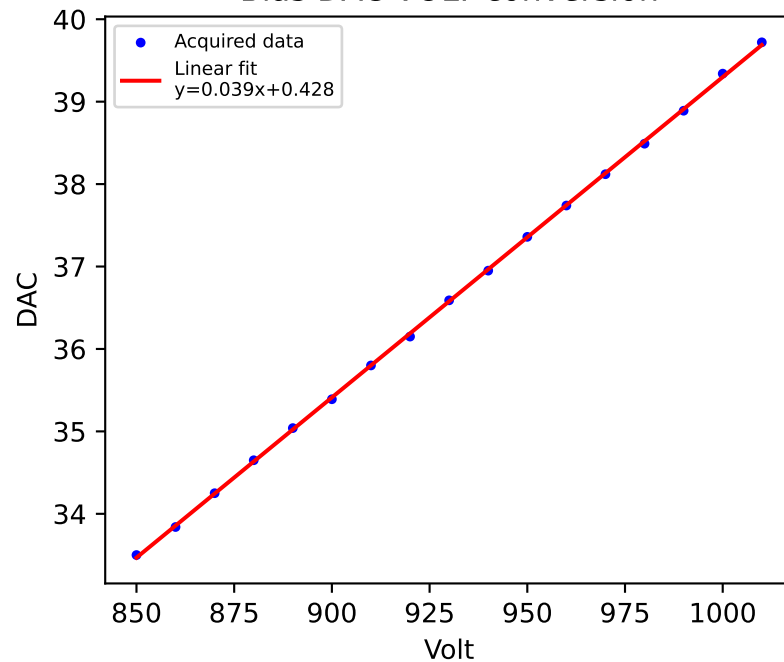
Trim REV IV curve



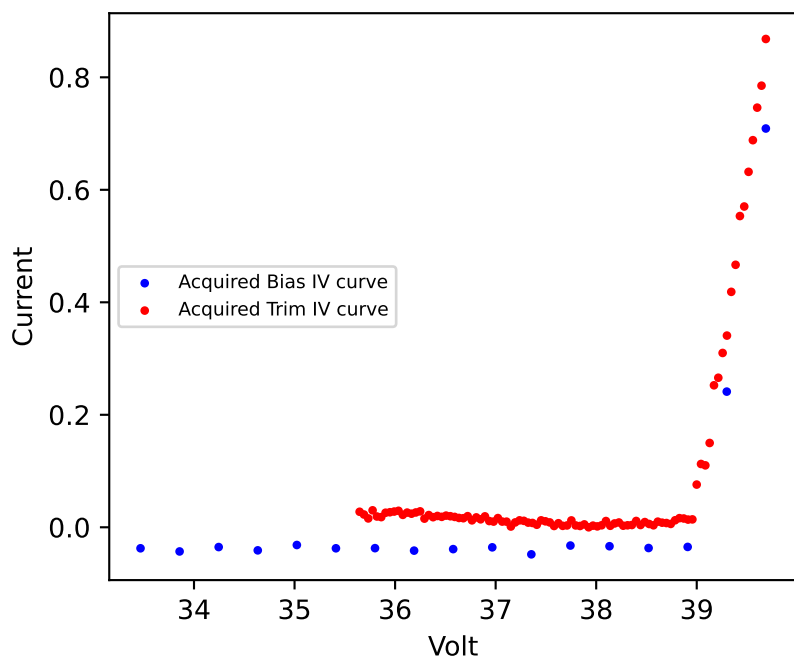
Bias REV IV curve



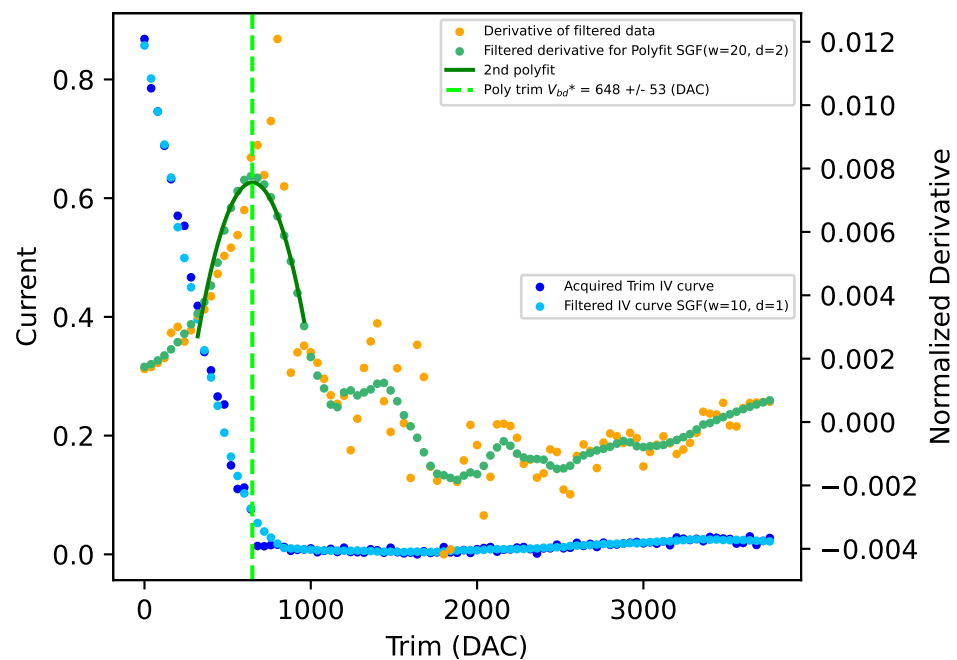
Bias DAC-VOLT conversion



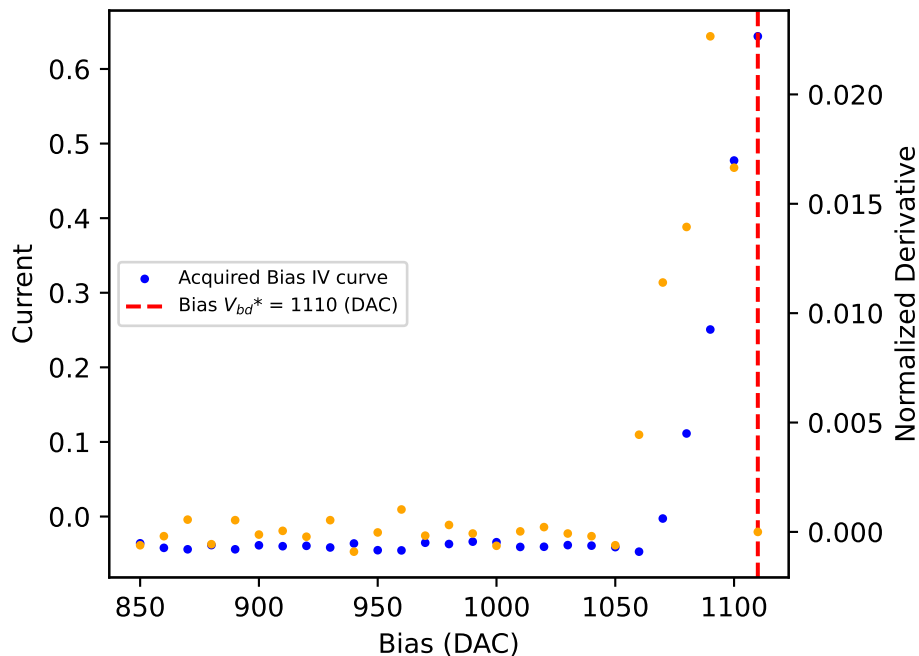
Bias + Trim REV IV curve



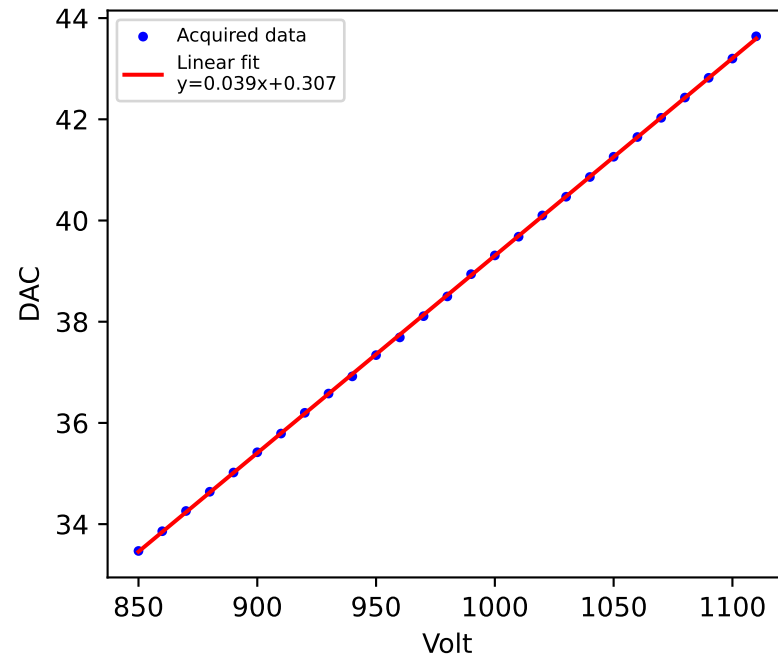
Trim REV IV curve



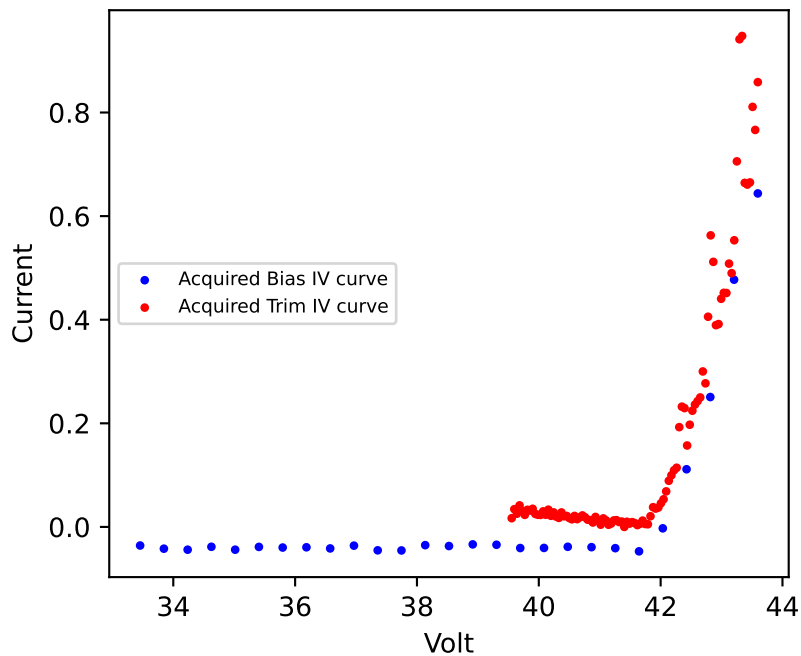
Bias REV IV curve



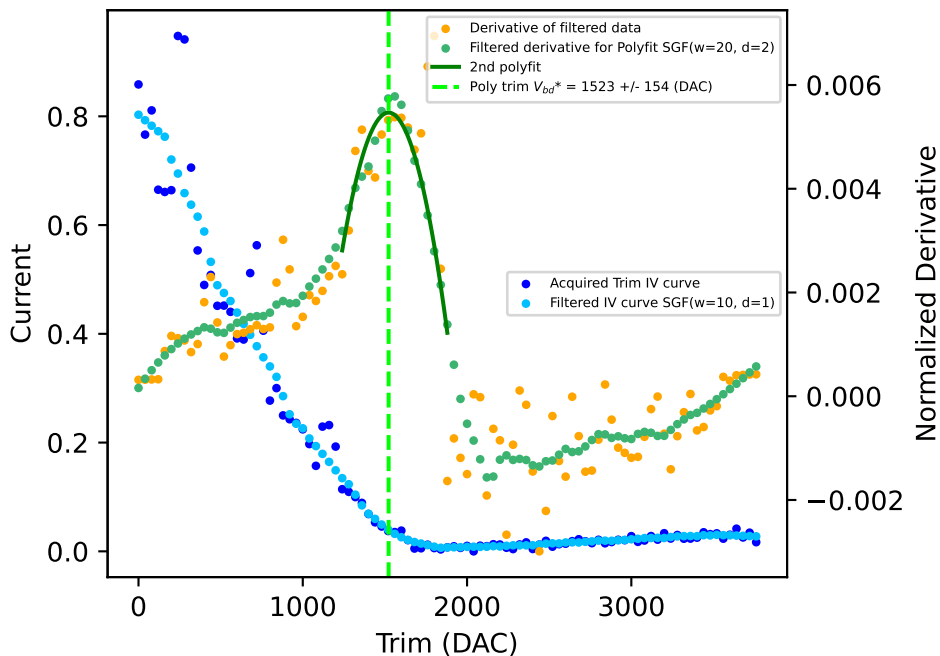
Bias DAC-VOLT conversion



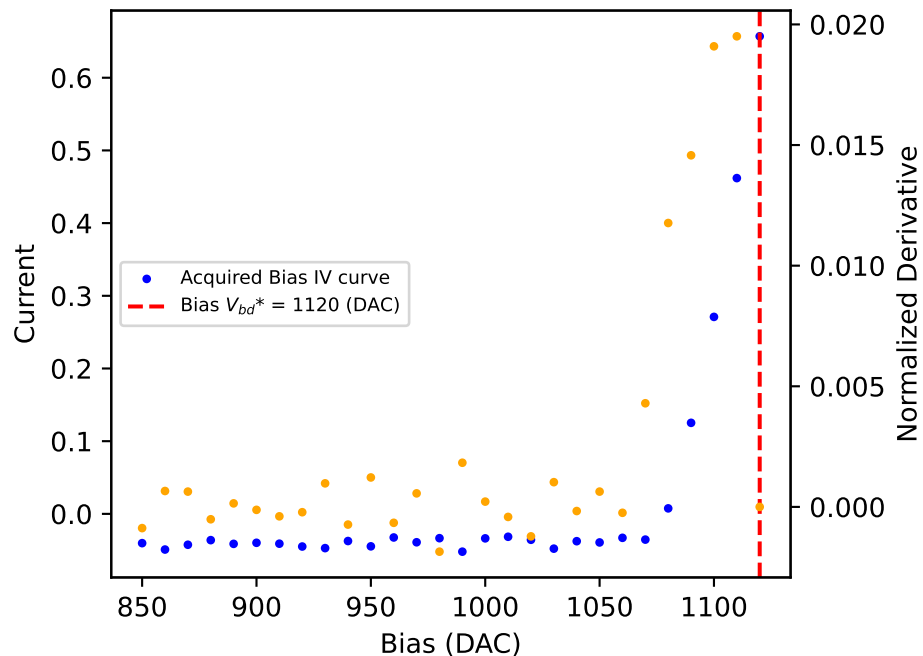
Bias + Trim REV IV curve



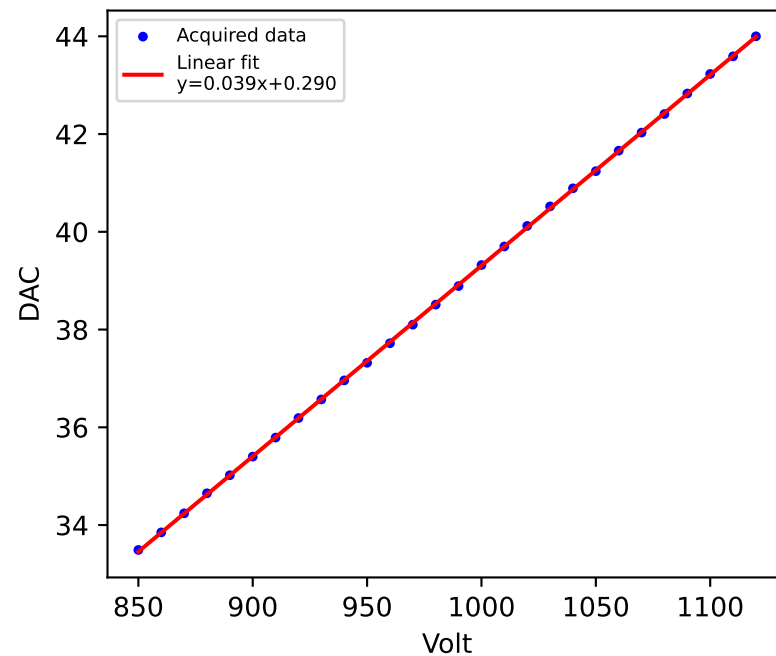
Trim REV IV curve



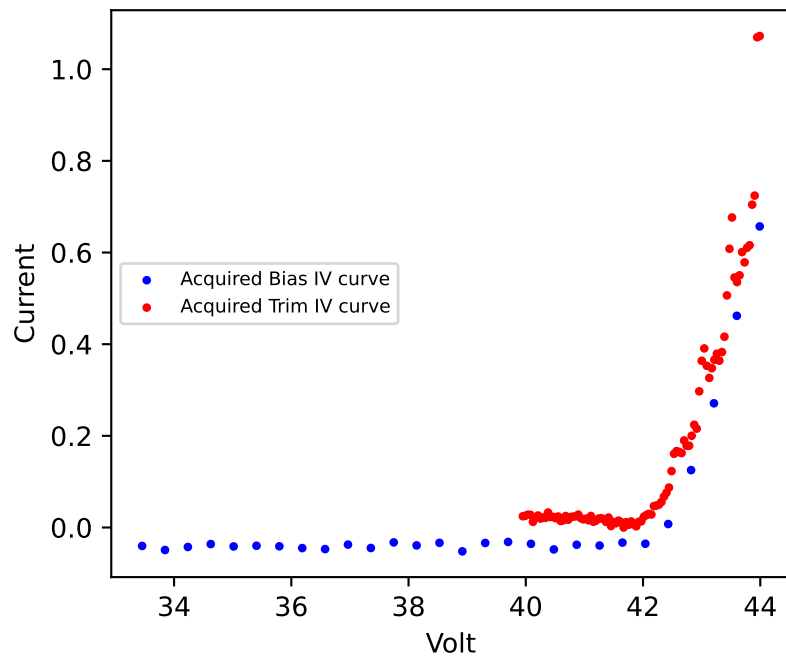
Bias REV IV curve



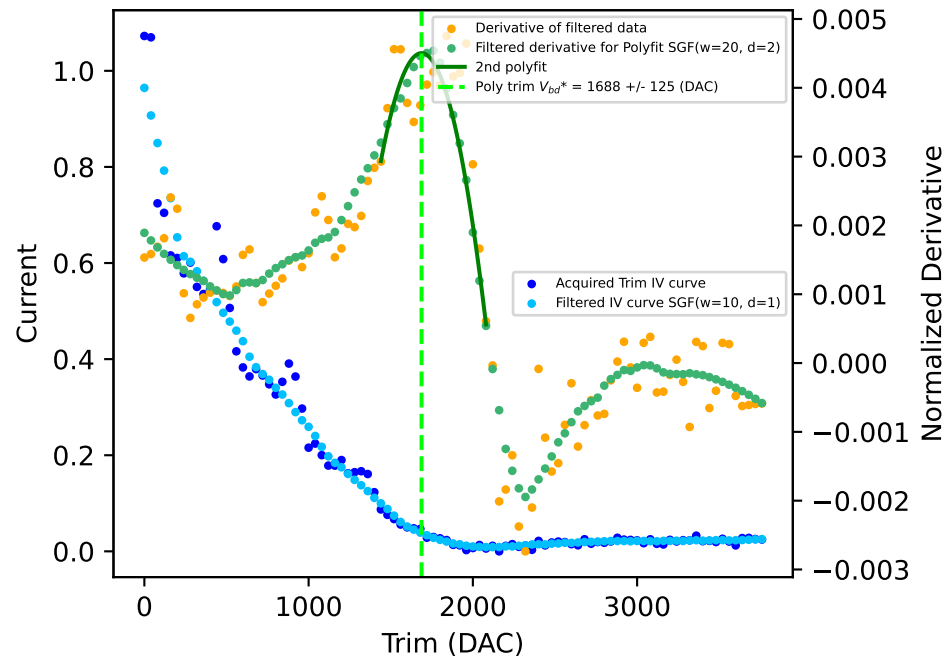
Bias DAC-VOLT conversion



Bias + Trim REV IV curve



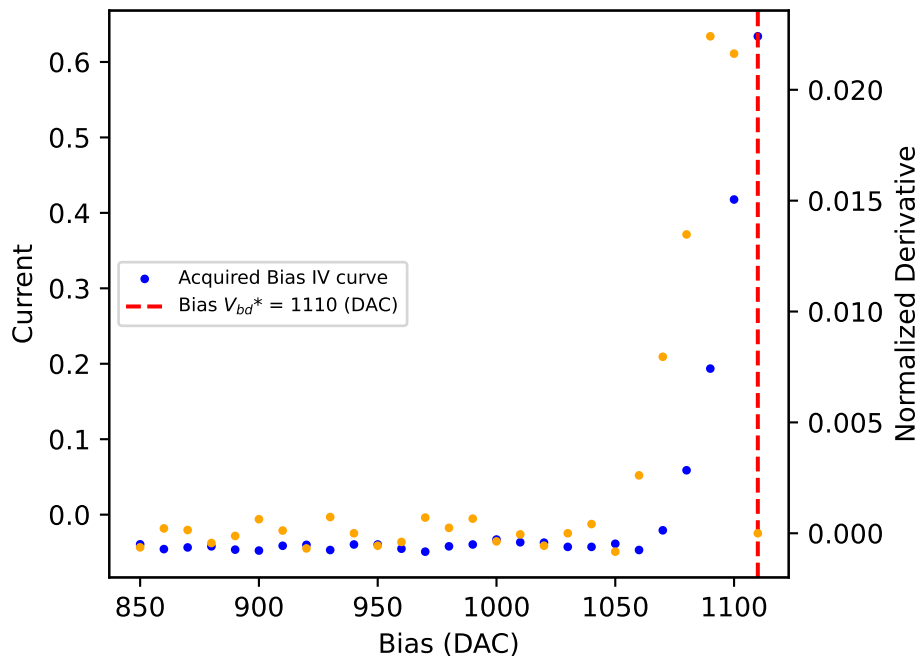
Trim REV IV curve



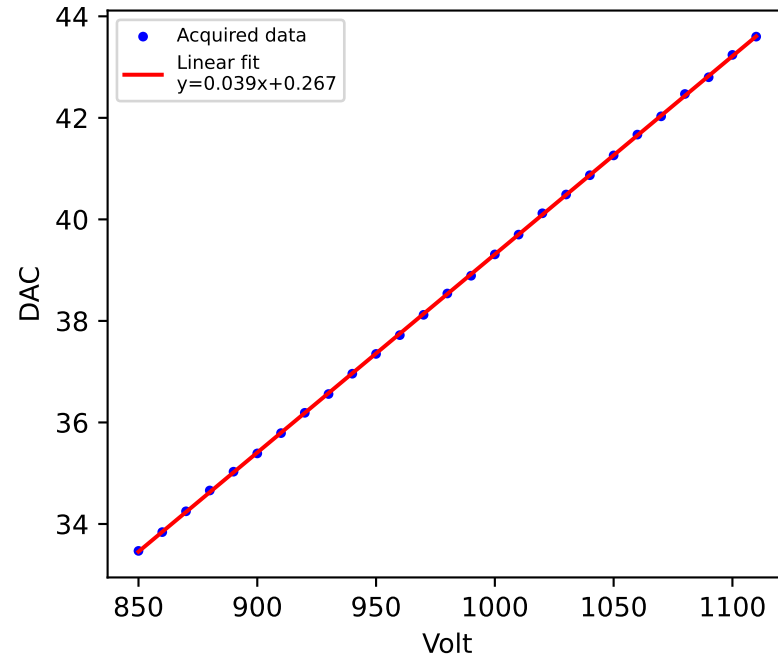
ENDPOINT:112 APA:4 AFE:1 Config_CH:12 CH_daq:14 SiPM:HPK --> MISSING FILE



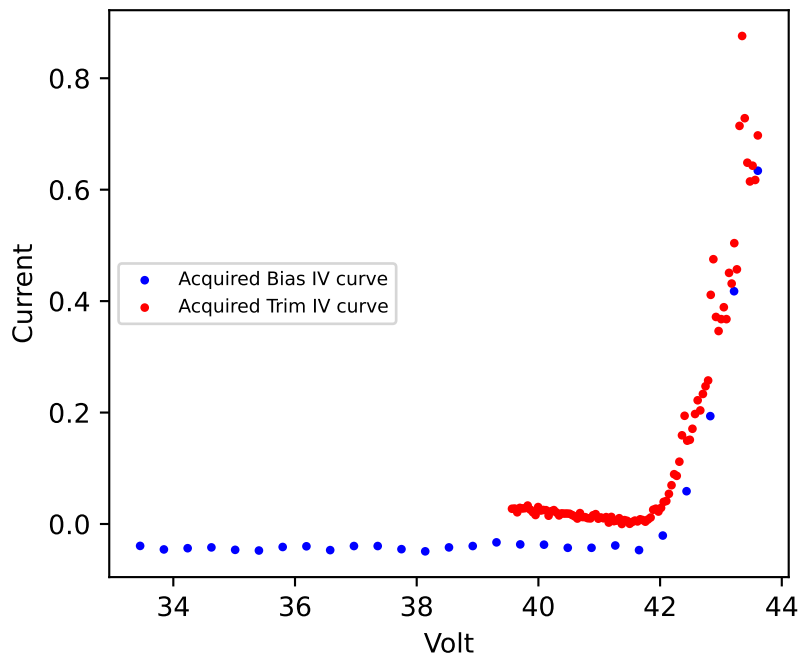
Bias REV IV curve



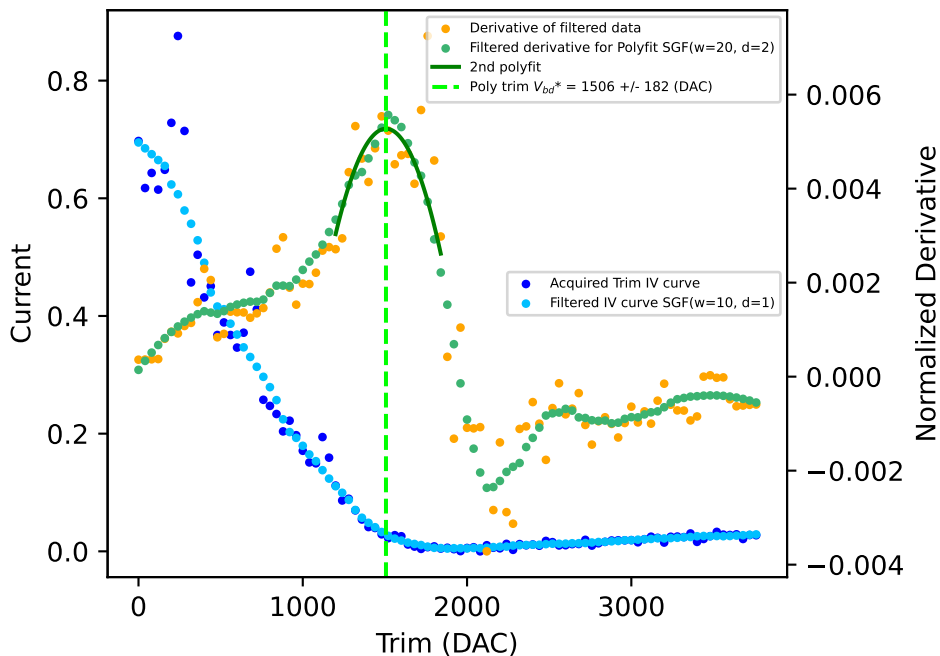
Bias DAC-VOLT conversion



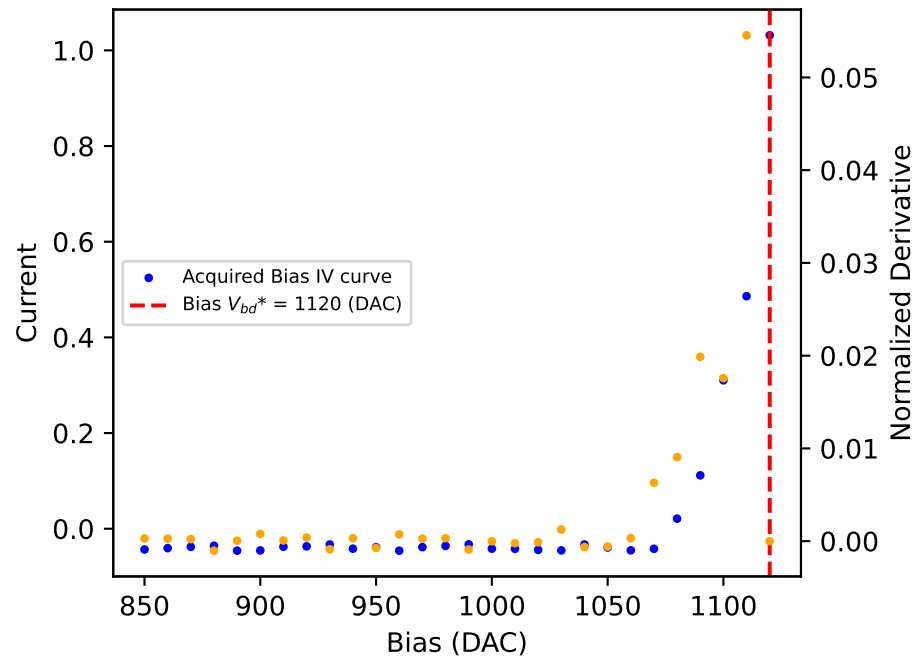
Bias + Trim REV IV curve



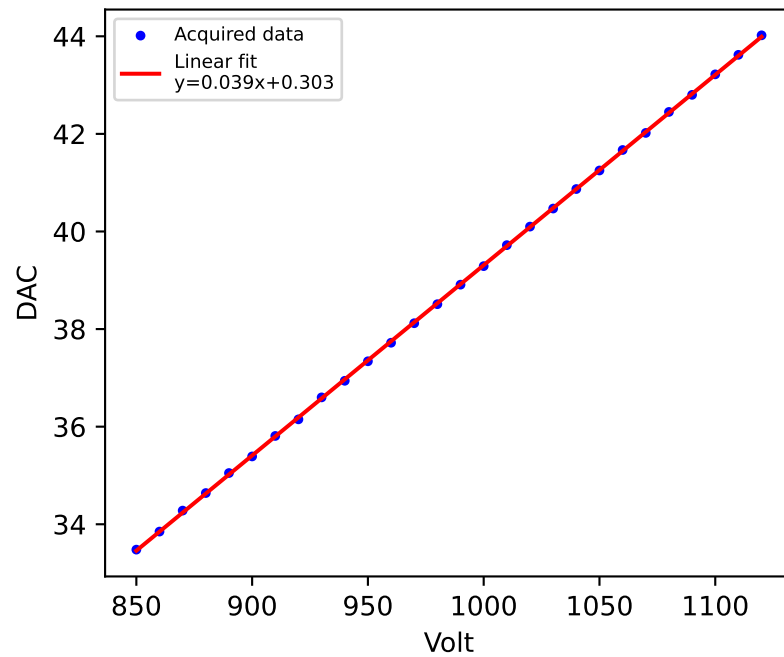
Trim REV IV curve



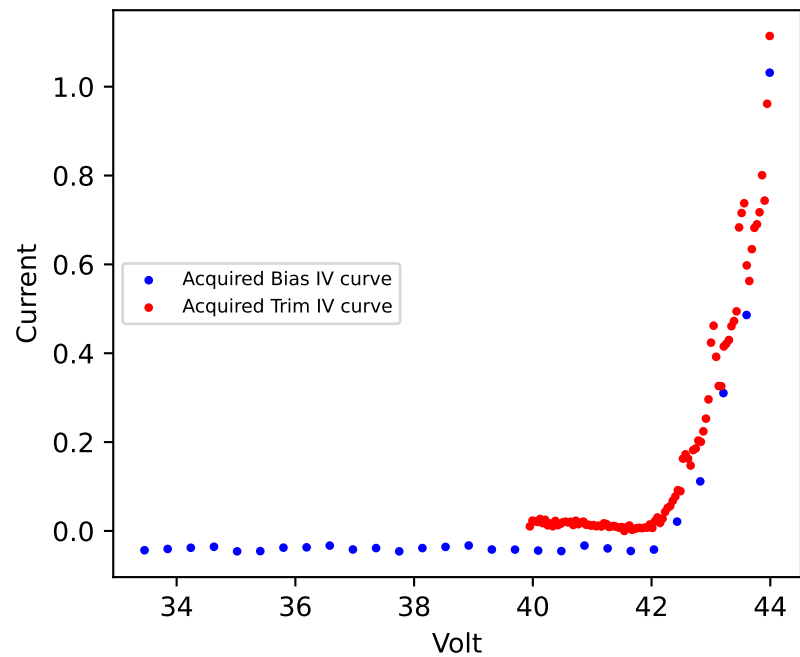
Bias REV IV curve



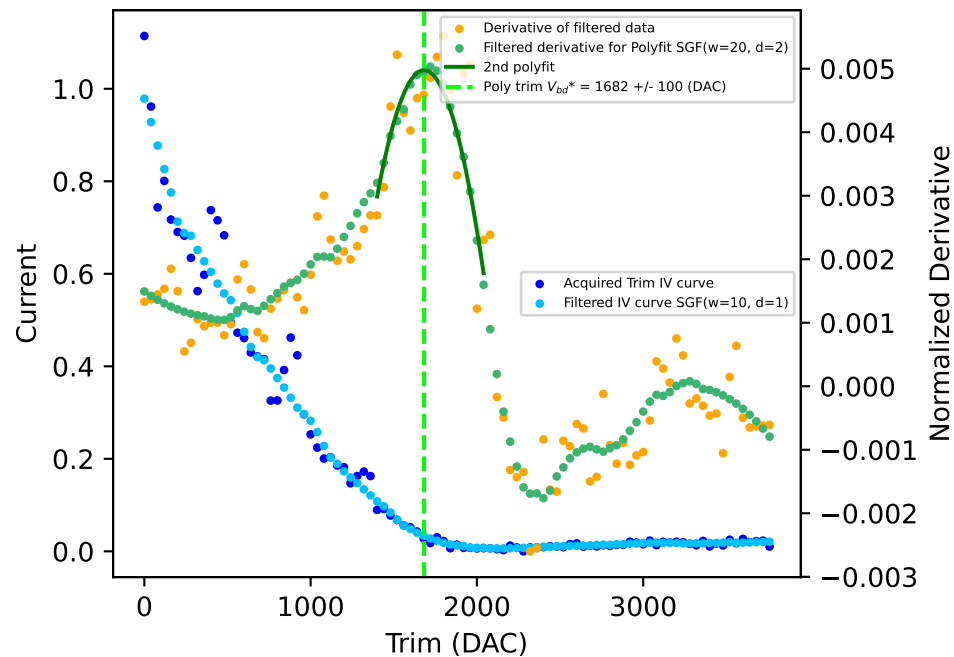
Bias DAC-VOLT conversion



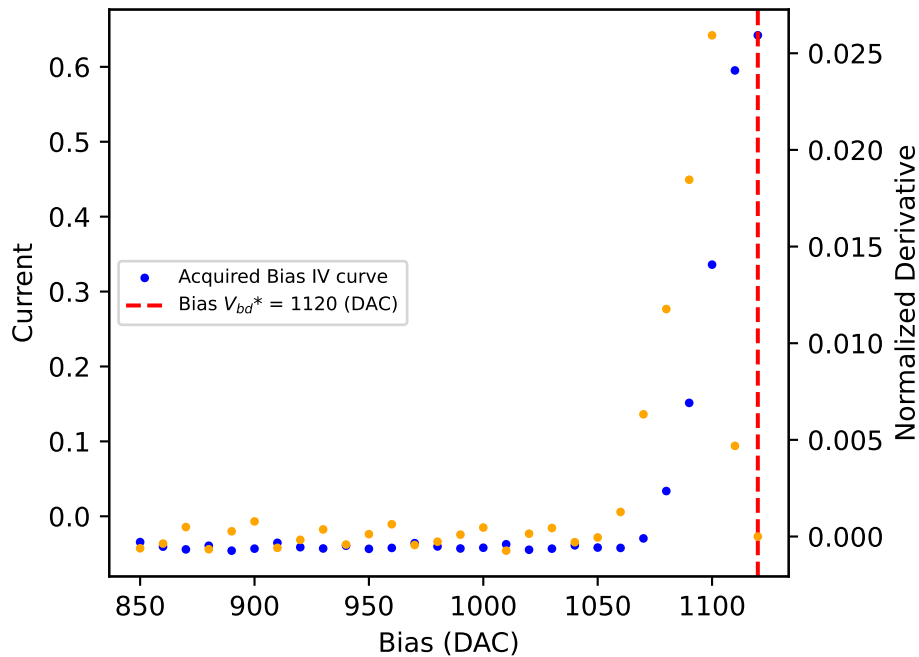
Bias + Trim REV IV curve



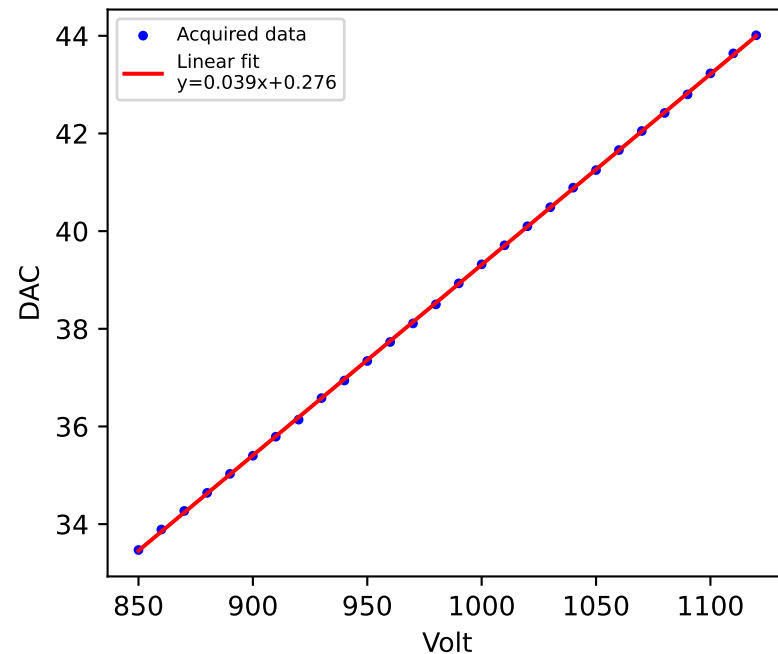
Trim REV IV curve



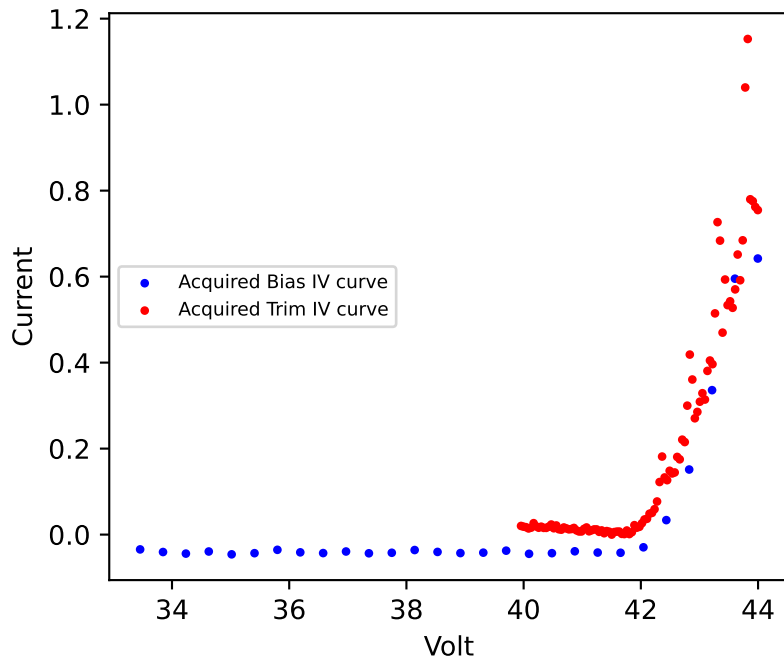
Bias REV IV curve



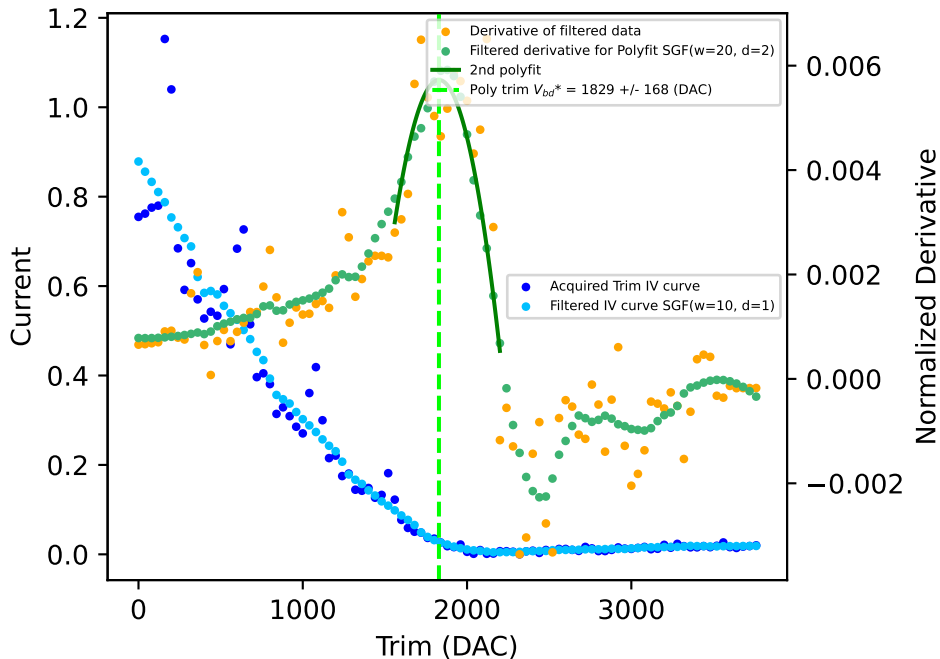
Bias DAC-VOLT conversion



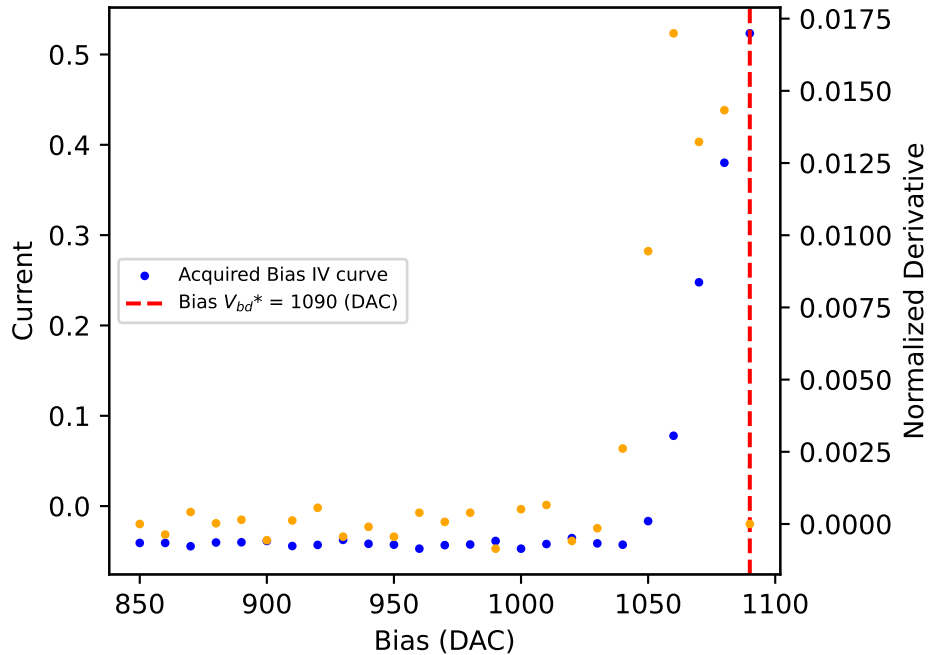
Bias + Trim REV IV curve



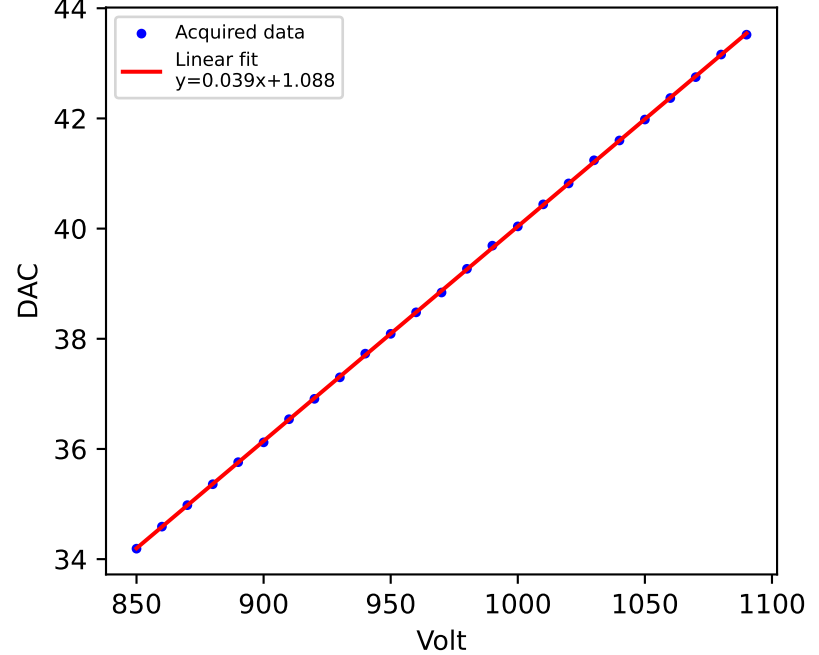
Trim REV IV curve



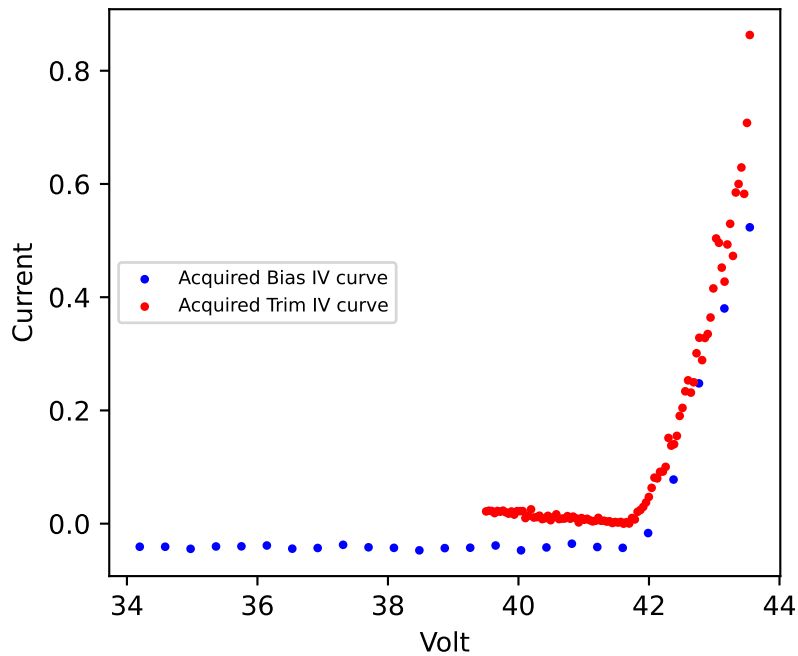
Bias REV IV curve



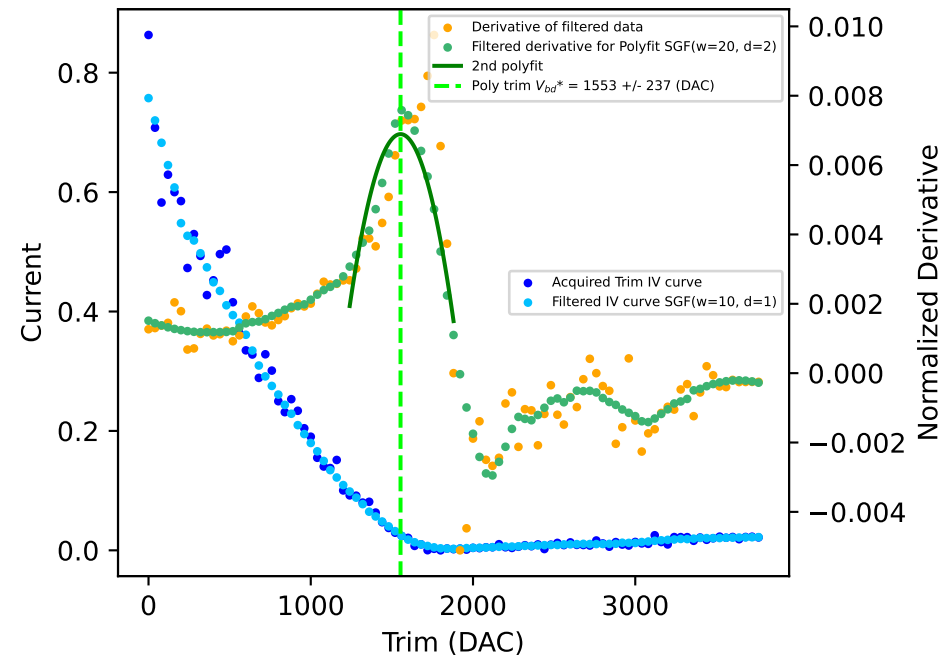
Bias DAC-VOLT conversion



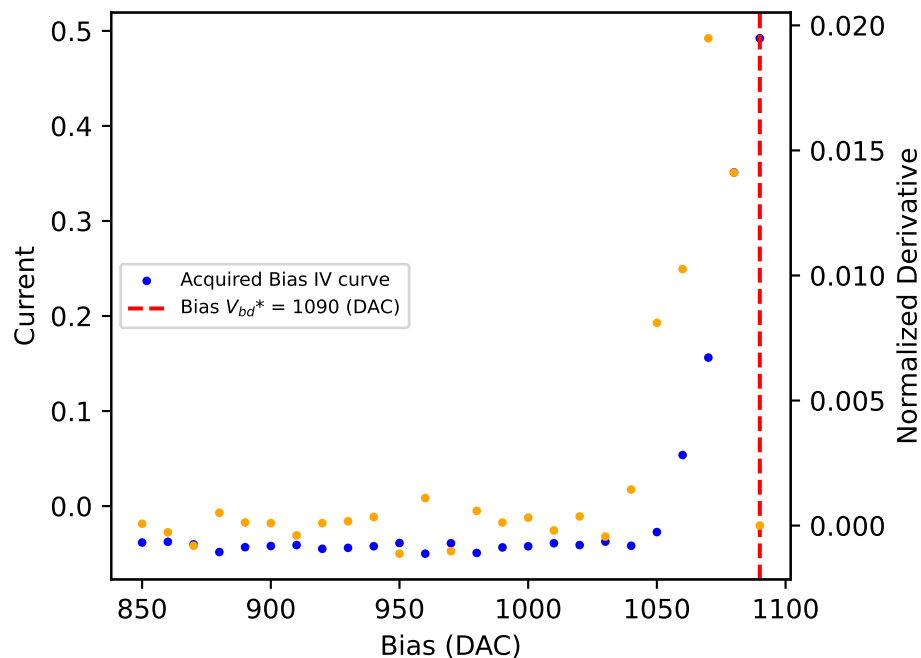
Bias + Trim REV IV curve



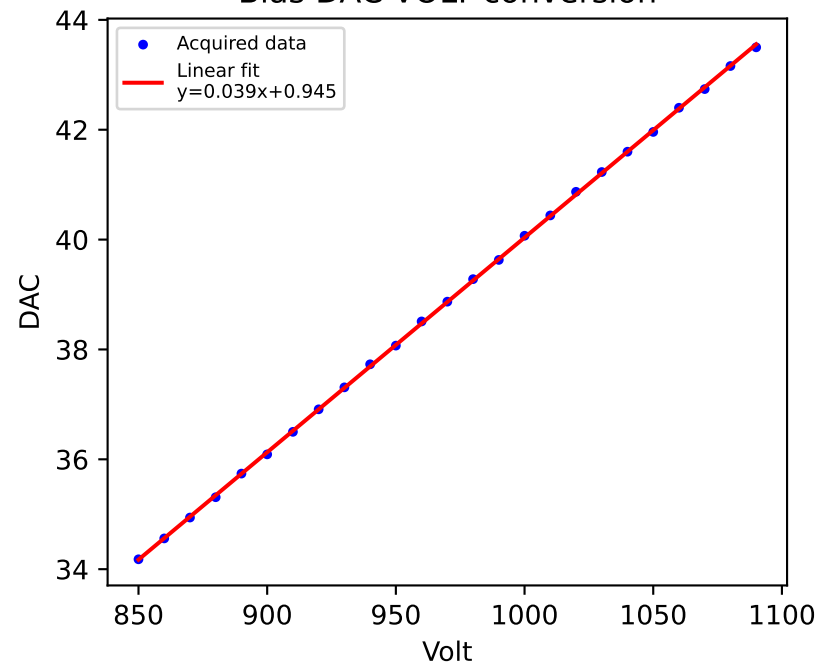
Trim REV IV curve



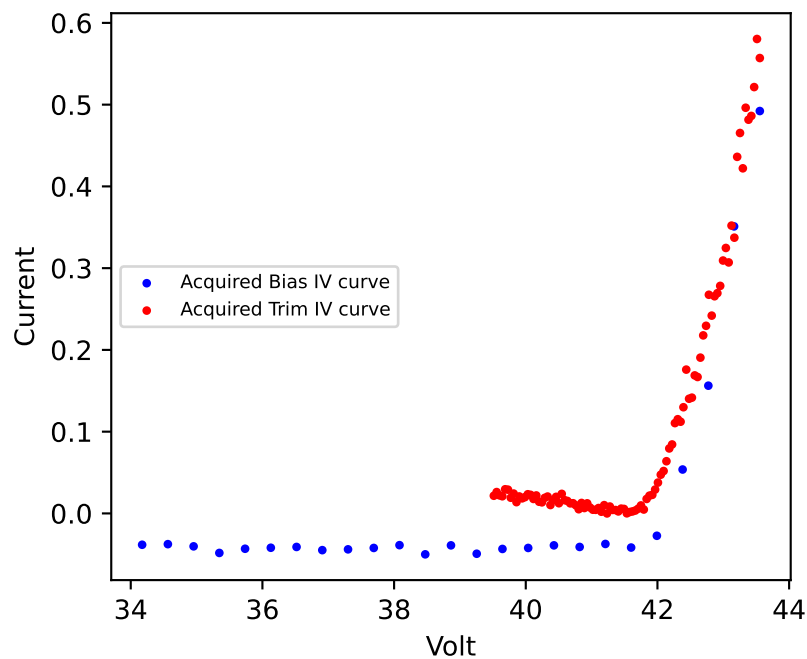
Bias REV IV curve



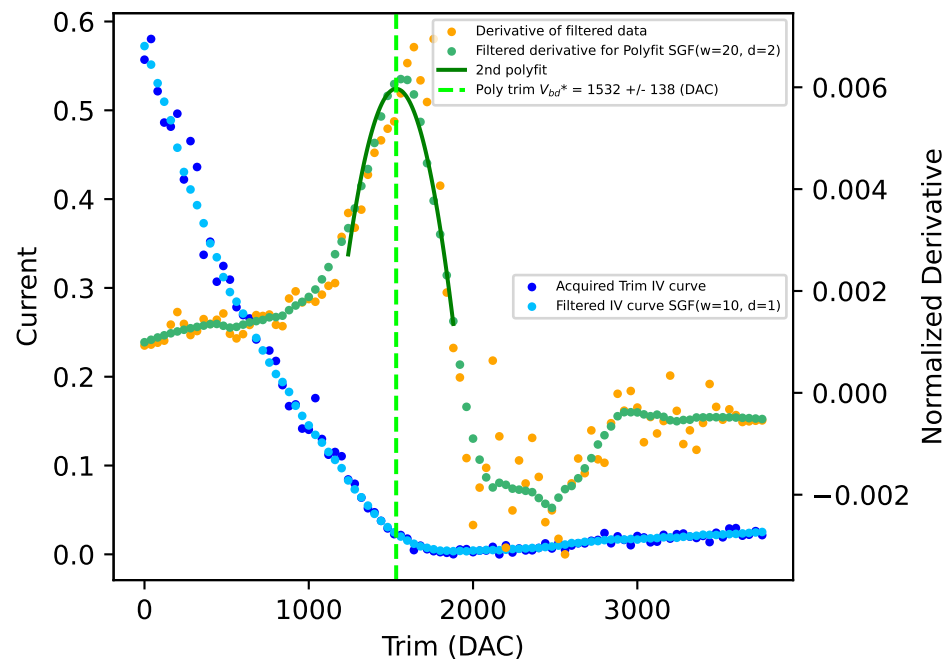
Bias DAC-VOLT conversion



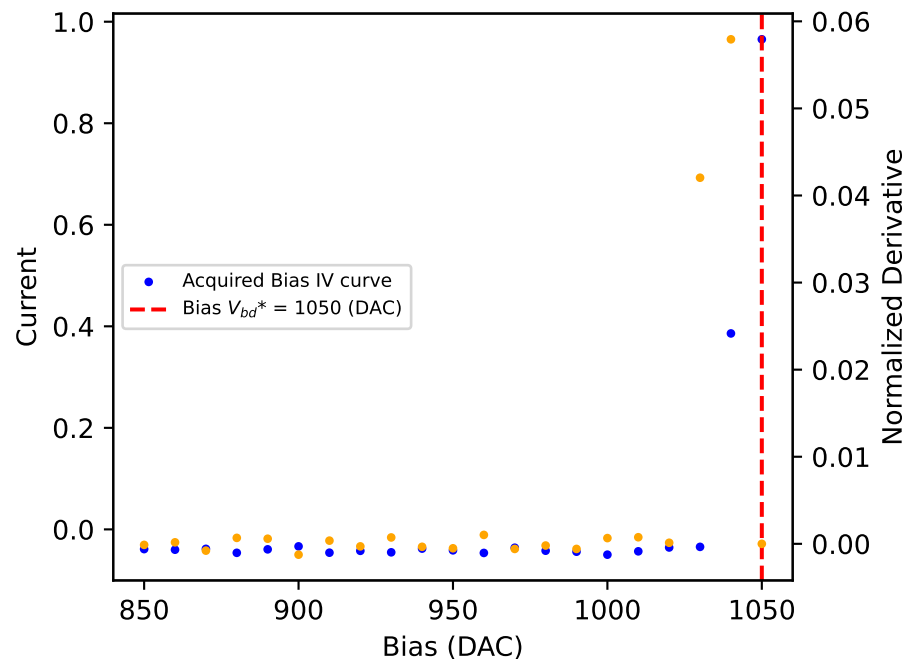
Bias + Trim REV IV curve



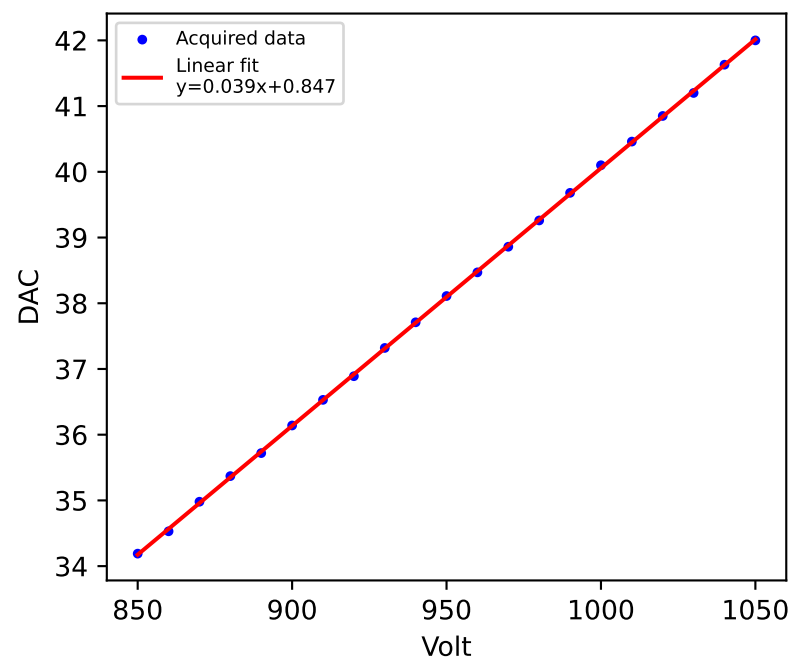
Trim REV IV curve



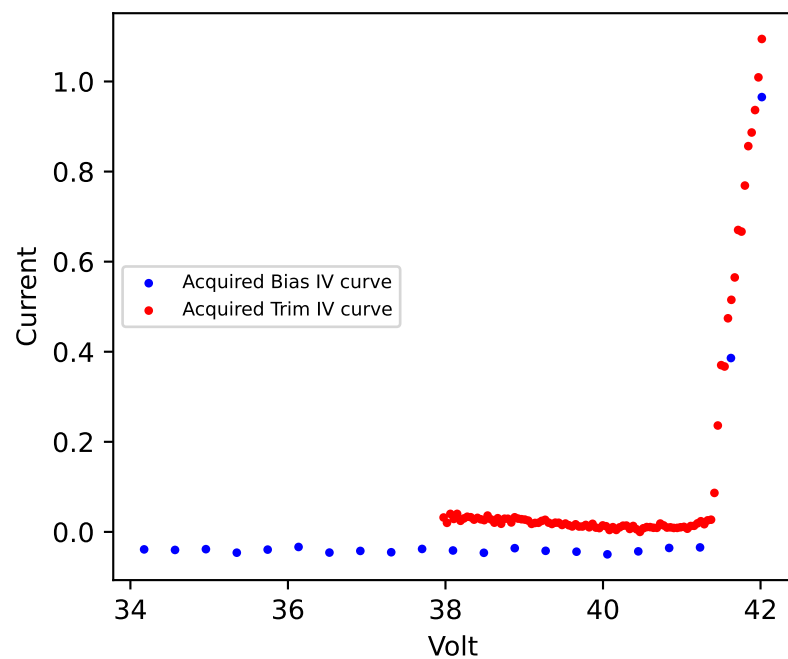
Bias REV IV curve



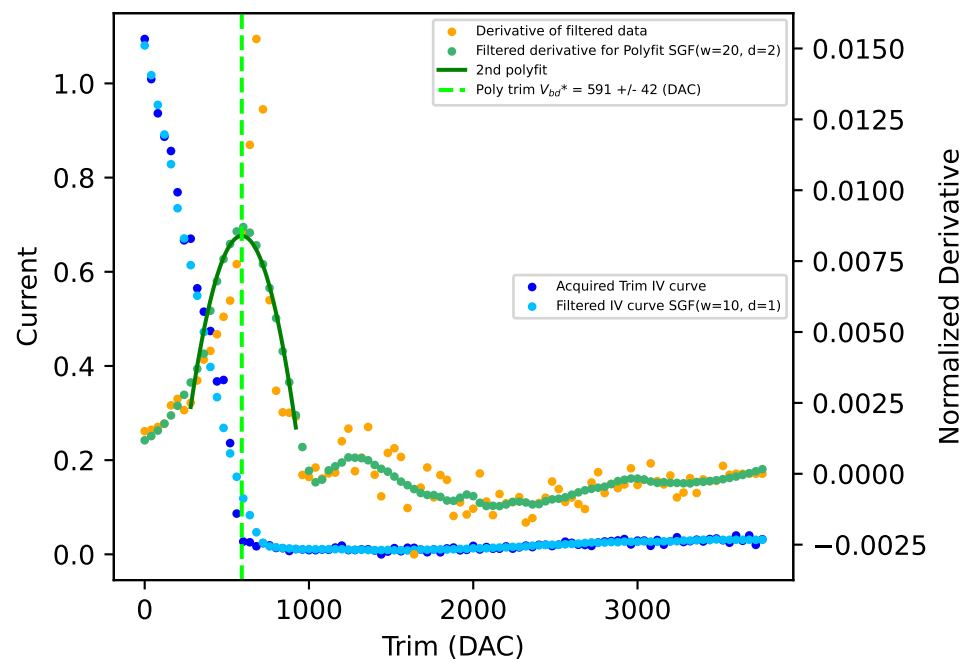
Bias DAC-VOLT conversion



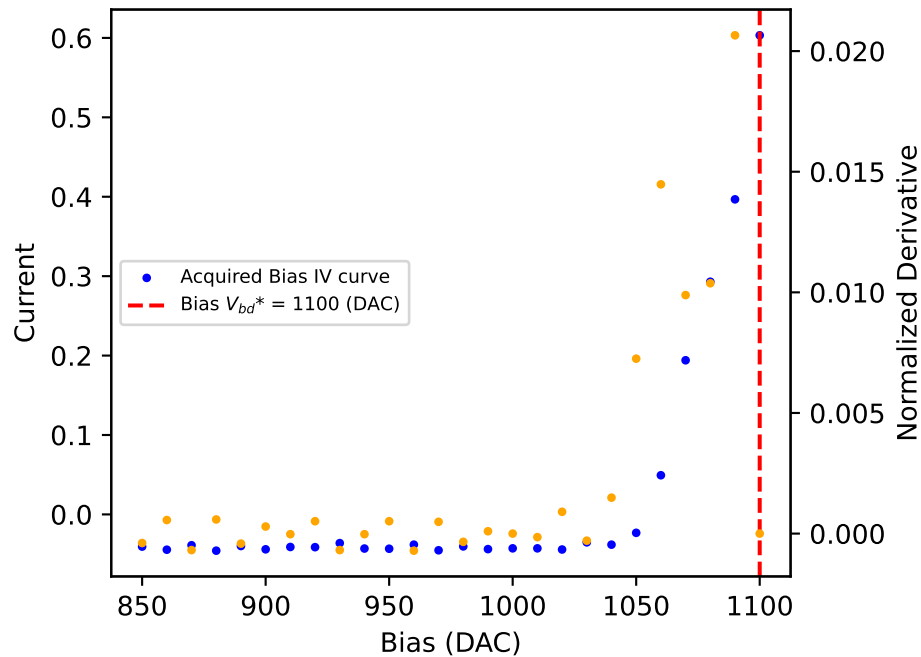
Bias + Trim REV IV curve



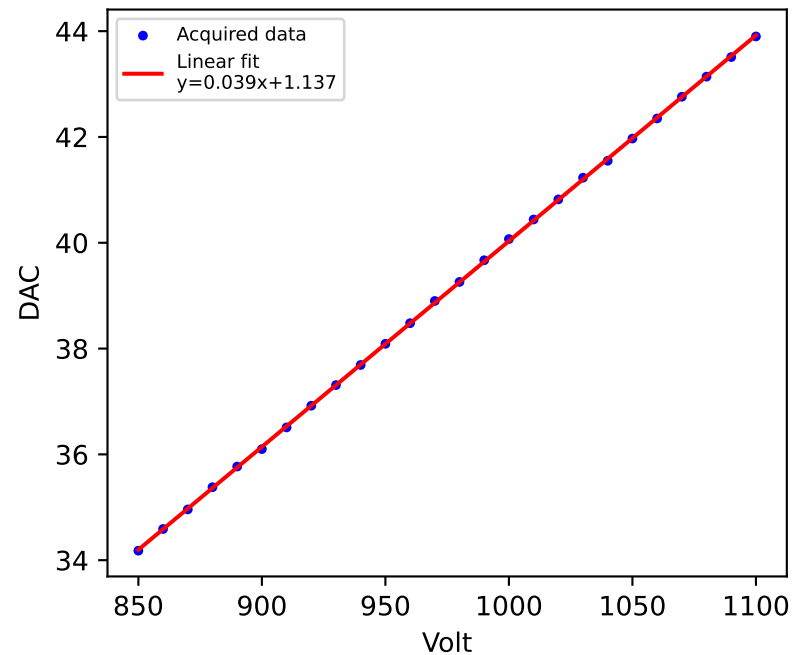
Trim REV IV curve



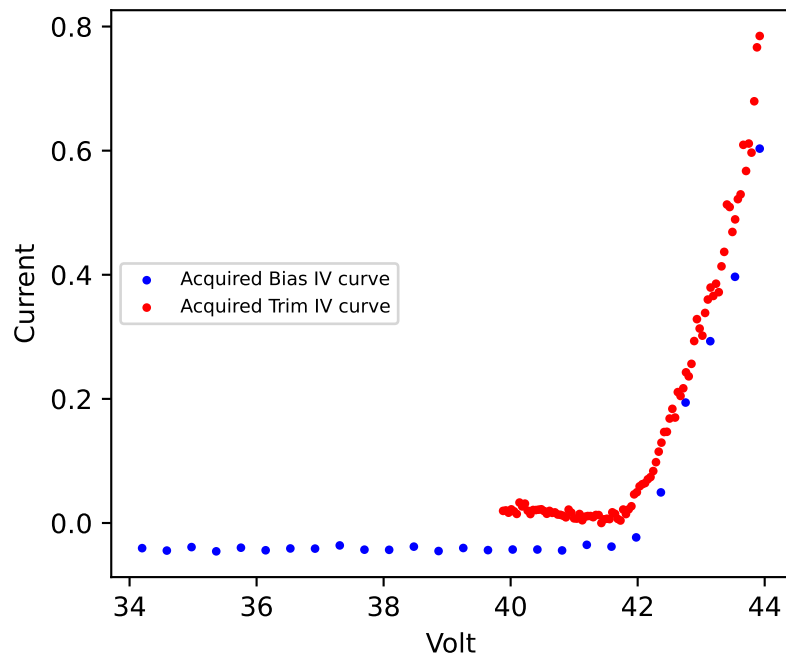
Bias REV IV curve



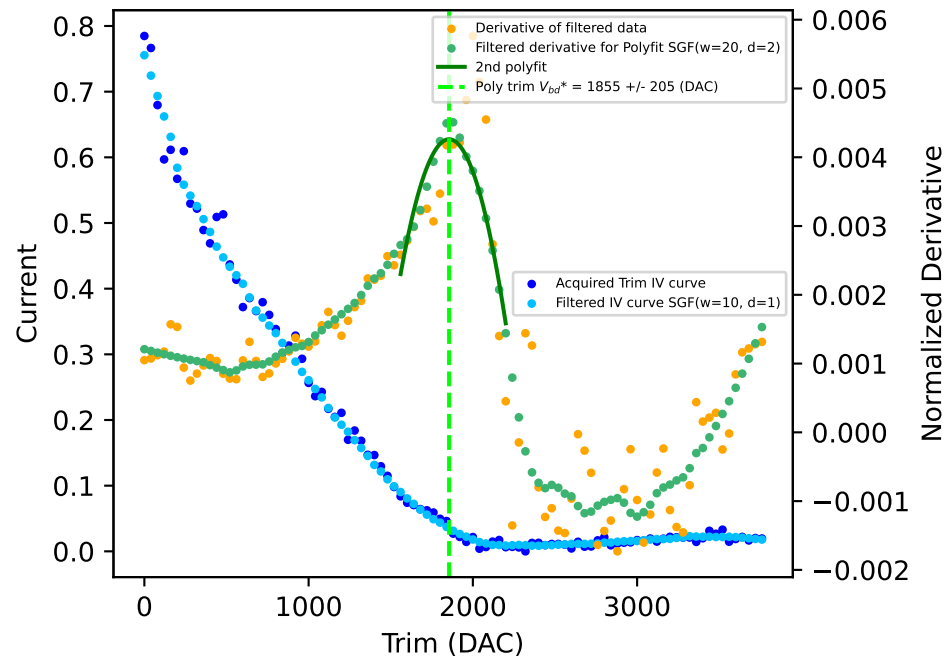
Bias DAC-VOLT conversion



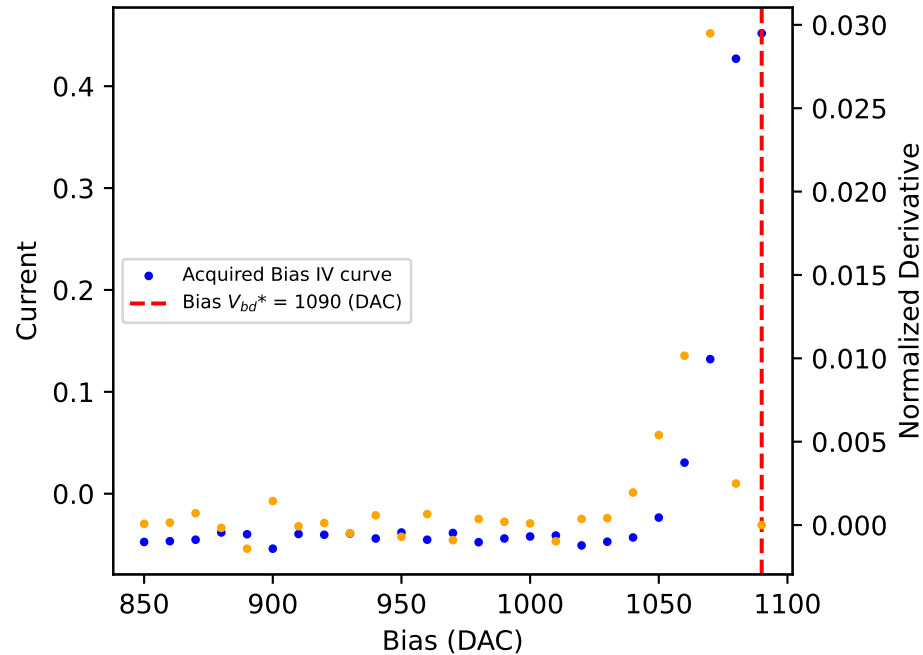
Bias + Trim REV IV curve



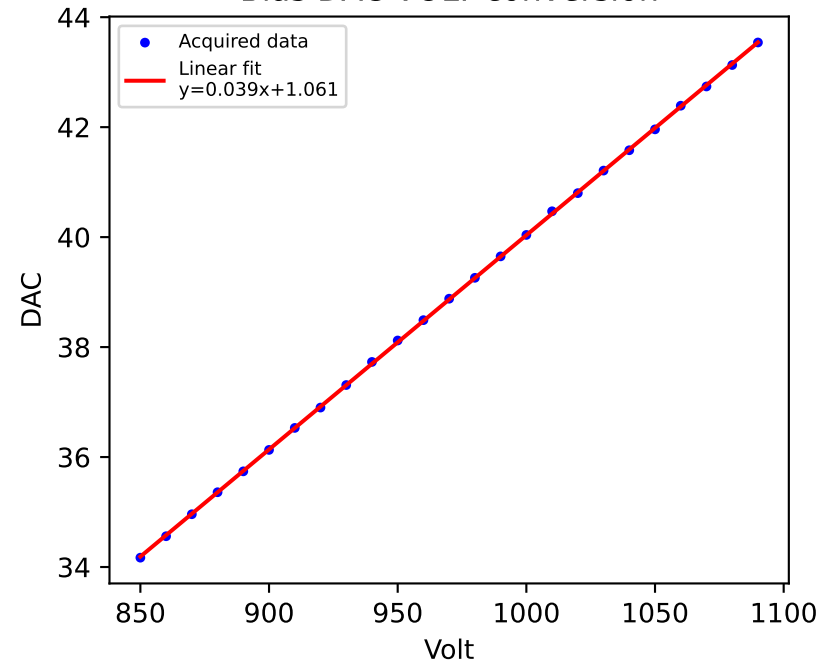
Trim REV IV curve



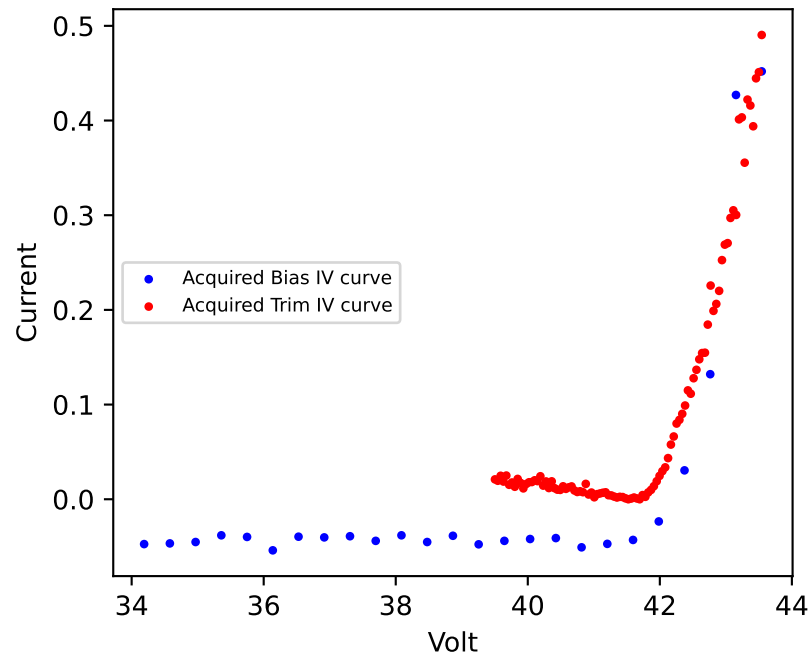
Bias REV IV curve



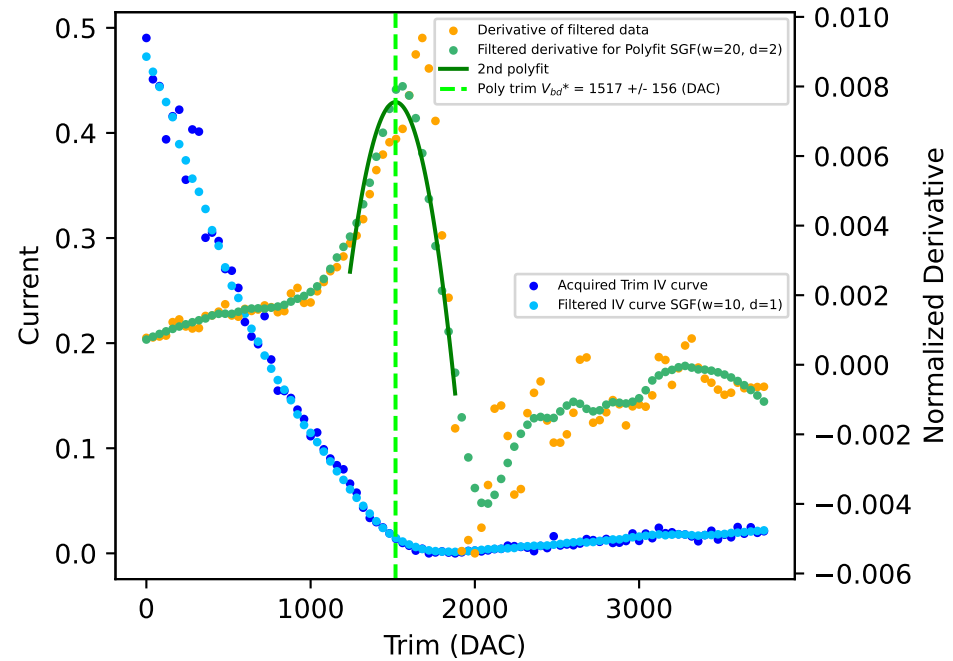
Bias DAC-VOLT conversion



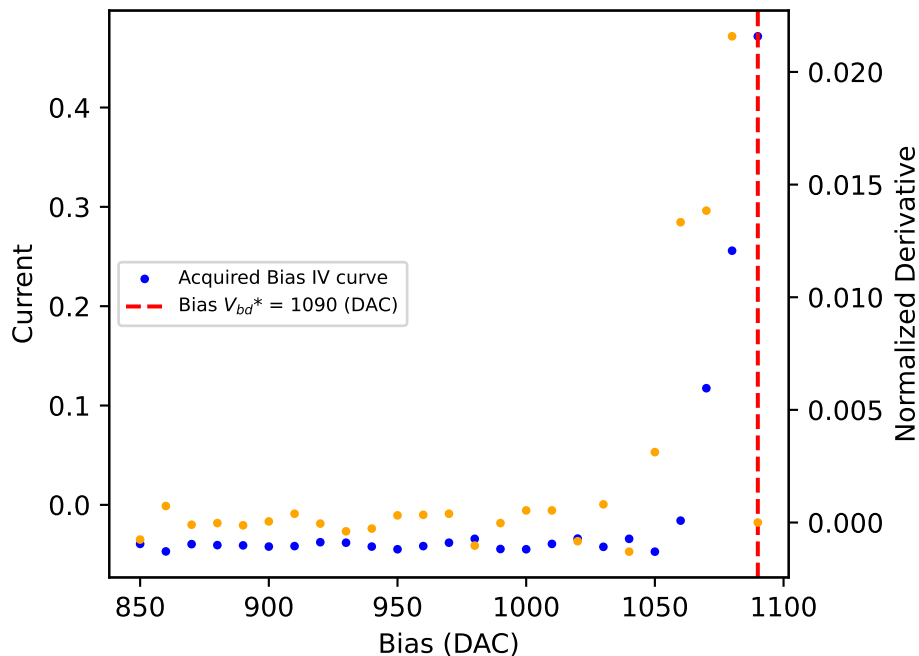
Bias + Trim REV IV curve



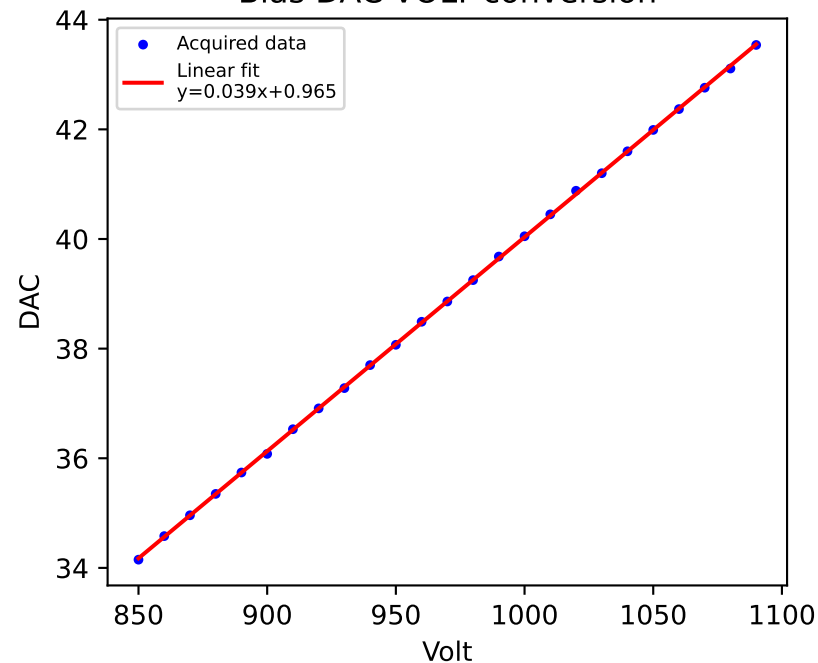
Trim REV IV curve



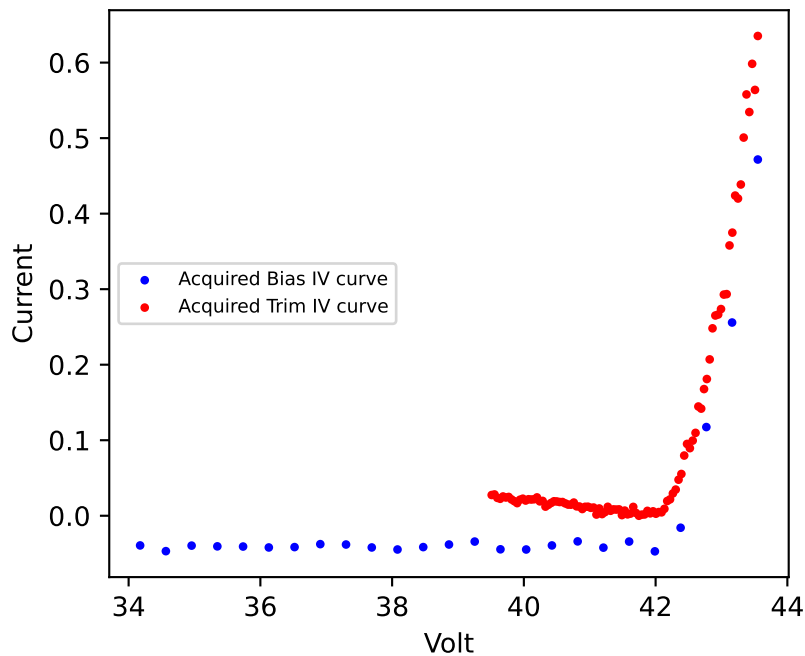
Bias REV IV curve



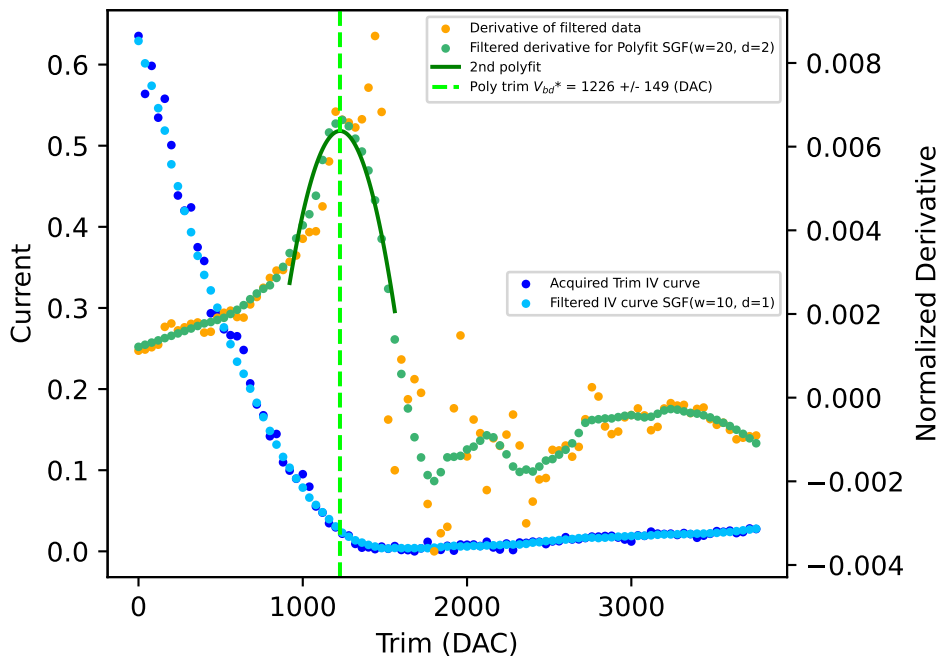
Bias DAC-VOLT conversion



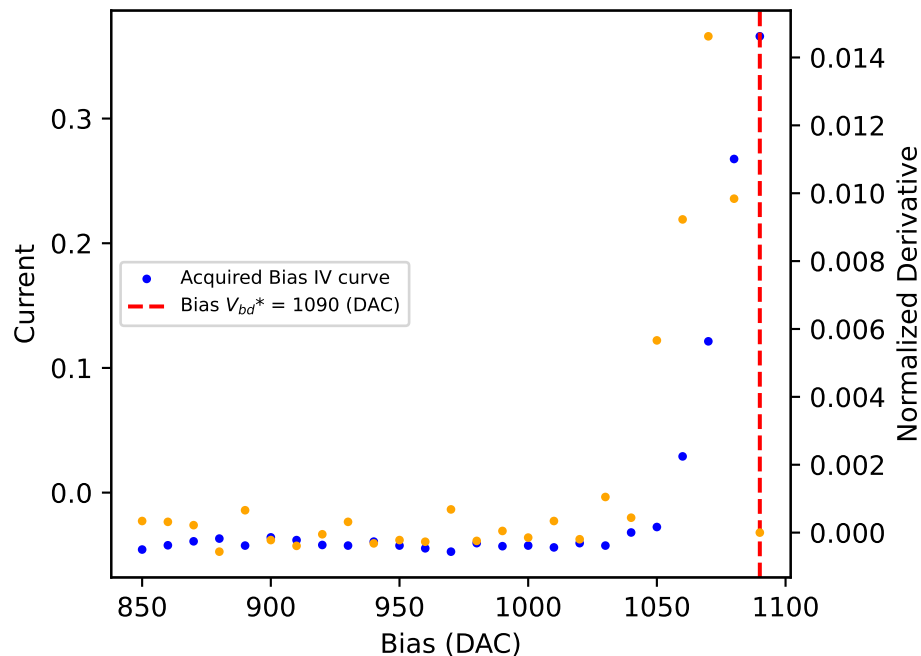
Bias + Trim REV IV curve



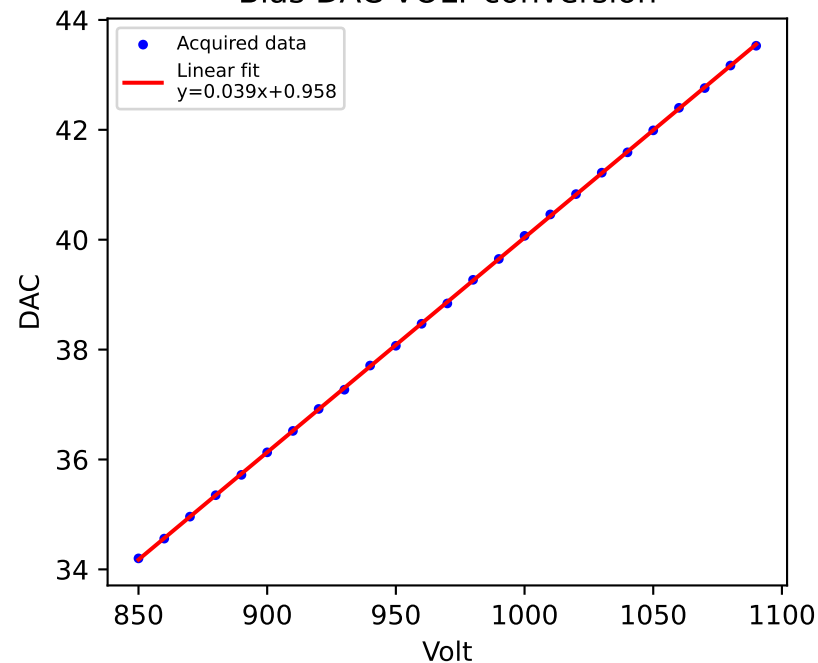
Trim REV IV curve



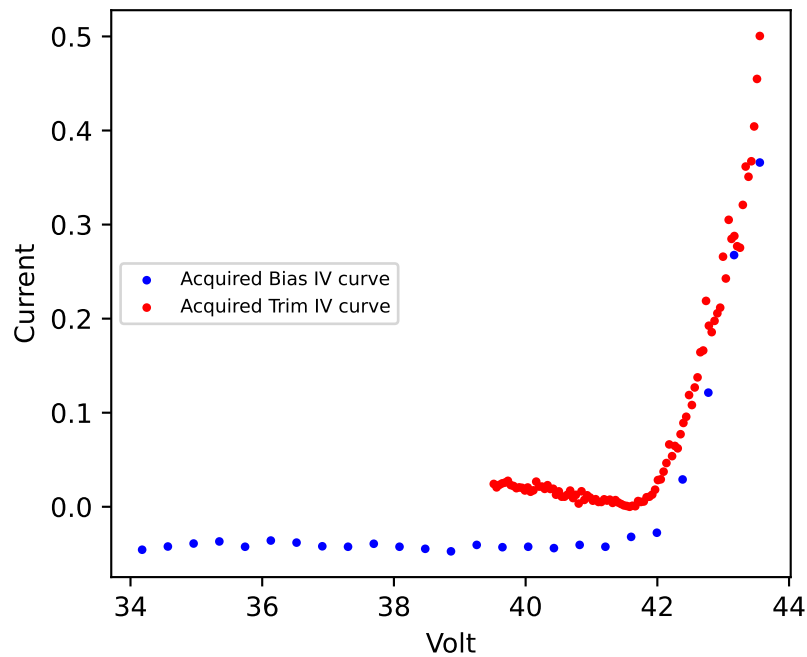
Bias REV IV curve



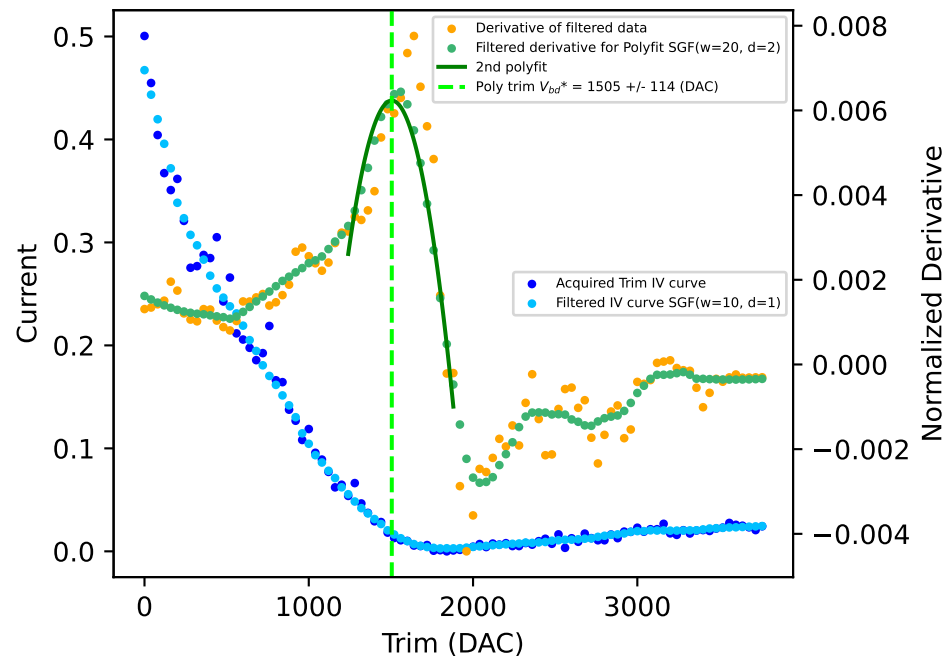
Bias DAC-VOLT conversion



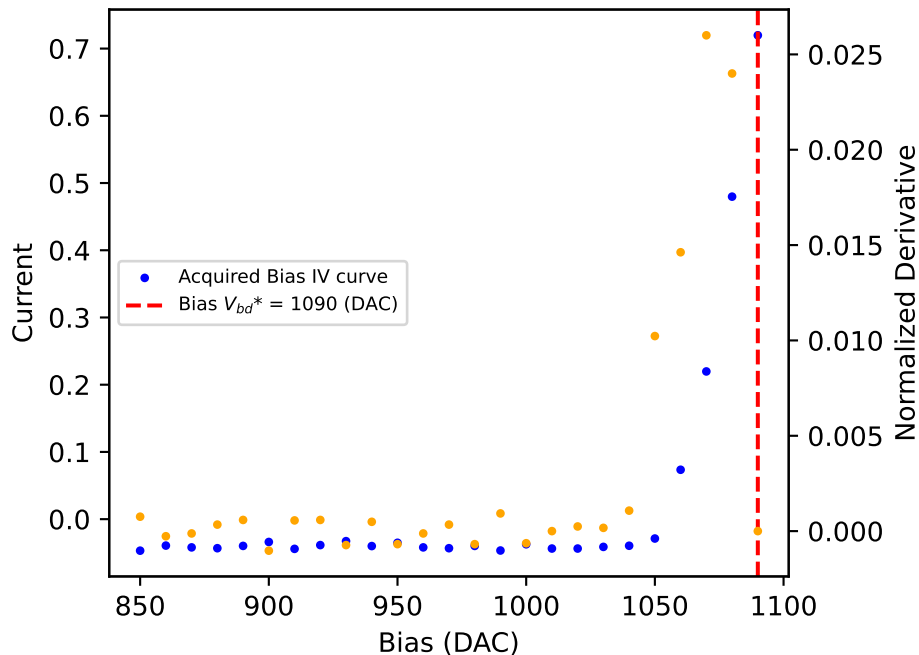
Bias + Trim REV IV curve



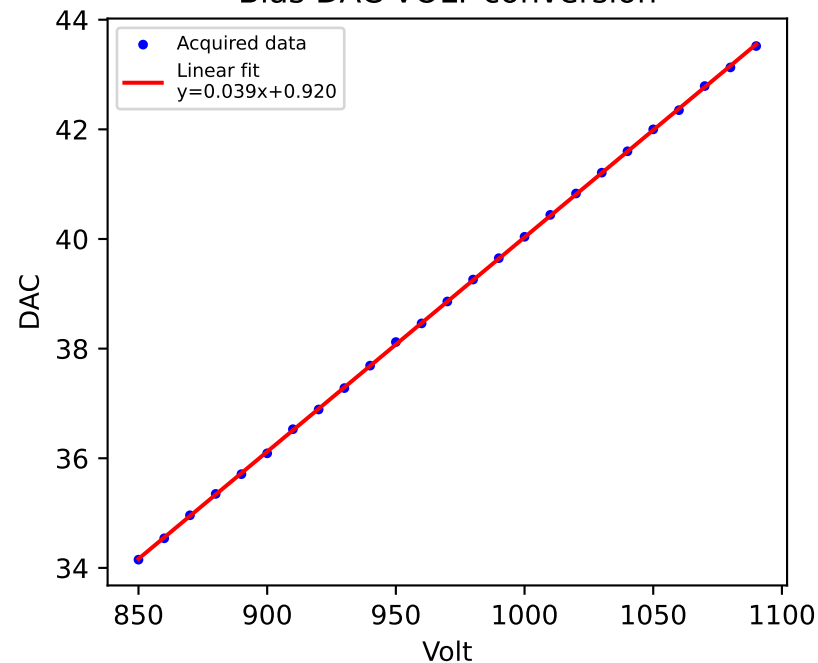
Trim REV IV curve



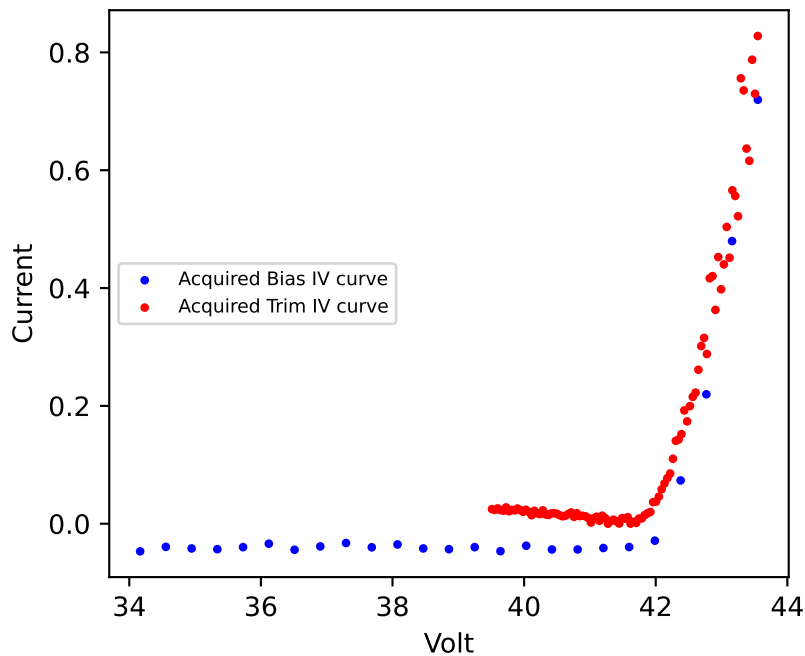
Bias REV IV curve



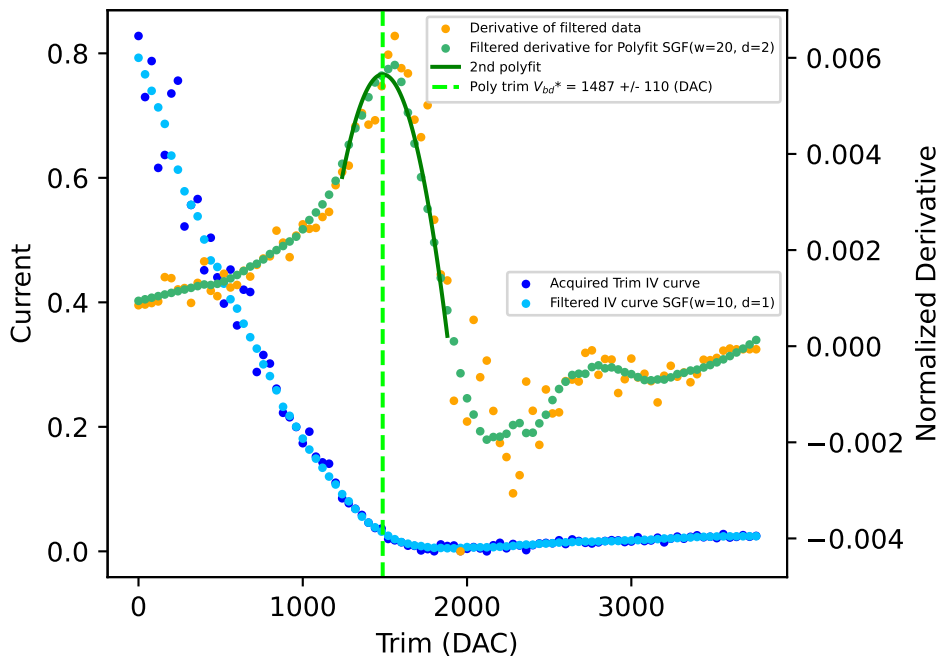
Bias DAC-VOLT conversion



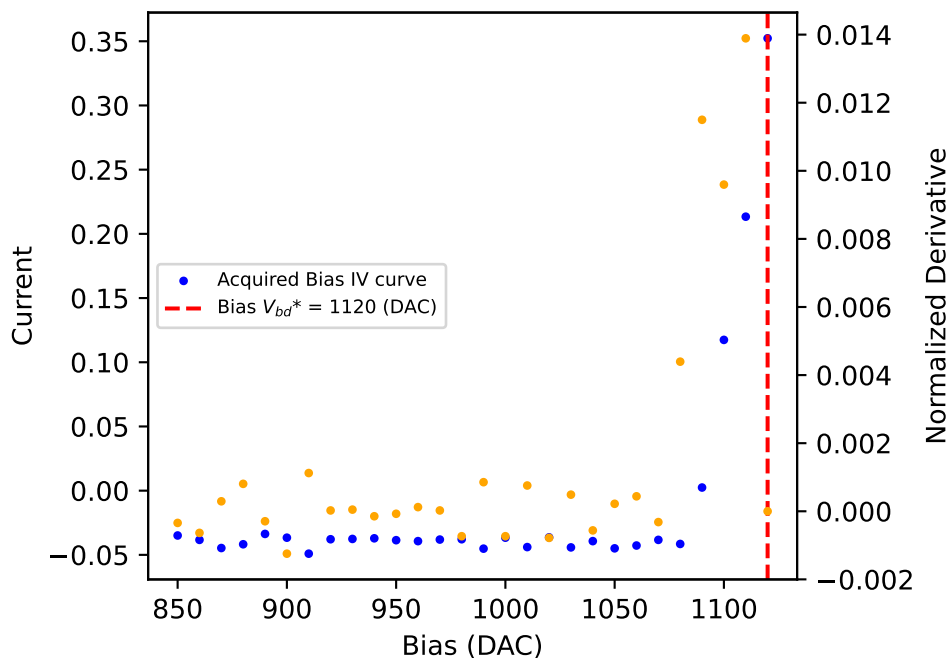
Bias + Trim REV IV curve



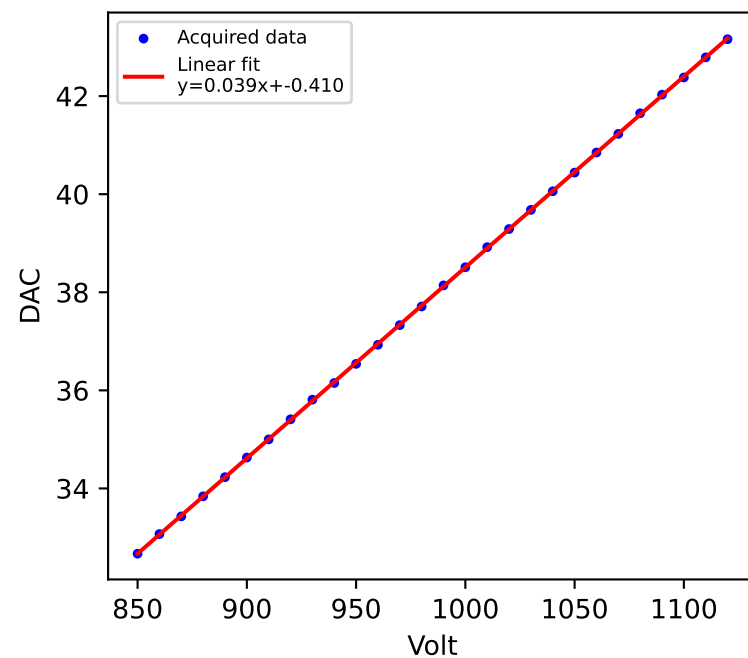
Trim REV IV curve



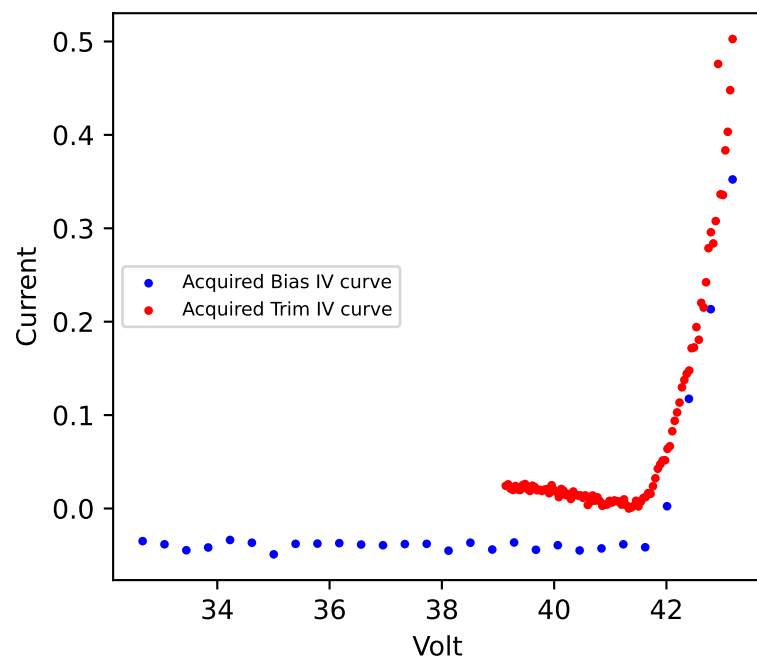
Bias REV IV curve



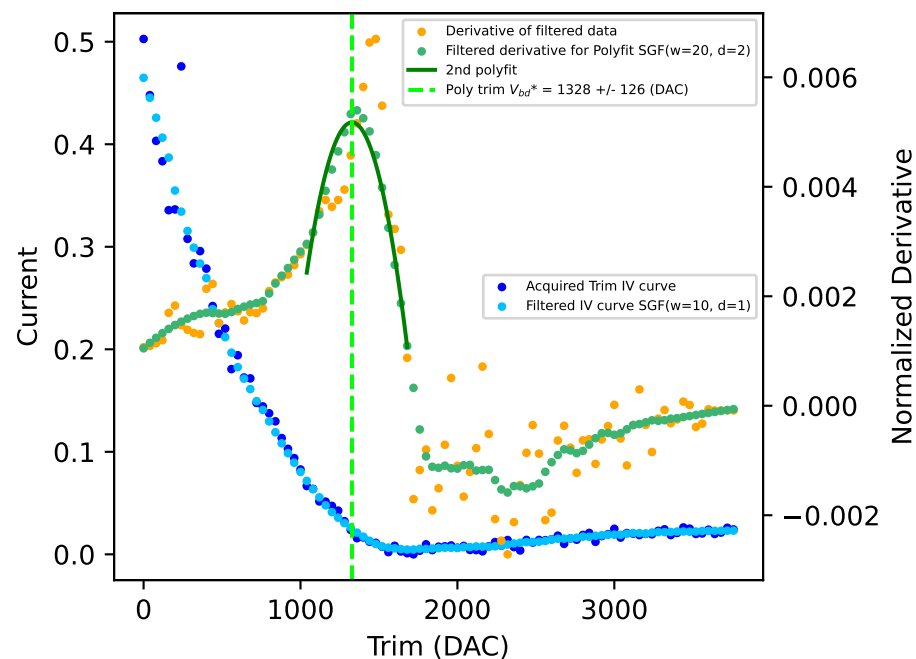
Bias DAC-VOLT conversion



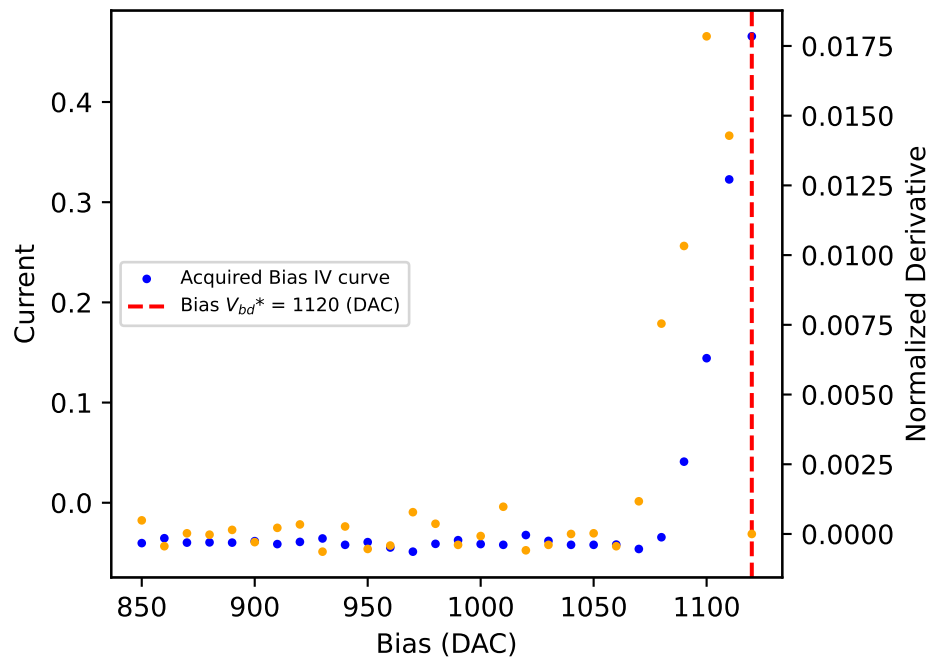
Bias + Trim REV IV curve



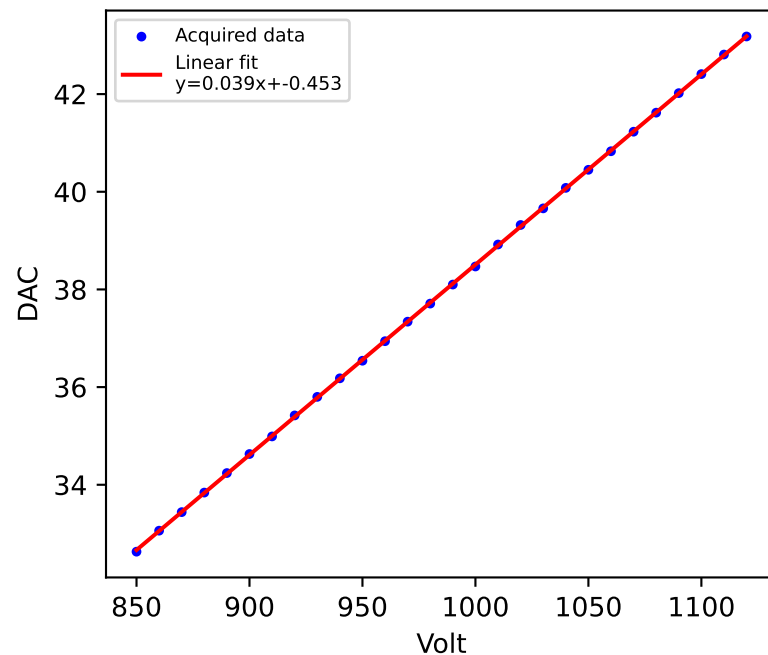
Trim REV IV curve



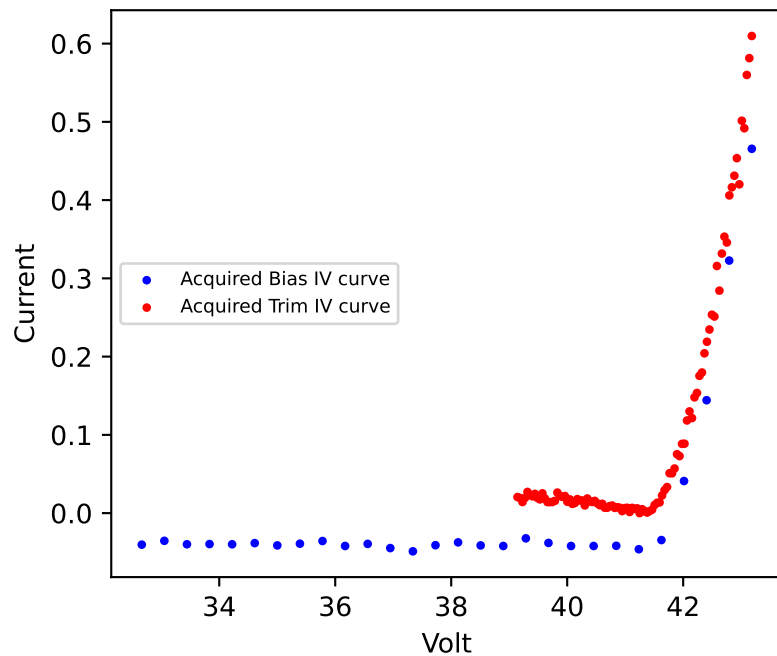
Bias REV IV curve



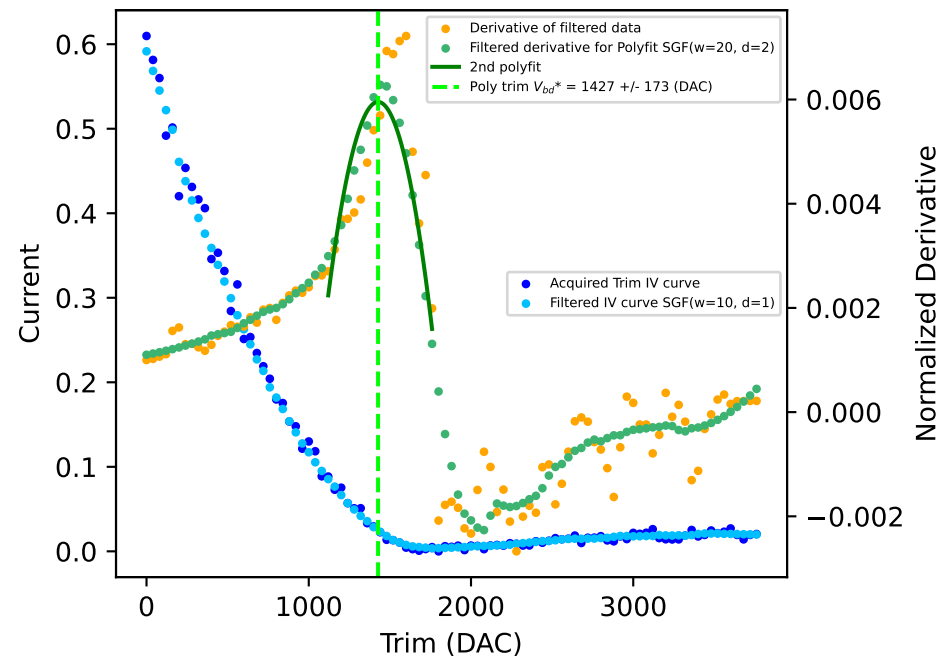
Bias DAC-VOLT conversion



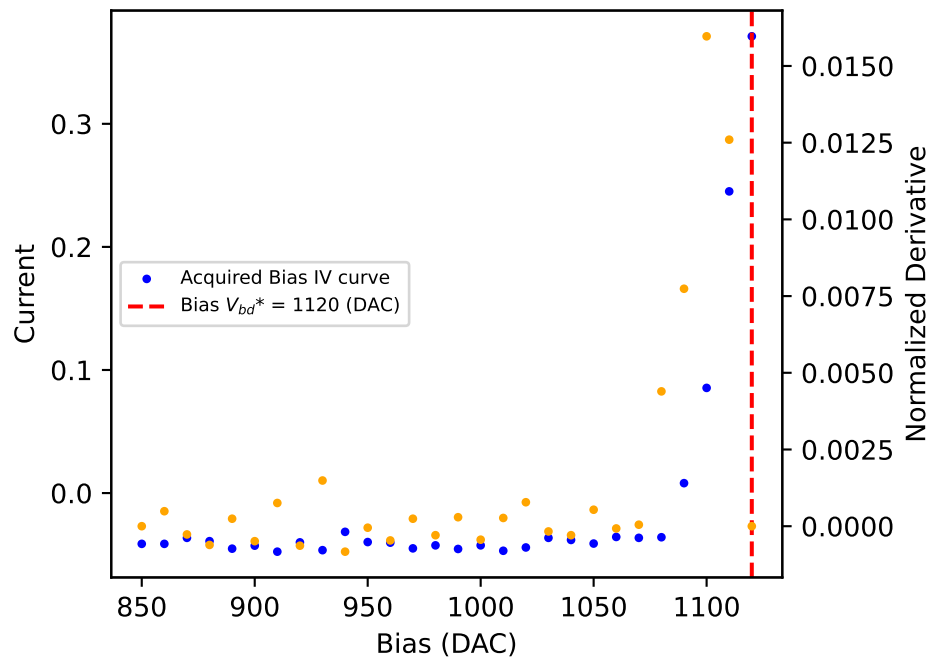
Bias + Trim REV IV curve



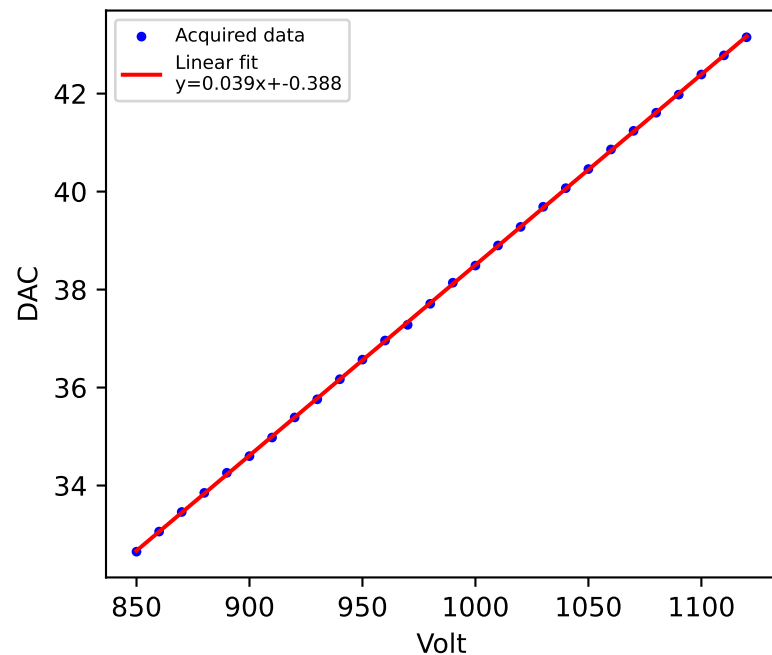
Trim REV IV curve



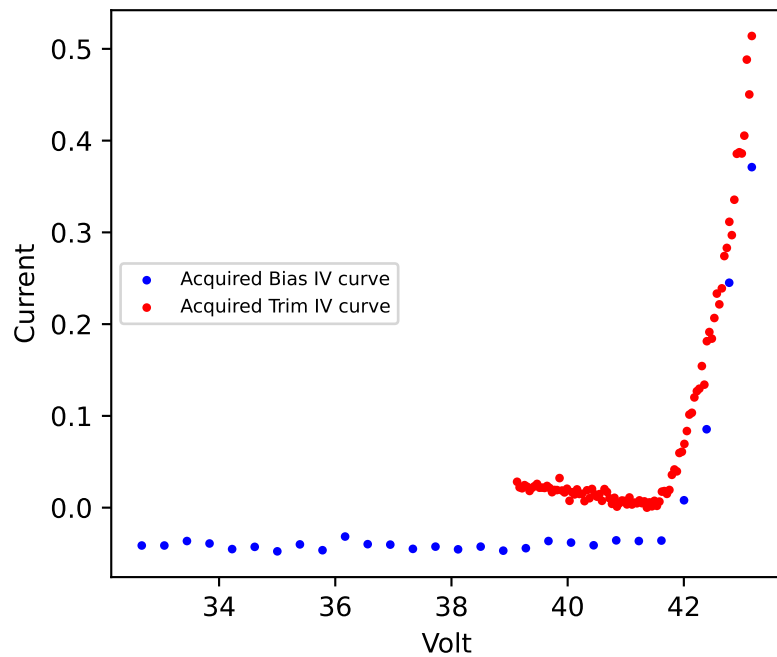
Bias REV IV curve



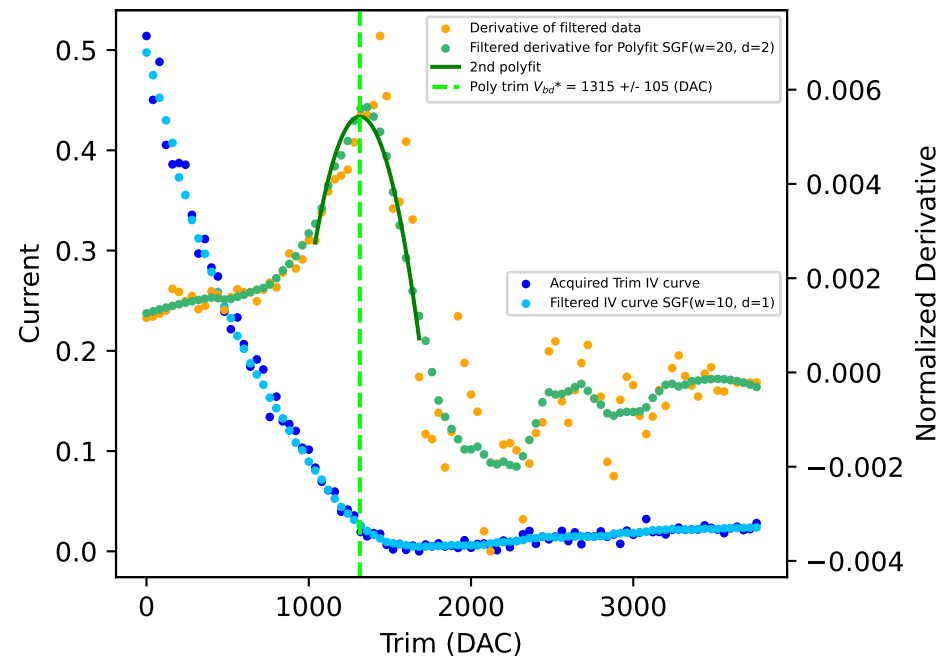
Bias DAC-VOLT conversion



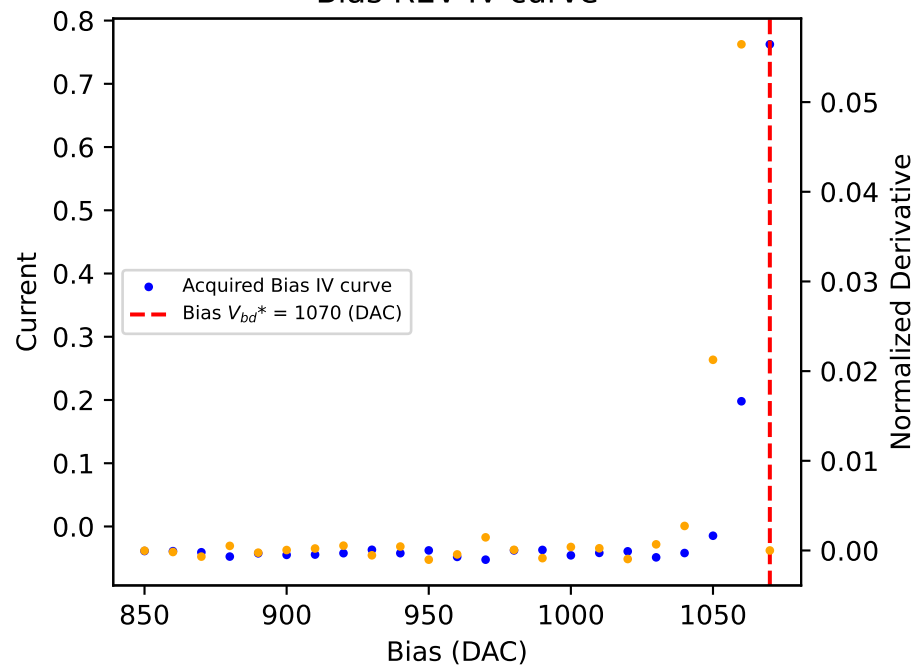
Bias + Trim REV IV curve



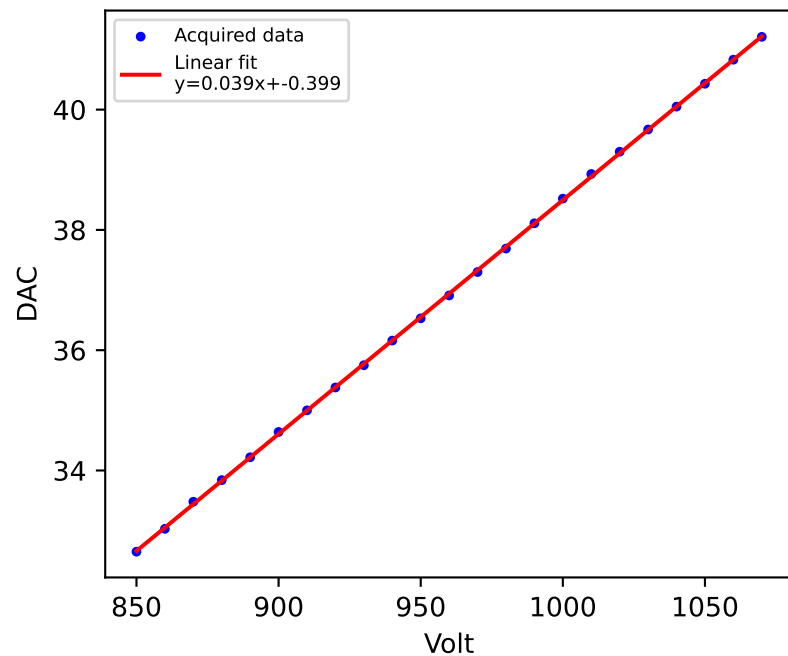
Trim REV IV curve



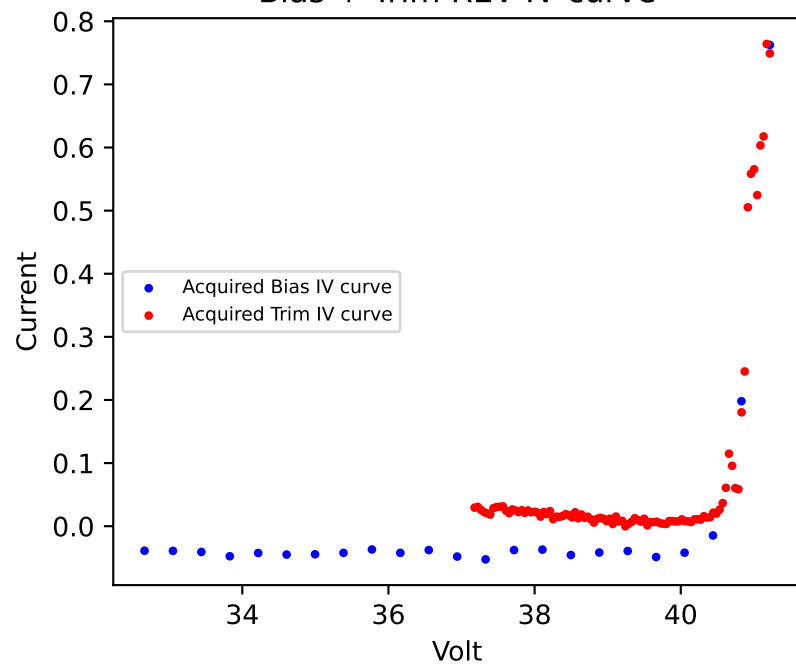
Bias REV IV curve



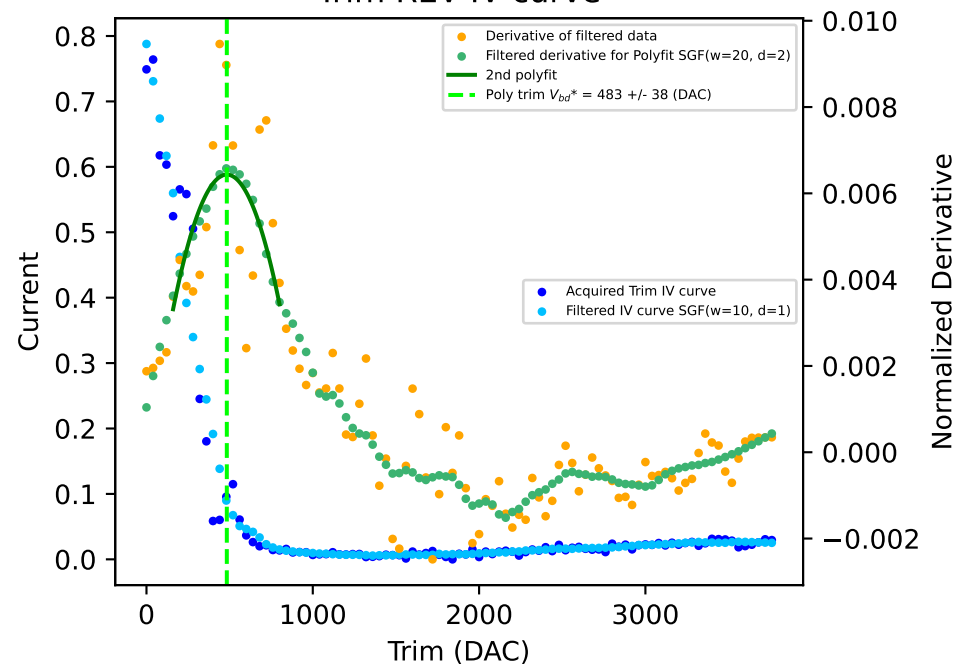
Bias DAC-VOLT conversion



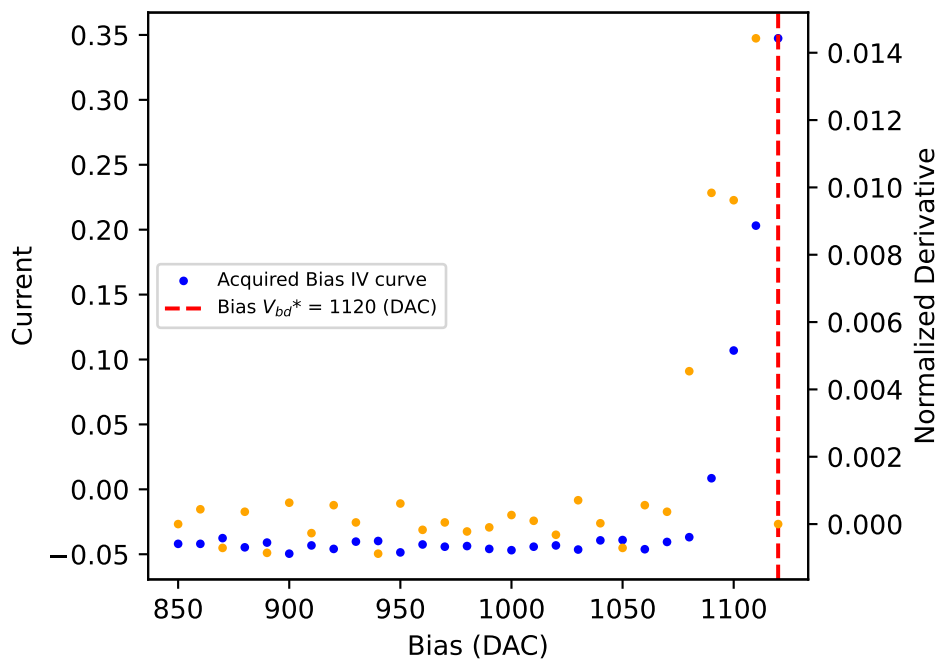
Bias + Trim REV IV curve



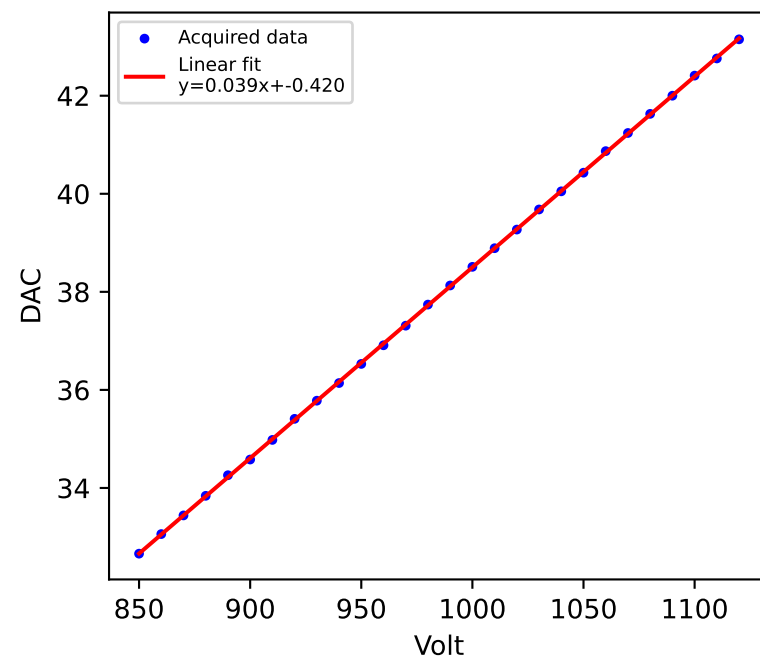
Trim REV IV curve



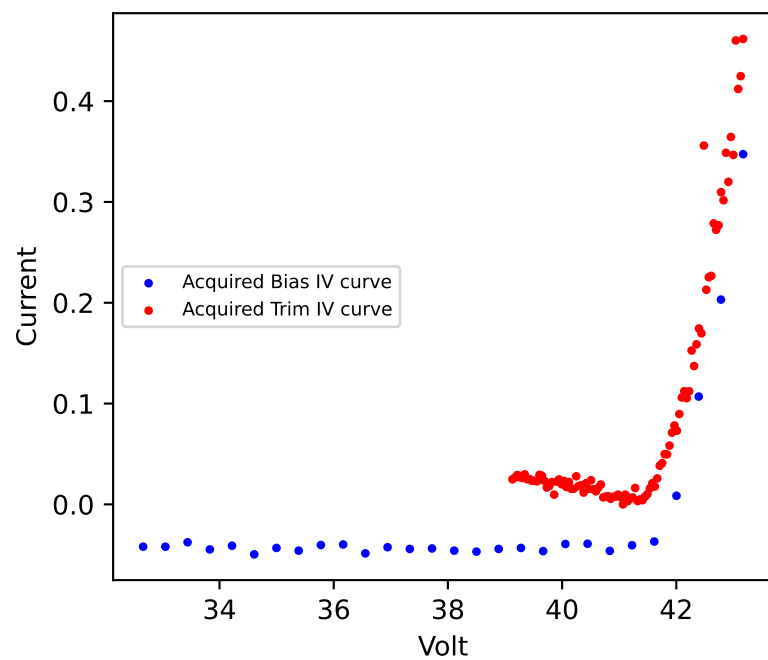
Bias REV IV curve



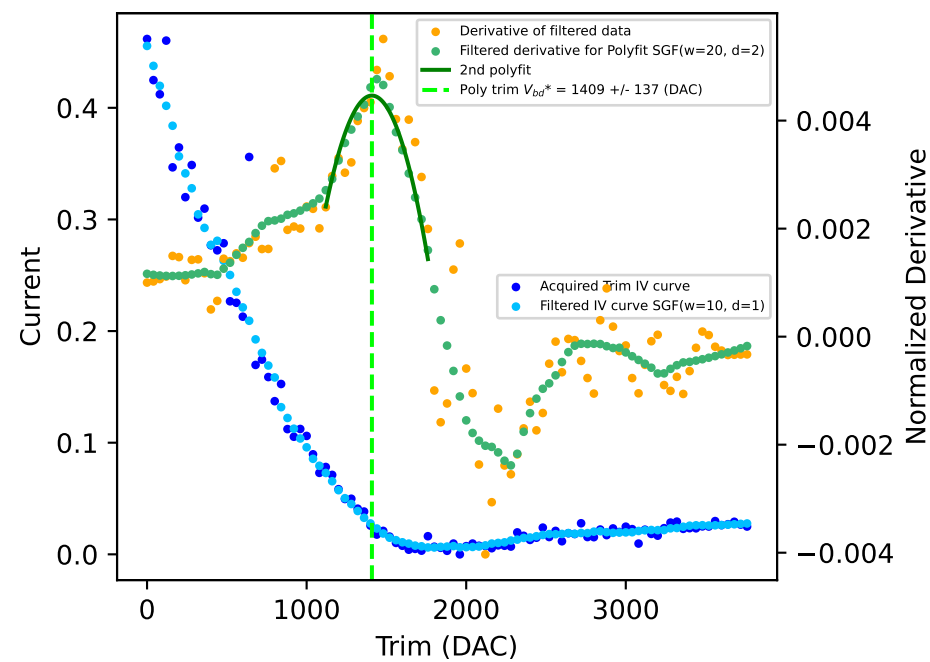
Bias DAC-VOLT conversion



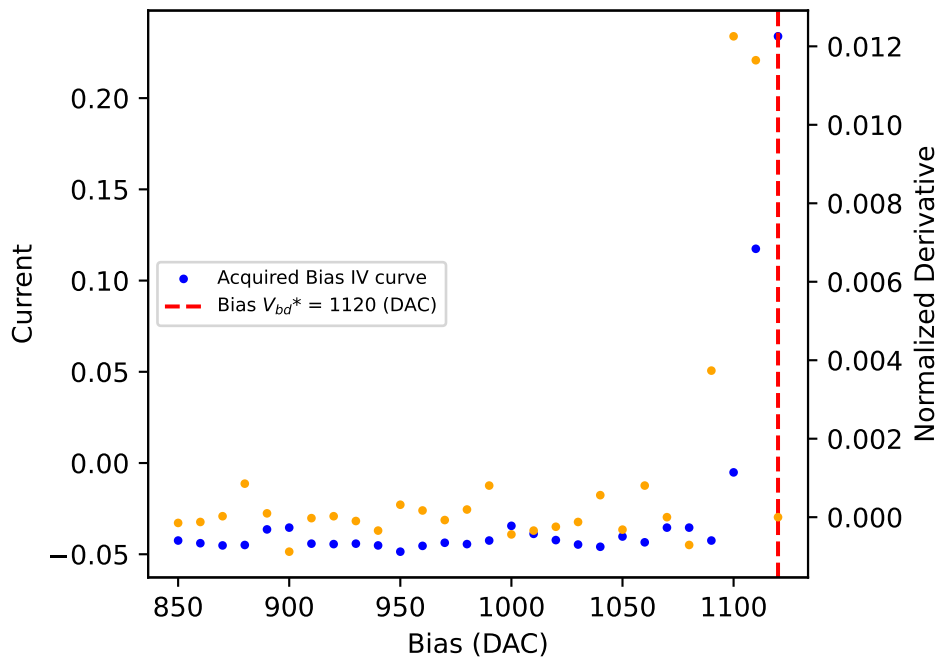
Bias + Trim REV IV curve



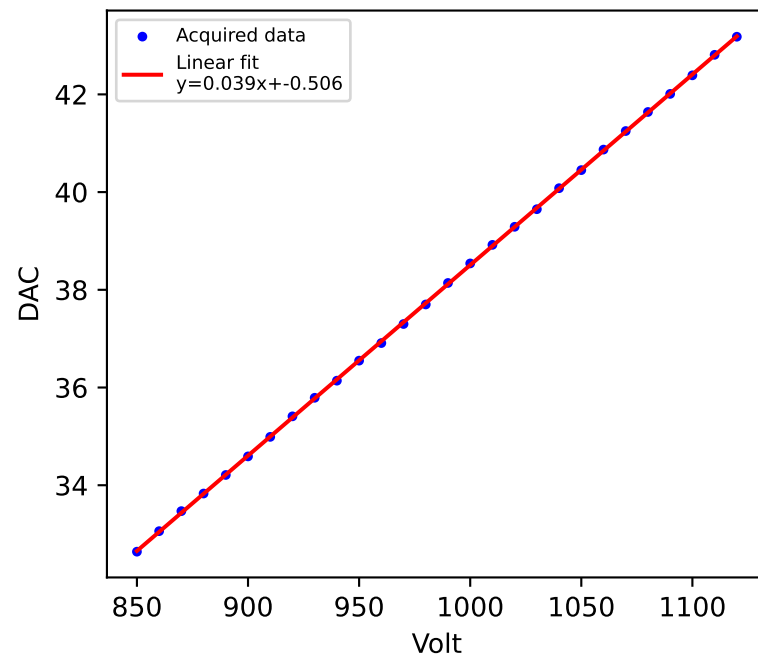
Trim REV IV curve



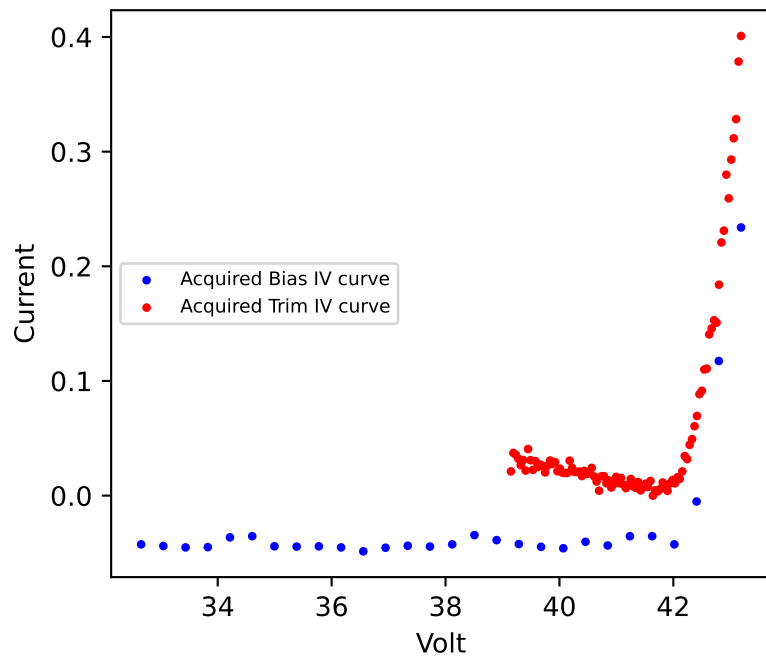
Bias REV IV curve



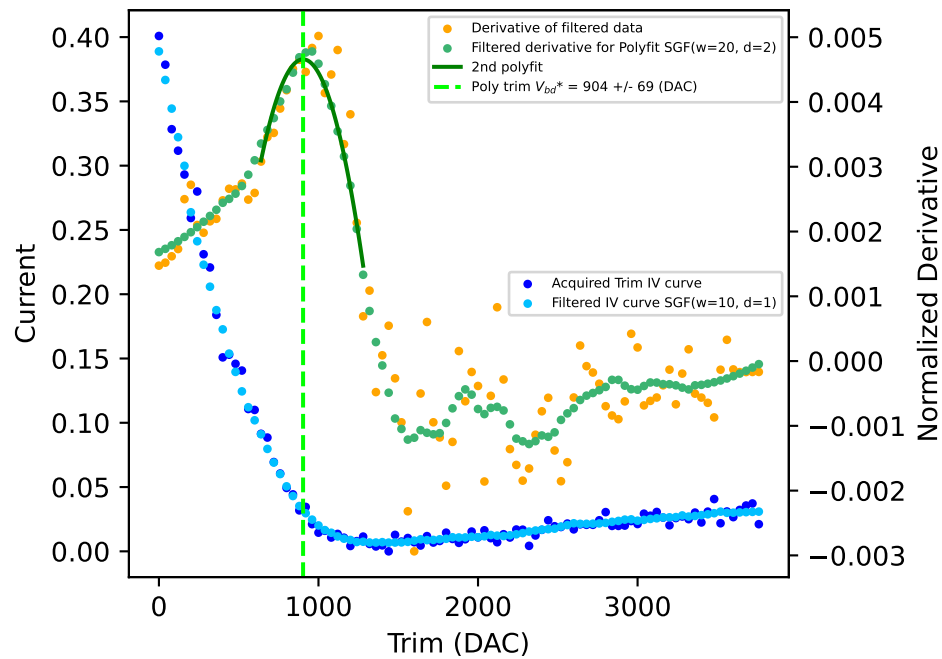
Bias DAC-VOLT conversion



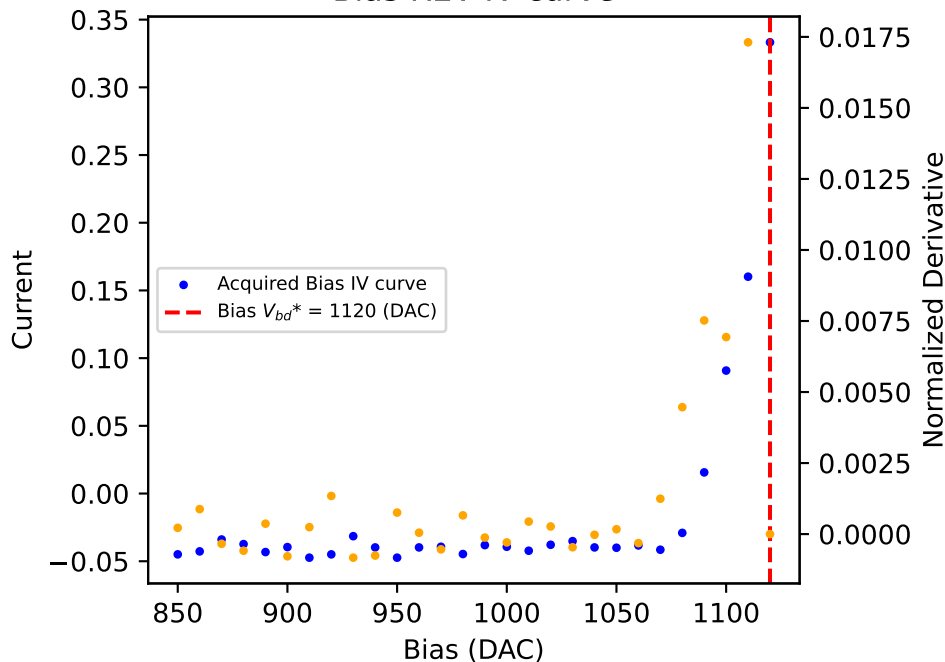
Bias + Trim REV IV curve



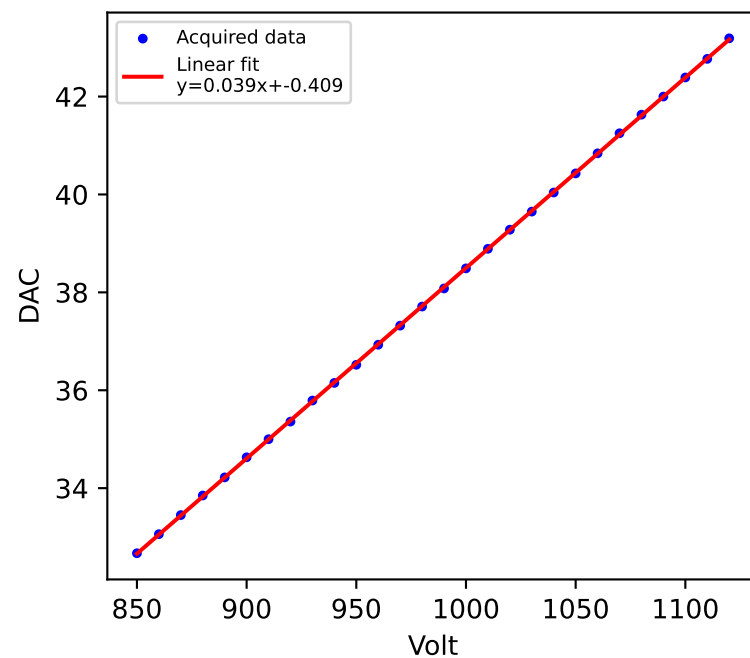
Trim REV IV curve



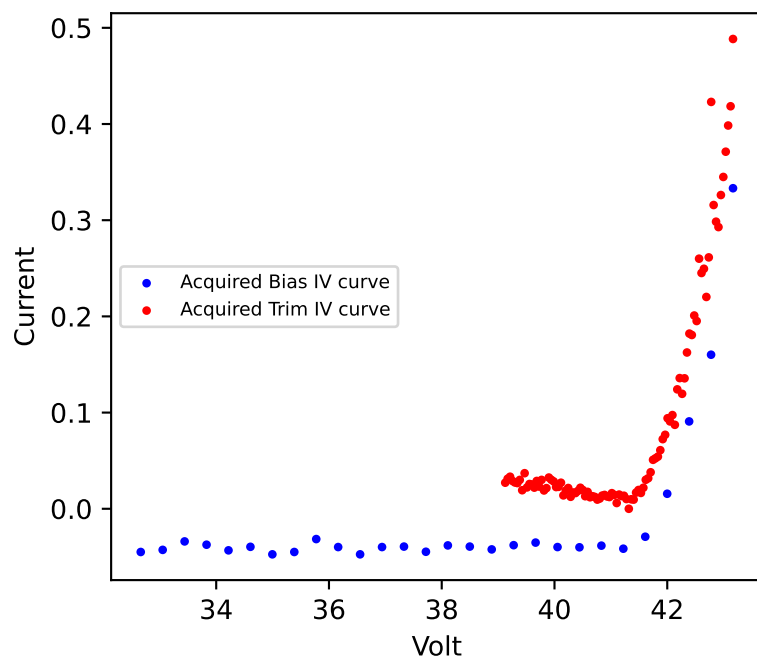
Bias REV IV curve



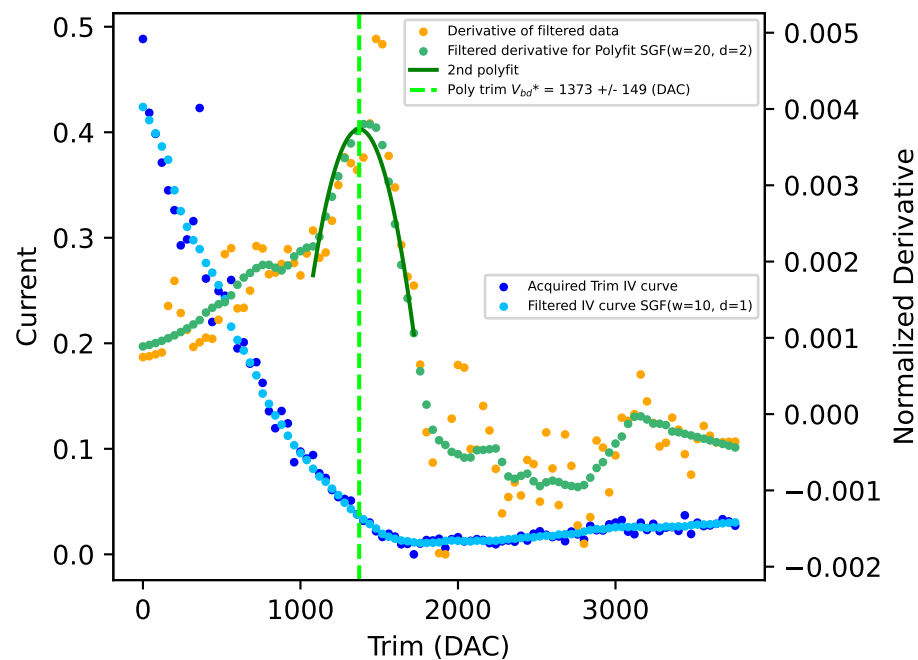
Bias DAC-VOLT conversion



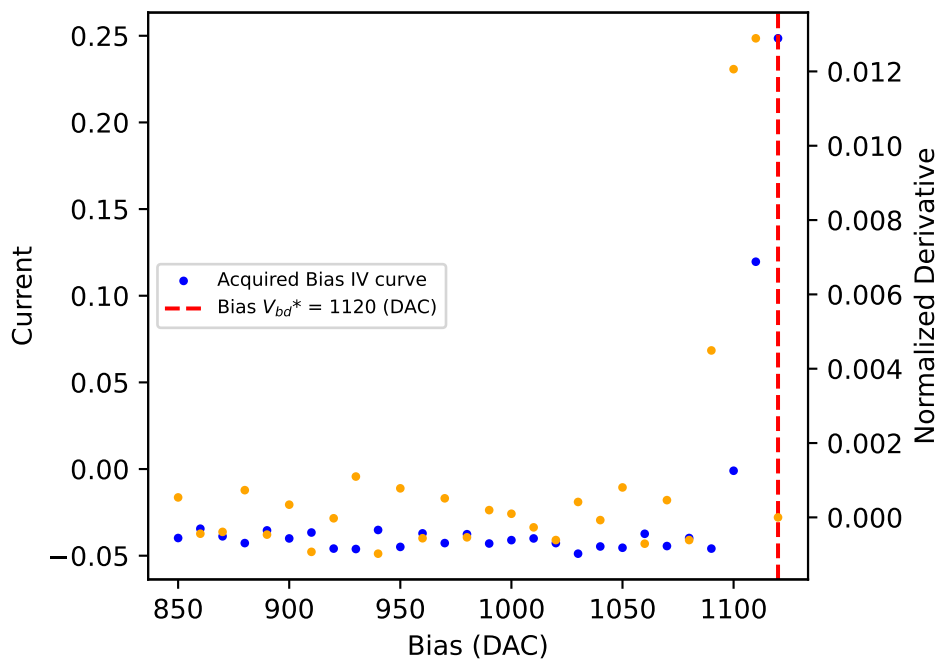
Bias + Trim REV IV curve



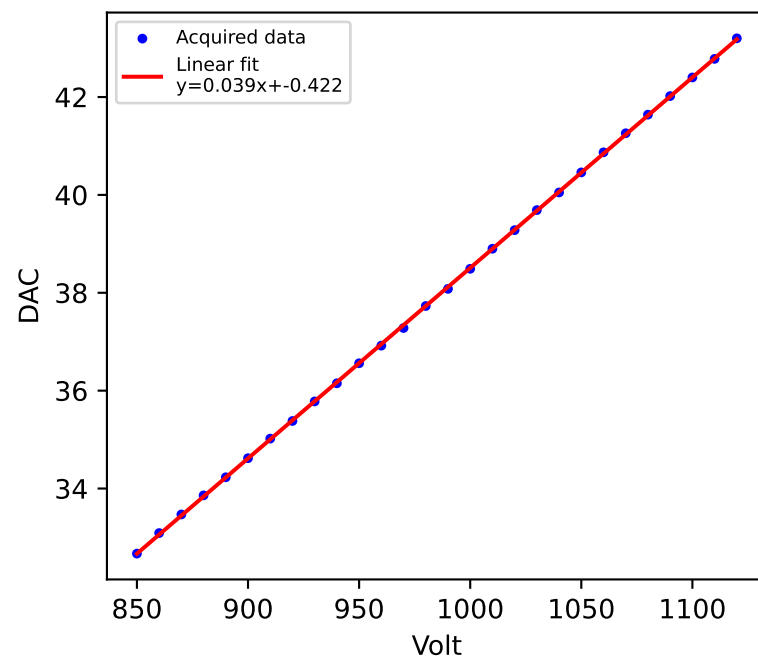
Trim REV IV curve



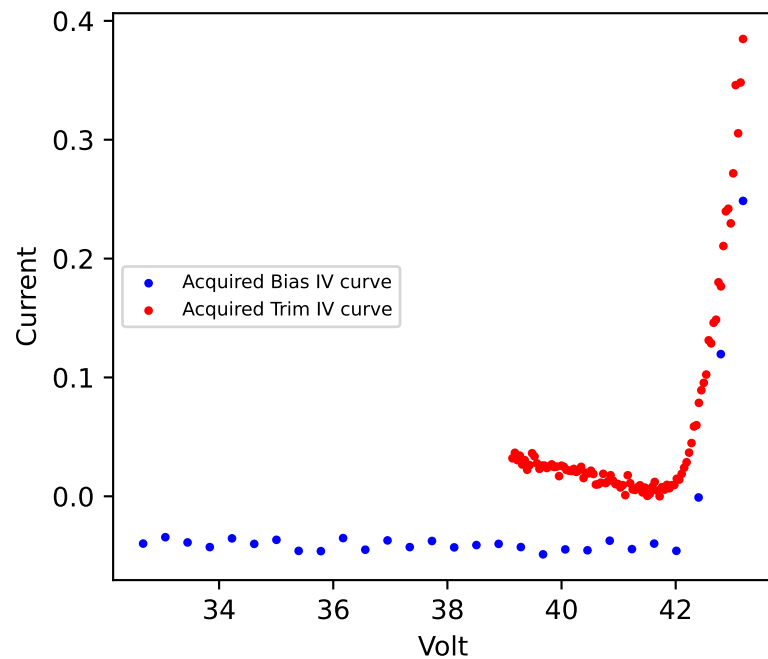
Bias REV IV curve



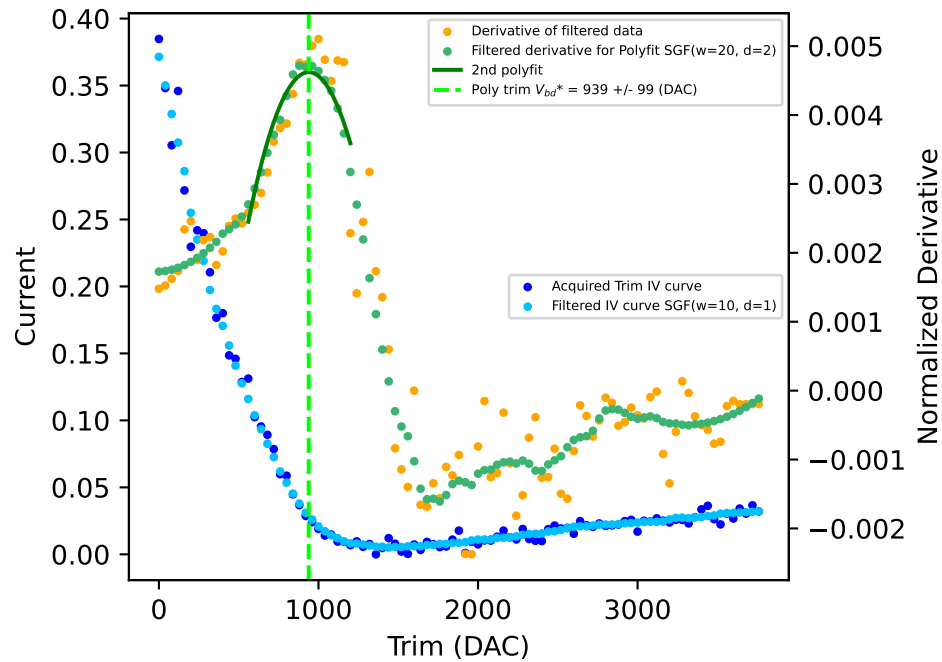
Bias DAC-VOLT conversion



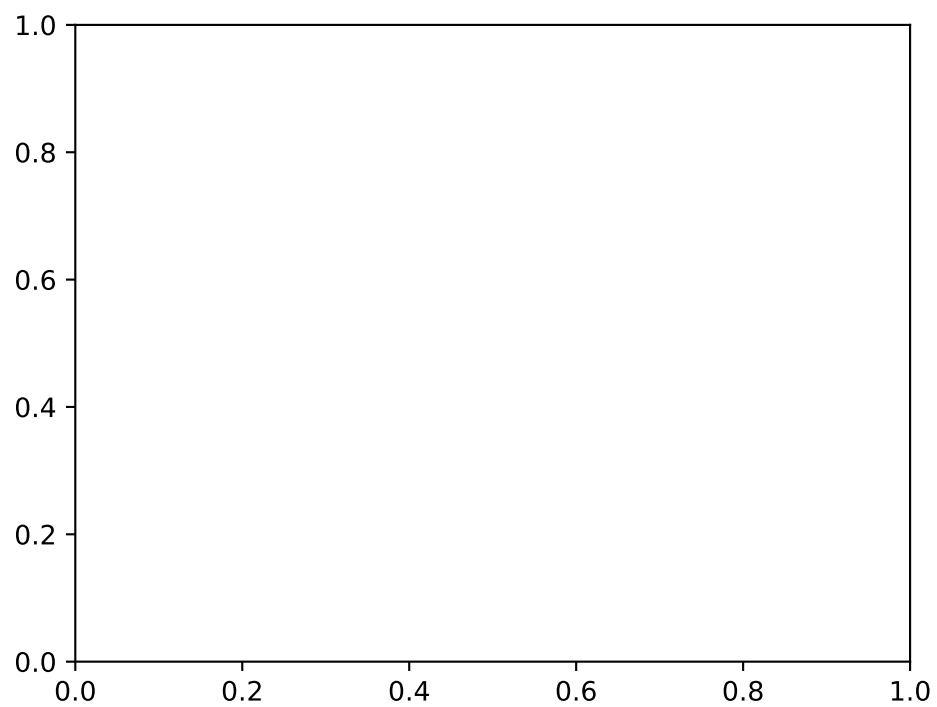
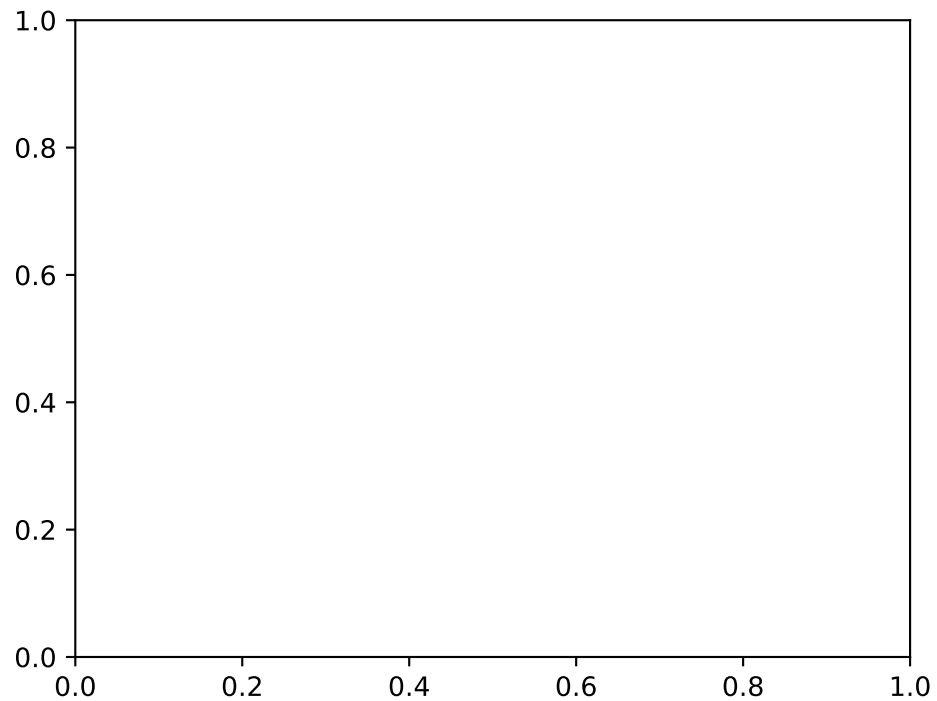
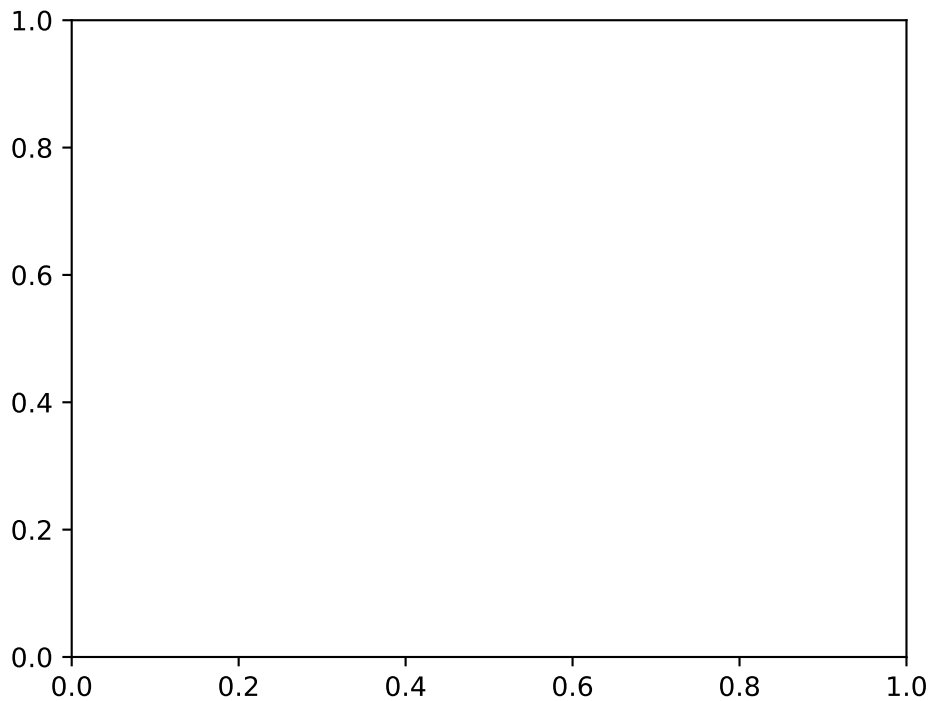
Bias + Trim REV IV curve



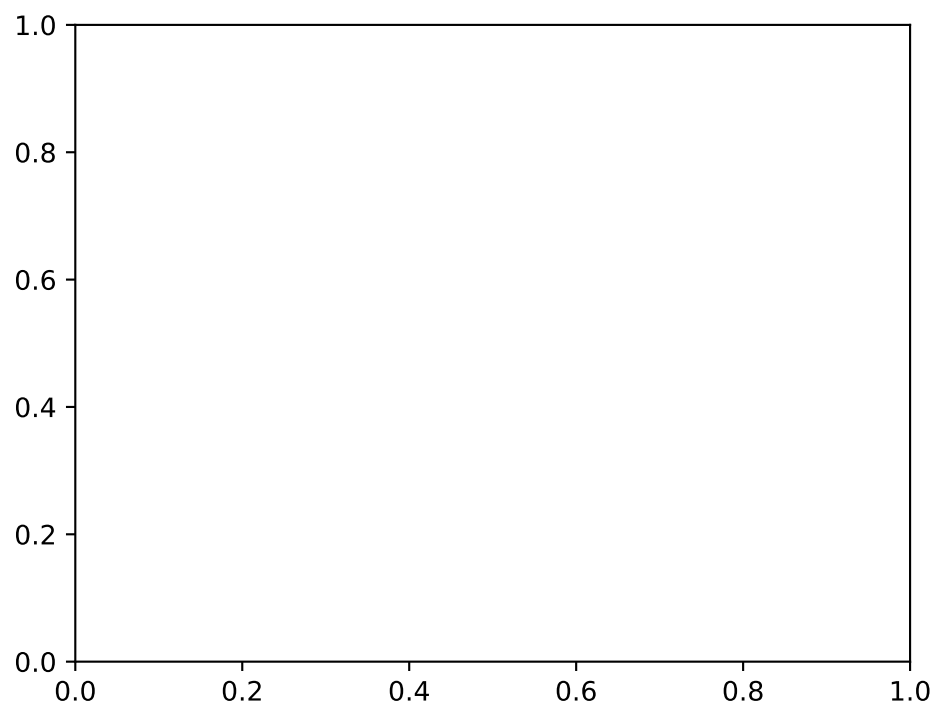
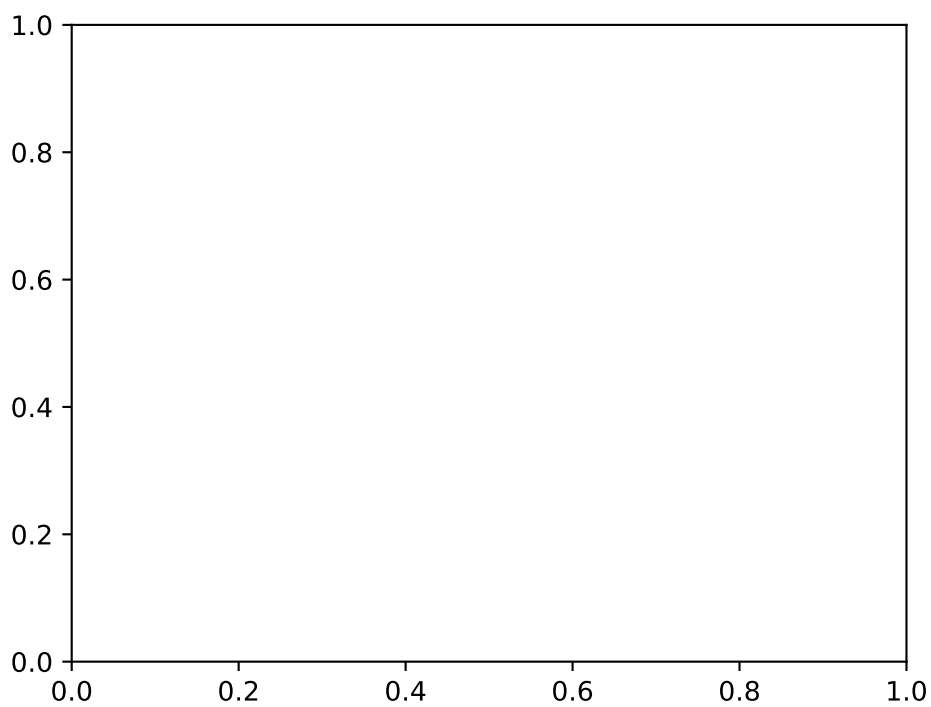
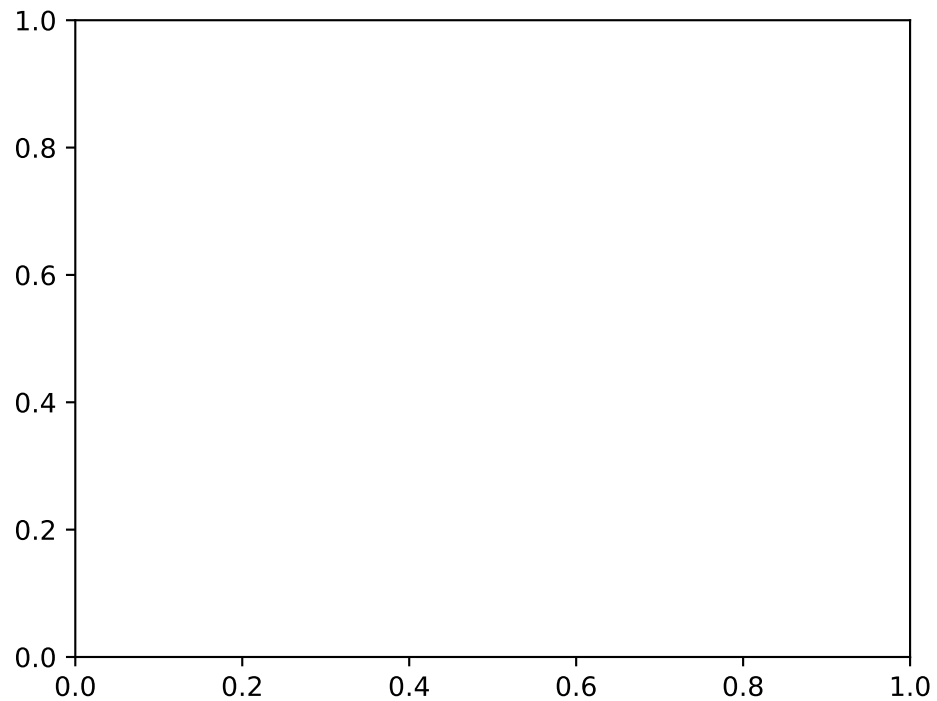
Trim REV IV curve



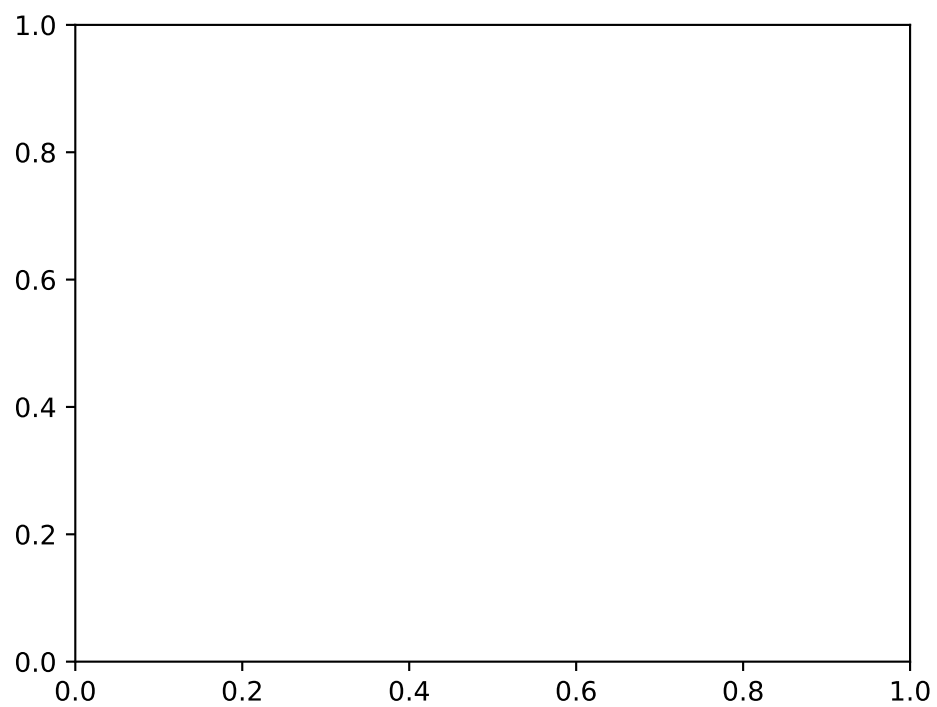
ENDPOINT:112 APA:4 AFE:4 Config_CH:32 CH_daq:40 SiPM:HPK --> MISSING FILE



ENDPOINT:112 APA:4 AFE:4 Config_CH:34 CH_daq:42 SiPM:HPK --> MISSING FILE



ENDPOINT:112 APA:4 AFE:4 Config_CH:37 CH_daq:45 SiPM:HPK --> MISSING FILE



ENDPOINT:112 APA:4 AFE:4 Config_CH:39 CH_daq:47 SiPM:HPK --> MISSING FILE

