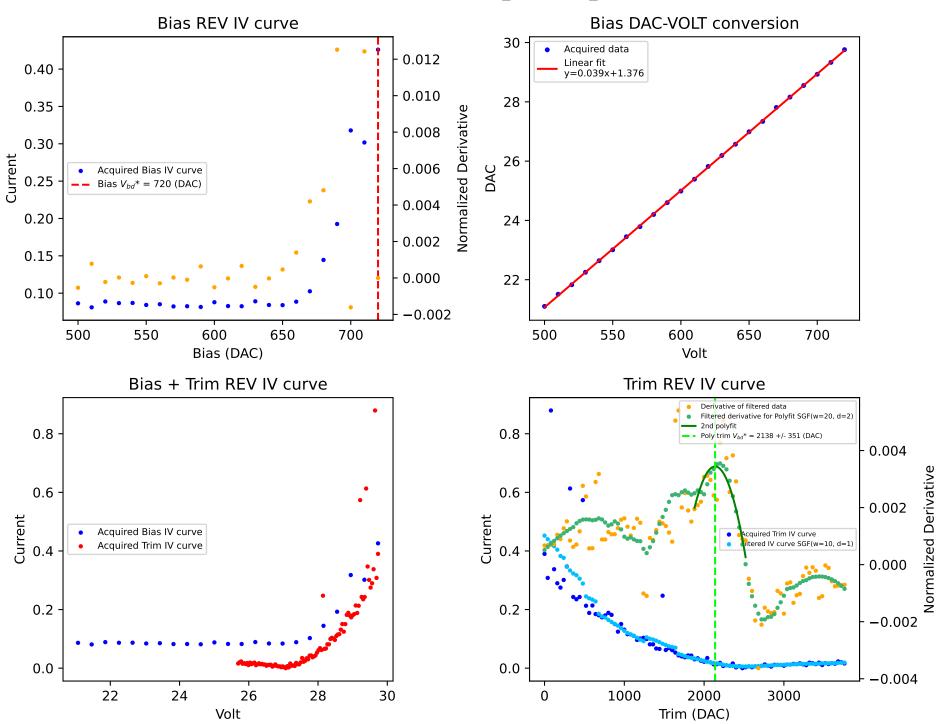
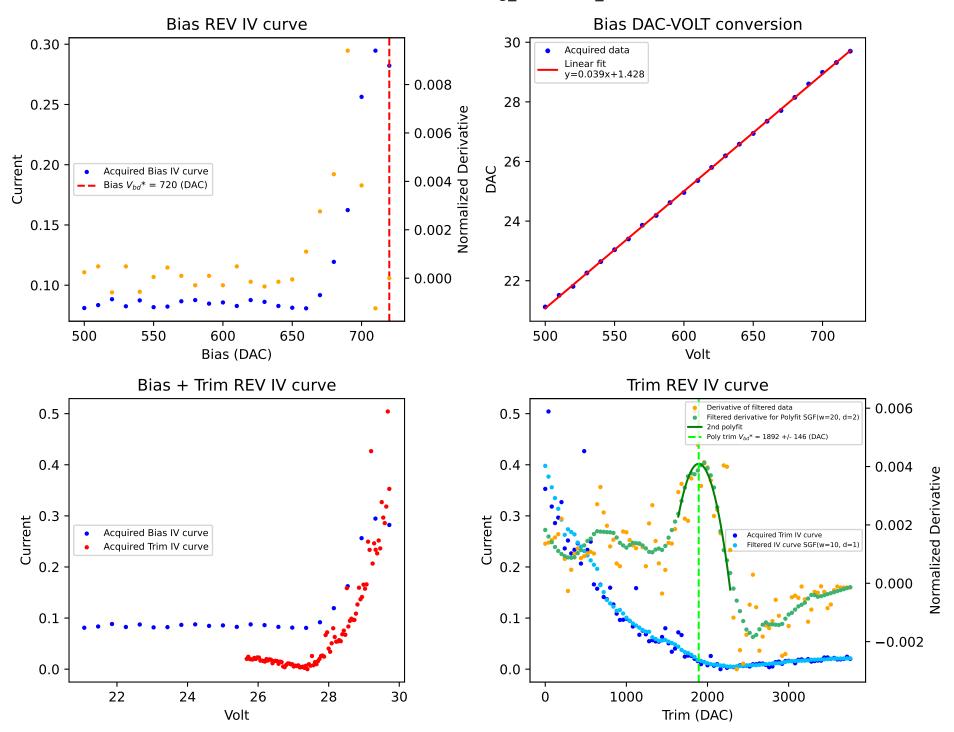
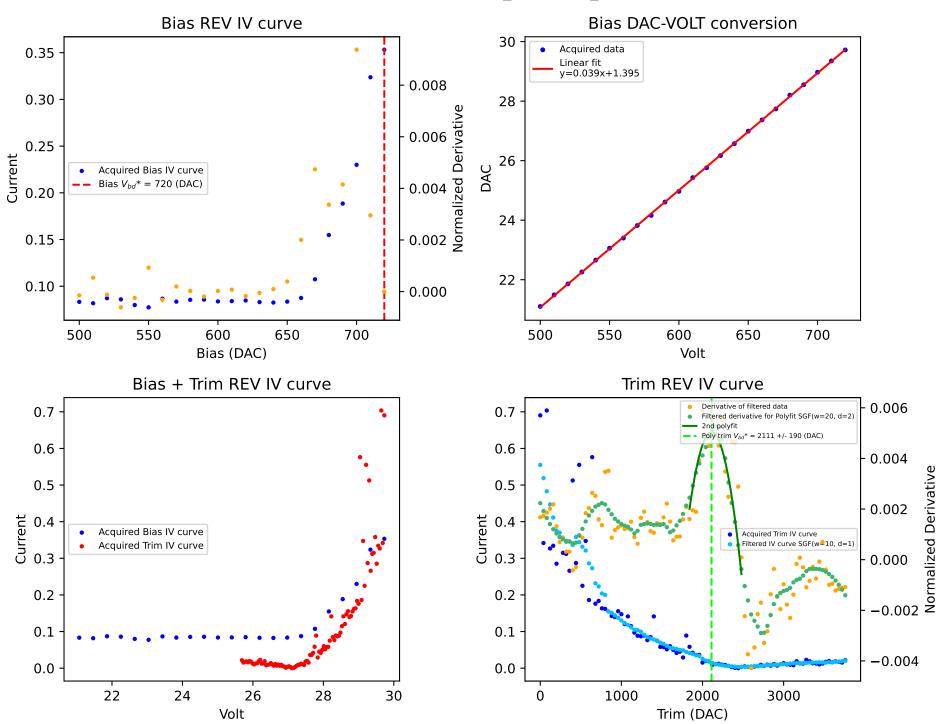
ENDPOINT:104 APA:1 AFE:0 Config_CH:0 DAQ_CH:0 SiPM:FBK



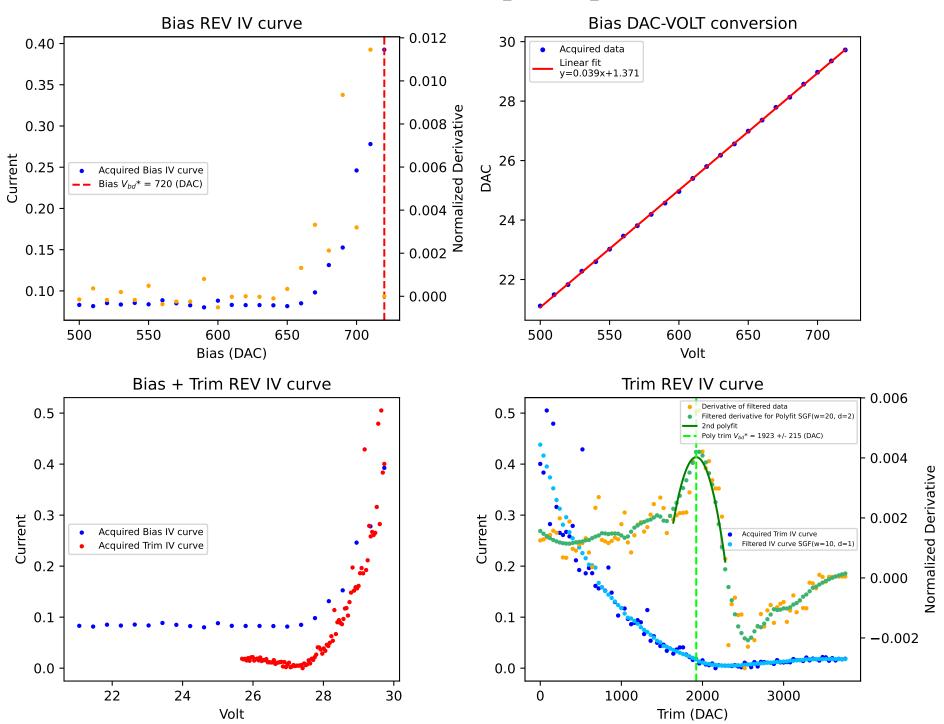
ENDPOINT:104 APA:1 AFE:0 Config CH:1 DAQ CH:1 SiPM:FBK



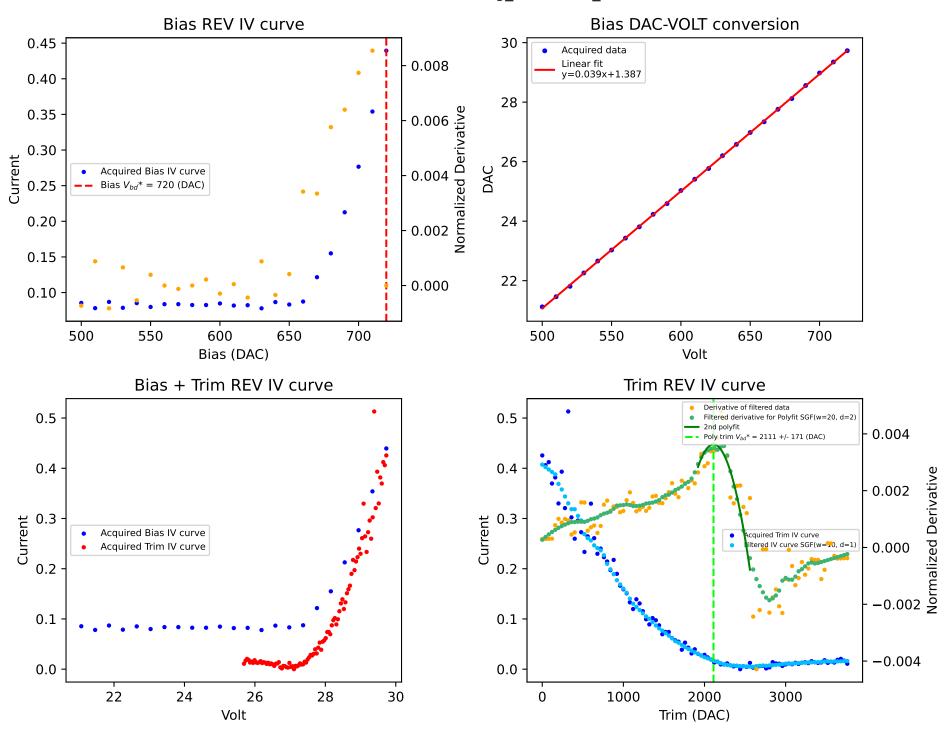
ENDPOINT:104 APA:1 AFE:0 Config_CH:2 DAQ_CH:2 SiPM:FBK



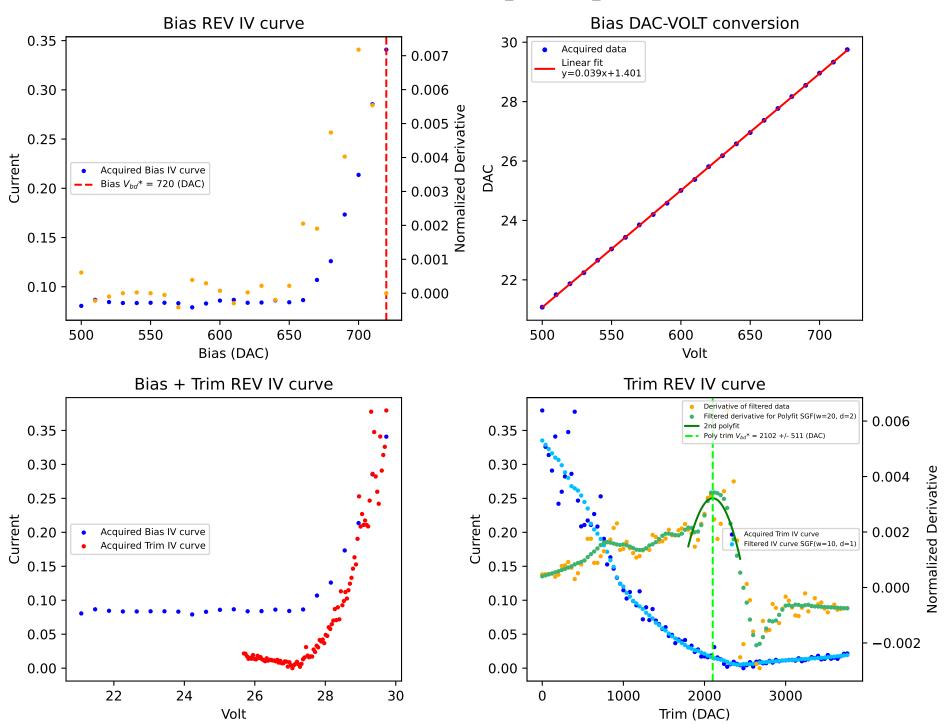
ENDPOINT:104 APA:1 AFE:0 Config_CH:3 DAQ_CH:3 SiPM:FBK

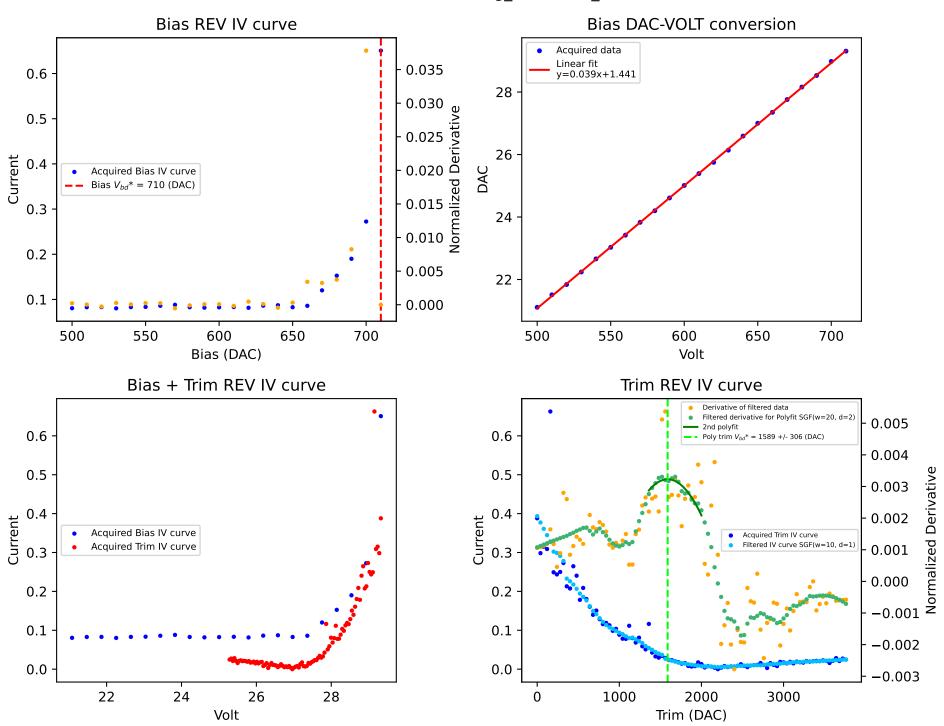


ENDPOINT:104 APA:1 AFE:0 Config CH:4 DAQ CH:4 SiPM:FBK

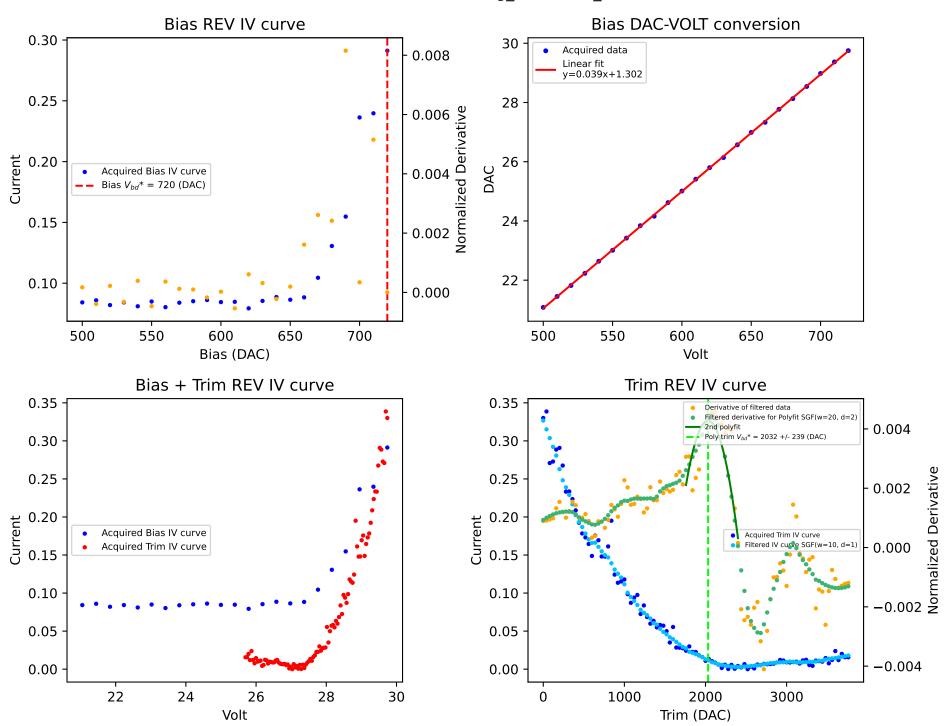


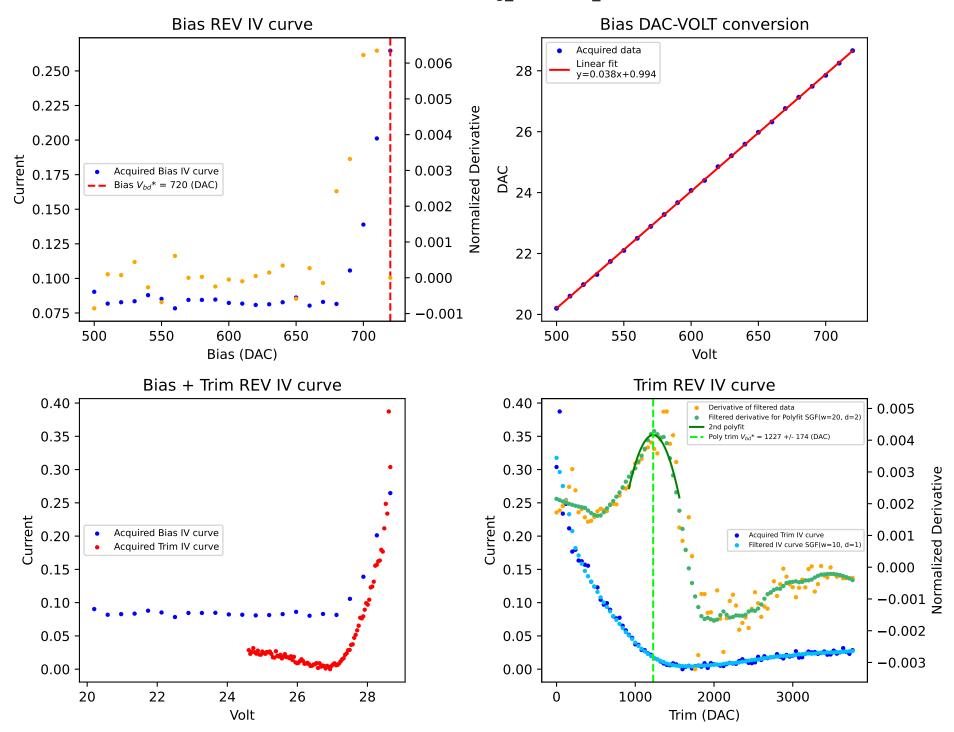
ENDPOINT:104 APA:1 AFE:0 Config_CH:5 DAQ_CH:5 SiPM:FBK

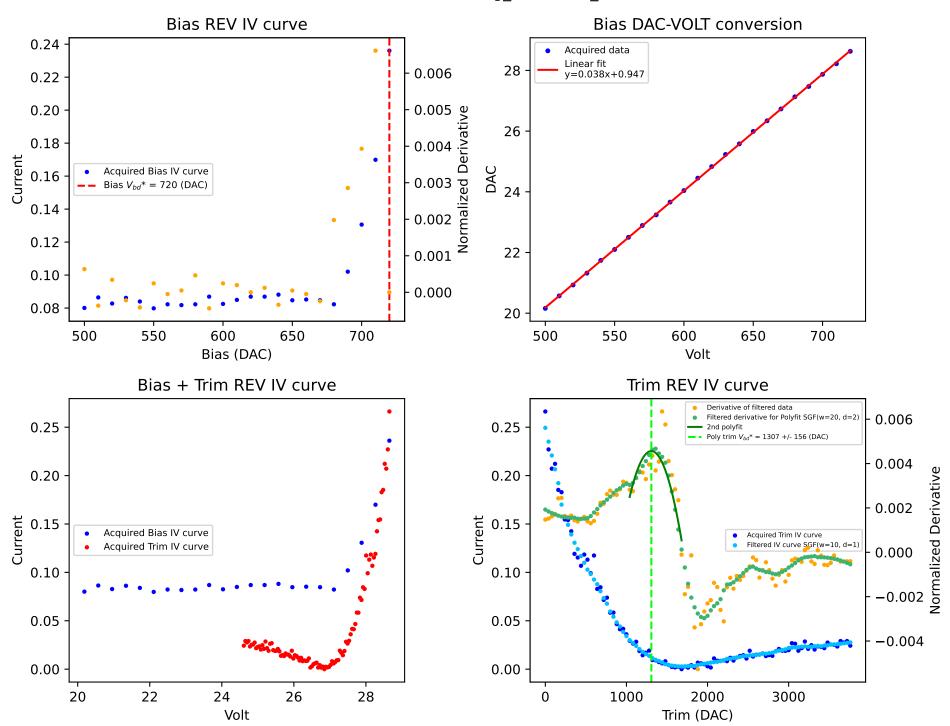




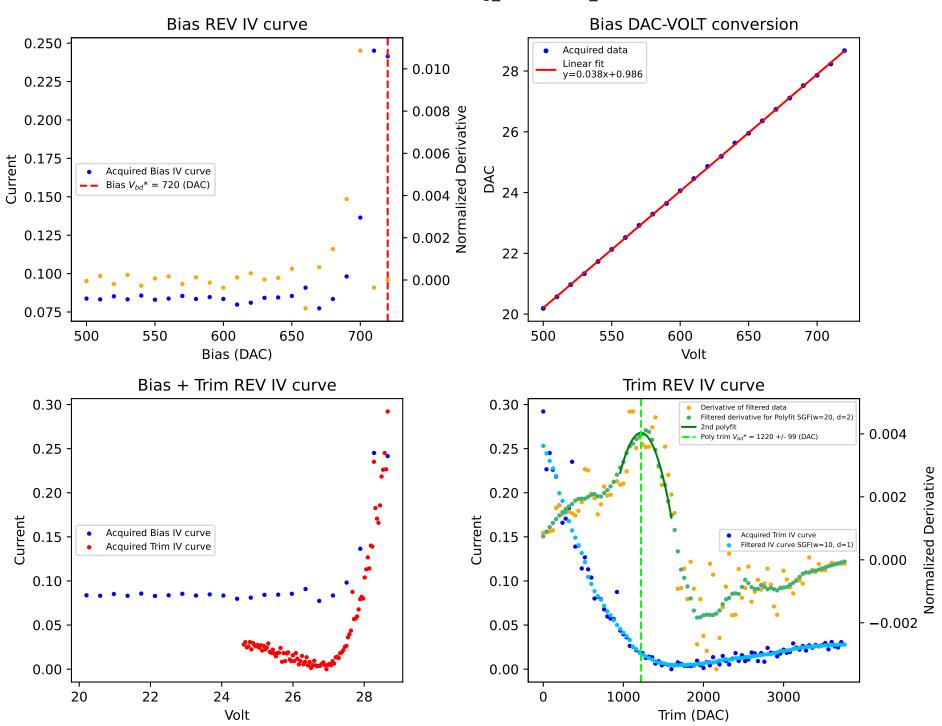
ENDPOINT:104 APA:1 AFE:0 Config CH:7 DAQ CH:7 SiPM:FBK



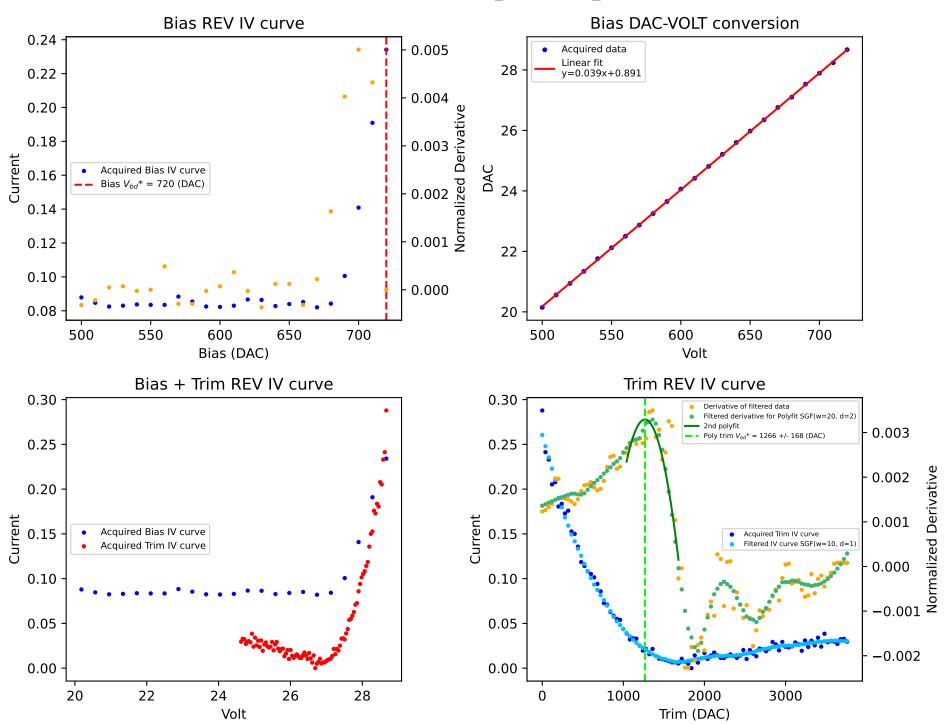




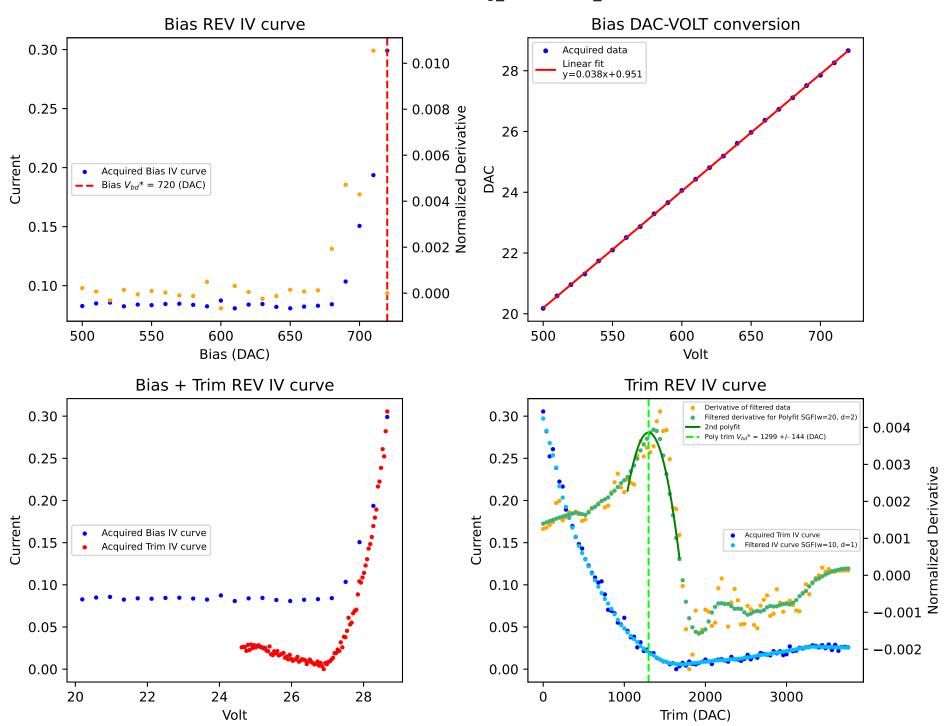
ENDPOINT:104 APA:1 AFE:1 Config CH:10 DAQ CH:12 SiPM:FBK



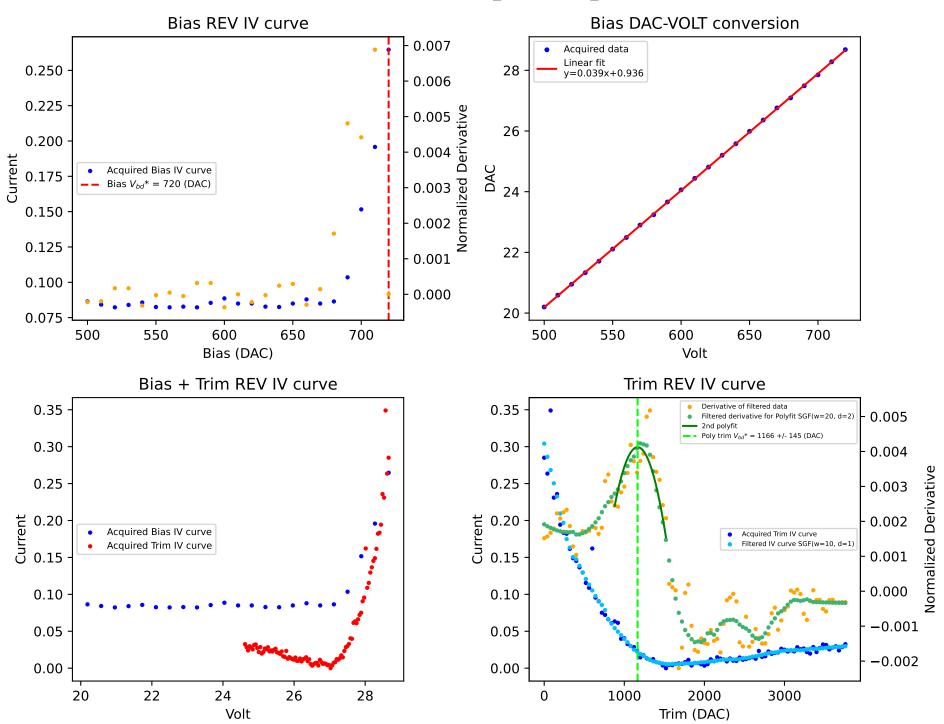
ENDPOINT:104 APA:1 AFE:1 Config_CH:11 DAQ_CH:13 SiPM:FBK



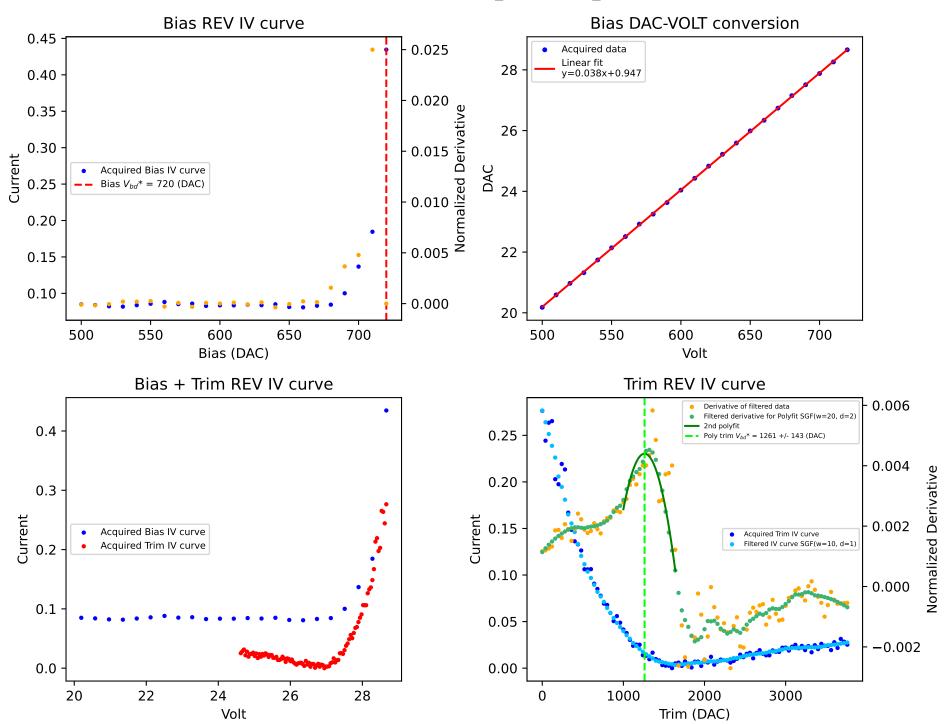
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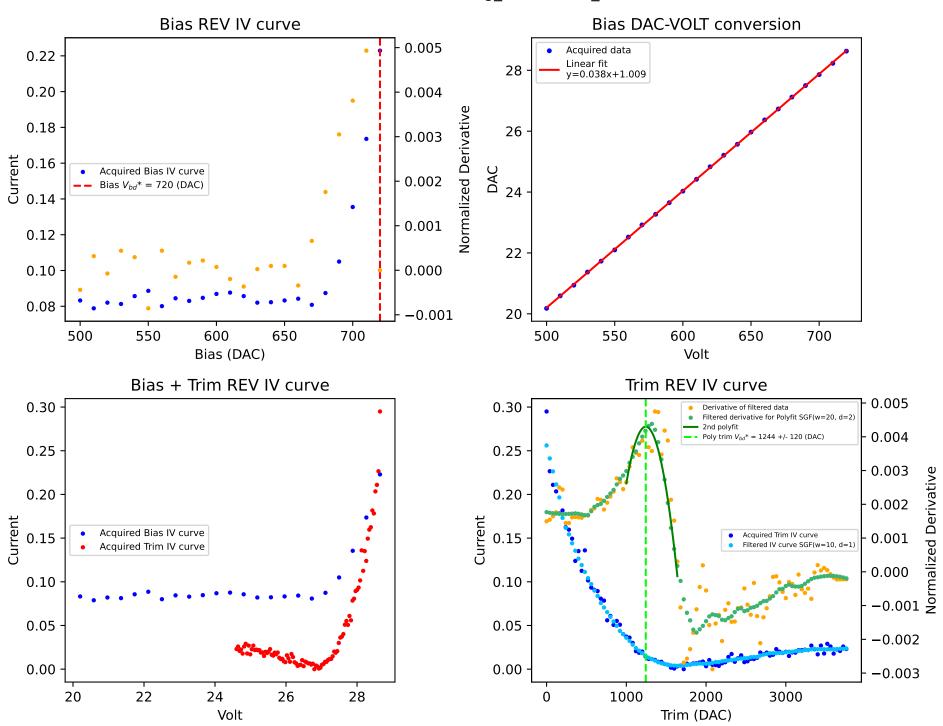
ENDPOINT:104 APA:1 AFE:1 Config_CH:13 DAQ_CH:15 SiPM:FBK



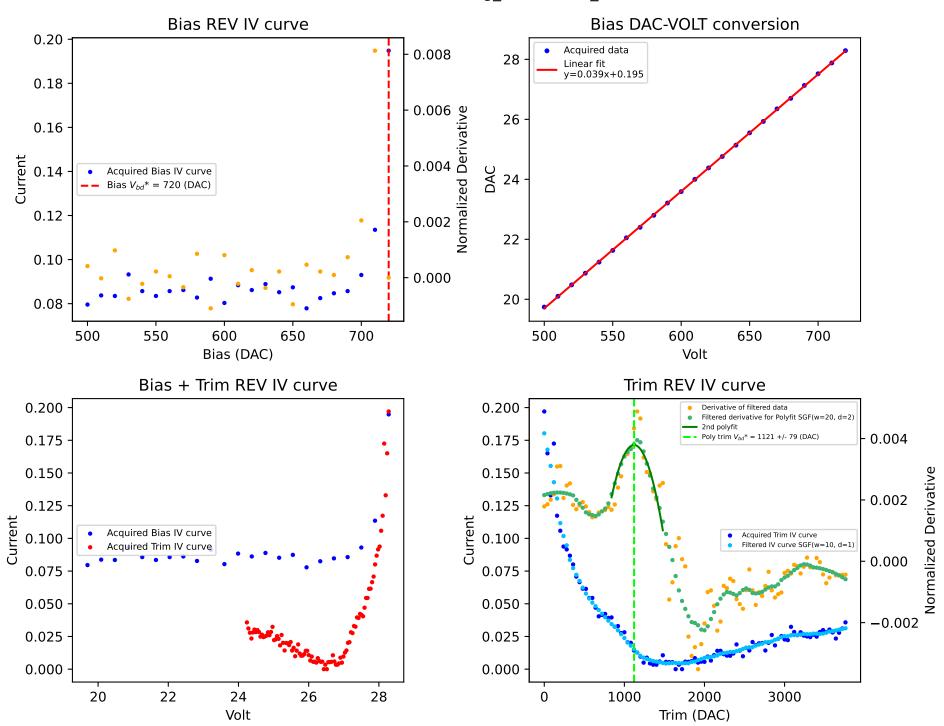
ENDPOINT:104 APA:1 AFE:1 Config_CH:14 DAQ_CH:16 SiPM:FBK

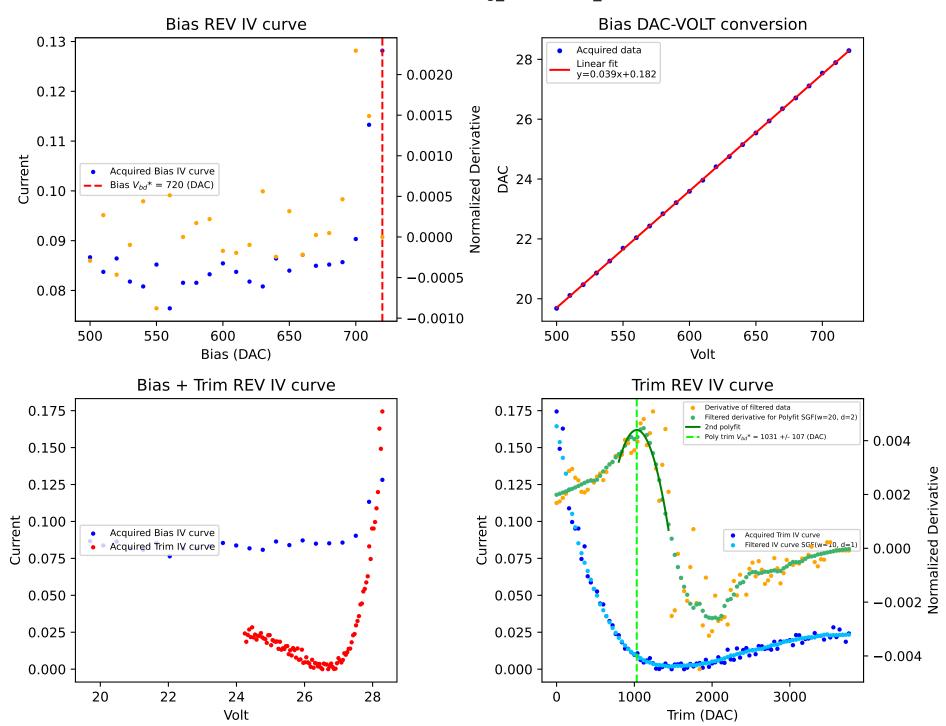


ENDPOINT:104 APA:1 AFE:1 Config CH:15 DAQ CH:17 SiPM:FBK

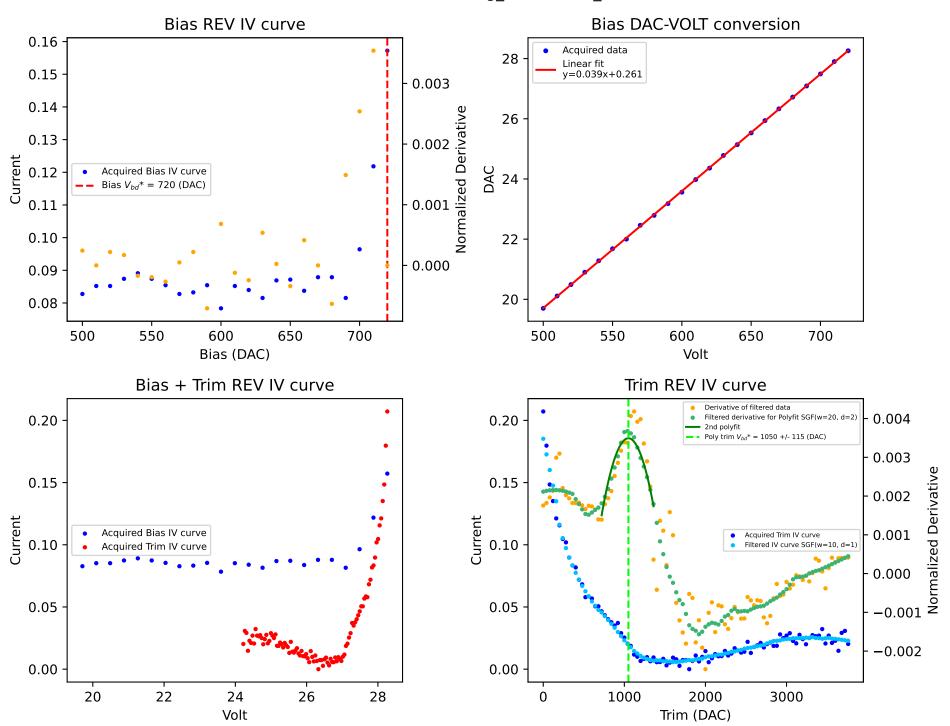


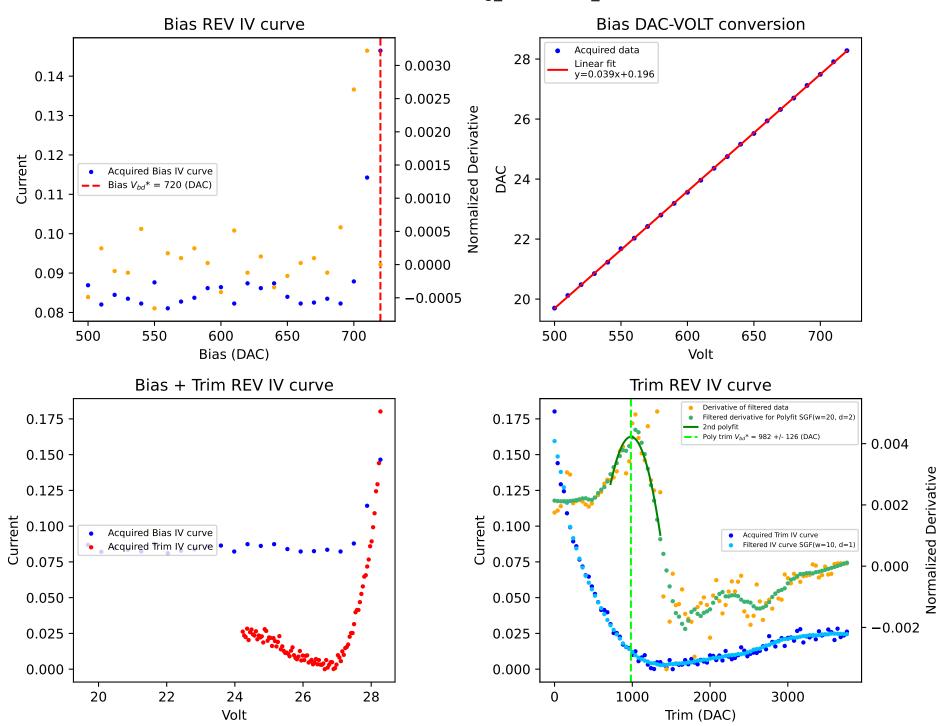
ENDPOINT:104 APA:1 AFE:2 Config CH:16 DAQ CH:20 SiPM:FBK



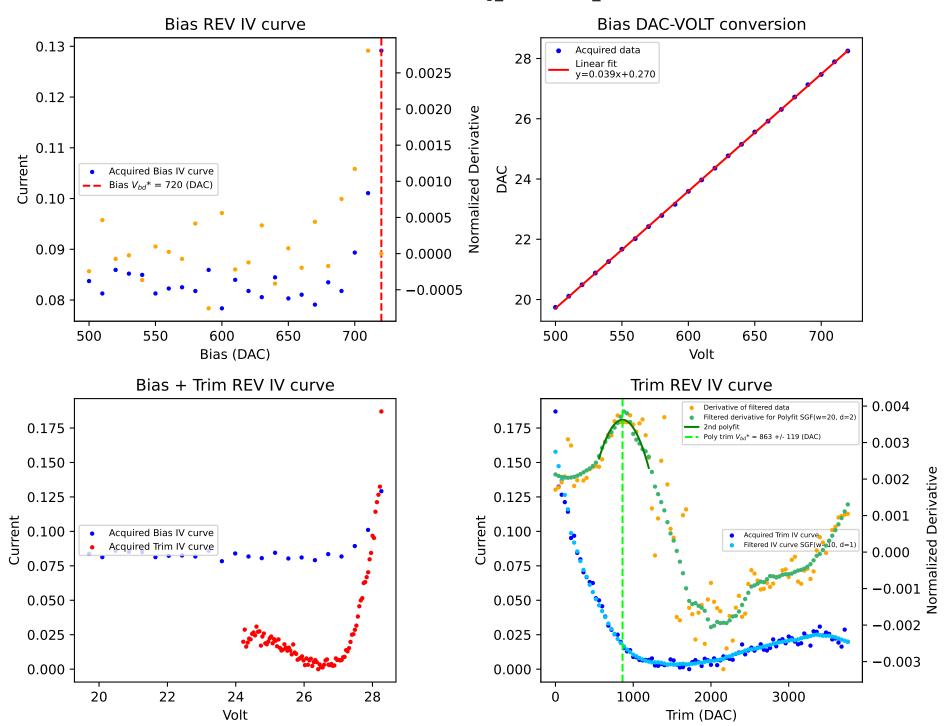


ENDPOINT:104 APA:1 AFE:2 Config CH:18 DAQ CH:22 SiPM:FBK

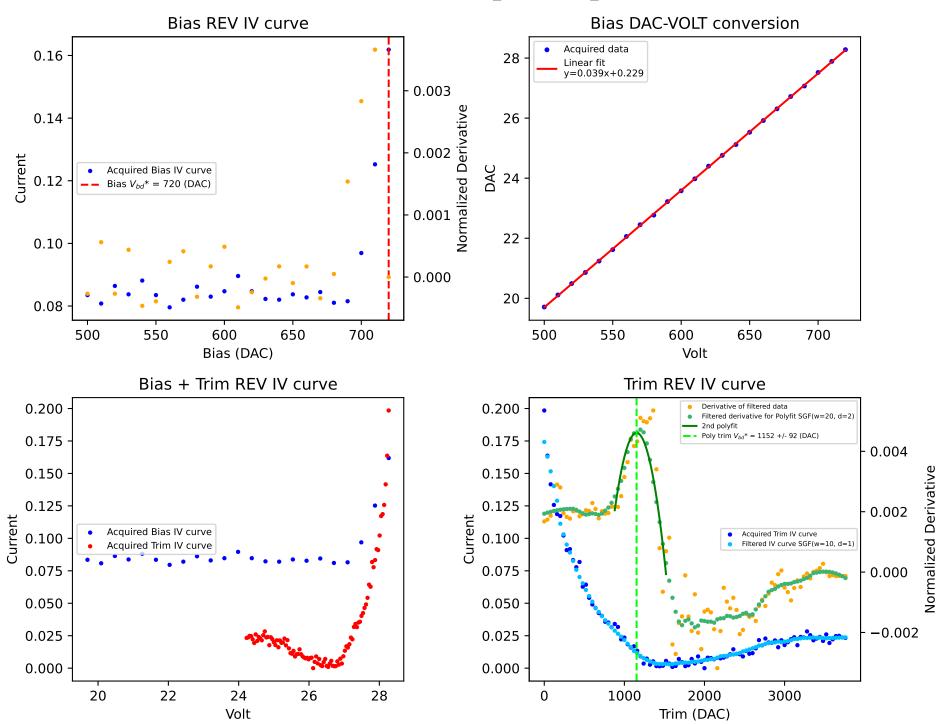


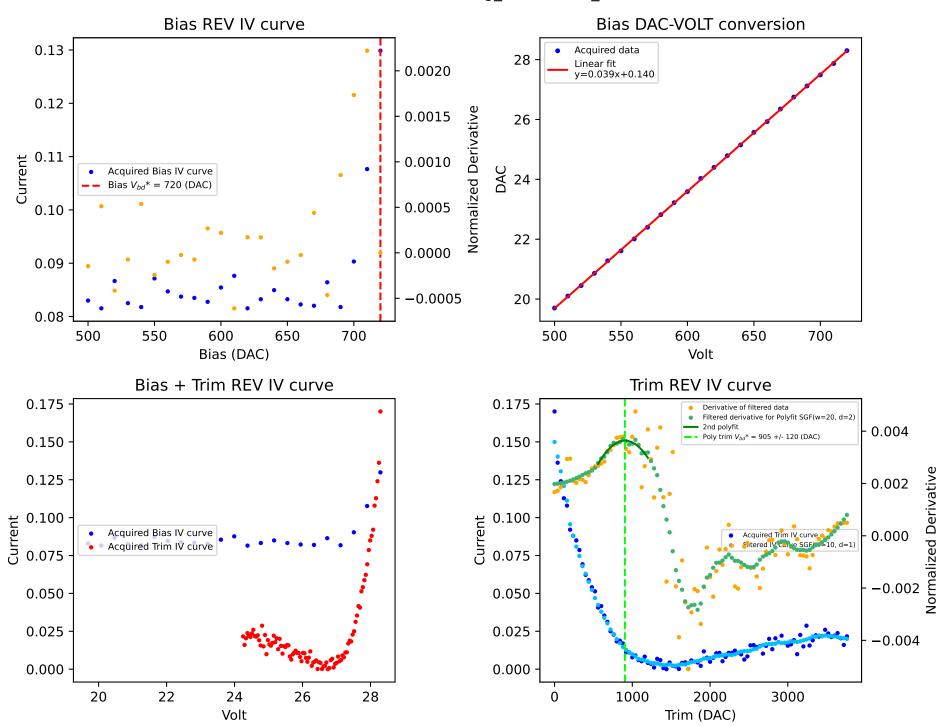


ENDPOINT:104 APA:1 AFE:2 Config CH:20 DAQ CH:24 SiPM:FBK

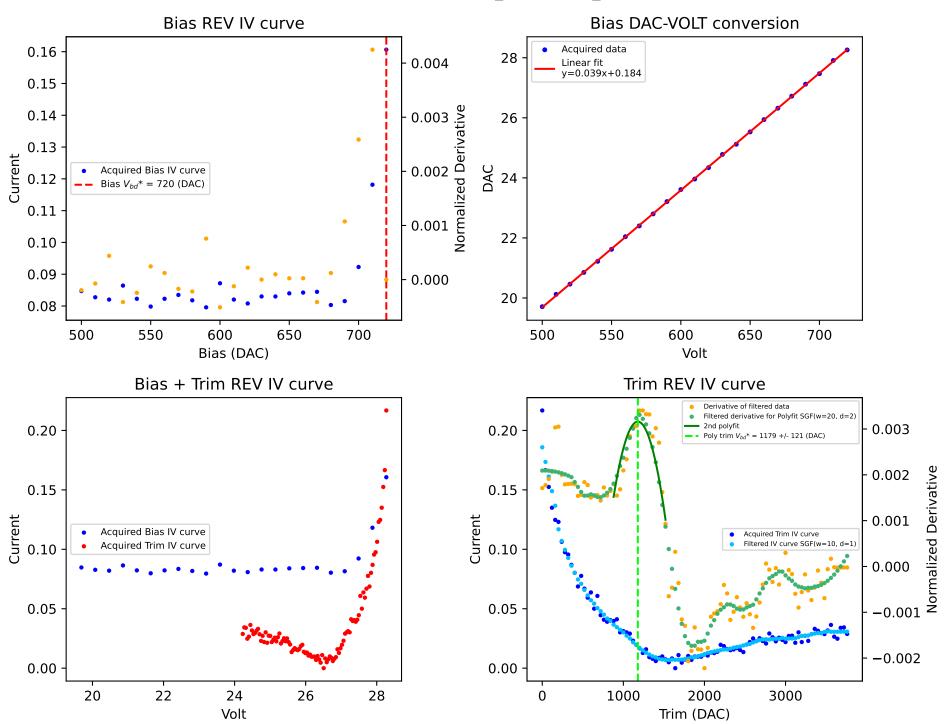


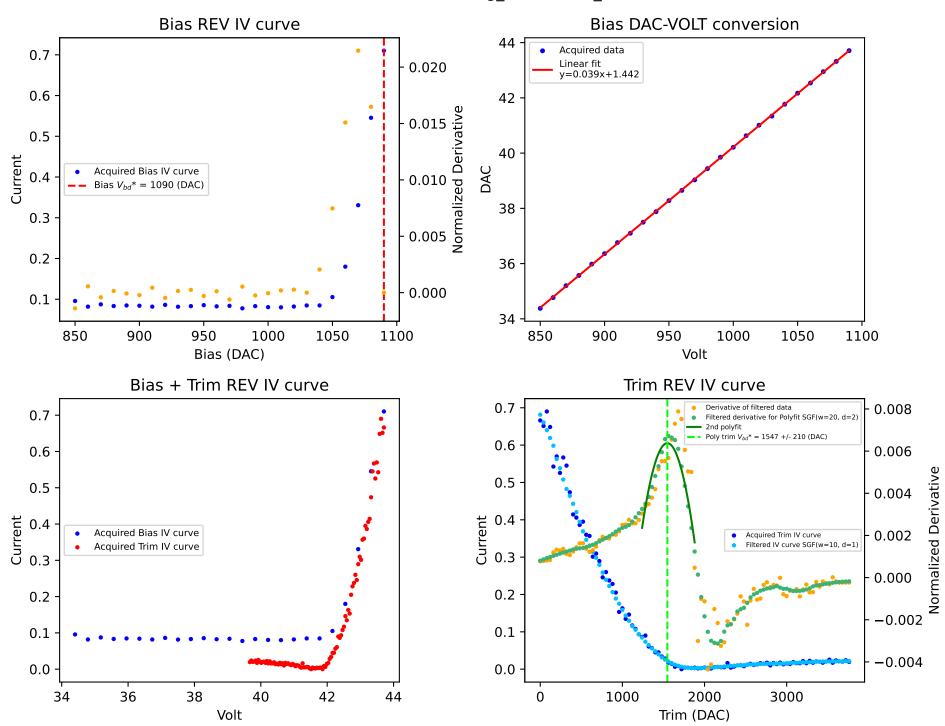
ENDPOINT:104 APA:1 AFE:2 Config_CH:21 DAQ_CH:25 SiPM:FBK

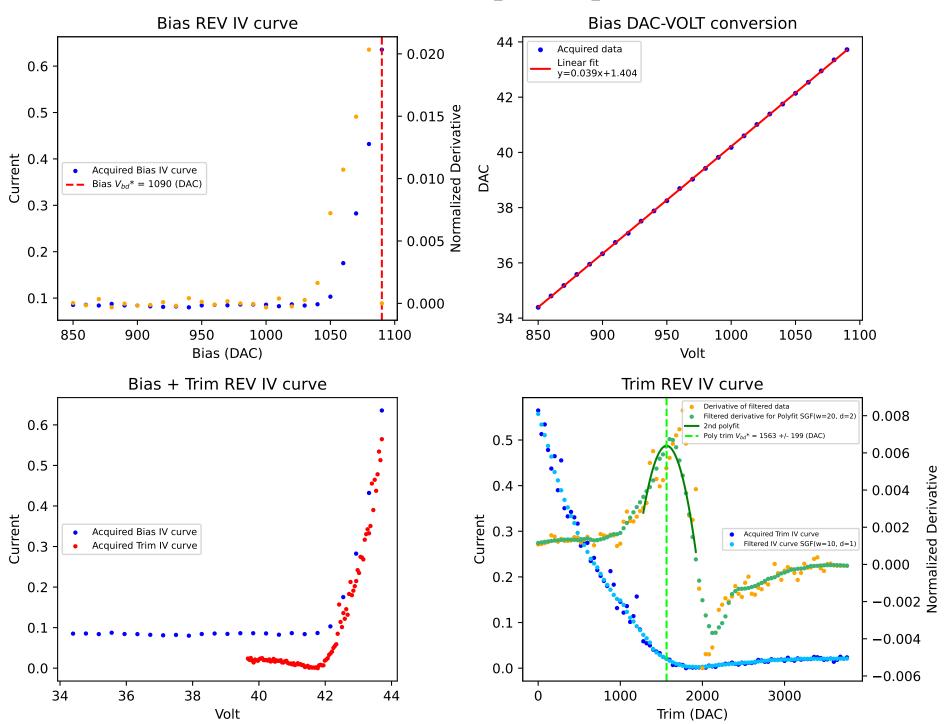


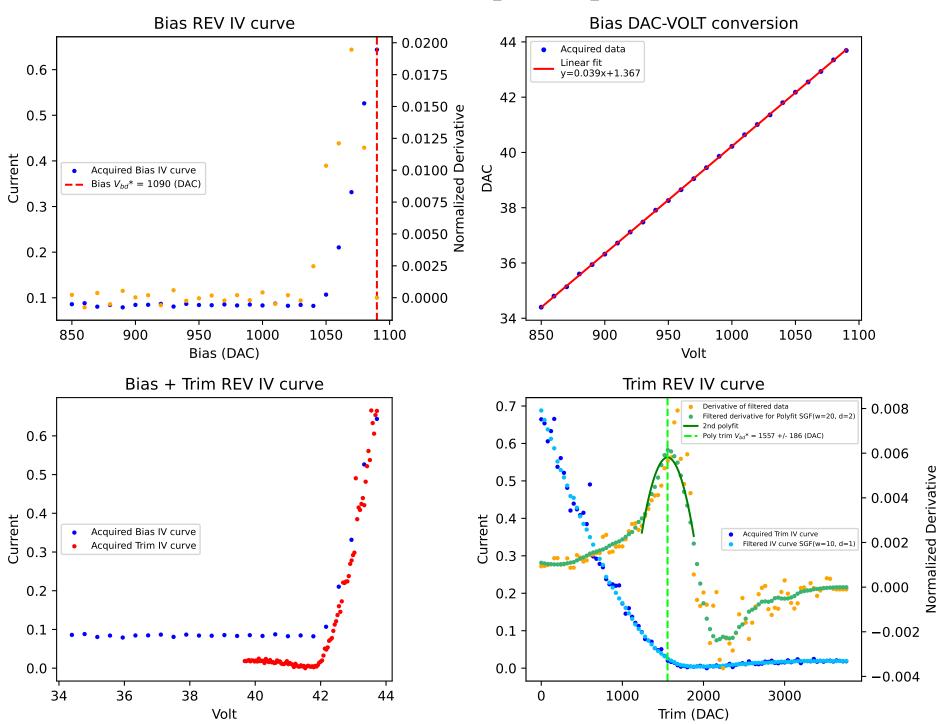


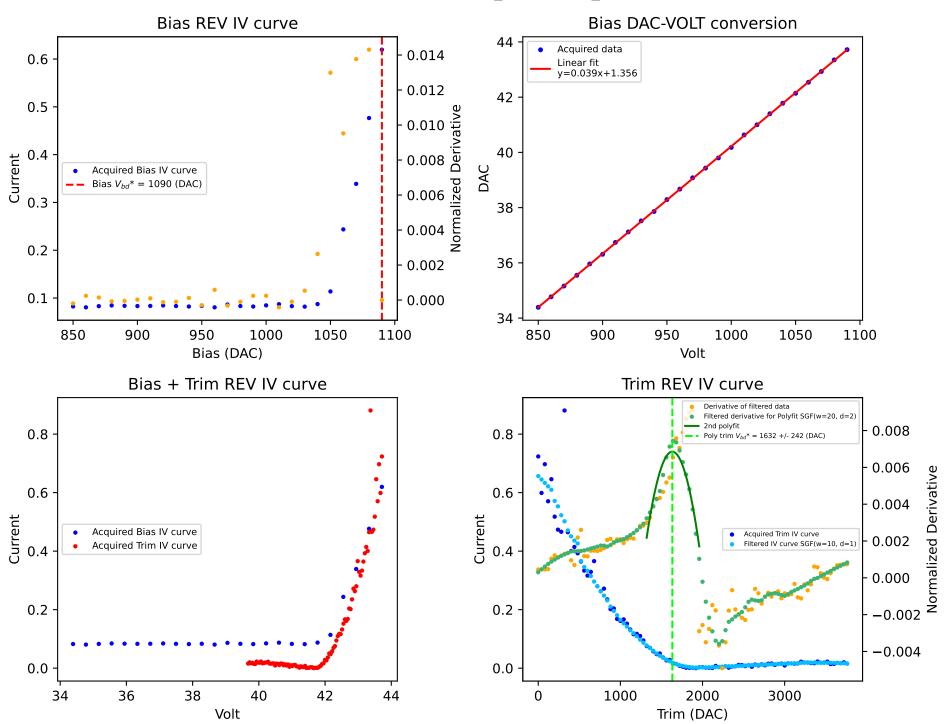
ENDPOINT:104 APA:1 AFE:2 Config_CH:23 DAQ_CH:27 SiPM:FBK

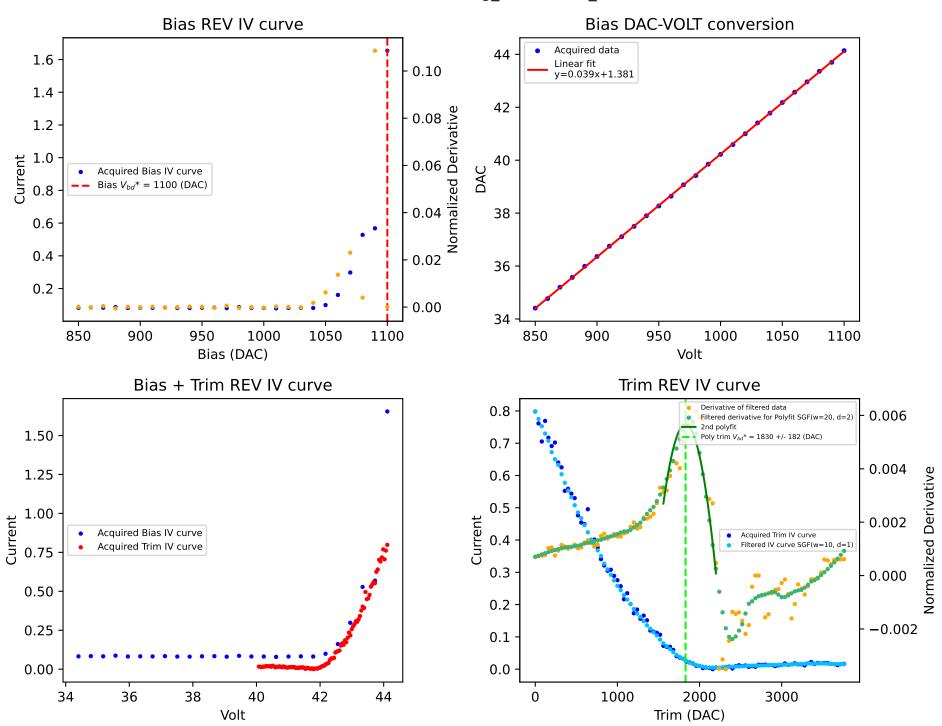




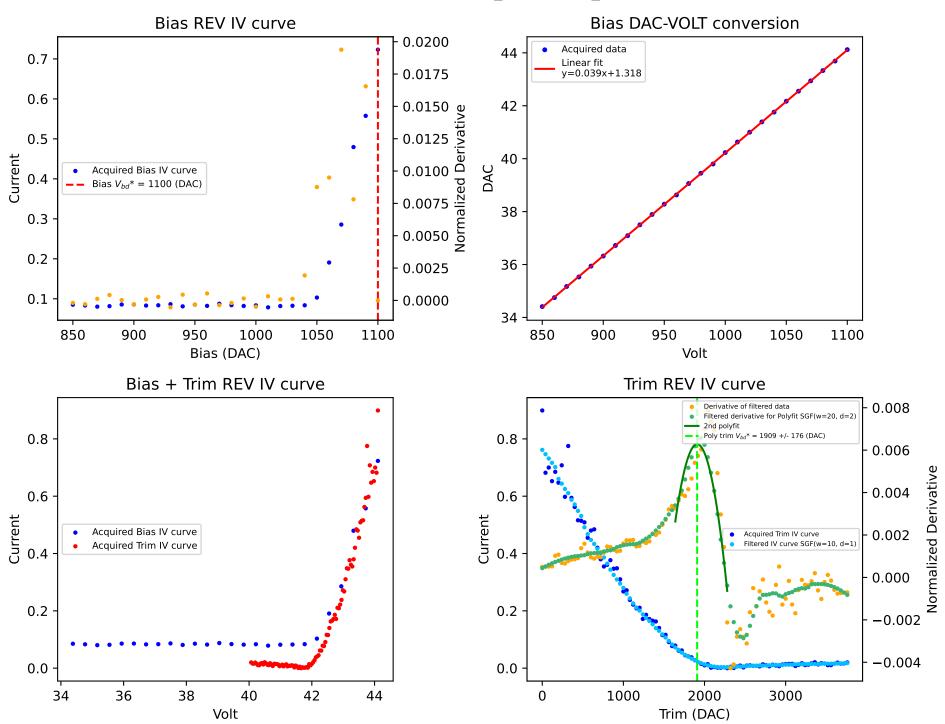




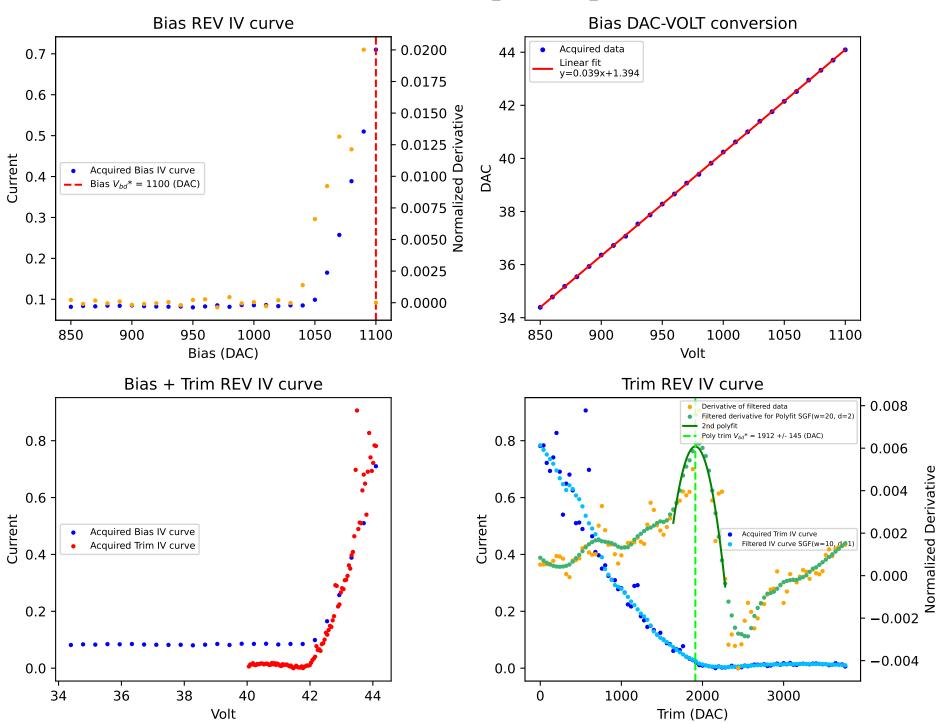


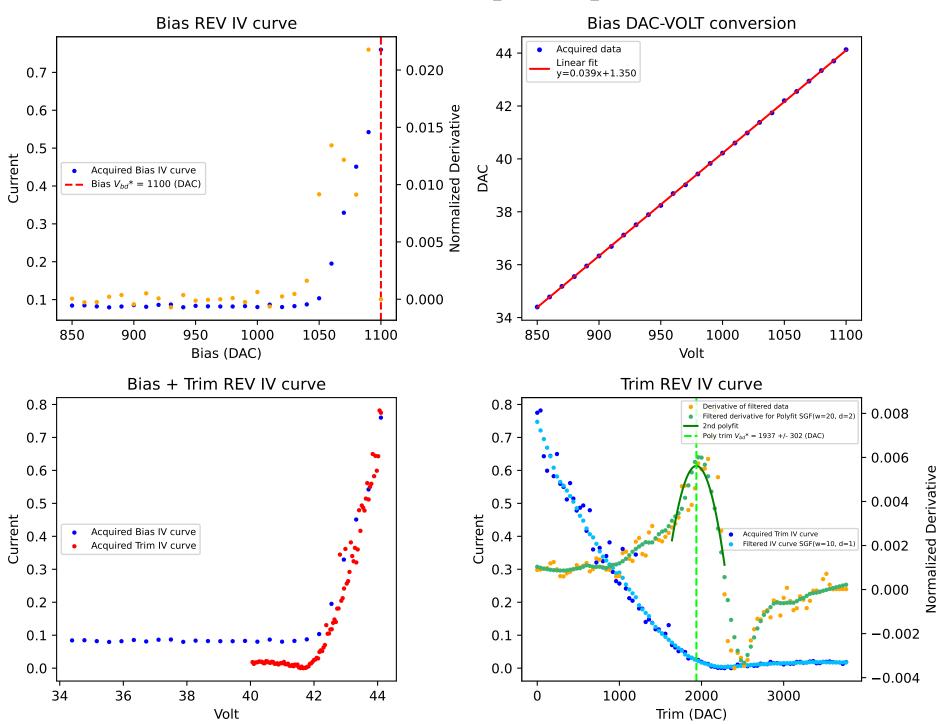


ENDPOINT:104 APA:1 AFE:3 Config_CH:29 DAQ_CH:35 SiPM:HPK

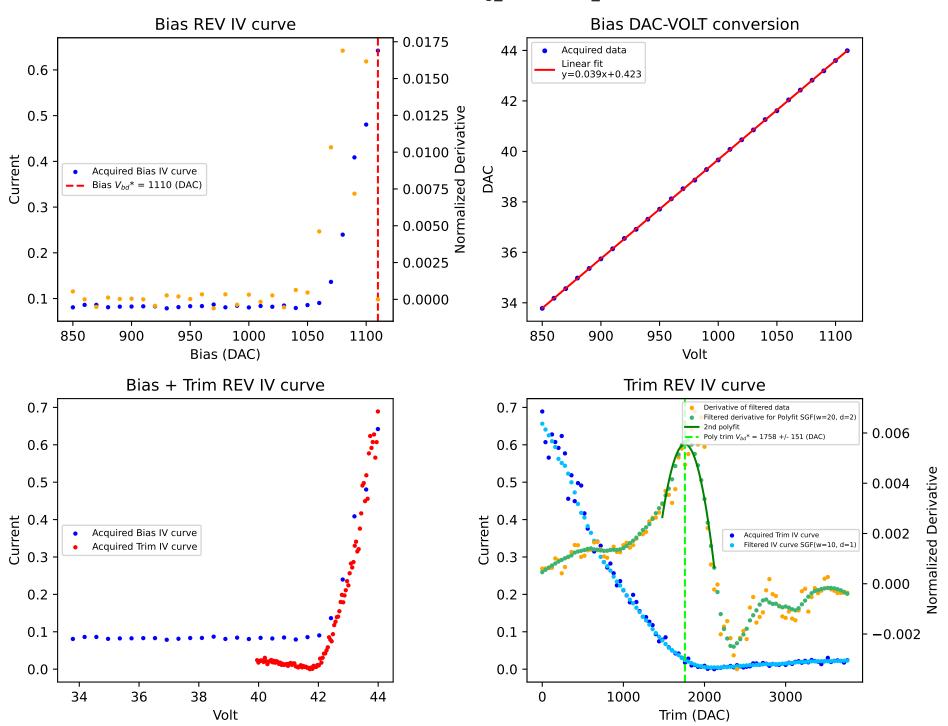


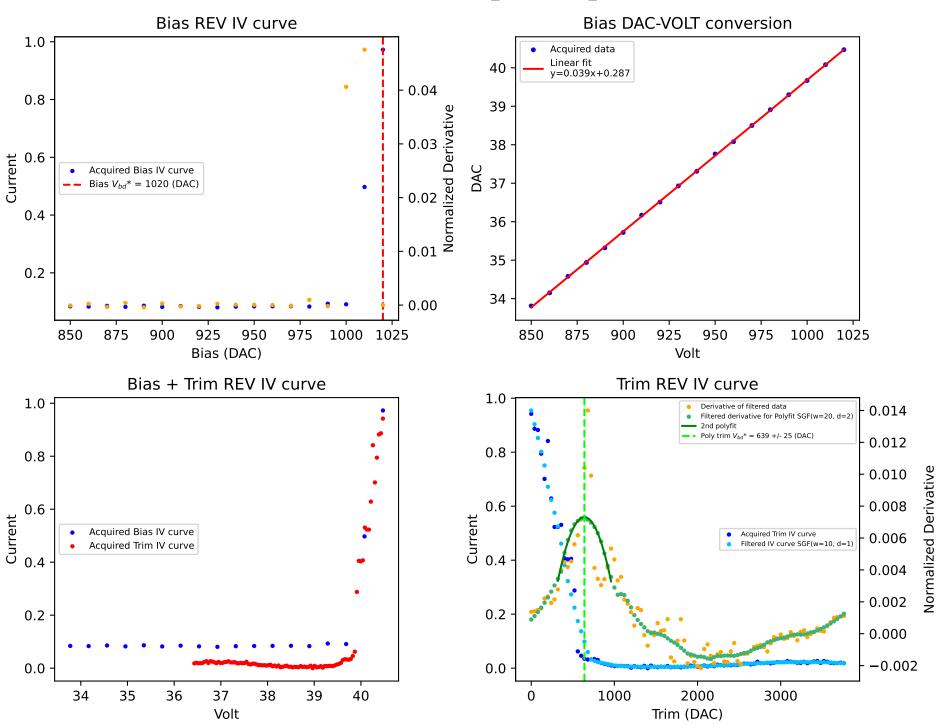
ENDPOINT:104 APA:1 AFE:3 Config_CH:30 DAQ_CH:36 SiPM:HPK

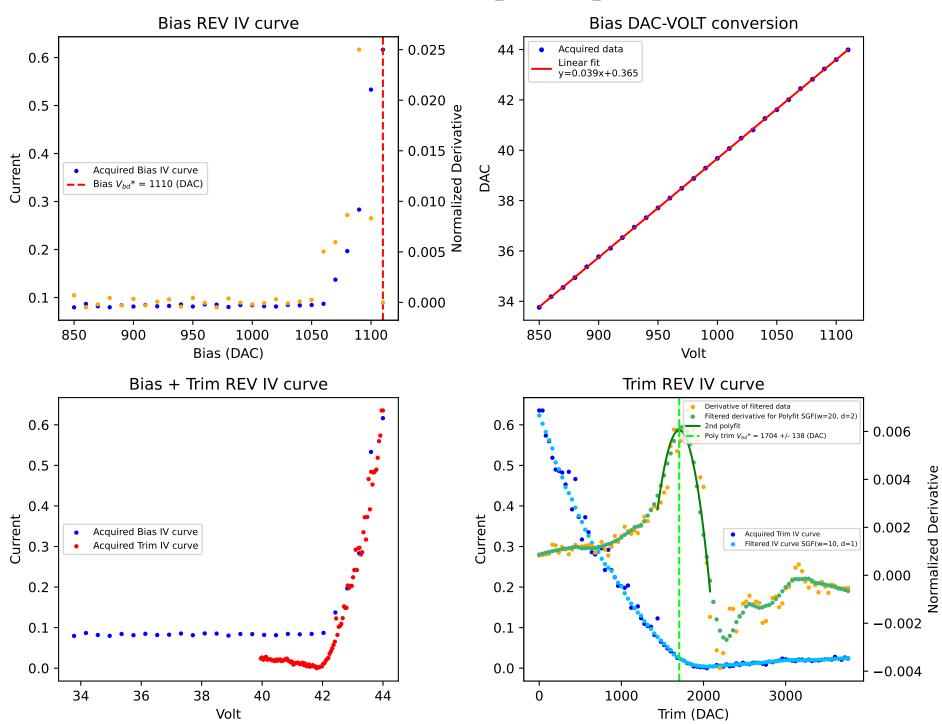


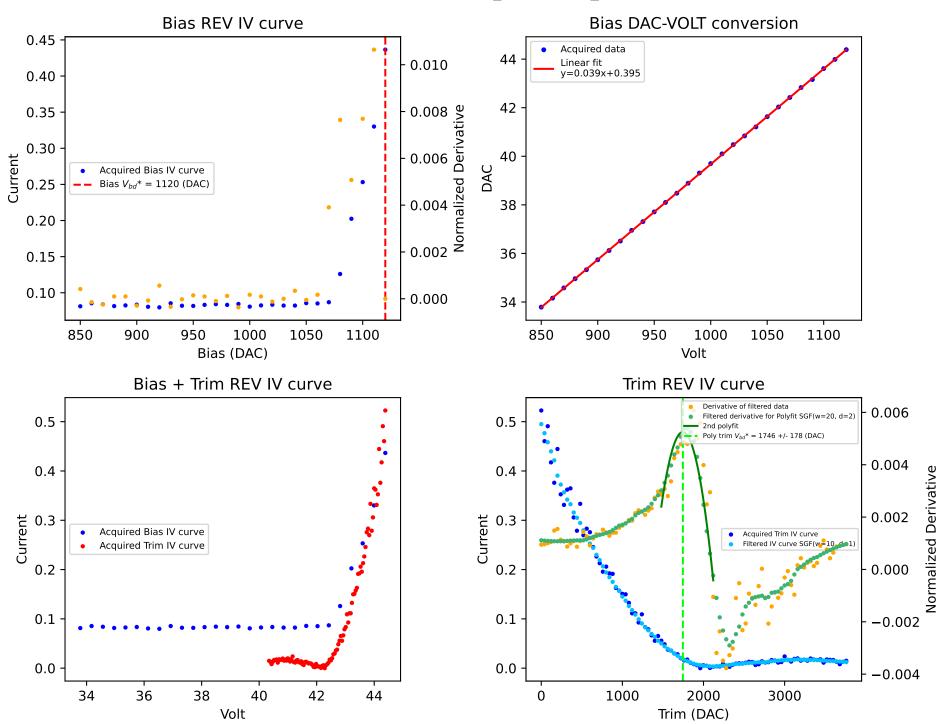


ENDPOINT:104 APA:1 AFE:4 Config CH:32 DAQ CH:40 SiPM:HPK

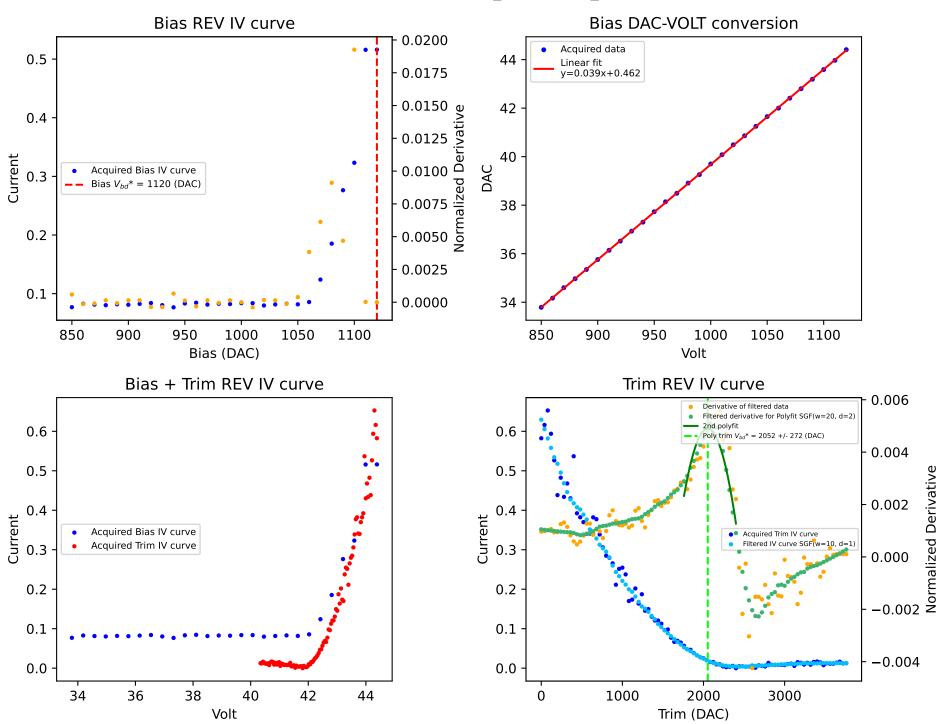








ENDPOINT:104 APA:1 AFE:4 Config_CH:36 DAQ_CH:44 SiPM:HPK



ENDPOINT:104 APA:1 AFE:4 Config_CH:37 DAQ_CH:45 SiPM:HPK

