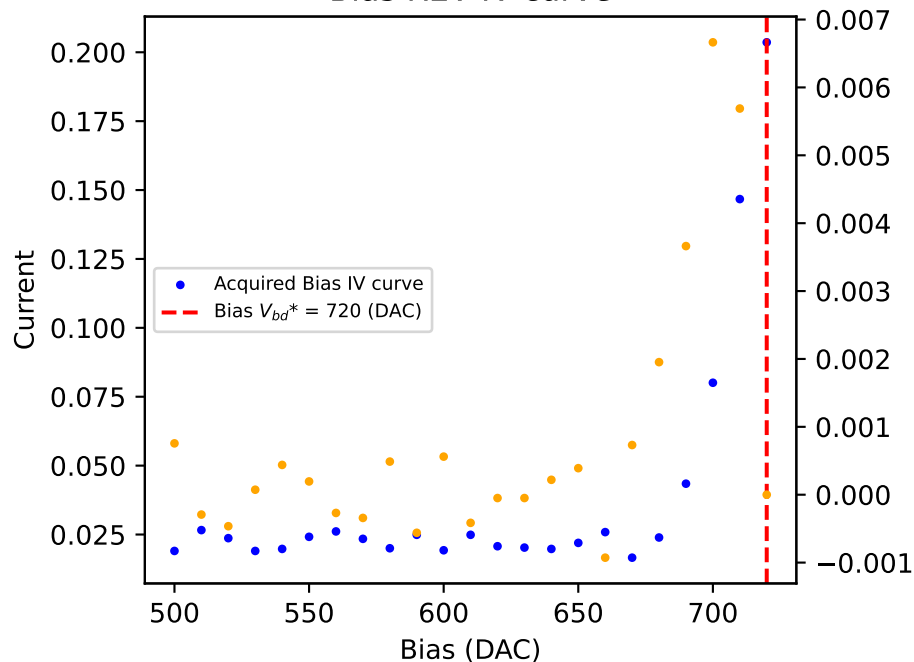
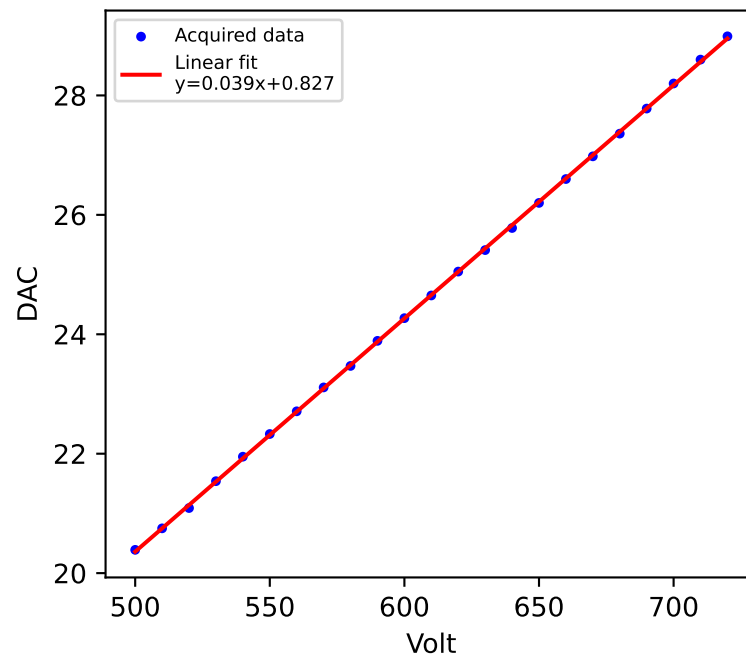


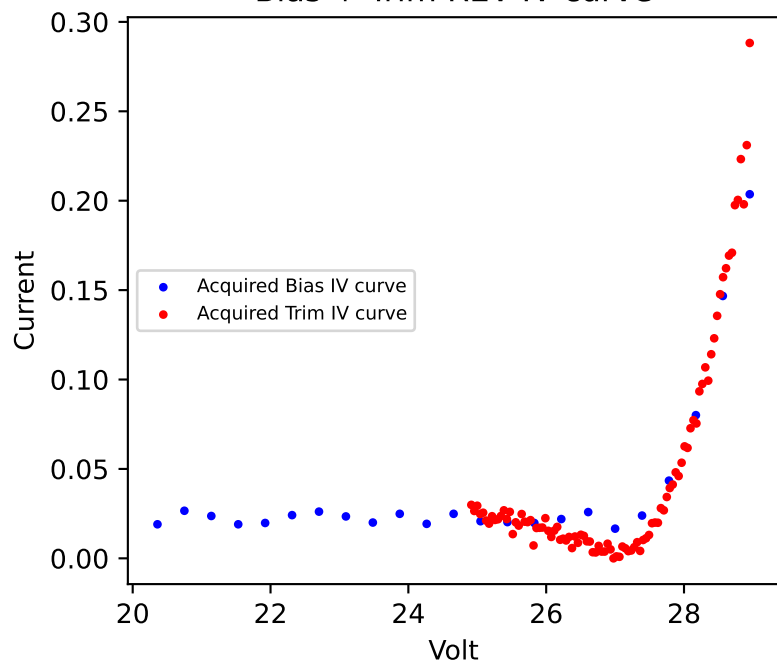
Bias REV IV curve



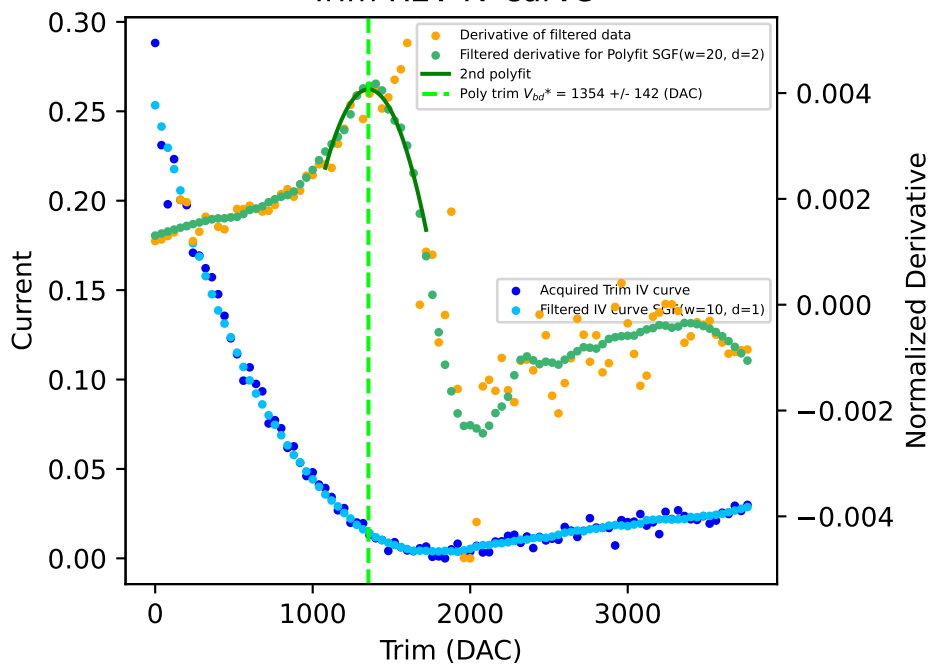
Bias DAC-VOLT conversion



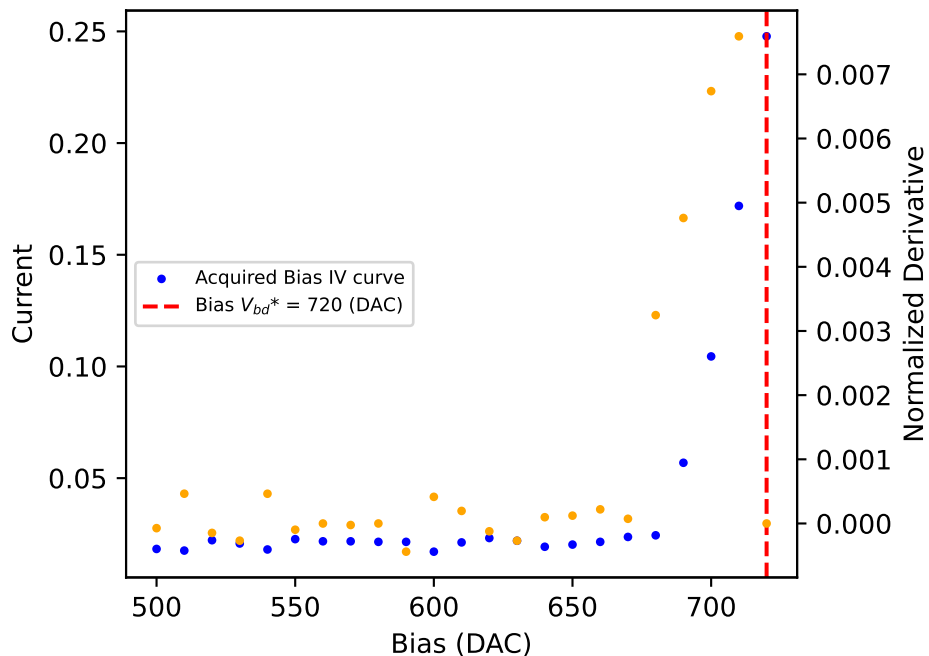
Bias + Trim REV IV curve



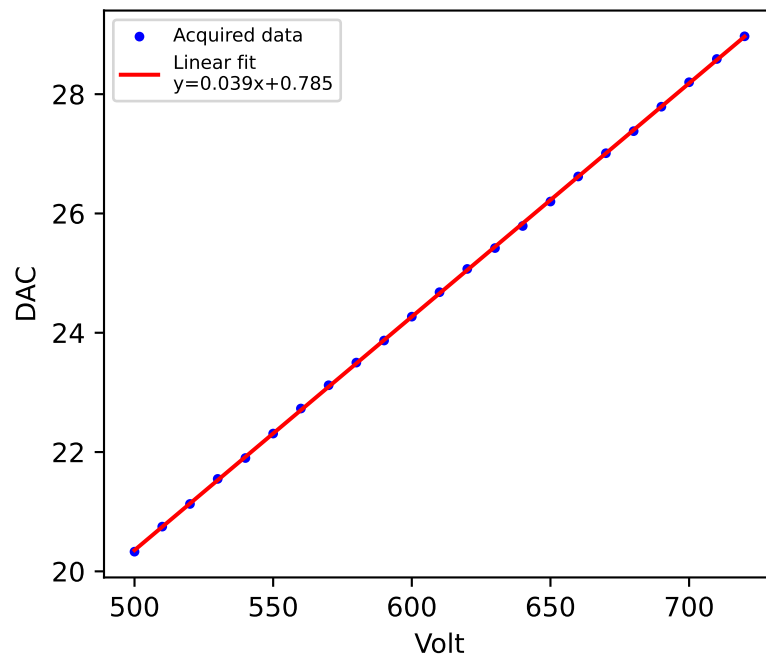
Trim REV IV curve



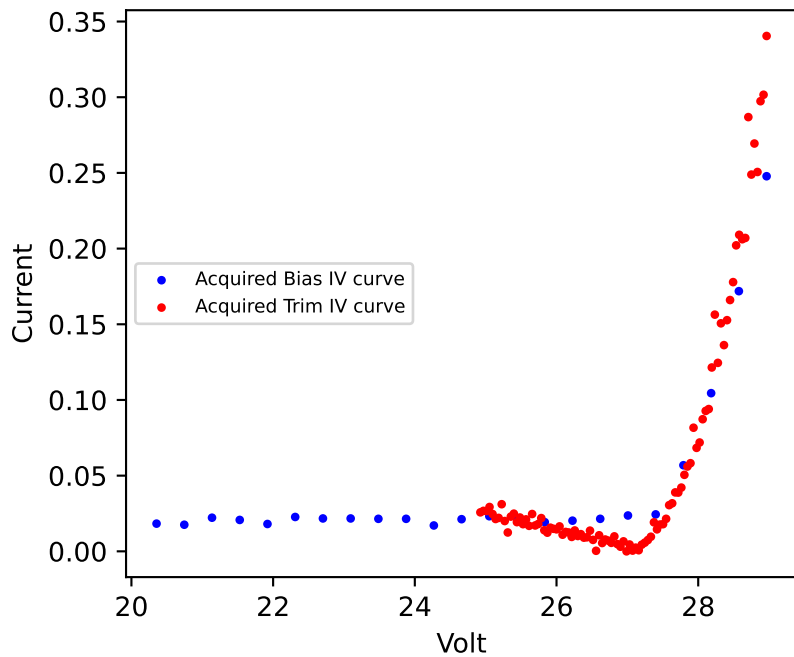
Bias REV IV curve



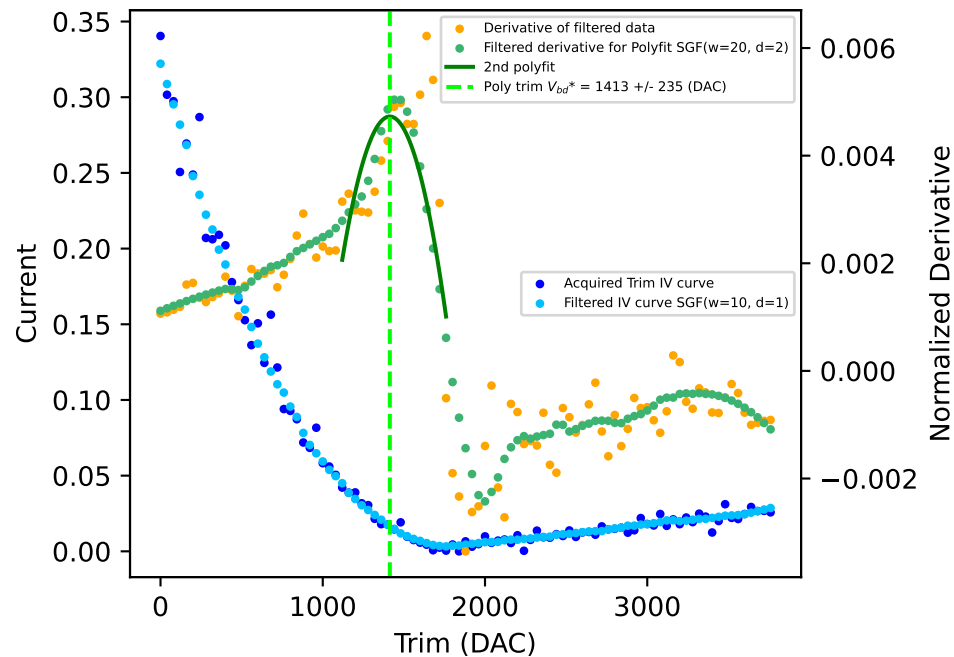
Bias DAC-VOLT conversion



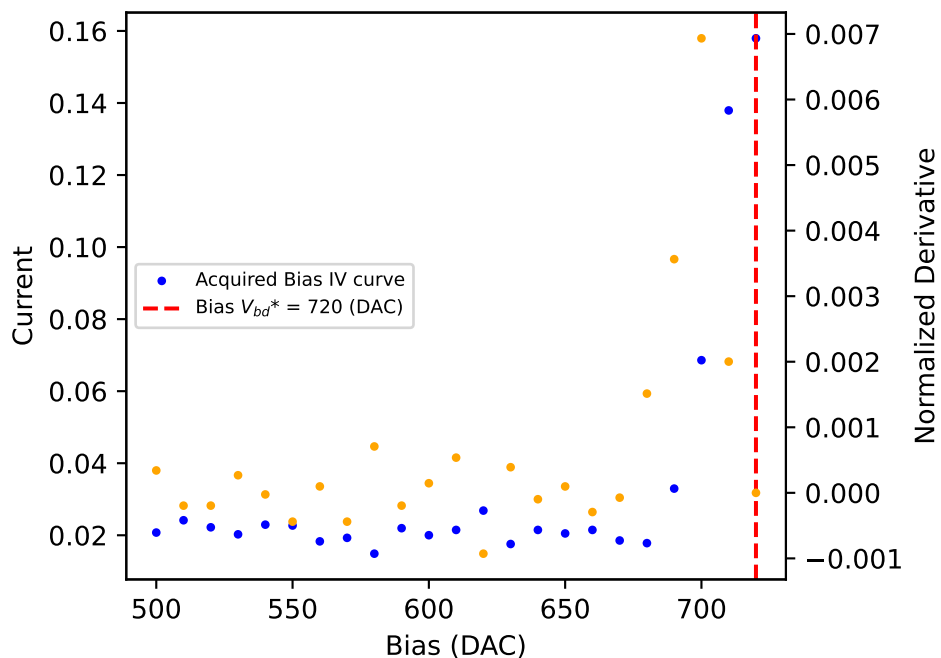
Bias + Trim REV IV curve



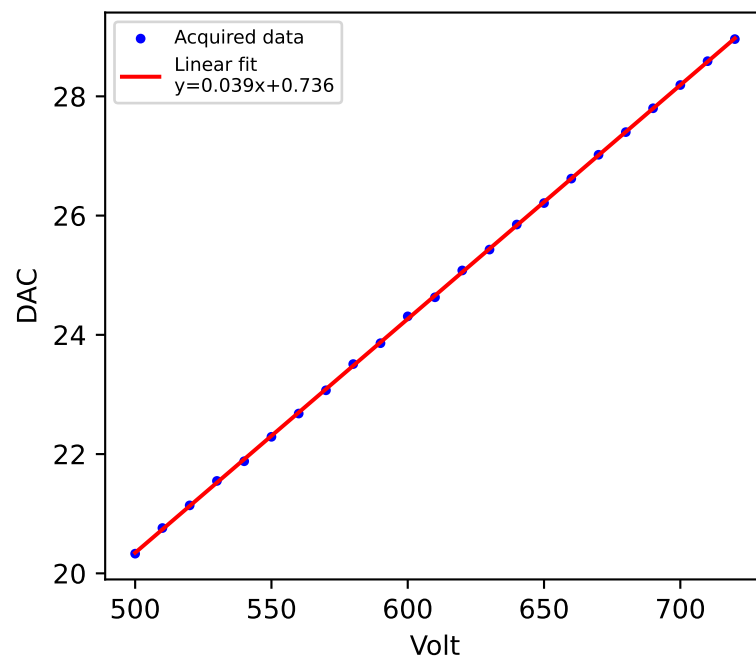
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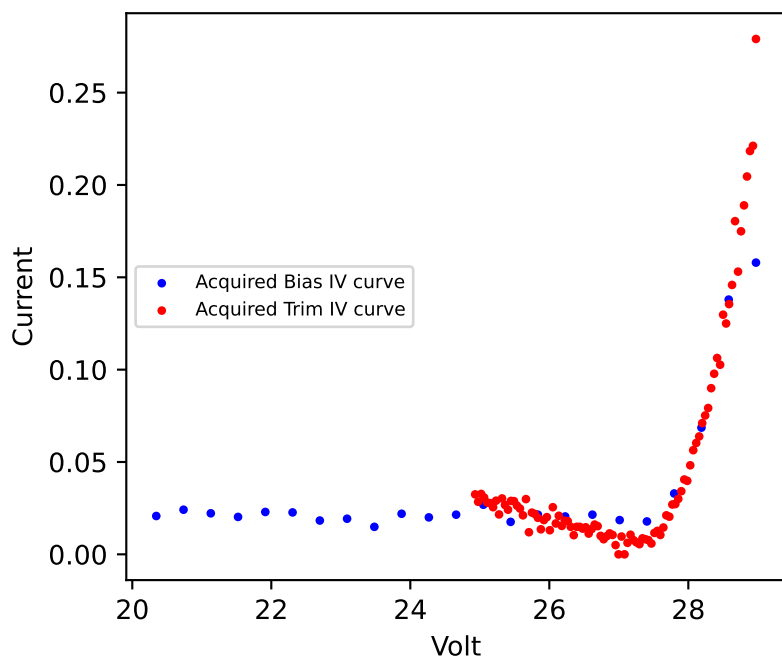
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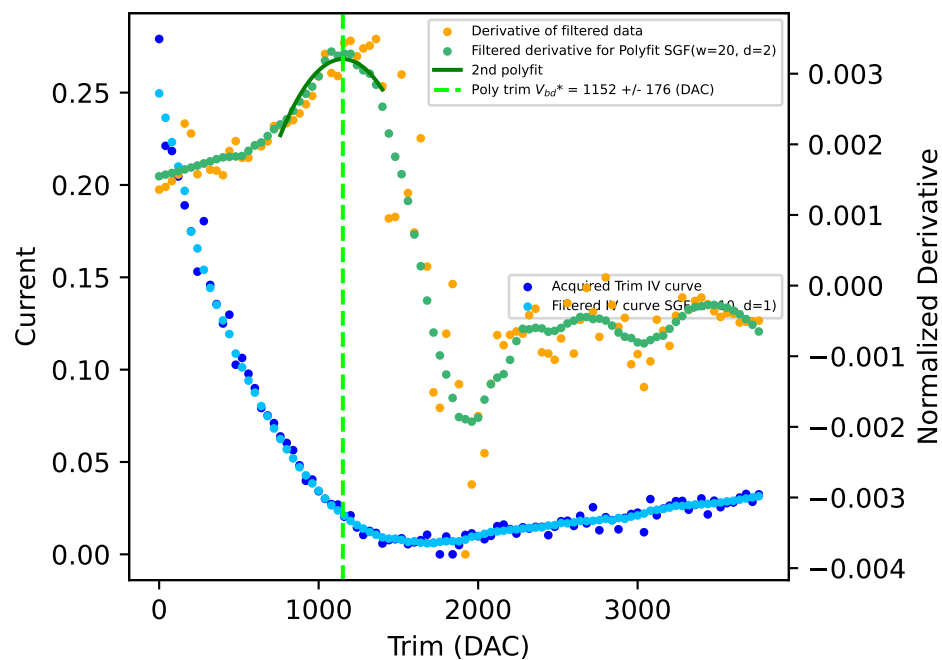
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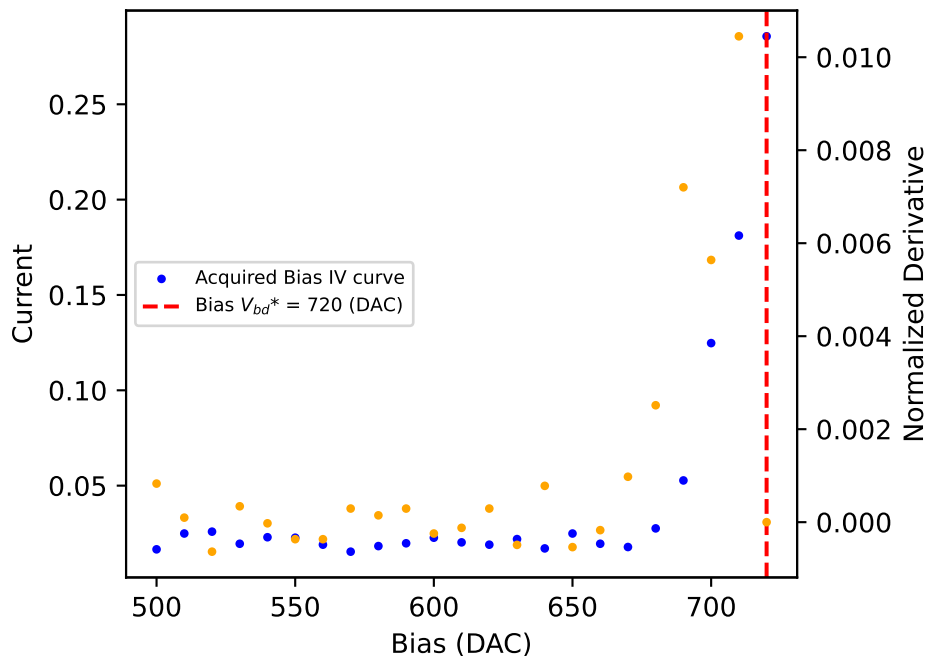
Bias + Trim REV IV curve



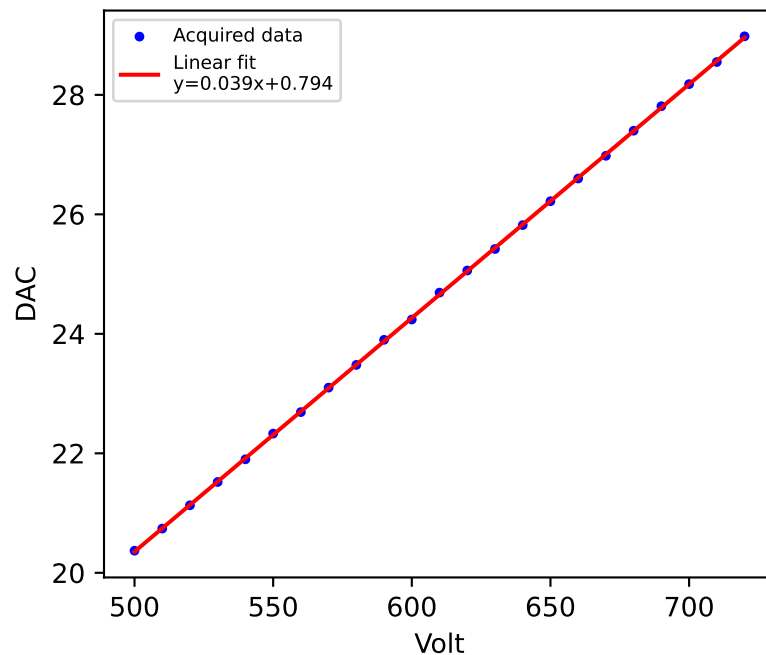
Trim REV IV curve



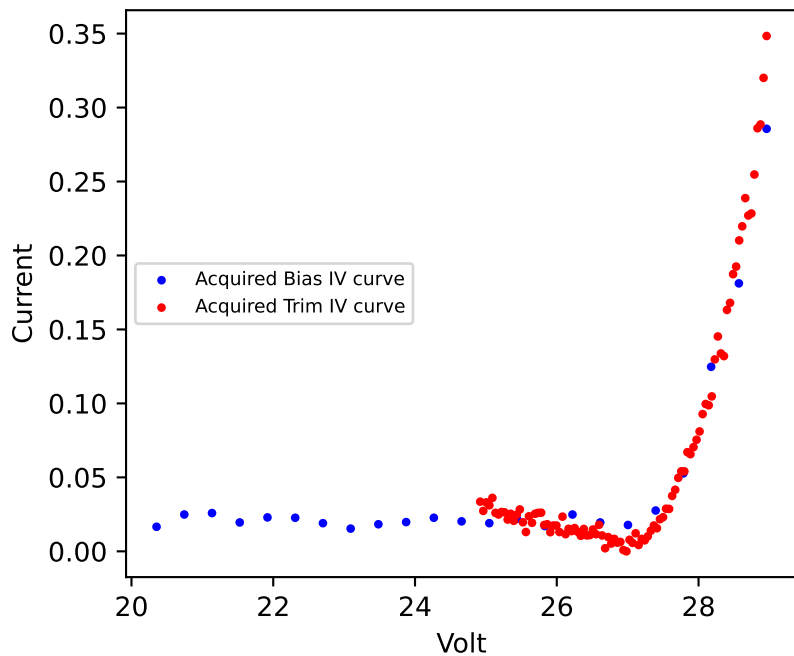
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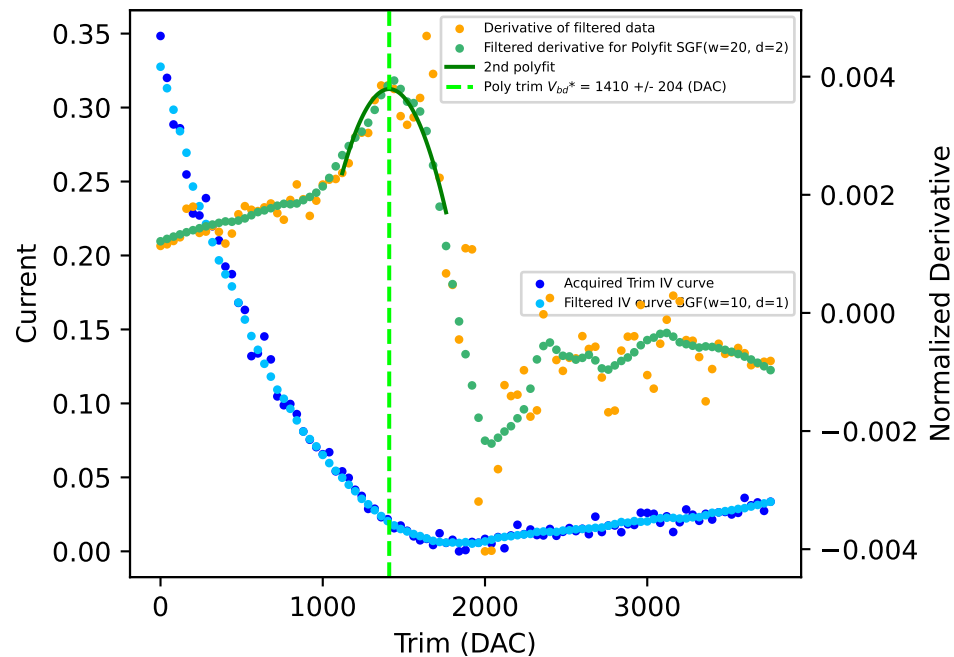
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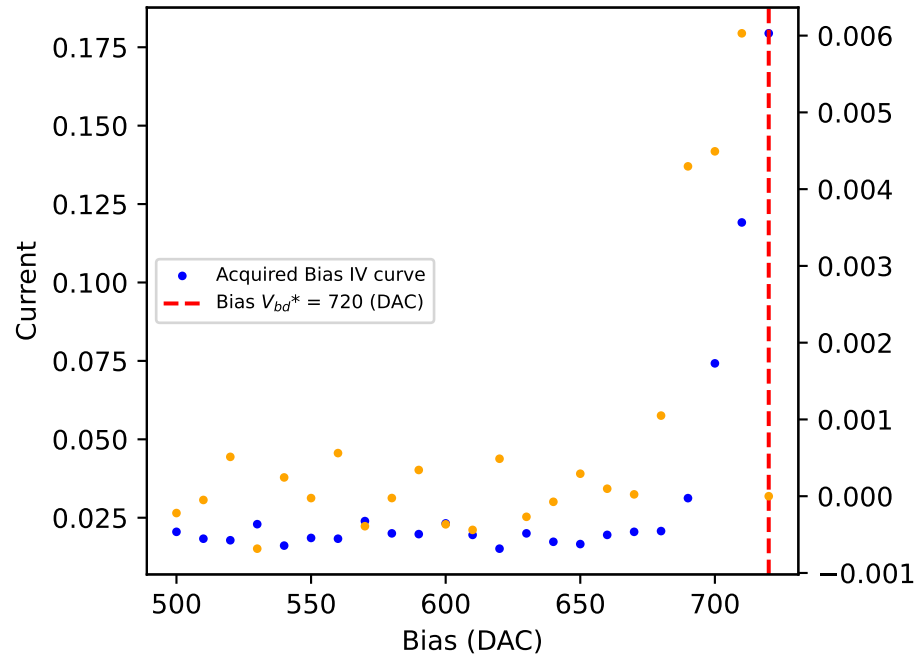
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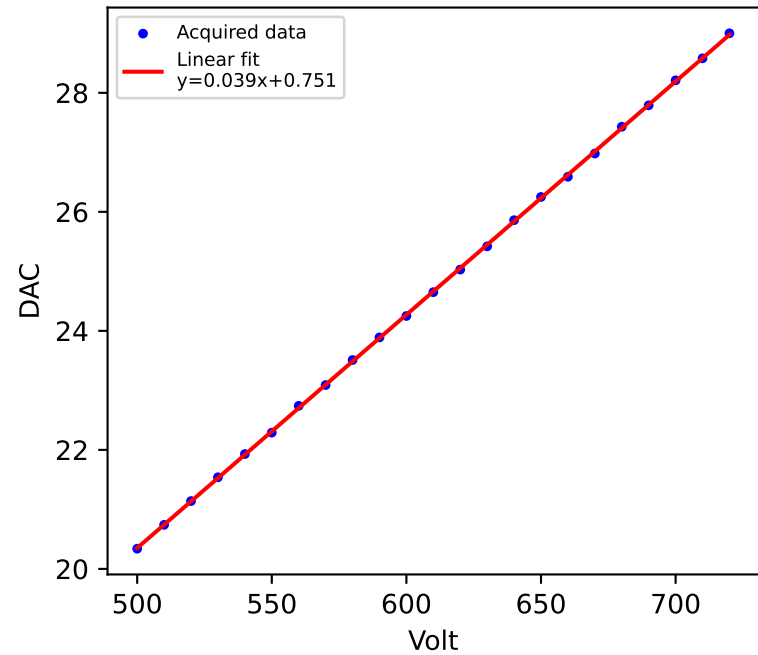
Trim REV IV curve



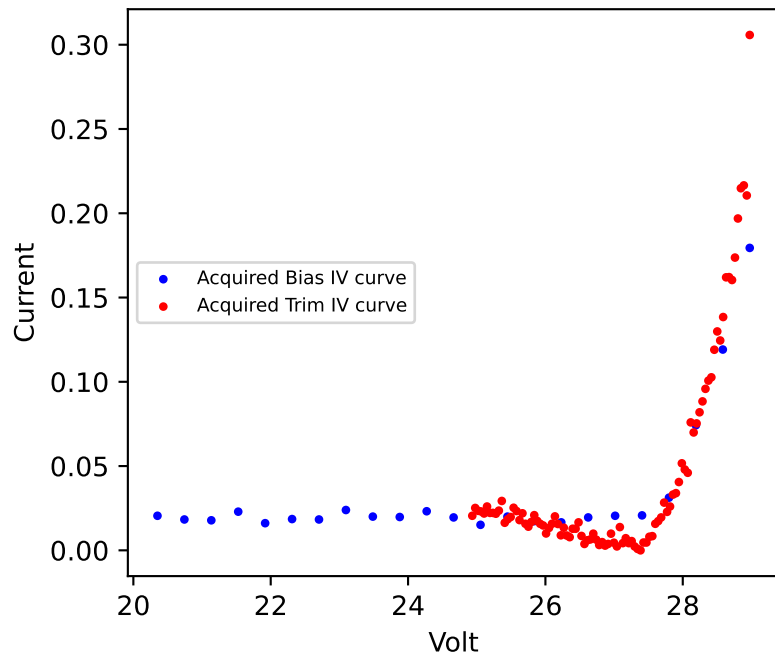
Bias REV IV curve



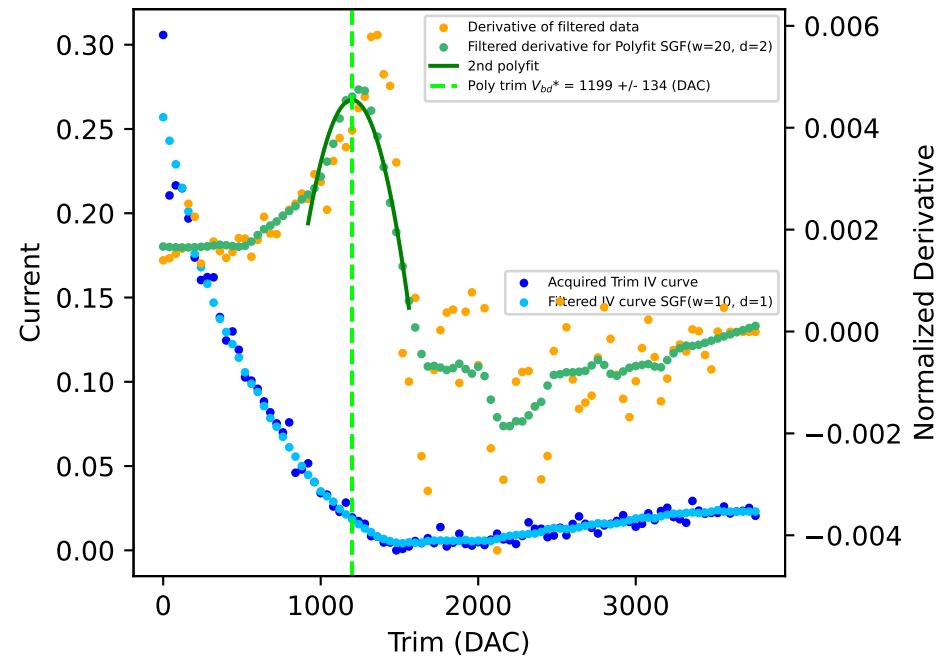
Bias DAC-VOLT conversion



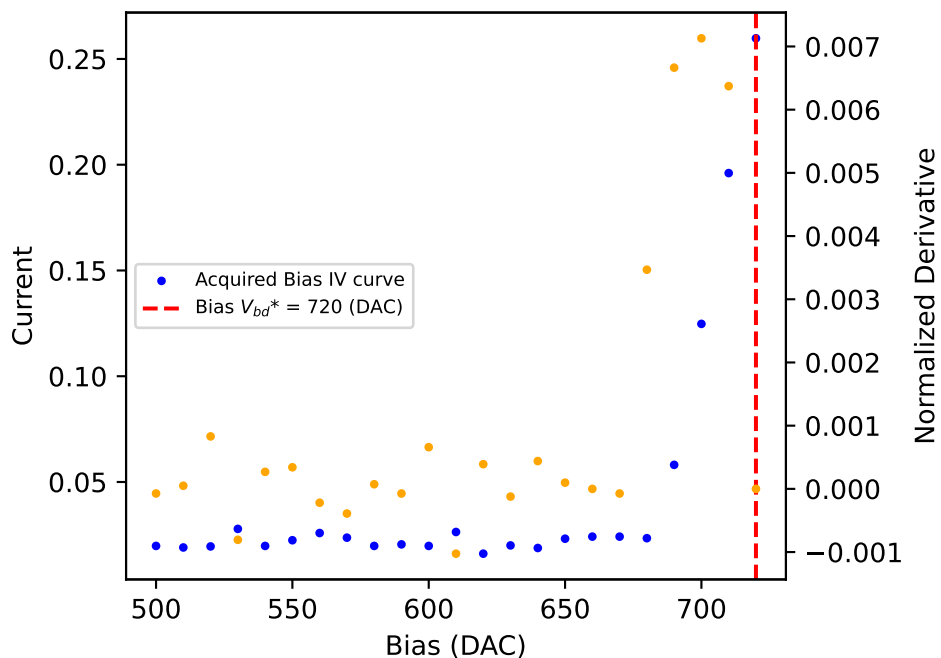
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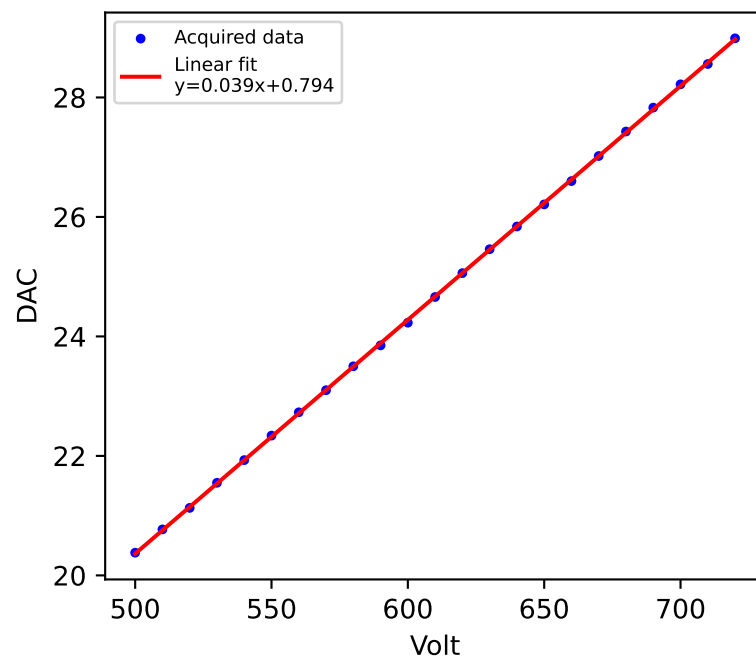
Trim REV IV curve



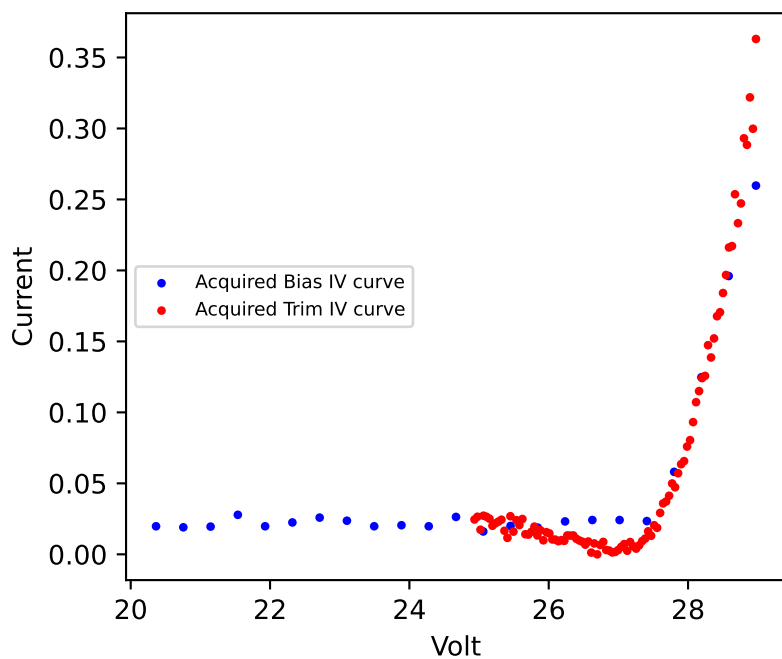
Bias REV IV curve



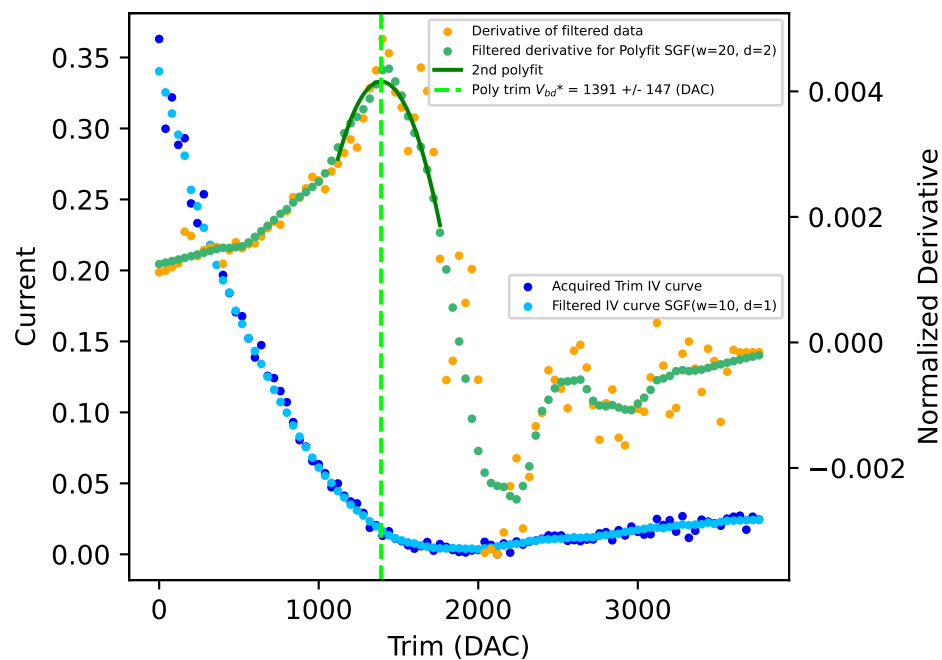
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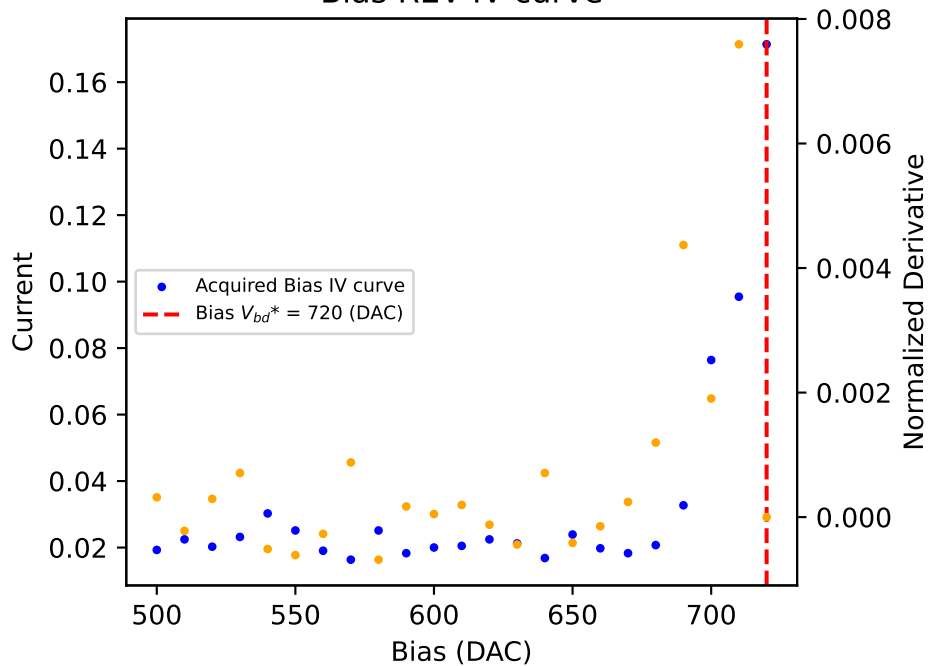
Bias + Trim REV IV curve



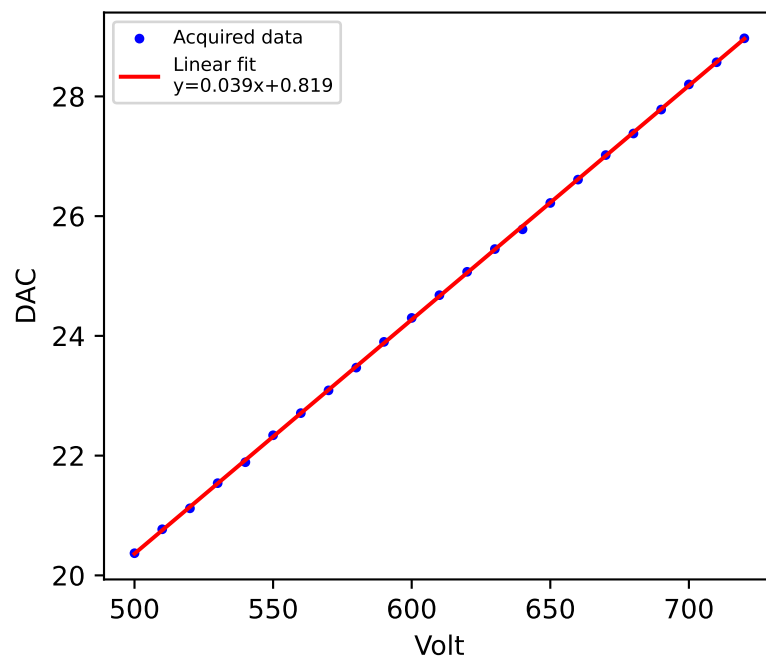
Trim REV IV curve



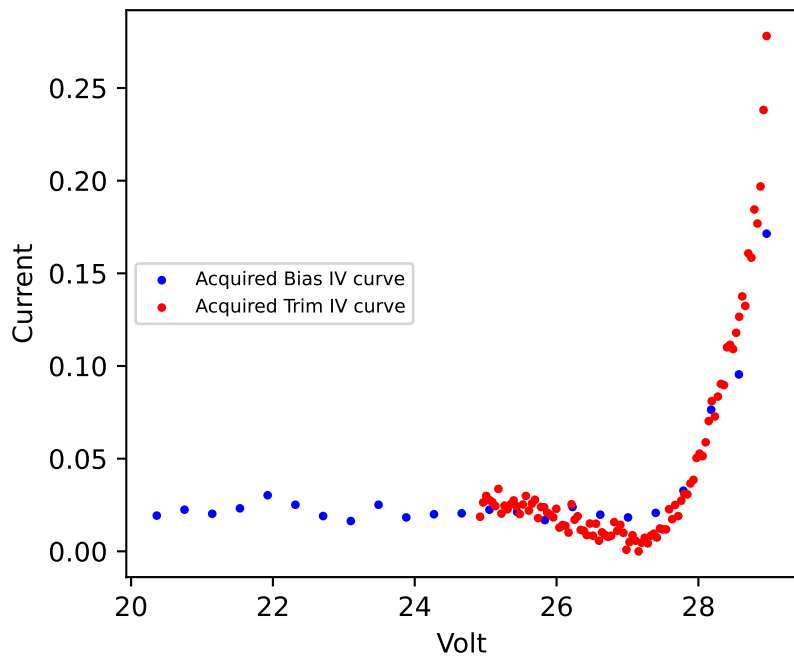
Bias REV IV curve



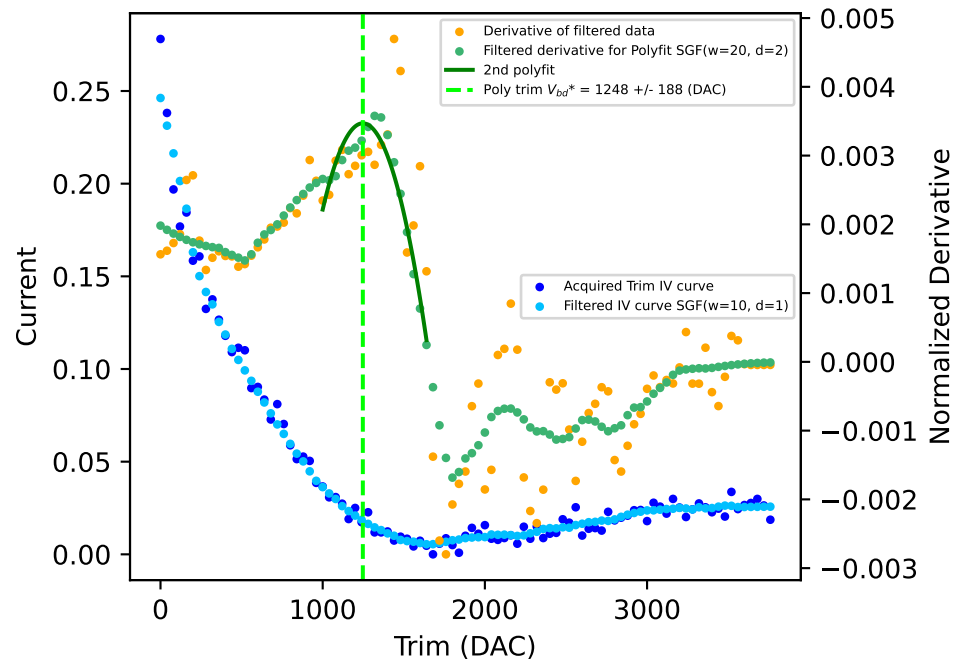
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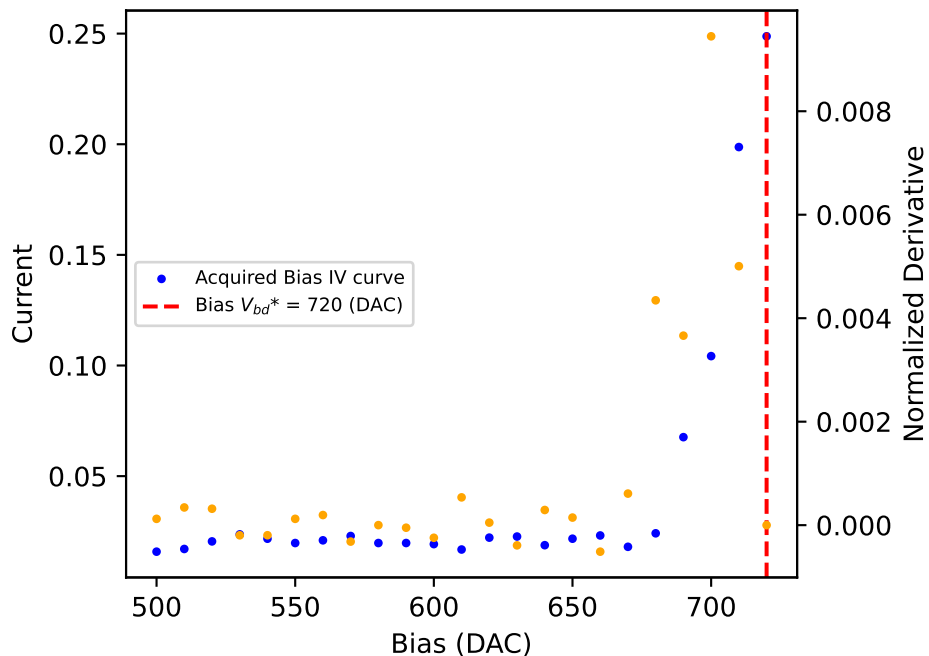
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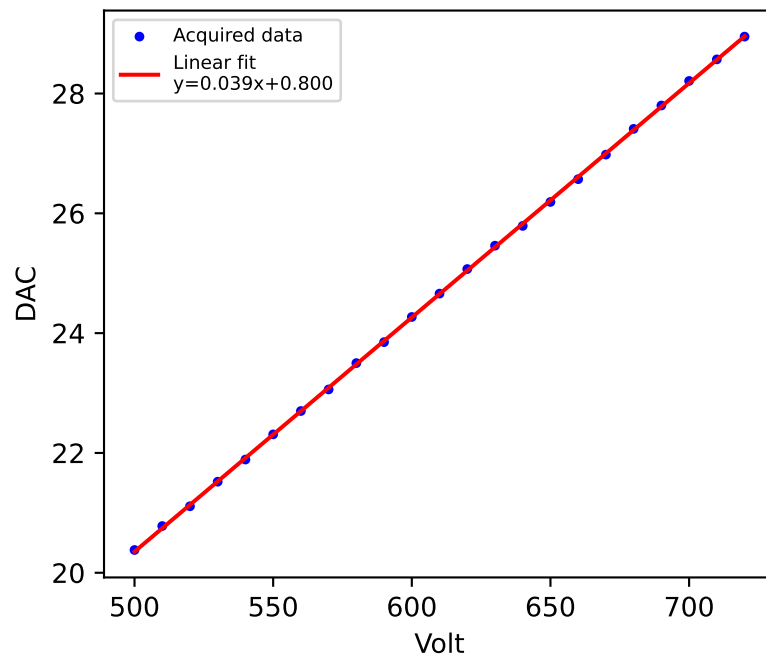
Trim REV IV curve



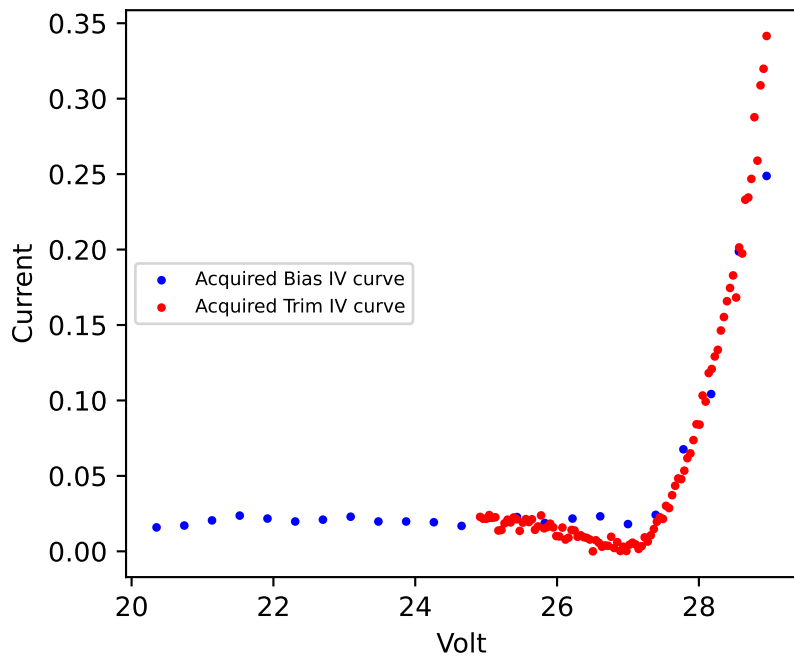
Bias REV IV curve



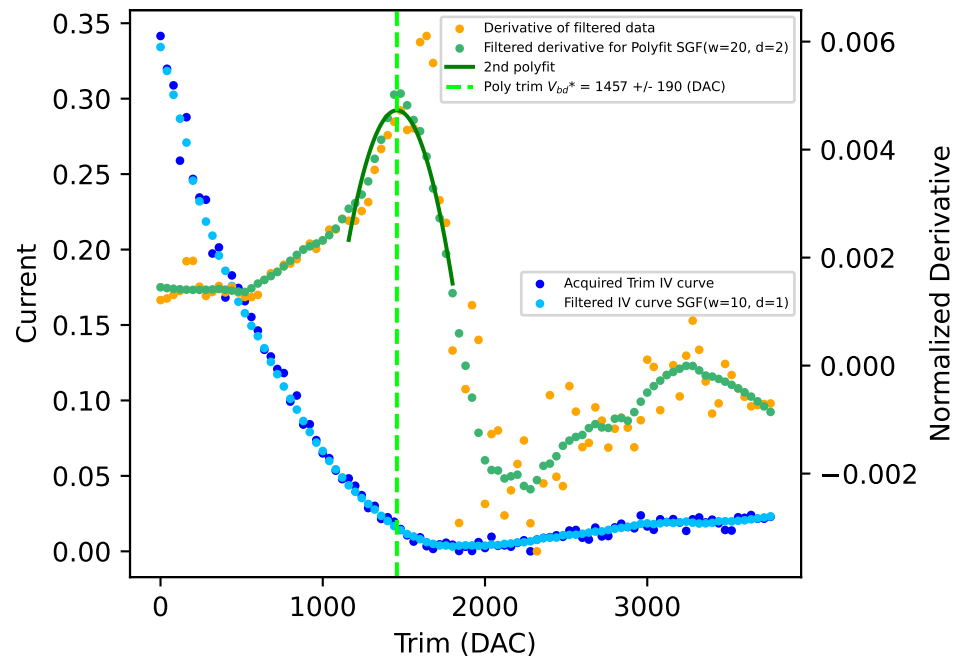
Bias DAC-VOLT conversion



Bias + Trim REV IV curve

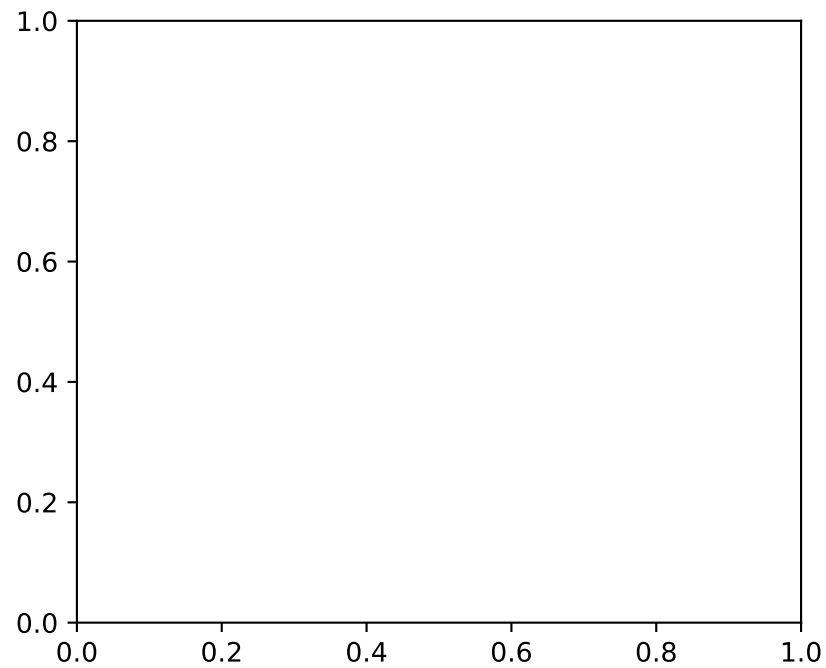
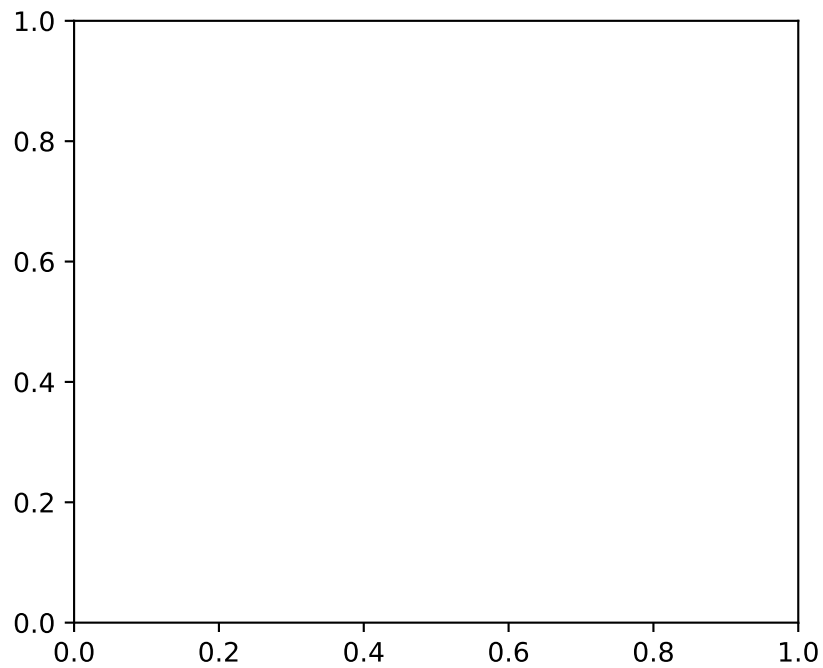
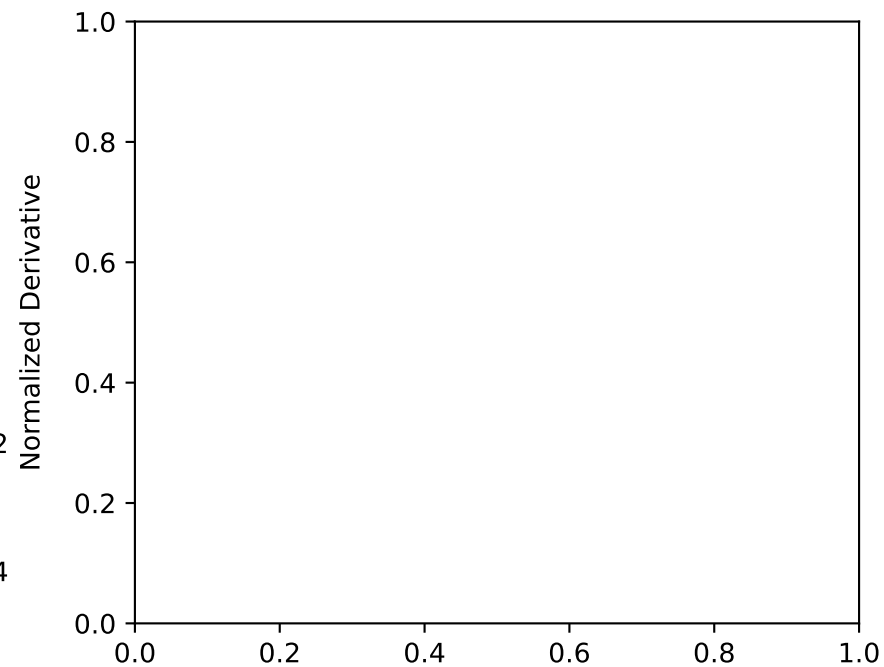
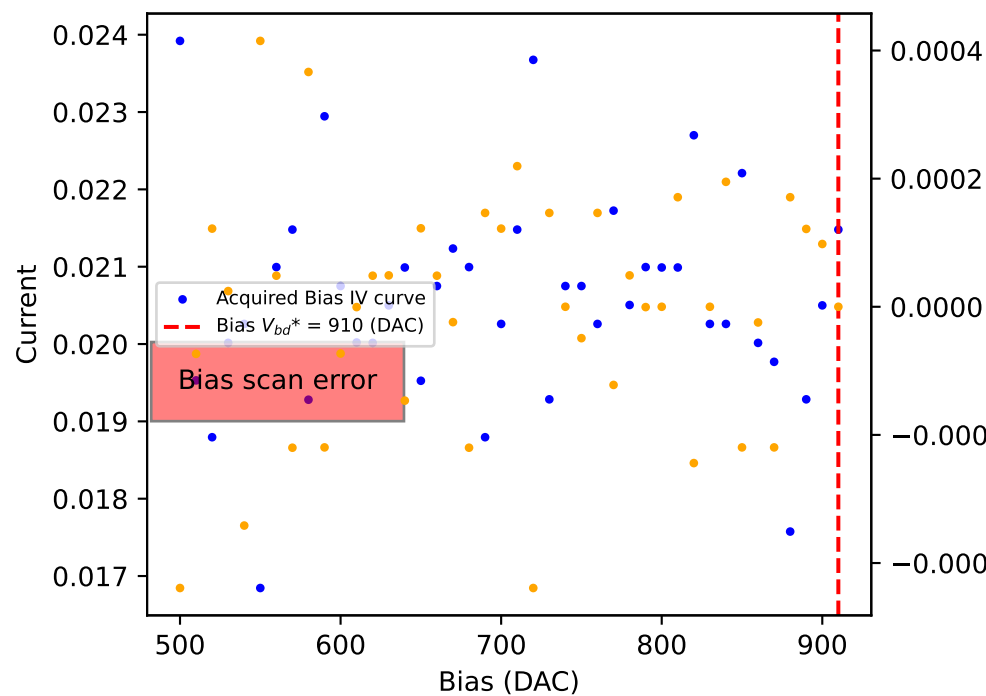


Trim REV IV curve





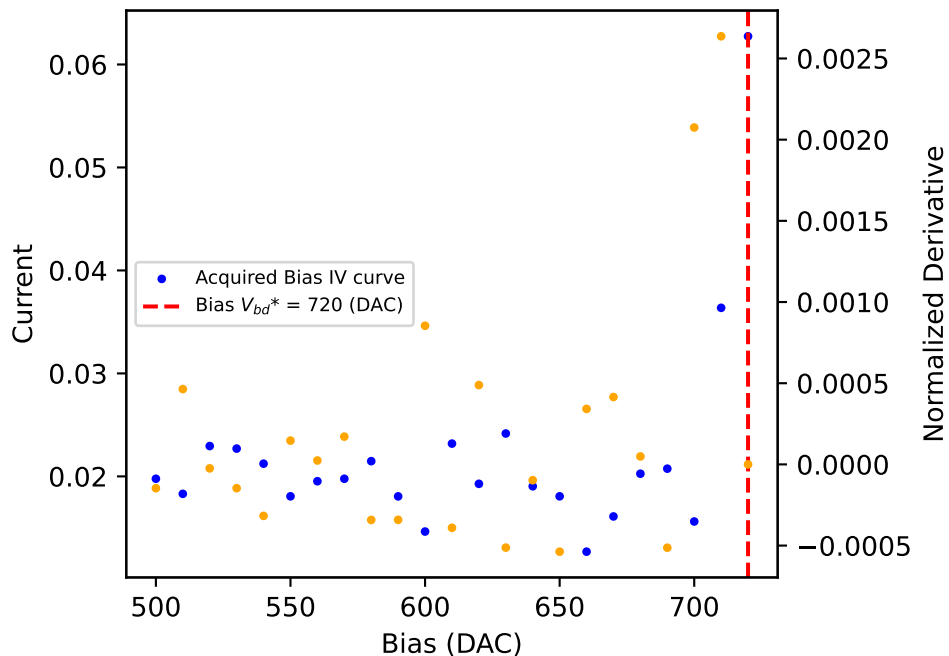
Bias REV IV curve



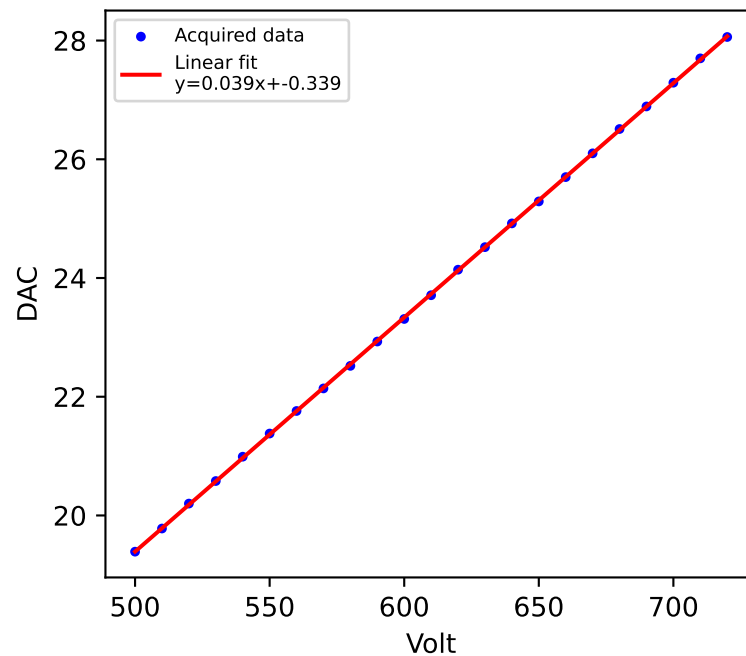
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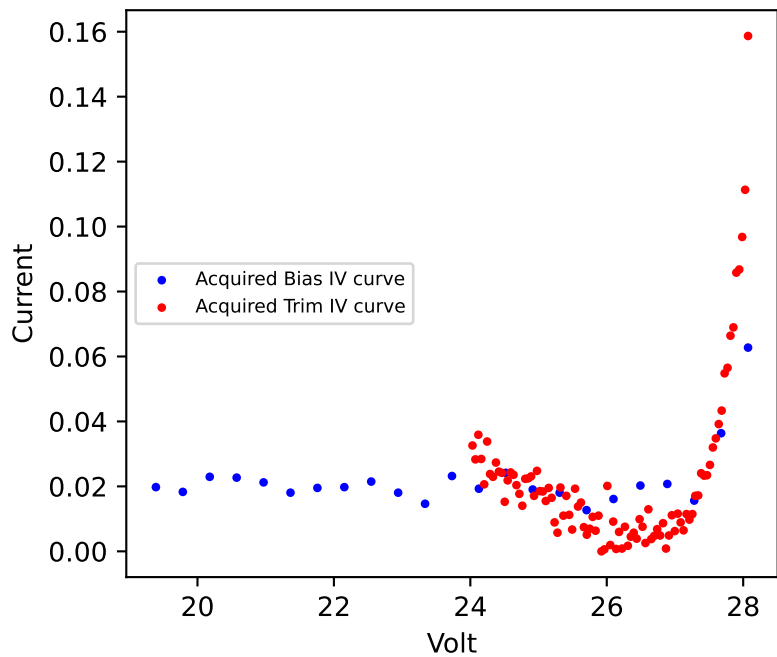
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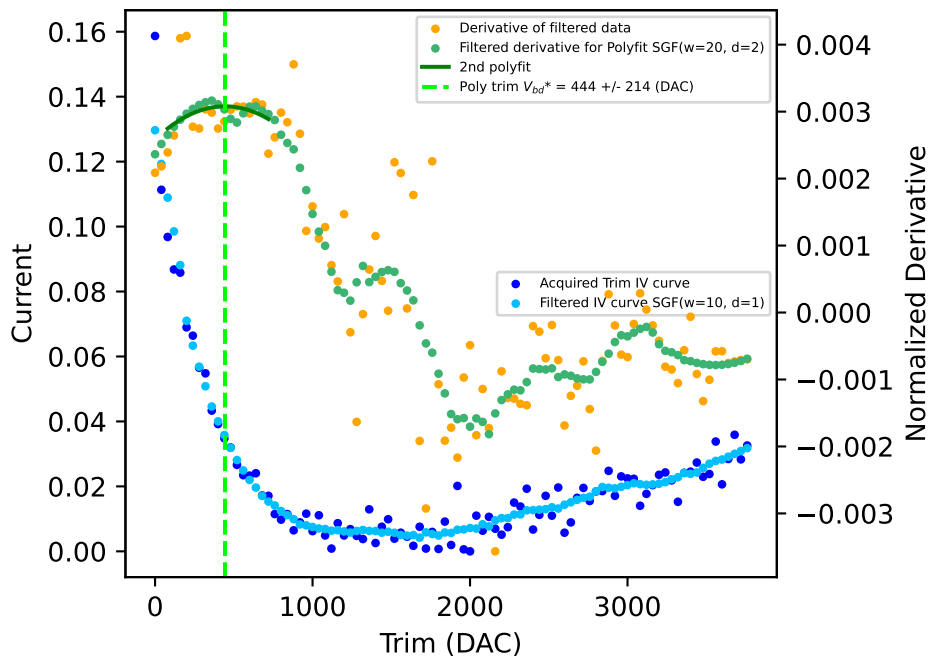
Bias DAC-VOLT conversion



Bias + Trim REV IV curve



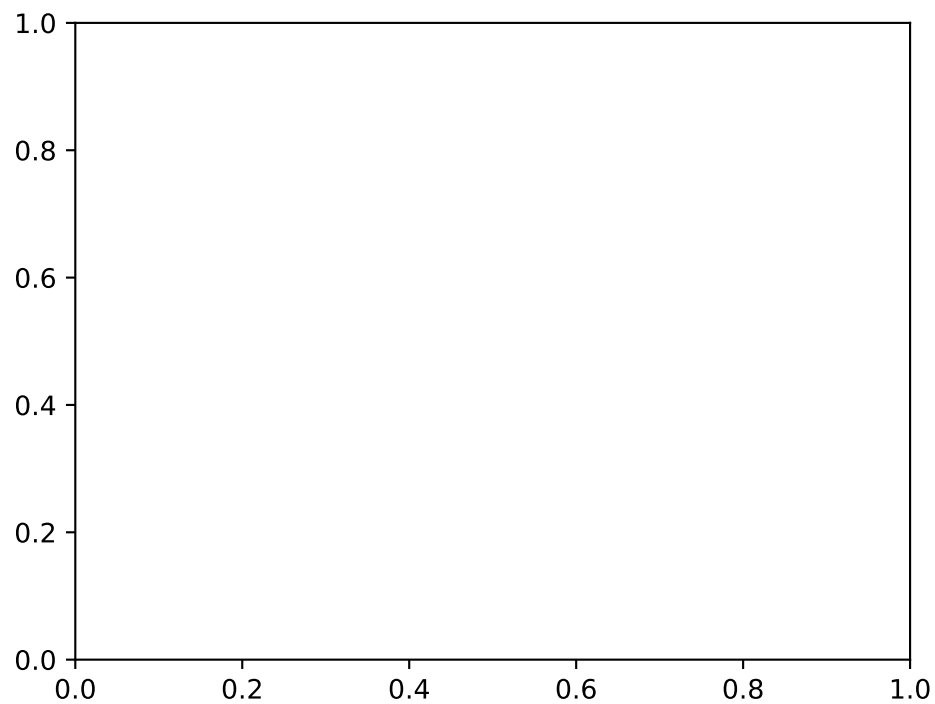
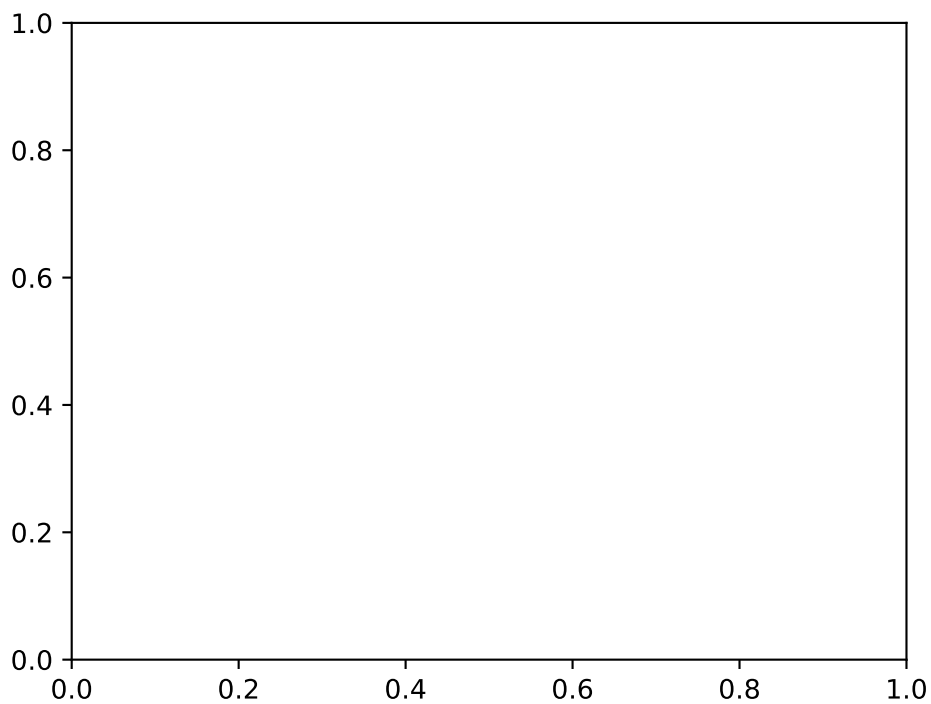
Trim REV IV curve



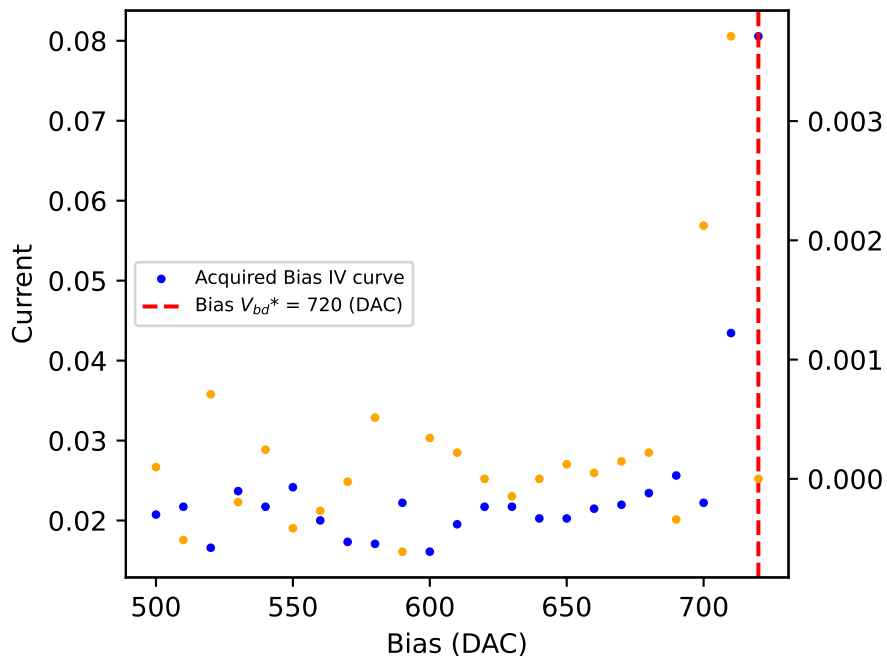
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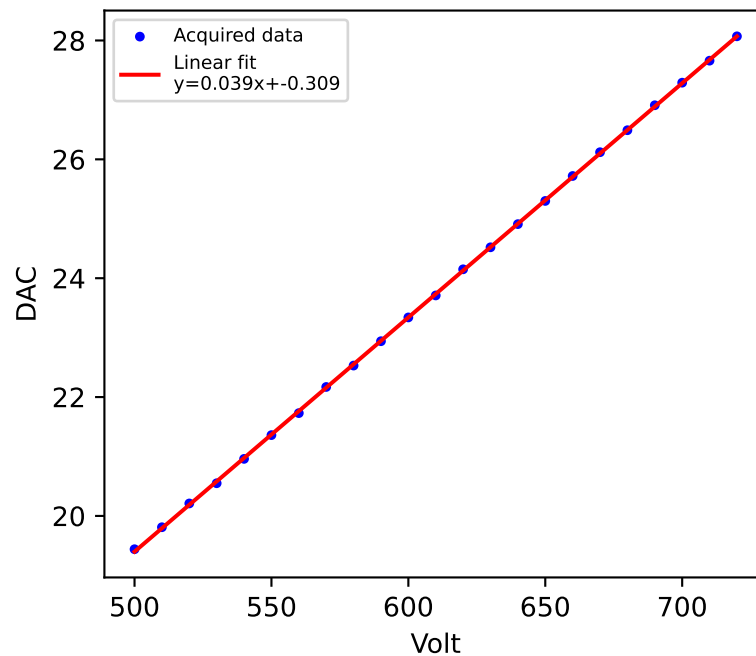
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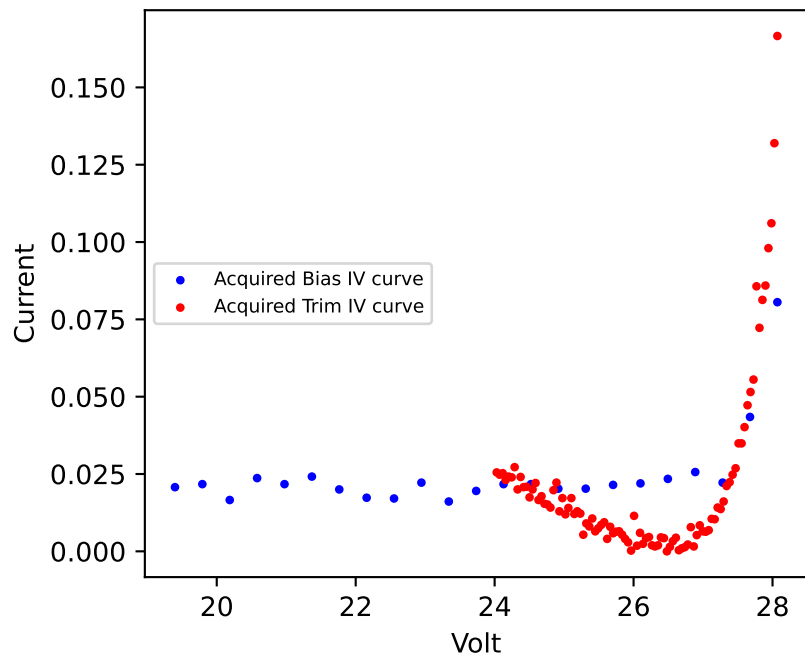
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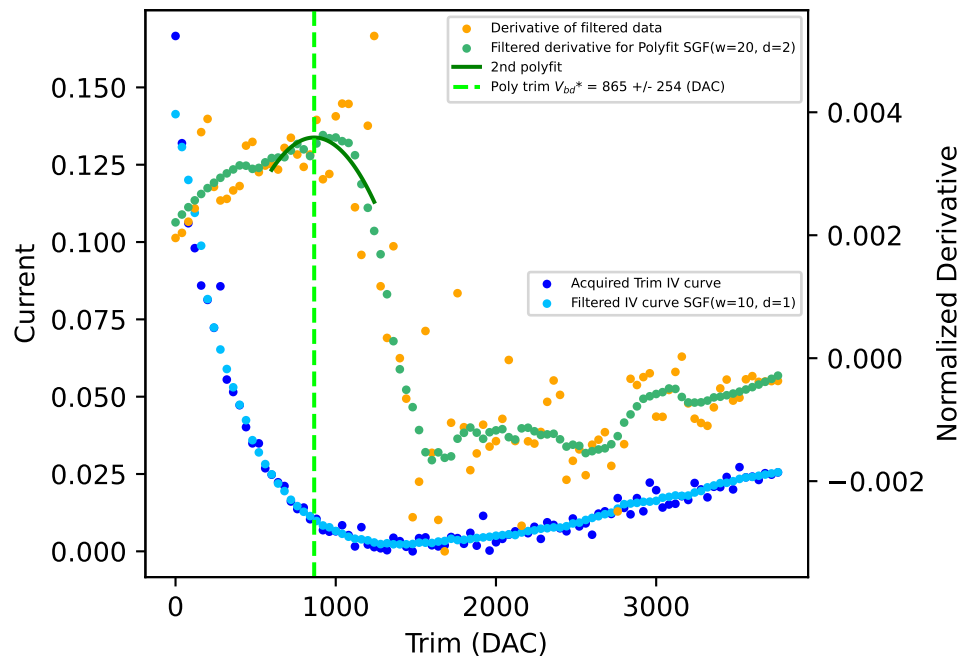
Bias DAC-VOLT conversion



Bias + Trim REV IV curve



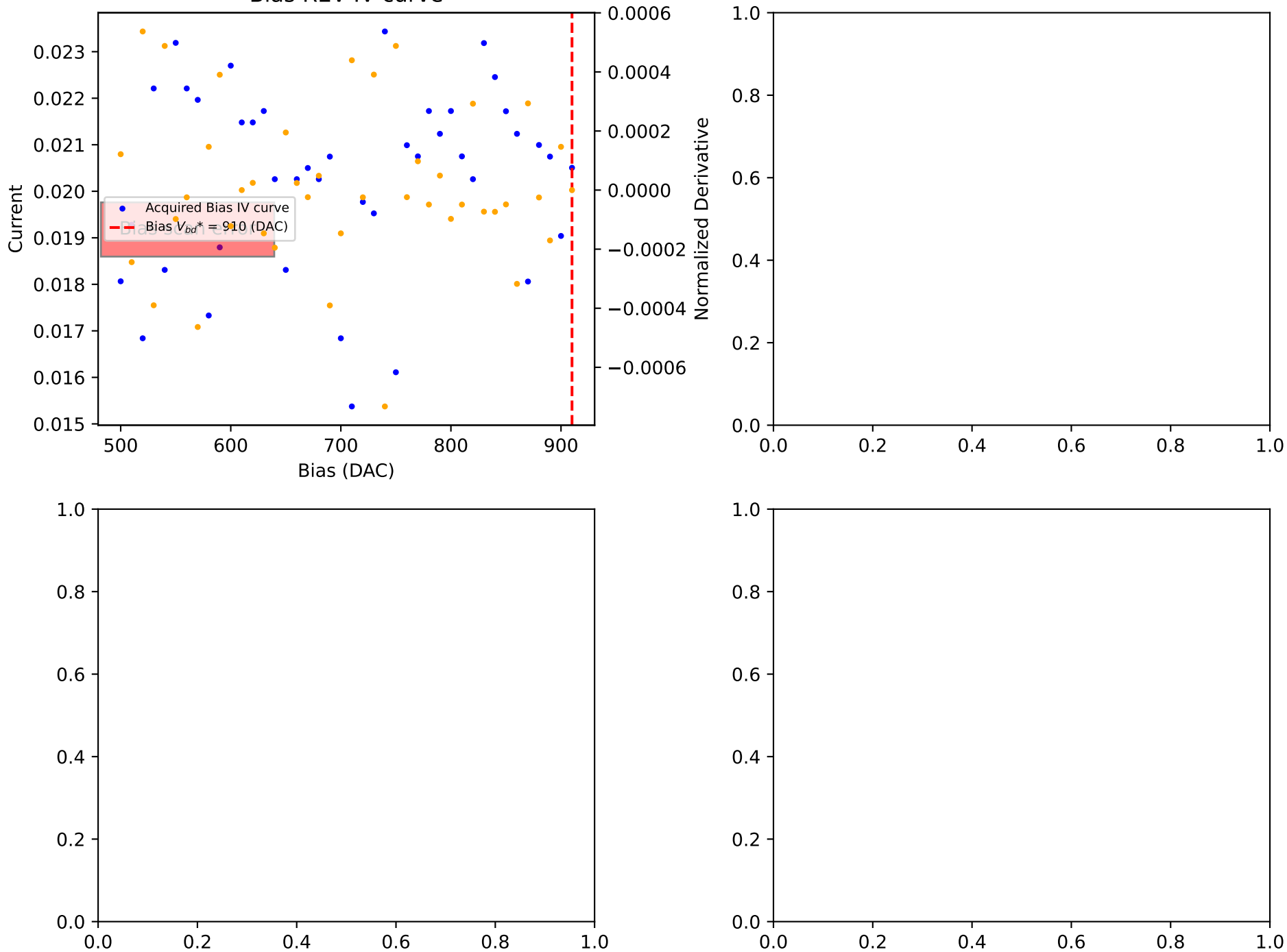
Trim REV IV curve



ENDPOINT:109 APA:2 AFE:1 Config\_CH:14 CH\_daq:16 SiPM:FBK --> MISSING FILE

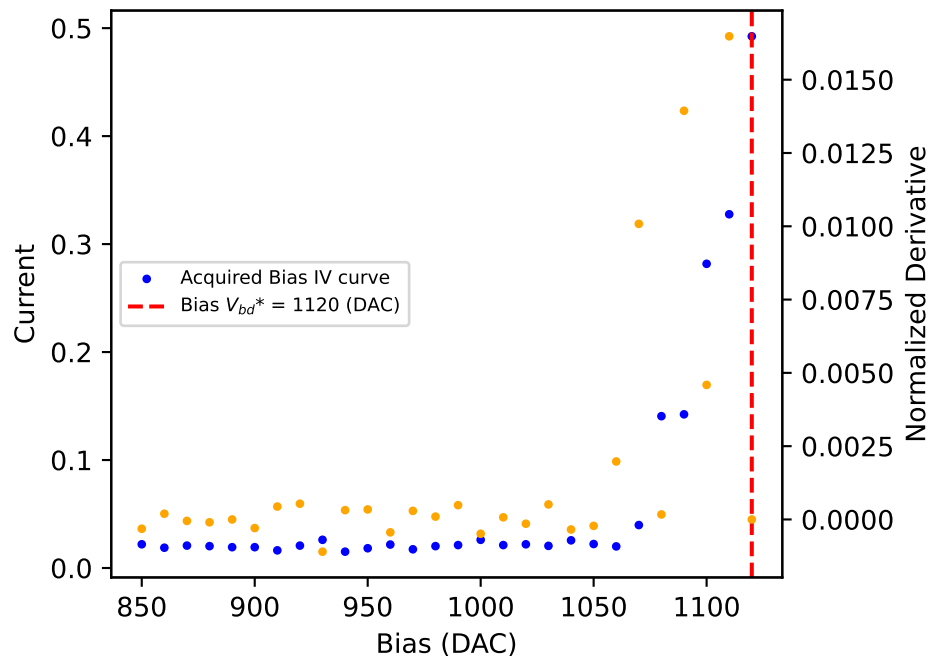


Bias REV IV curve

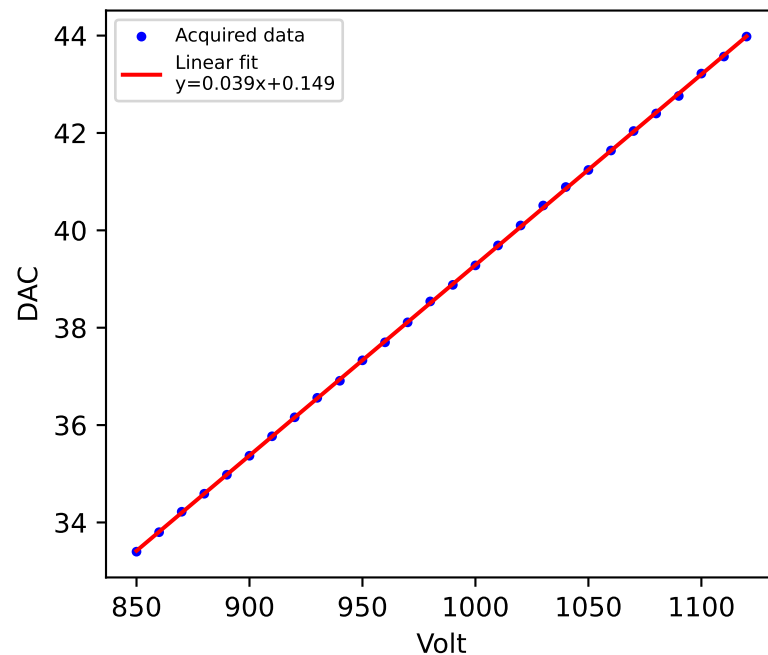




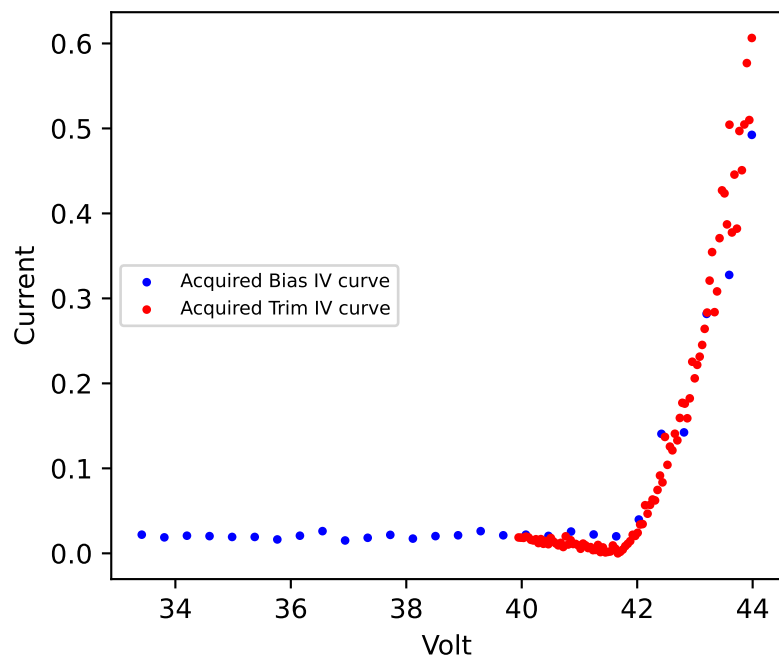
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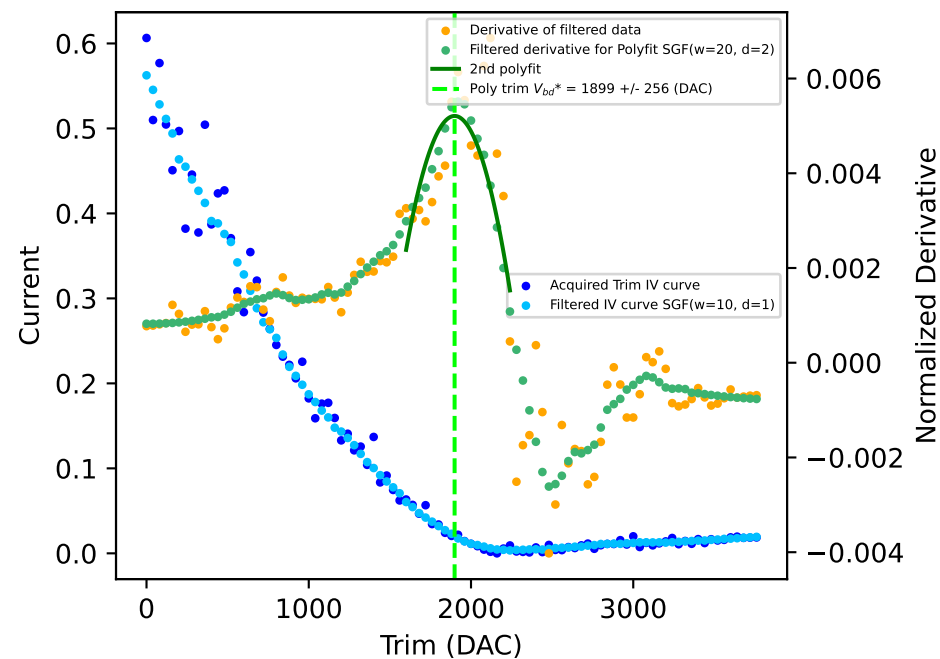
Bias DAC-VOLT conversion



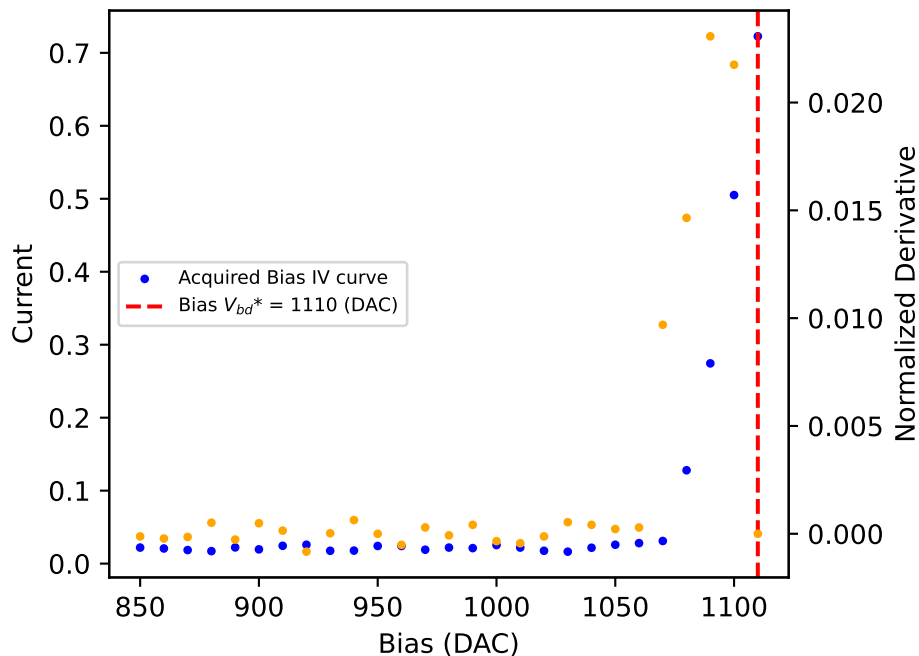
Bias + Trim REV IV curve



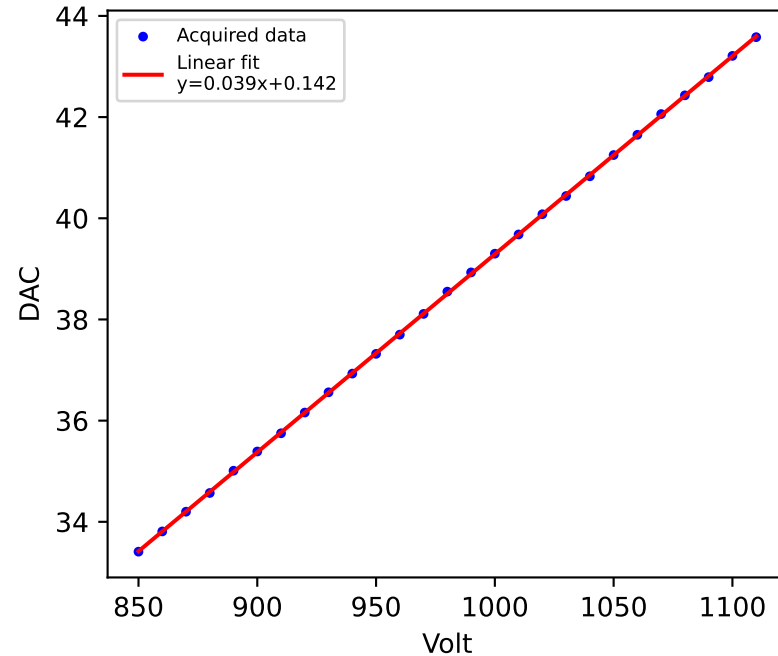
Trim REV IV curve



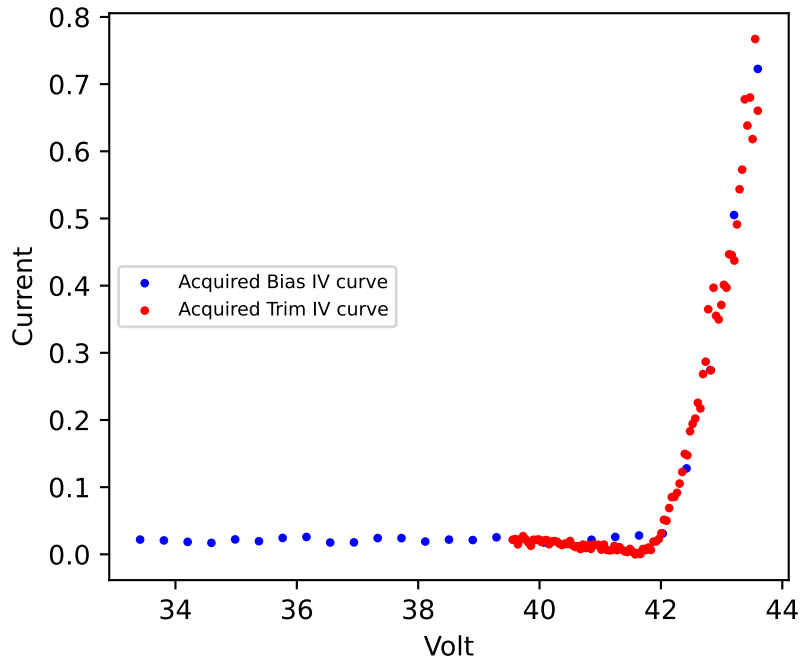
Bias REV IV curve



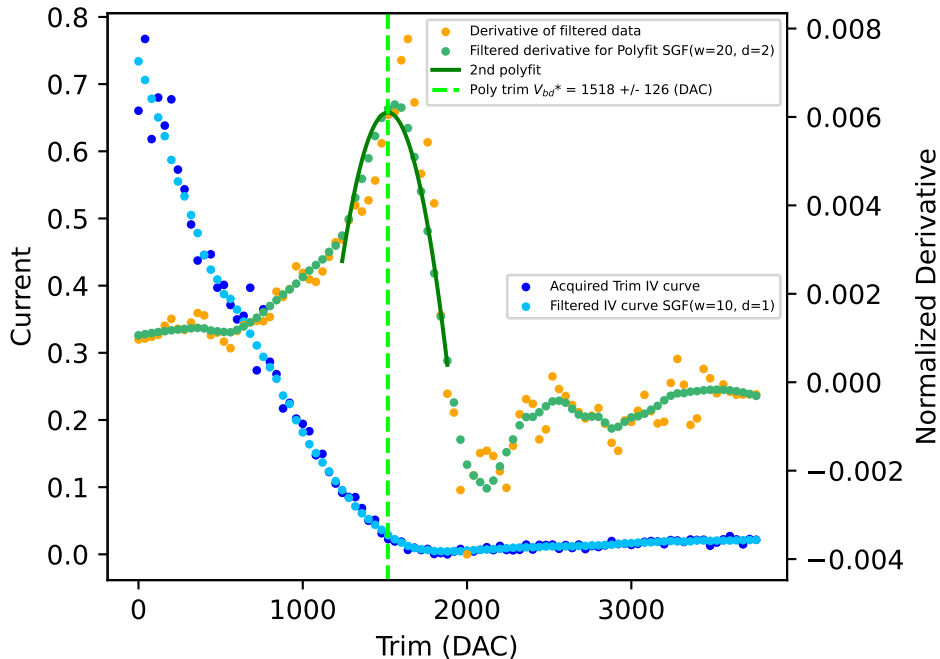
Bias DAC-VOLT conversion



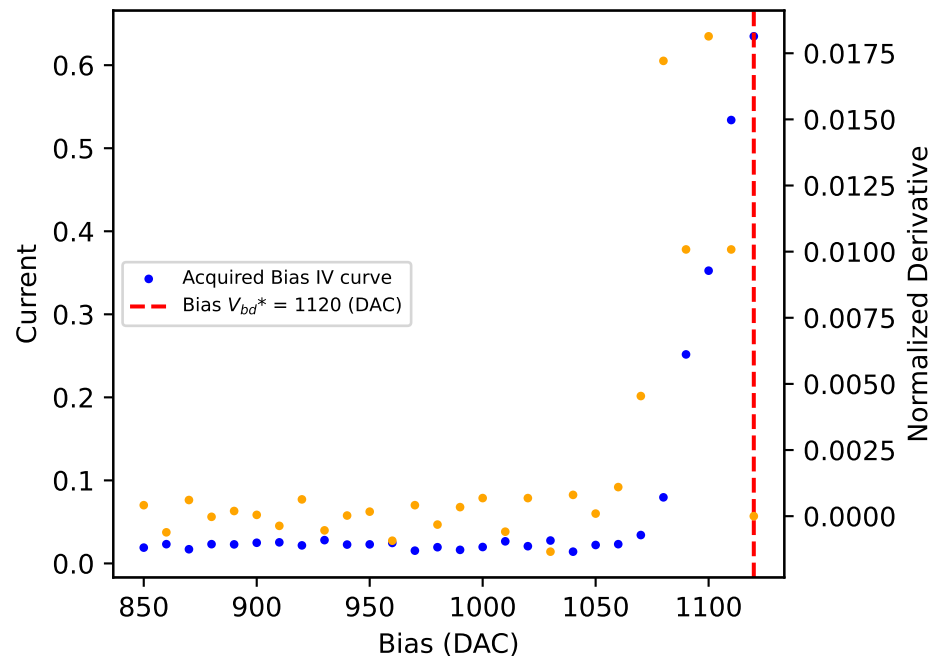
Bias + Trim REV IV curve



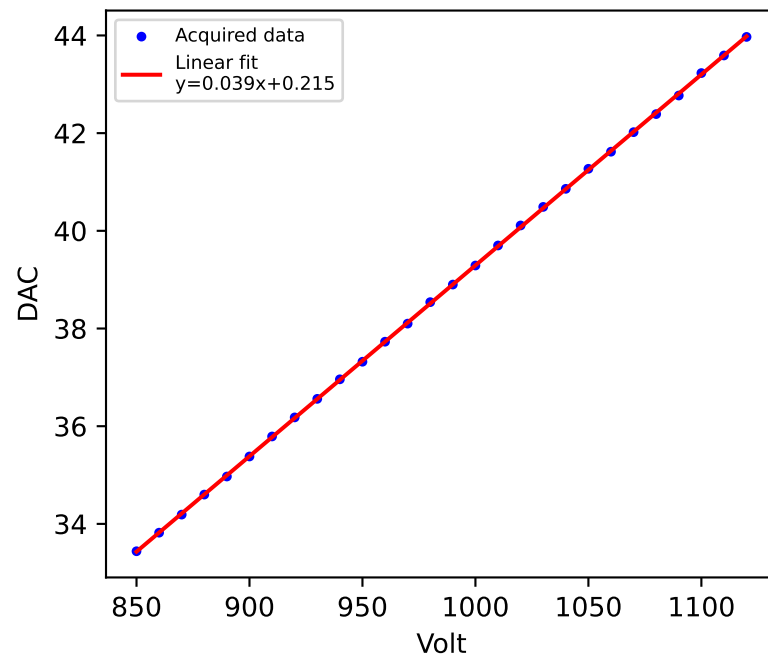
Trim REV IV curve



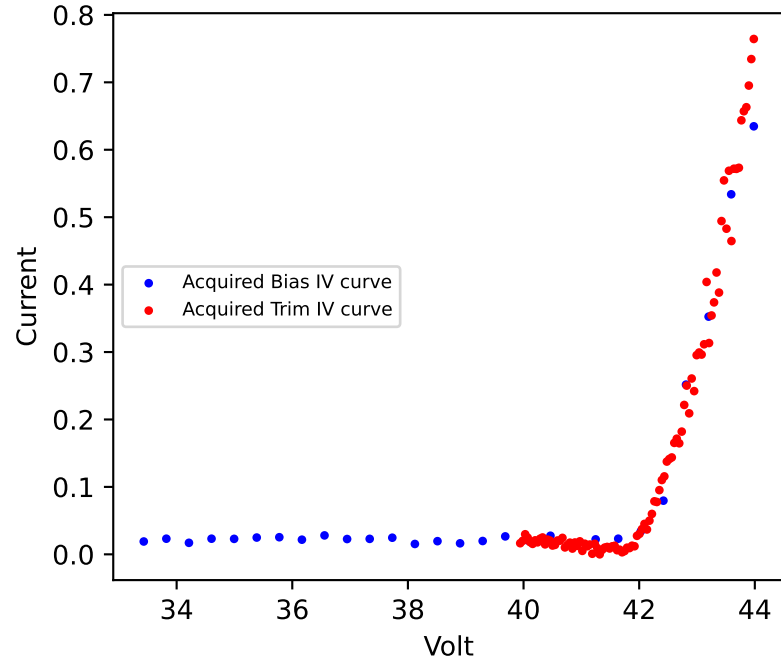
Bias REV IV curve



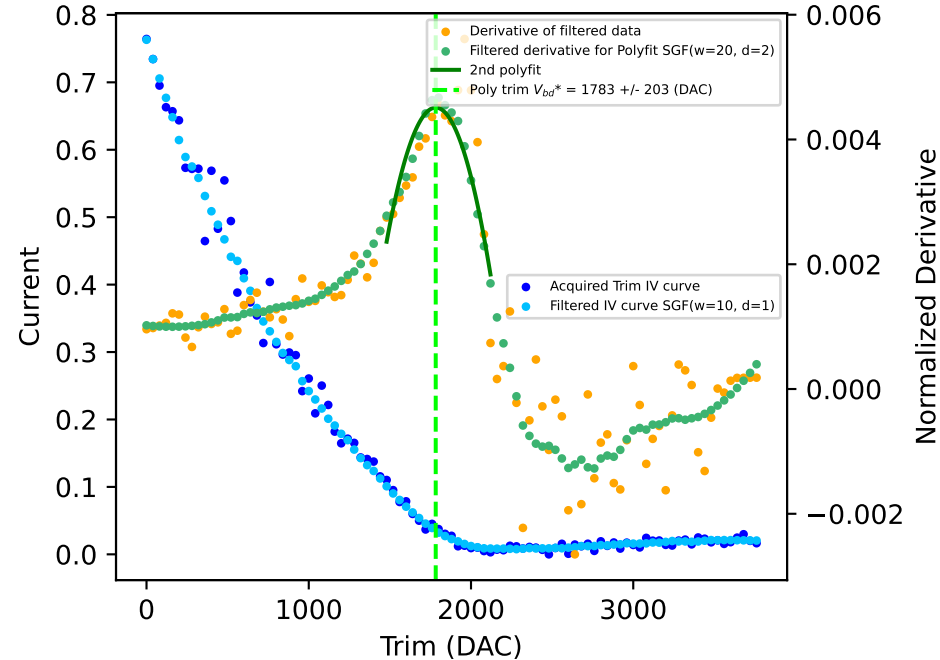
Bias DAC-VOLT conversion



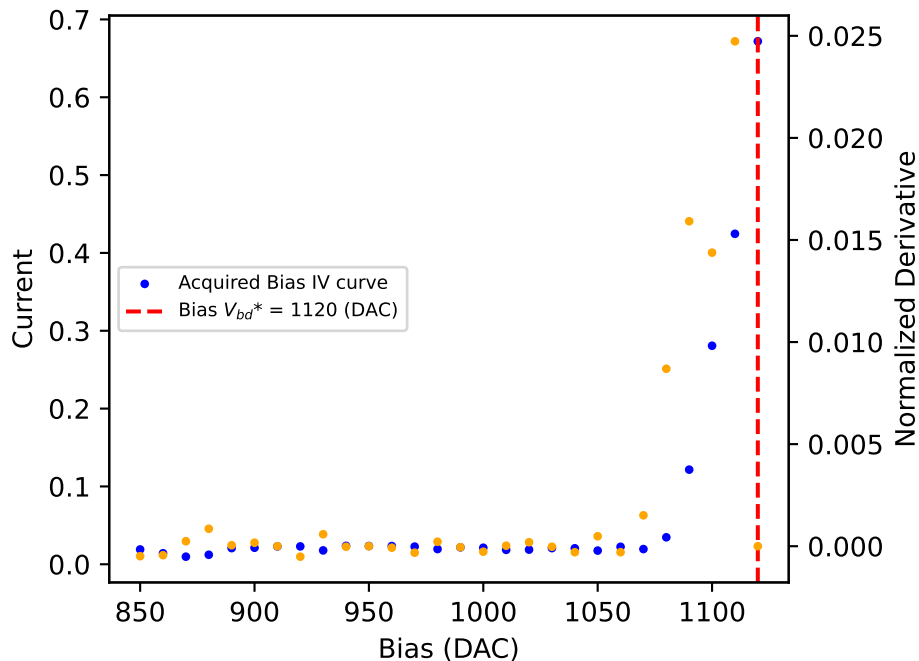
Bias + Trim REV IV curve



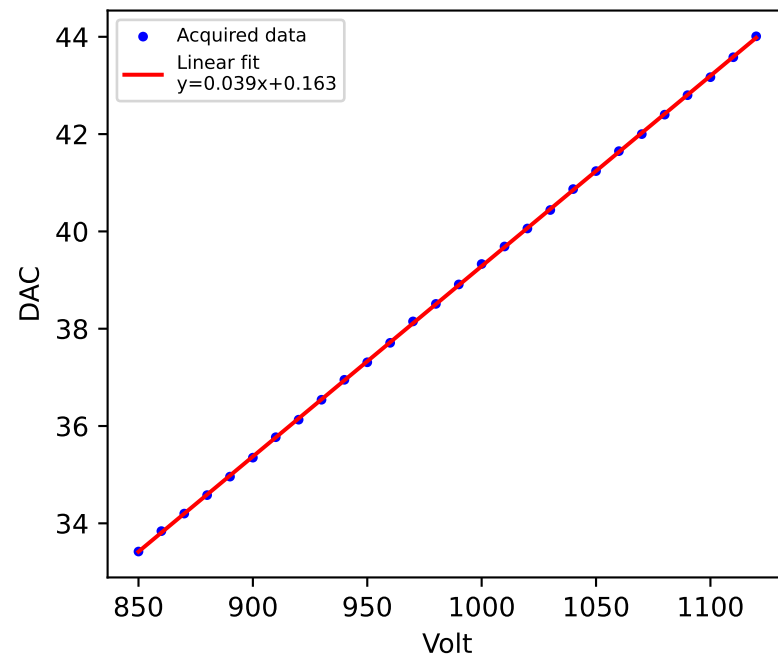
Trim REV IV curve



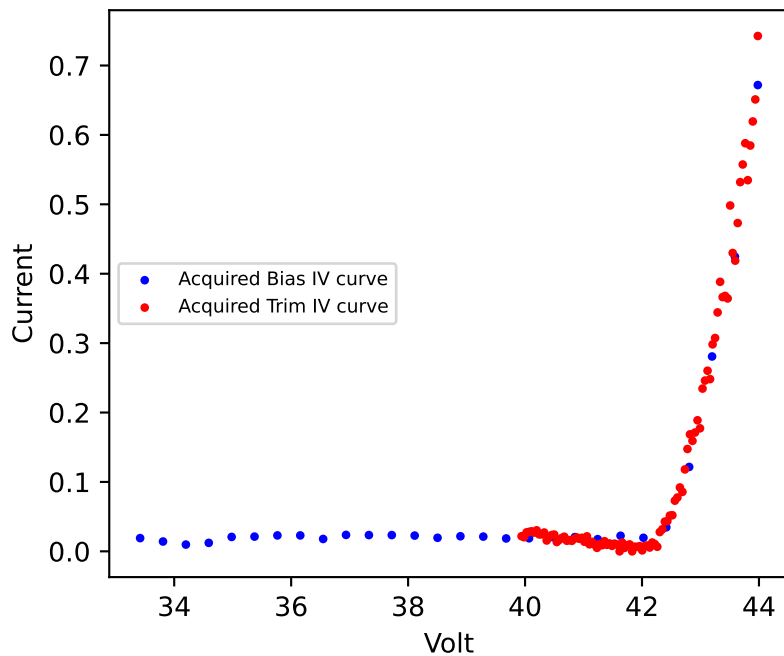
Bias REV IV curve



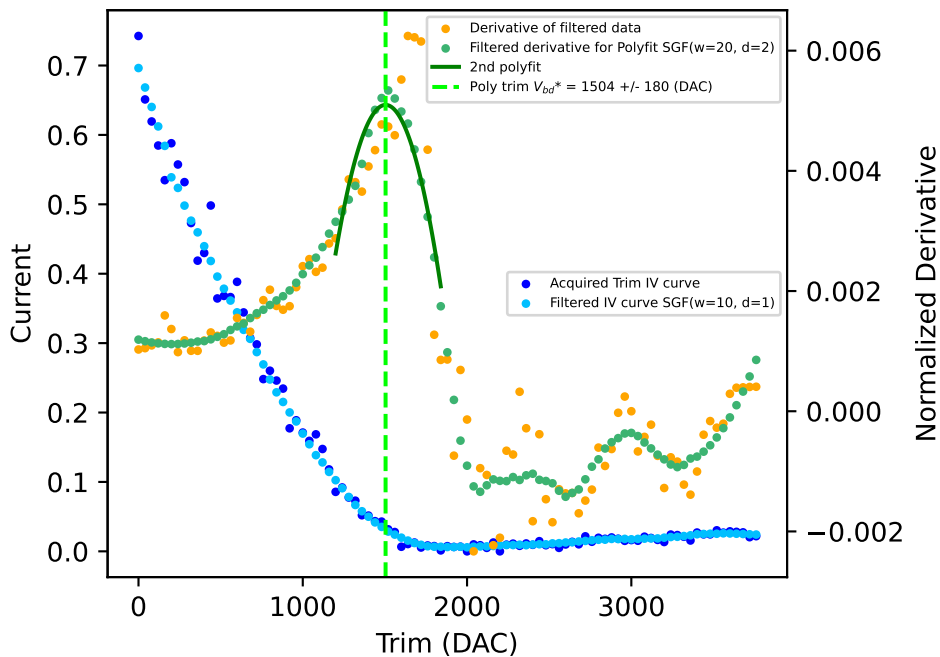
Bias DAC-VOLT conversion



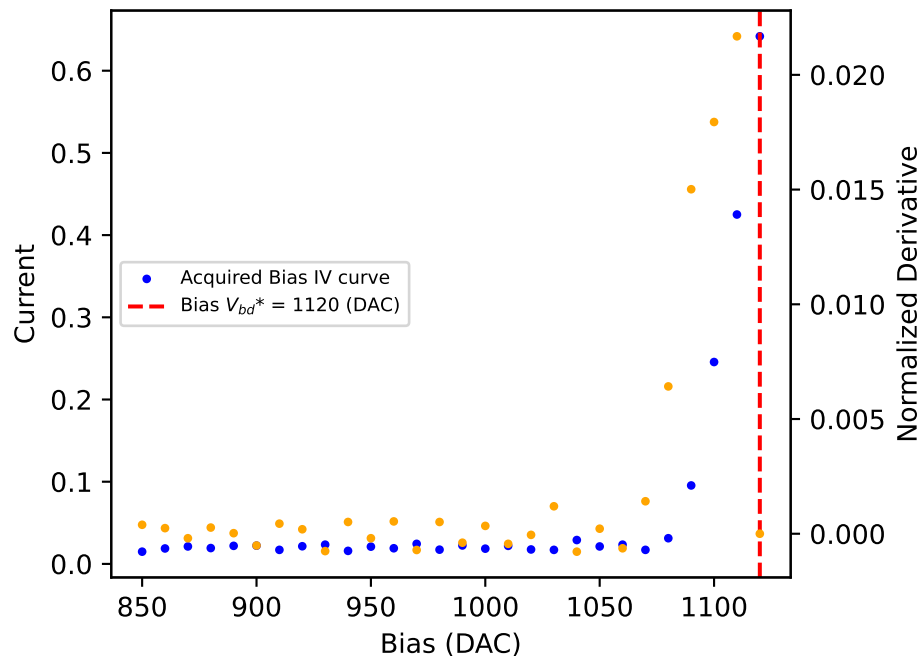
Bias + Trim REV IV curve



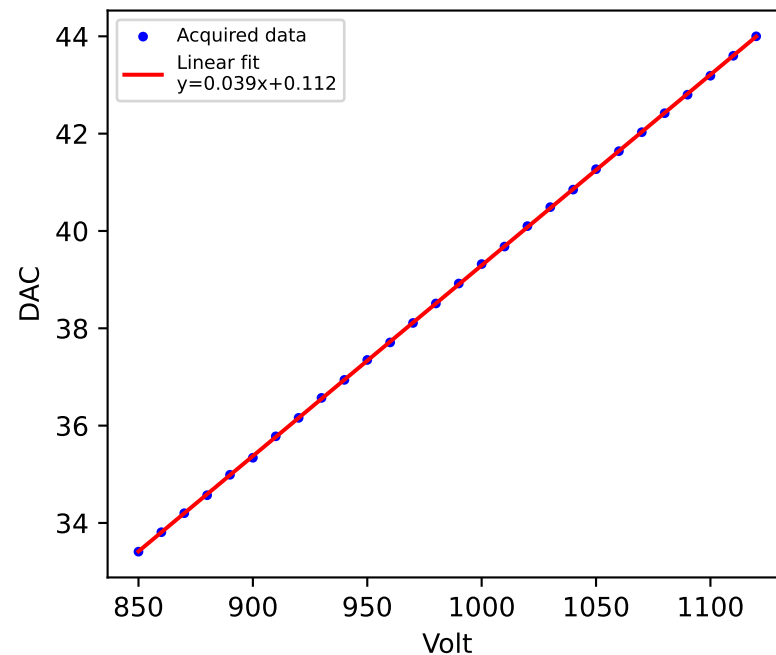
Trim REV IV curve



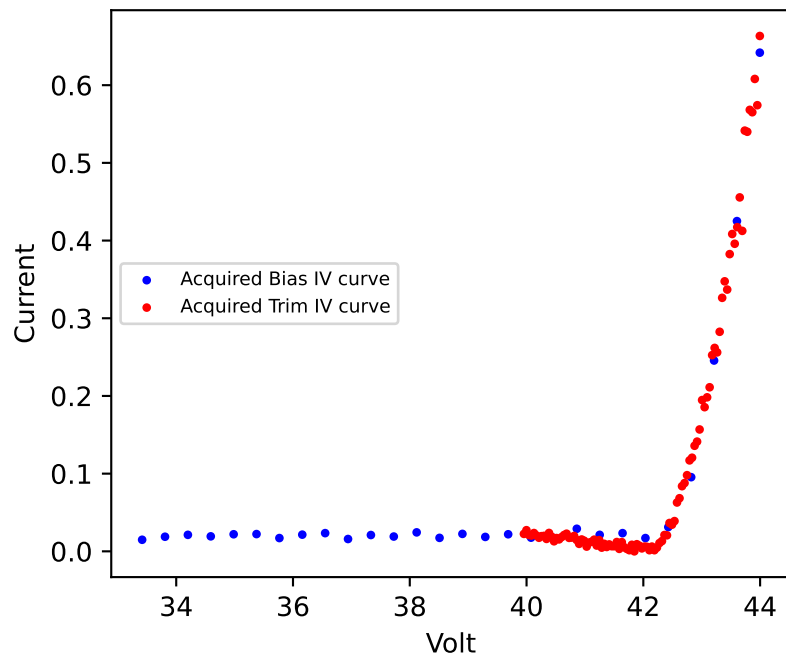
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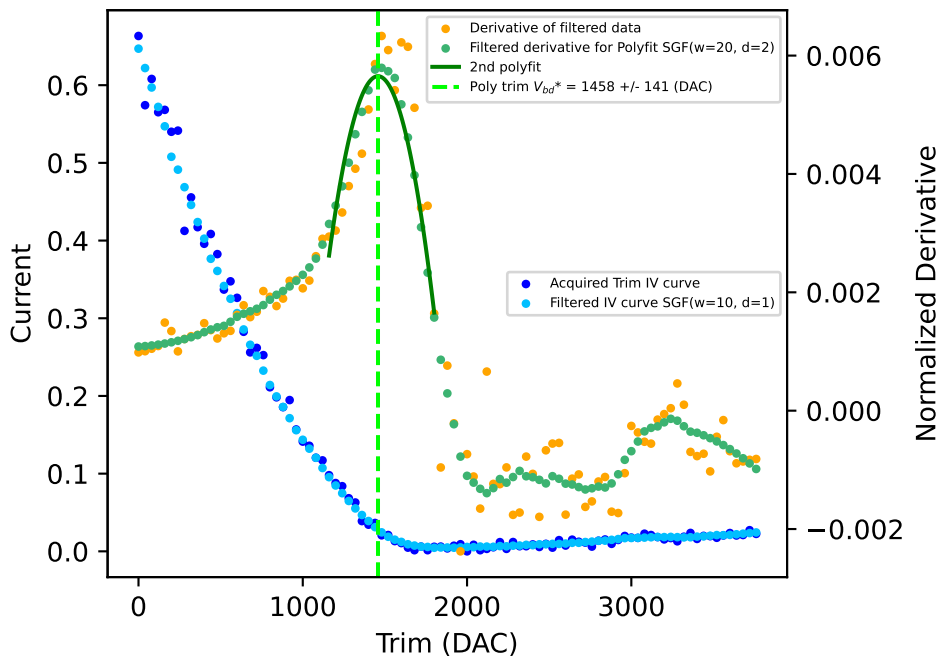
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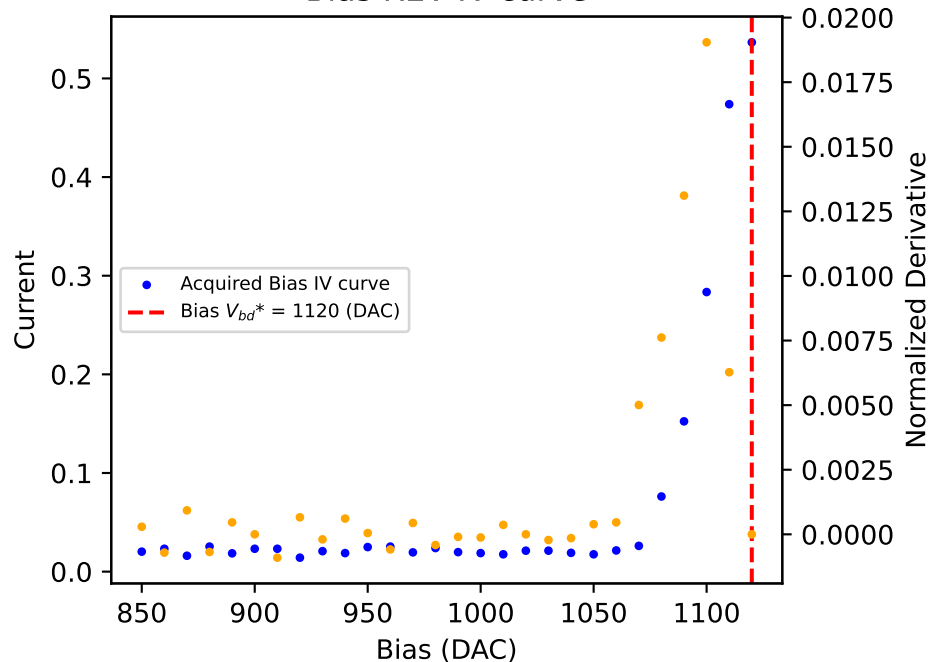
Bias + Trim REV IV curve



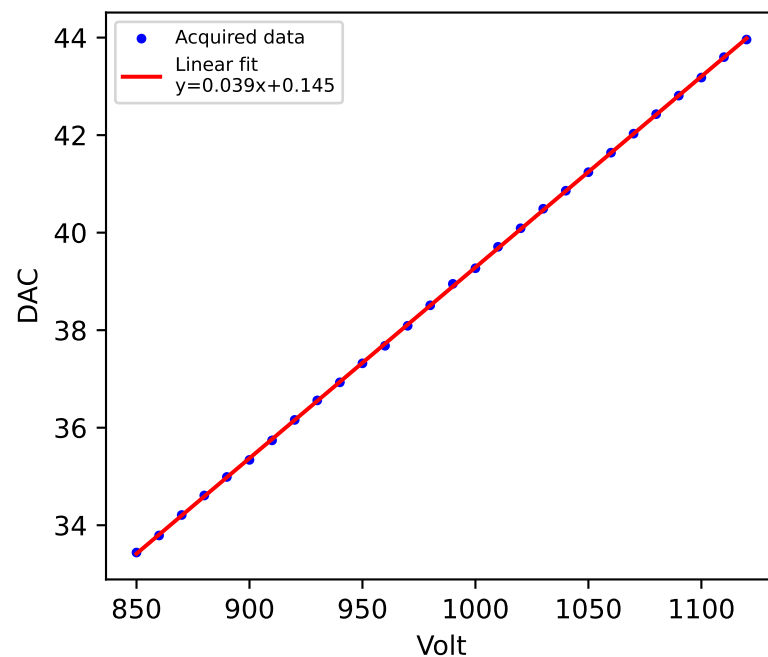
Trim REV IV curve



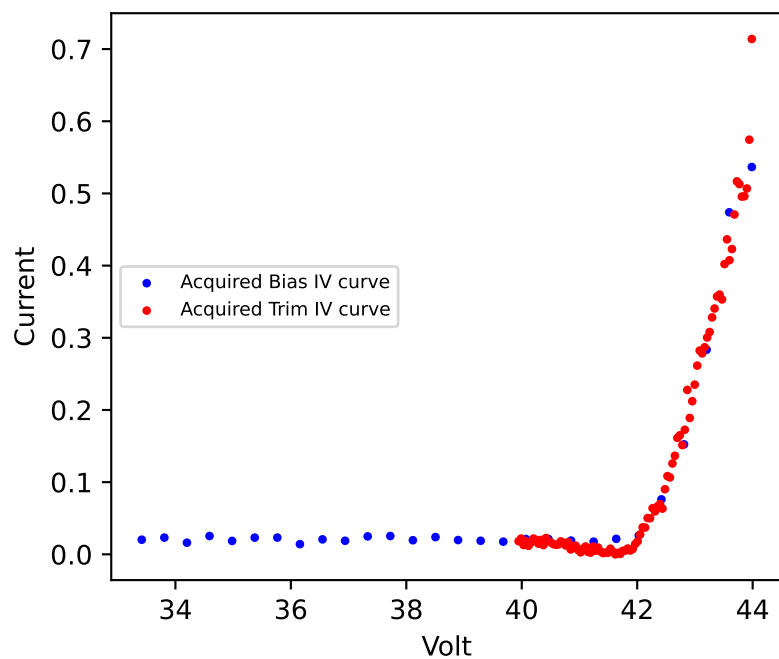
Bias REV IV curve



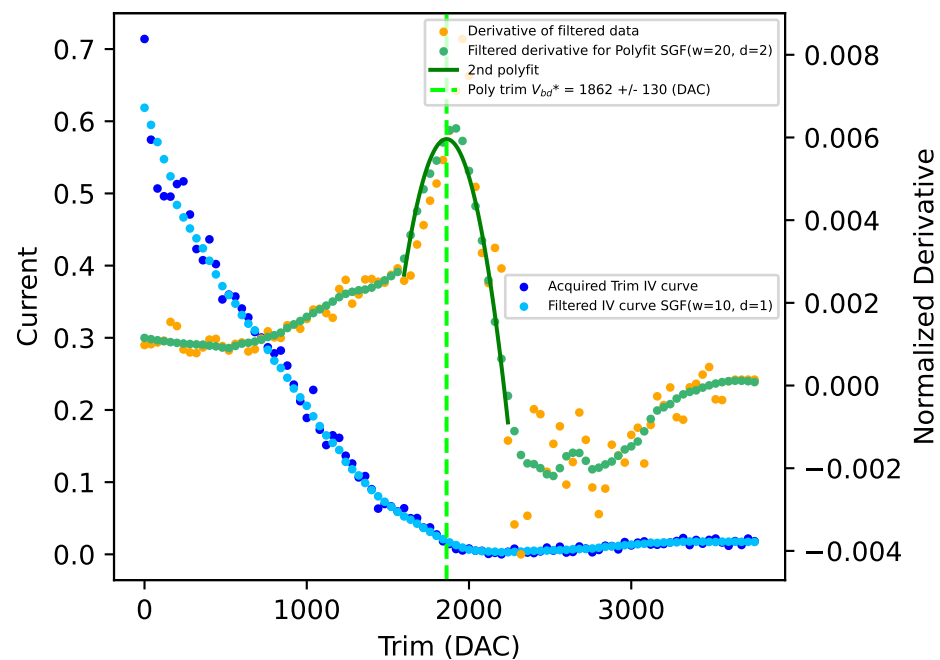
Bias DAC-VOLT conversion



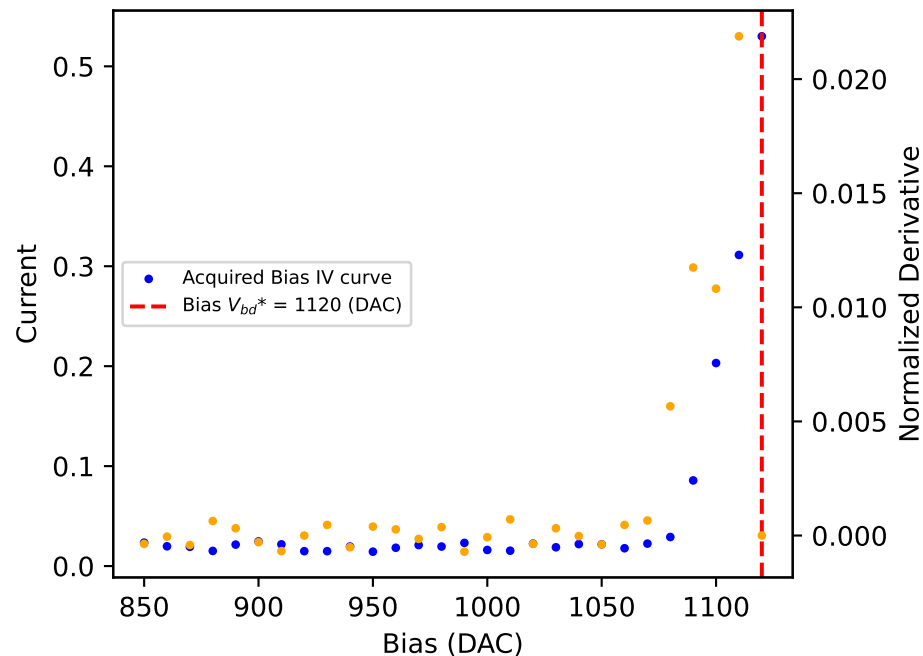
Bias + Trim REV IV curve



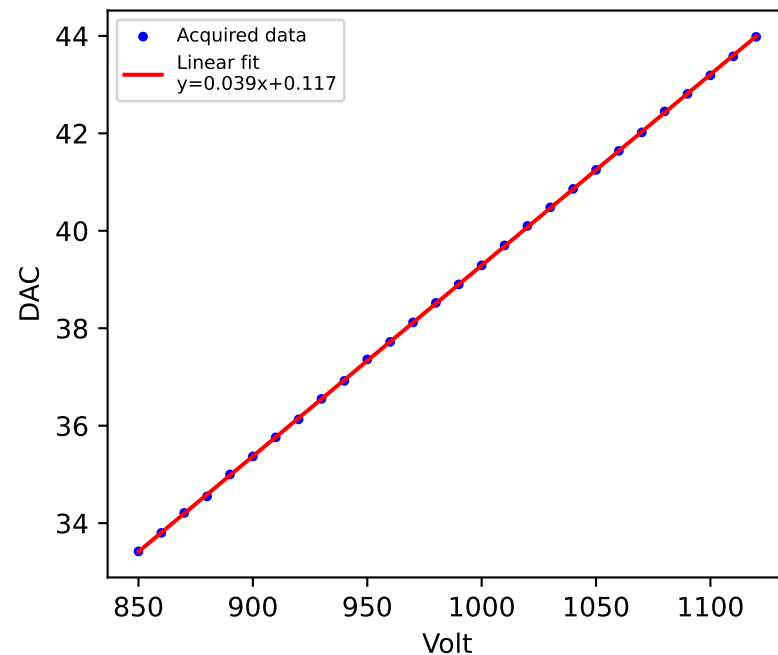
Trim REV IV curve



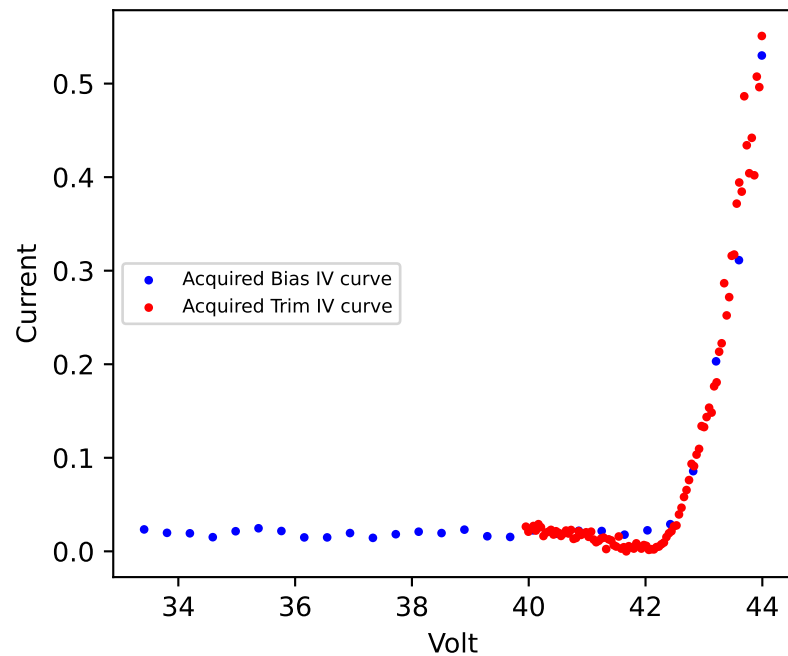
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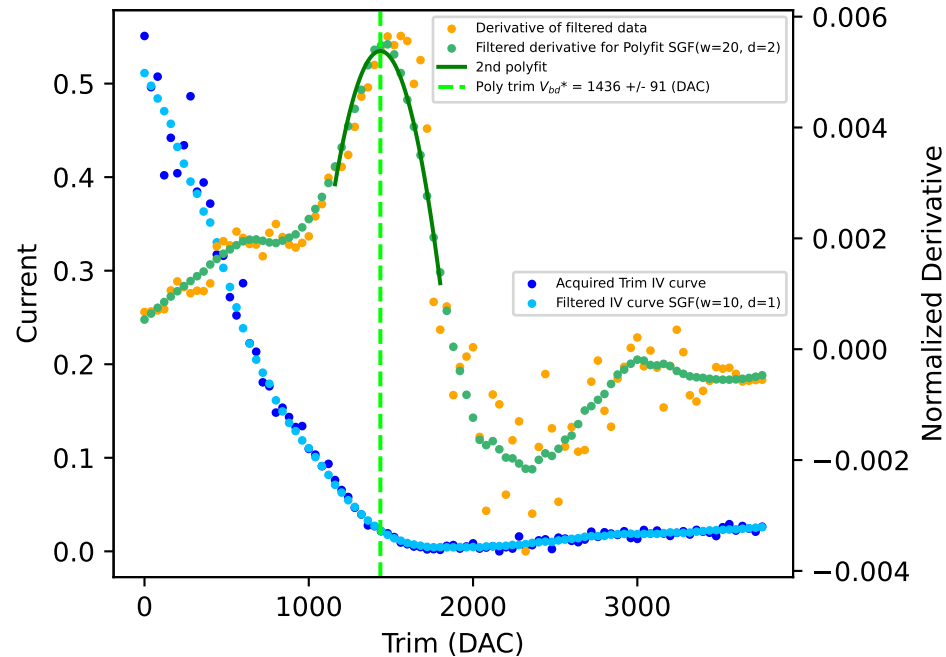
Bias DAC-VOLT conversion



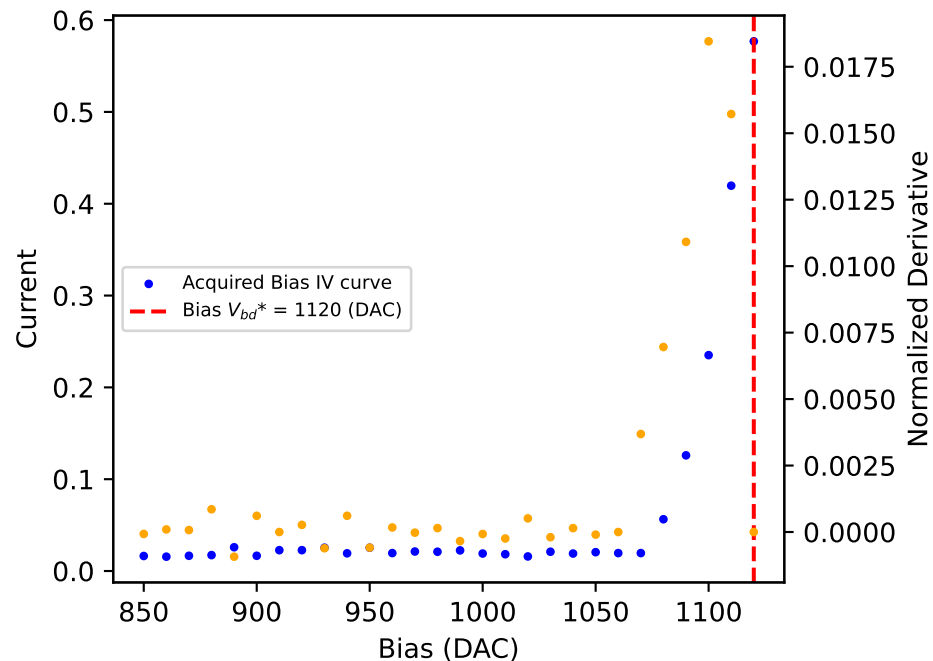
Bias + Trim REV IV curve



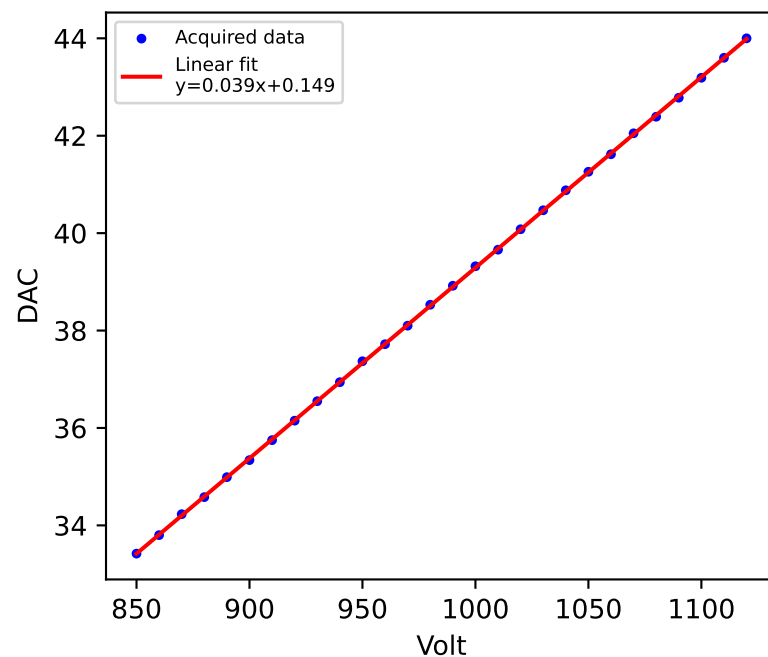
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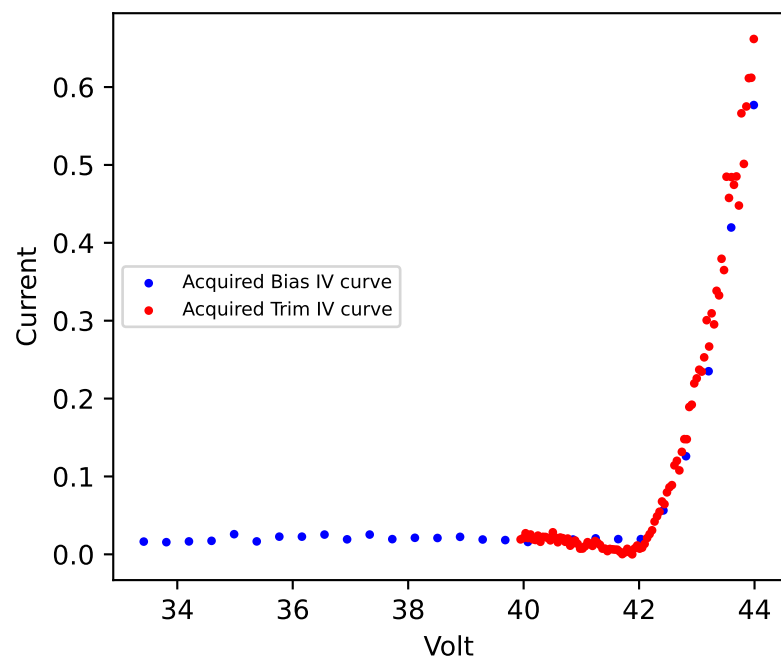
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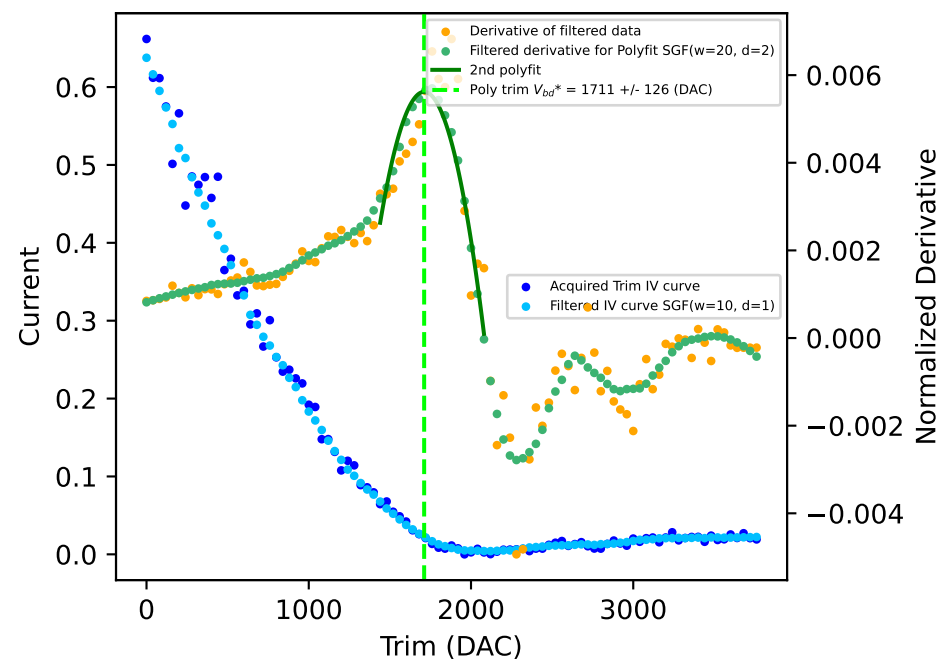
Bias DAC-VOLT conversion



Bias + Trim REV IV curve

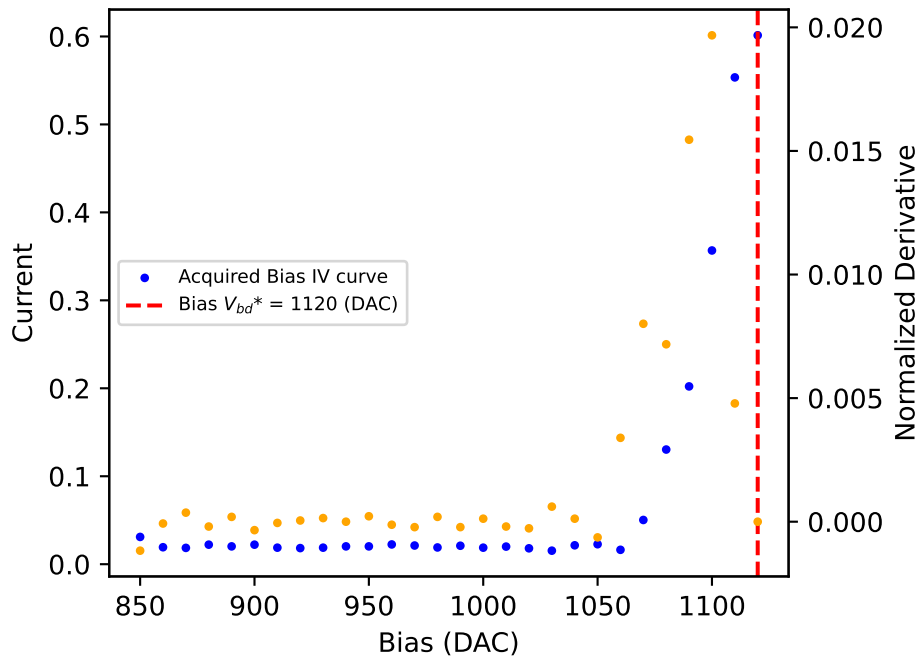


Trim REV IV curve

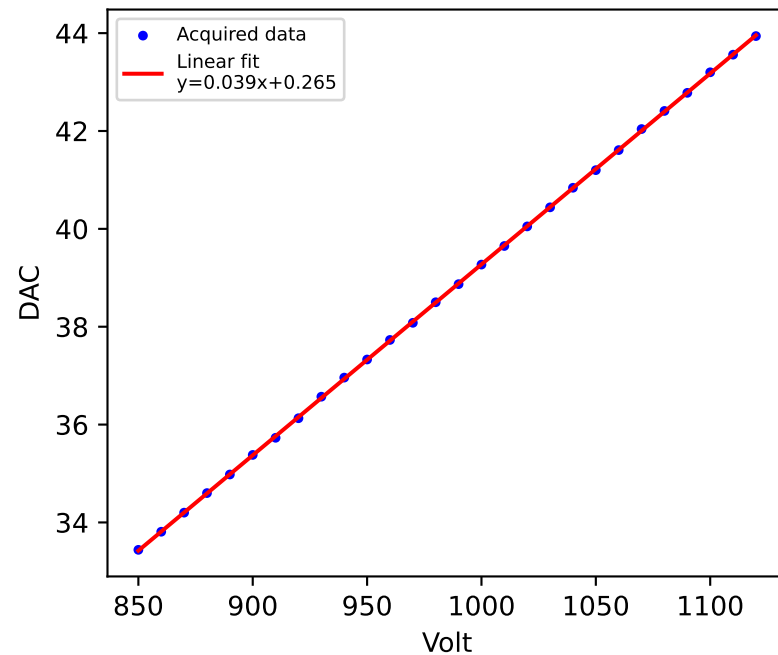




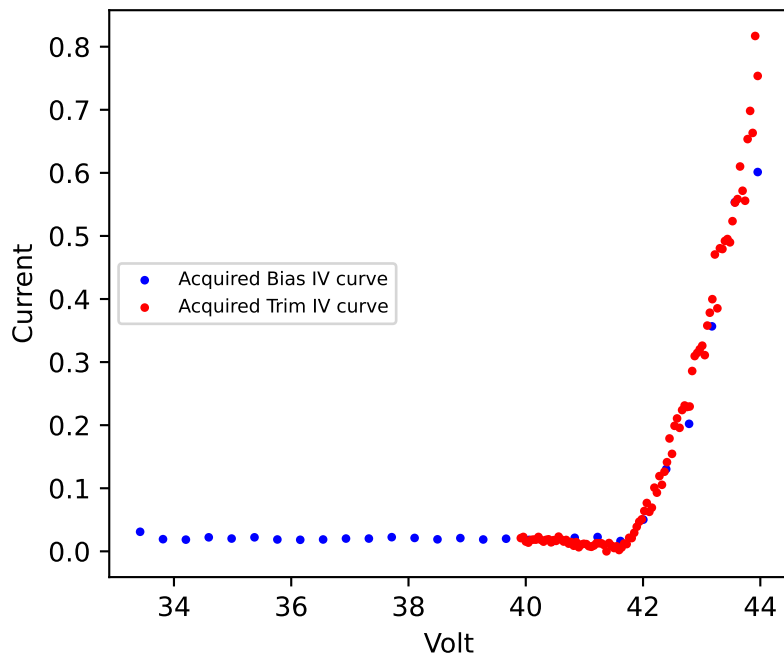
Bias REV IV curve



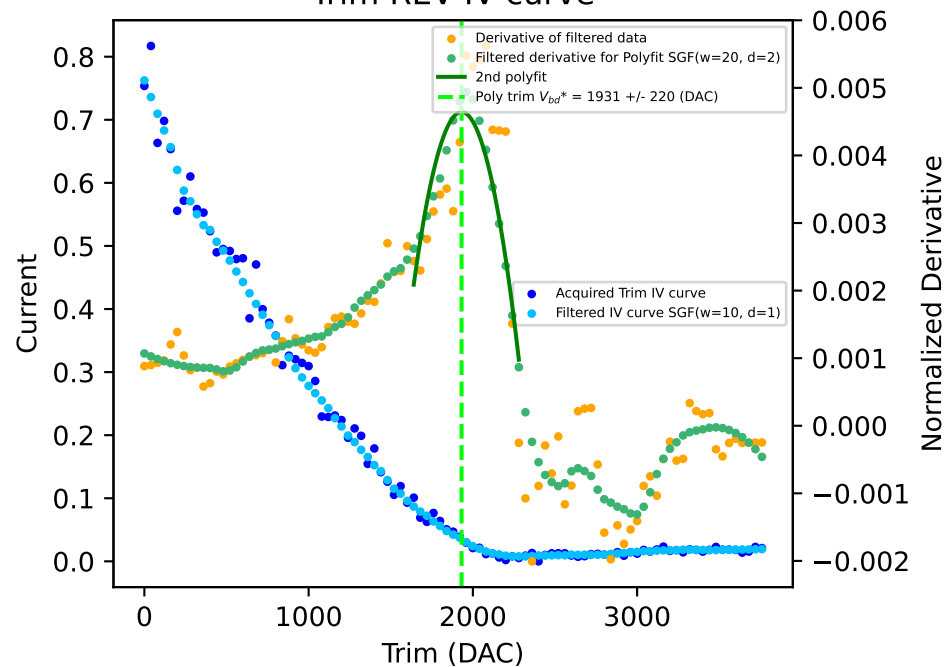
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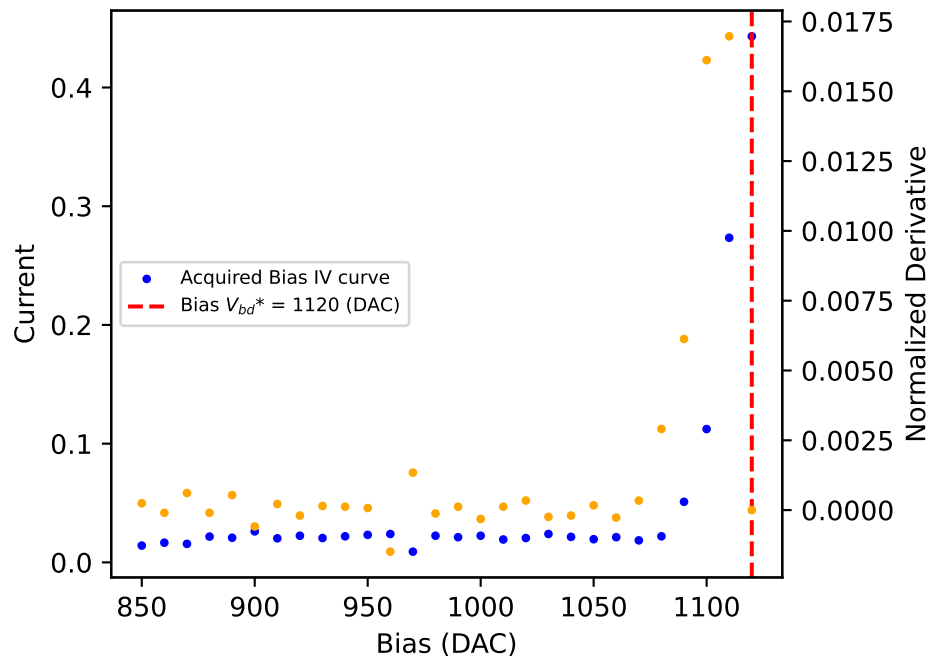
Bias + Trim REV IV curve



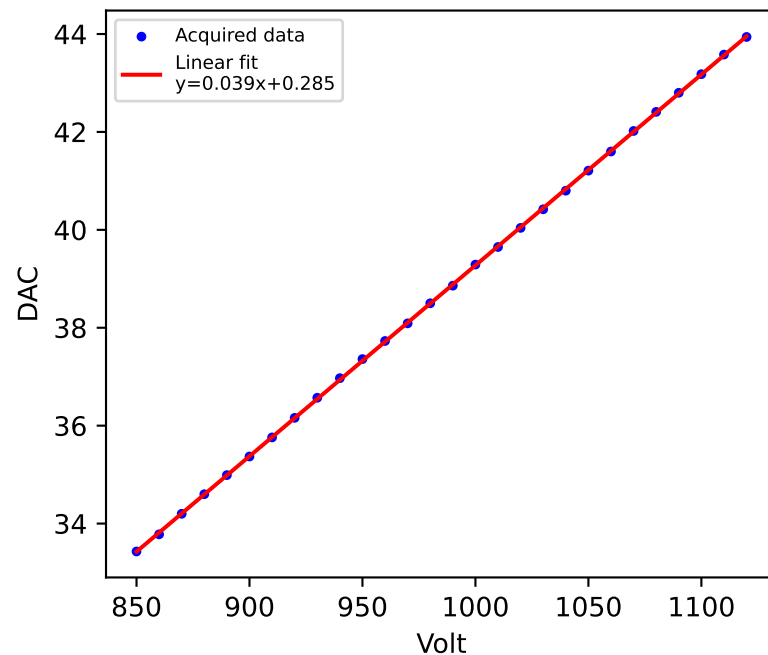
Trim REV IV curve



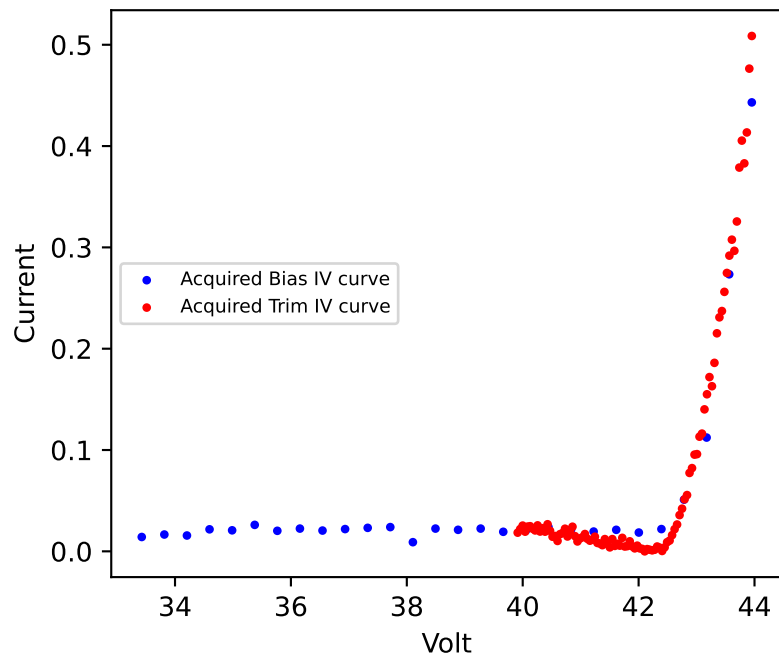
Bias REV IV curve



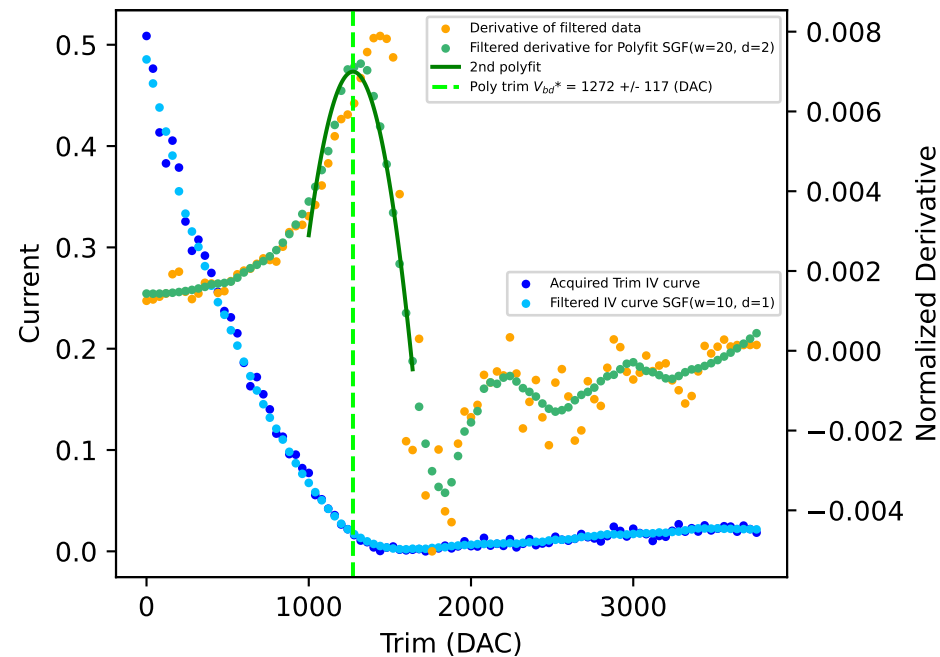
Bias DAC-VOLT conversion



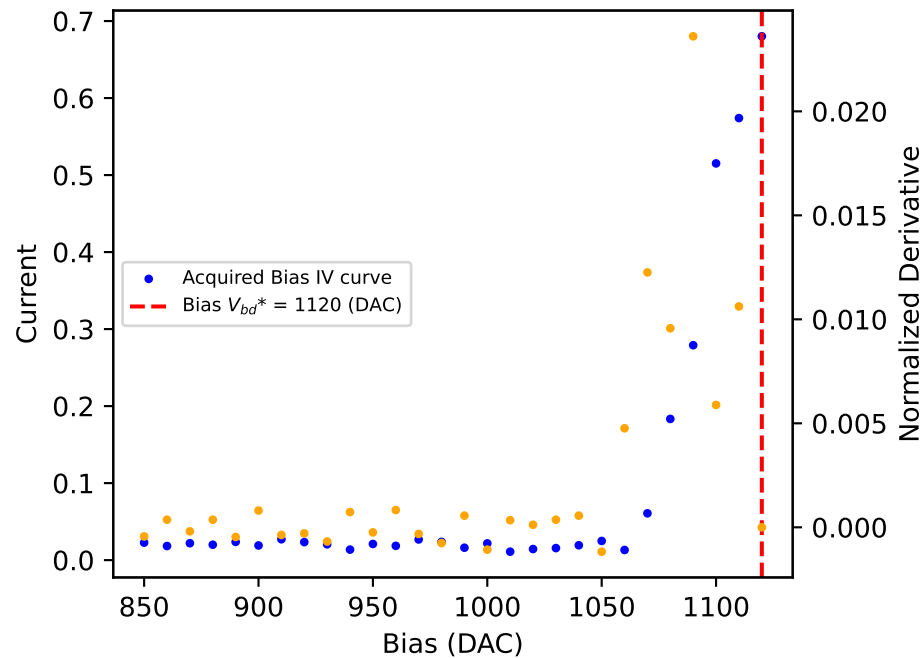
Bias + Trim REV IV curve



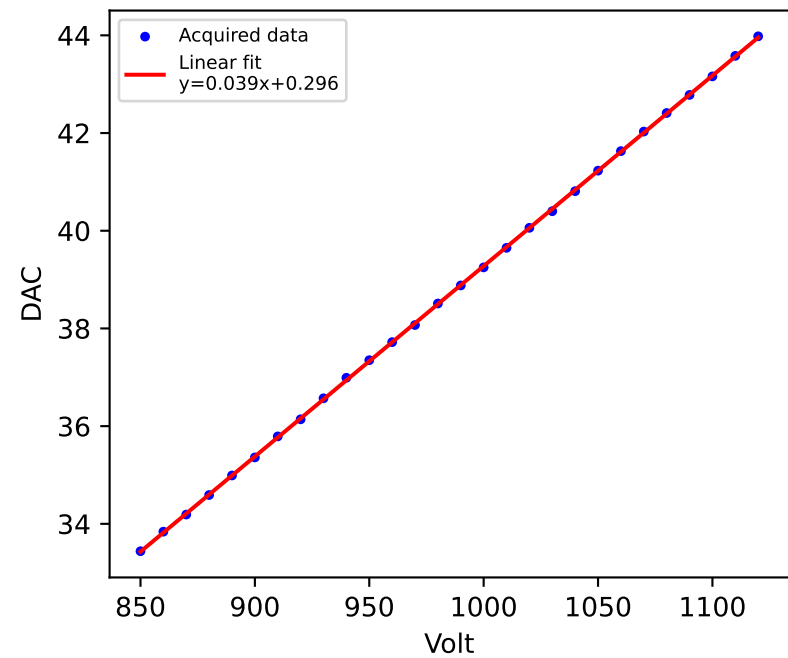
Trim REV IV curve



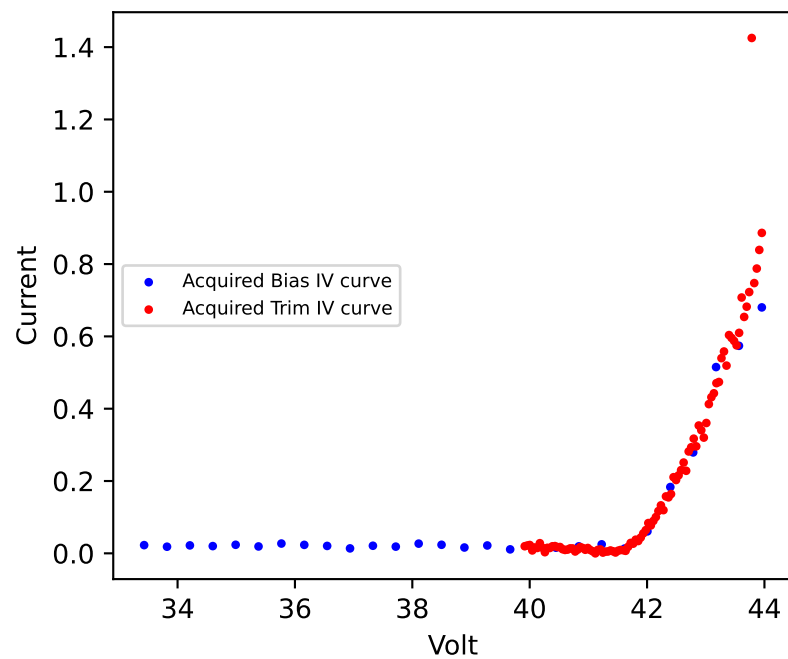
Bias REV IV curve



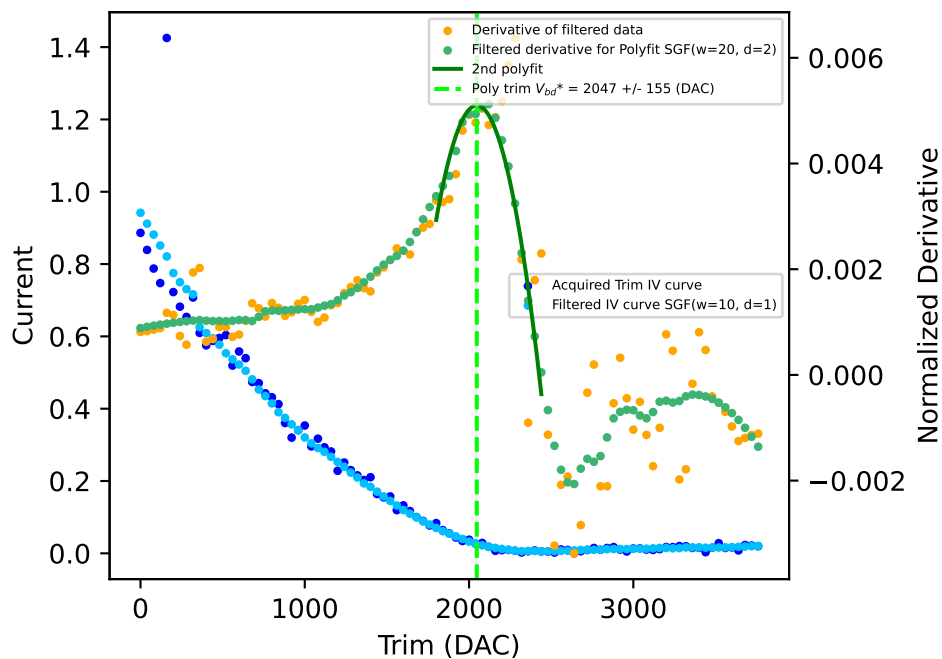
Bias DAC-VOLT conversion



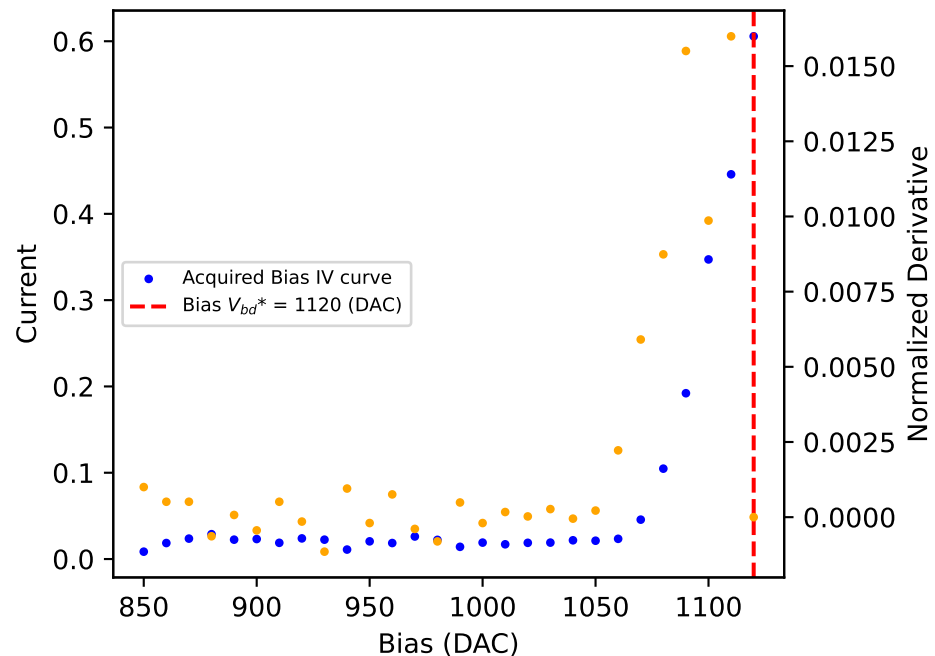
Bias + Trim REV IV curve



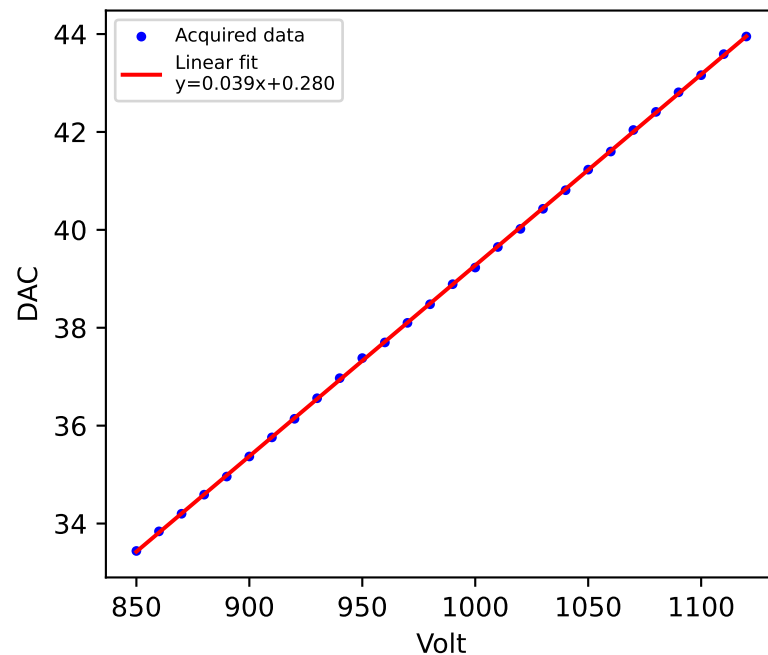
Trim REV IV curve



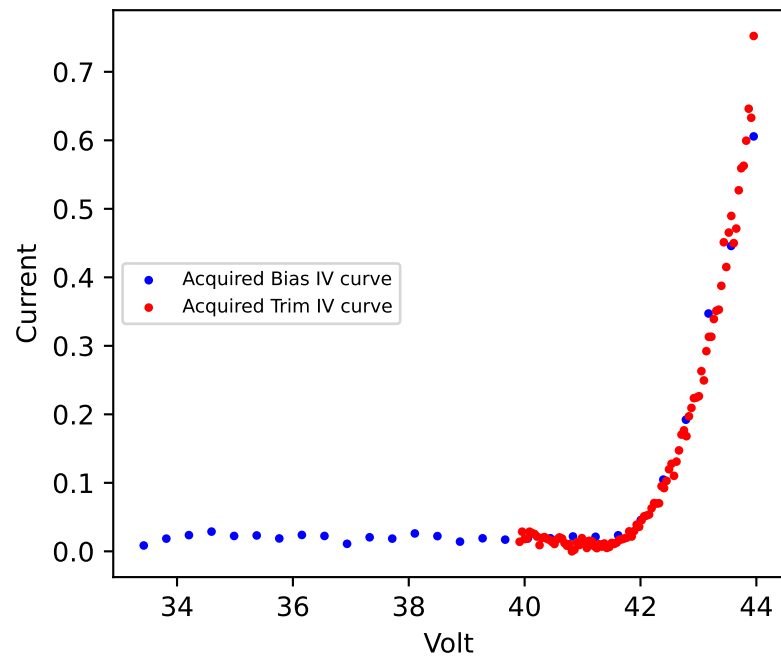
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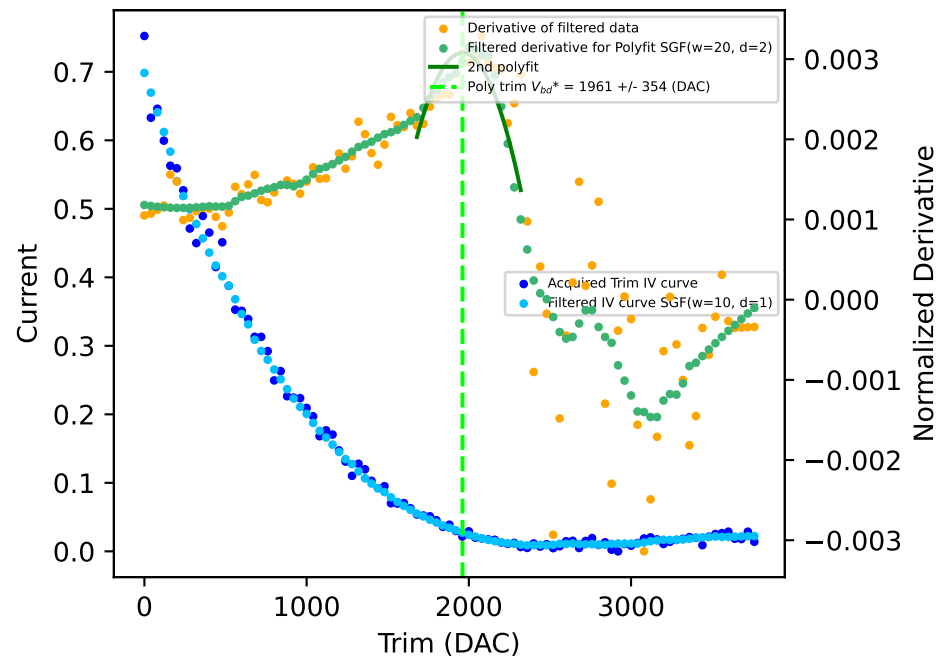
Bias DAC-VOLT conversion



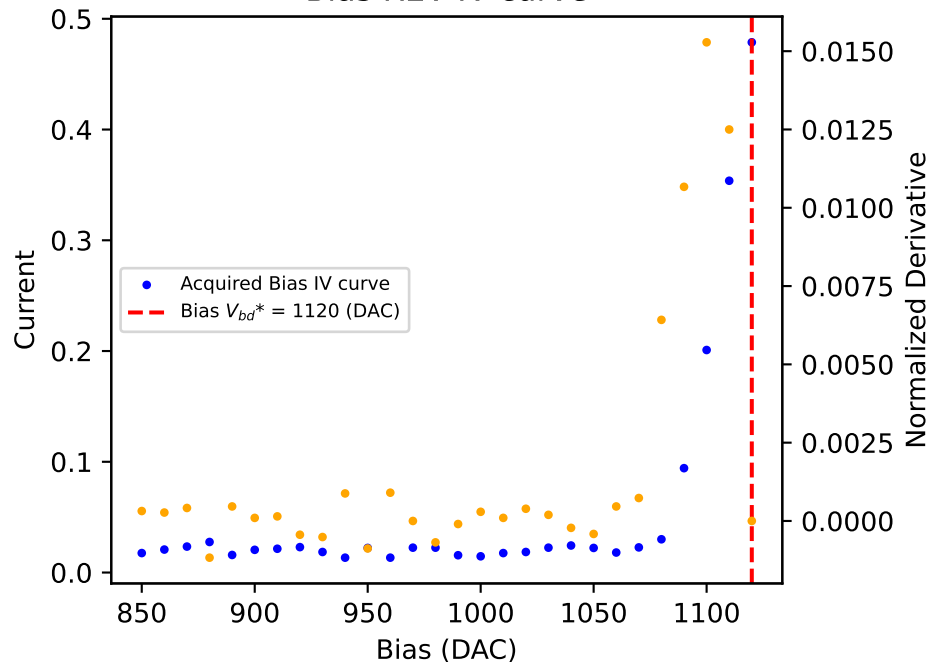
Bias + Trim REV IV curve



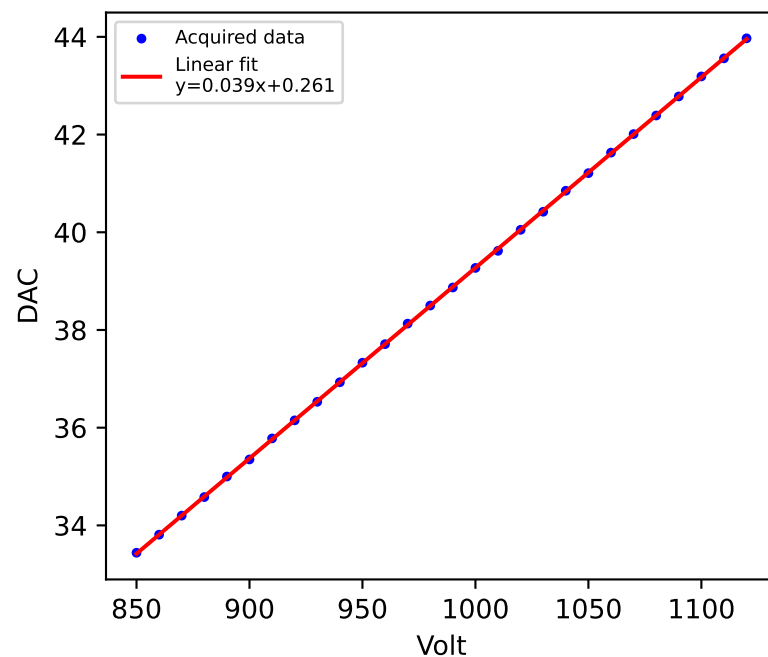
Trim REV IV curve



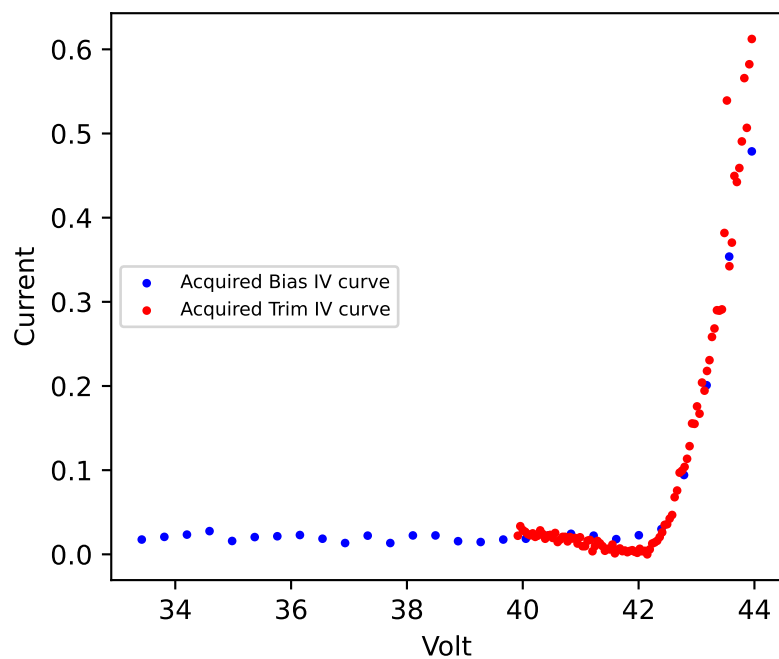
Bias REV IV curve



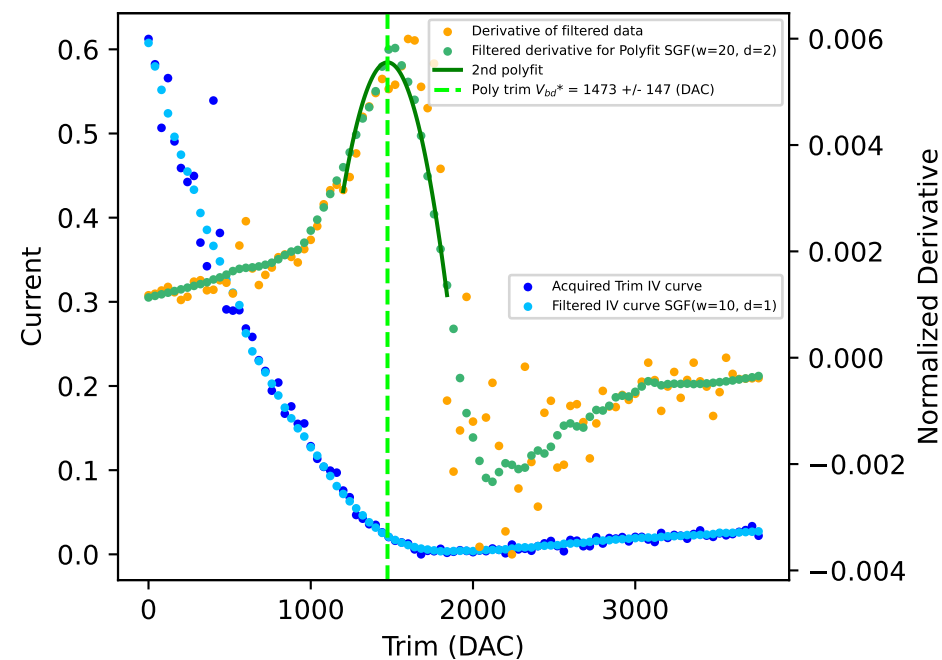
Bias DAC-VOLT conversion



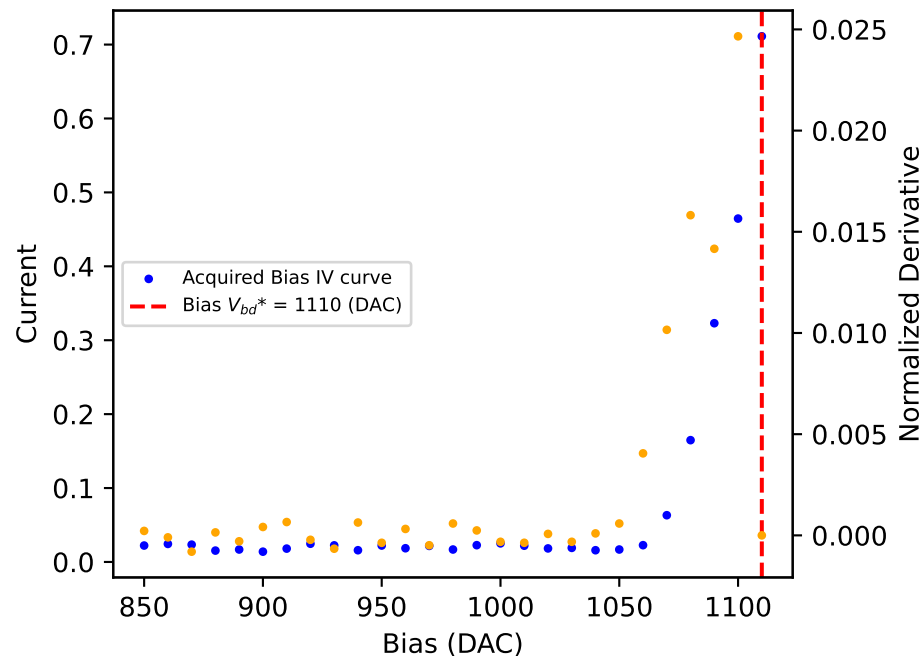
Bias + Trim REV IV curve



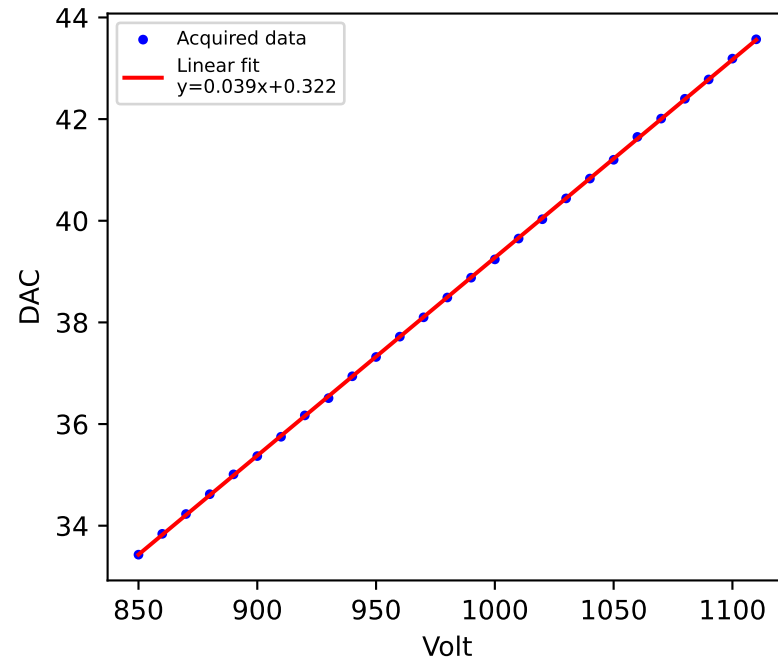
Trim REV IV curve



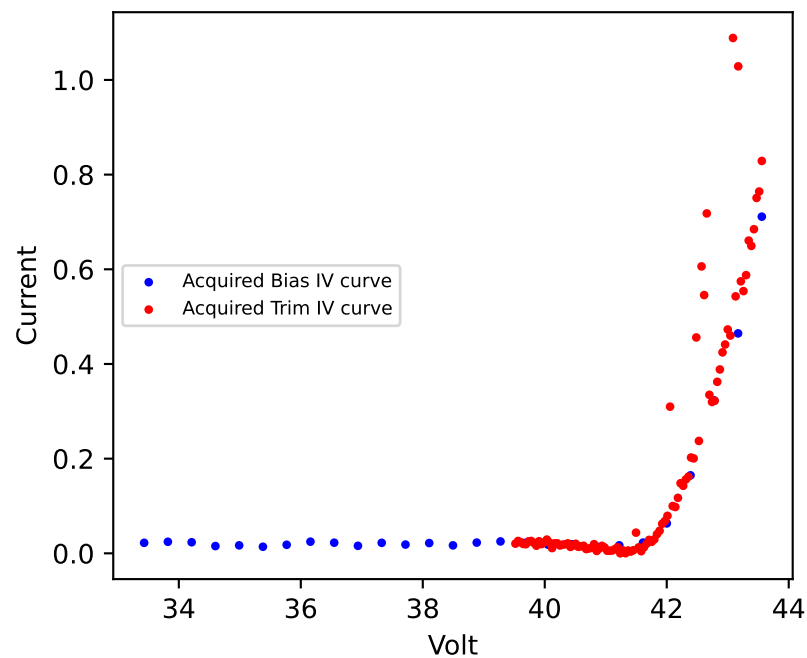
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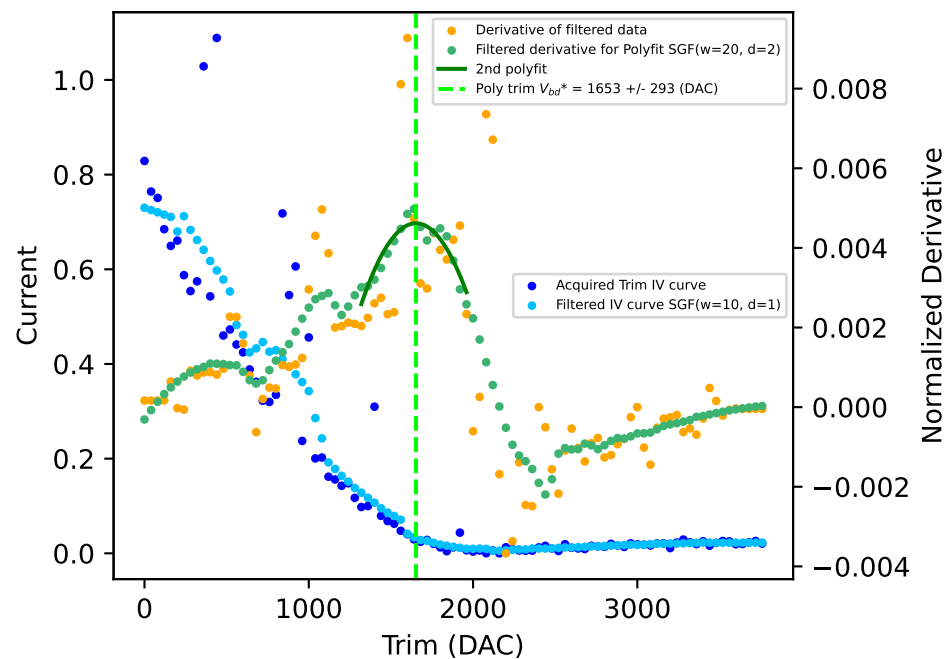
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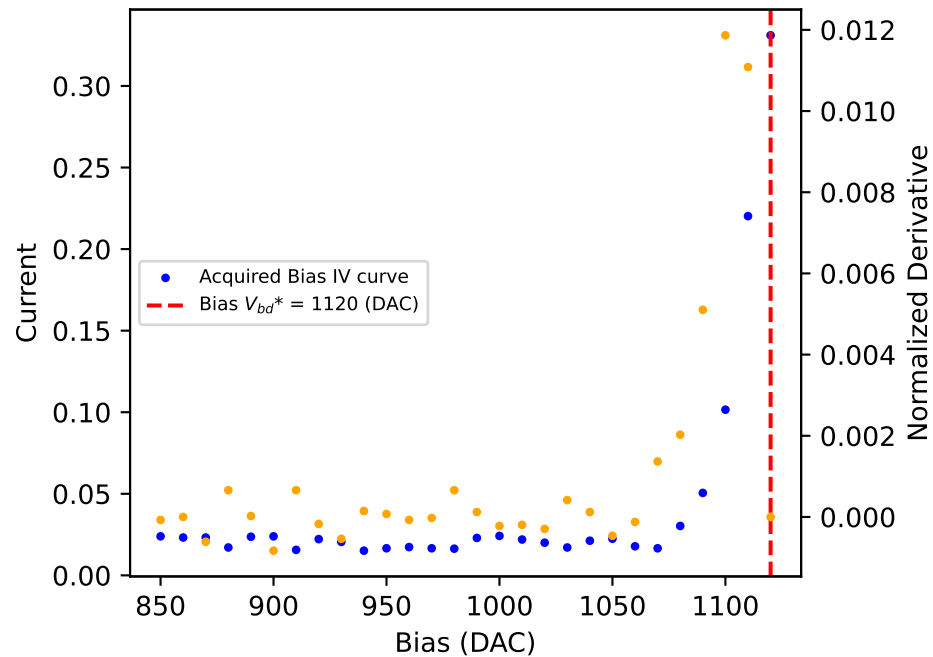
Bias + Trim REV IV curve



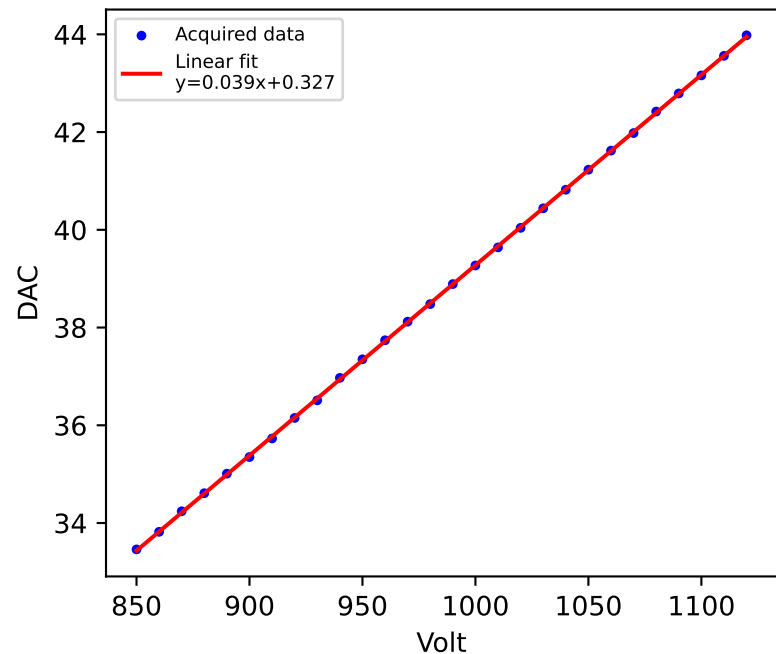
Trim REV IV curve



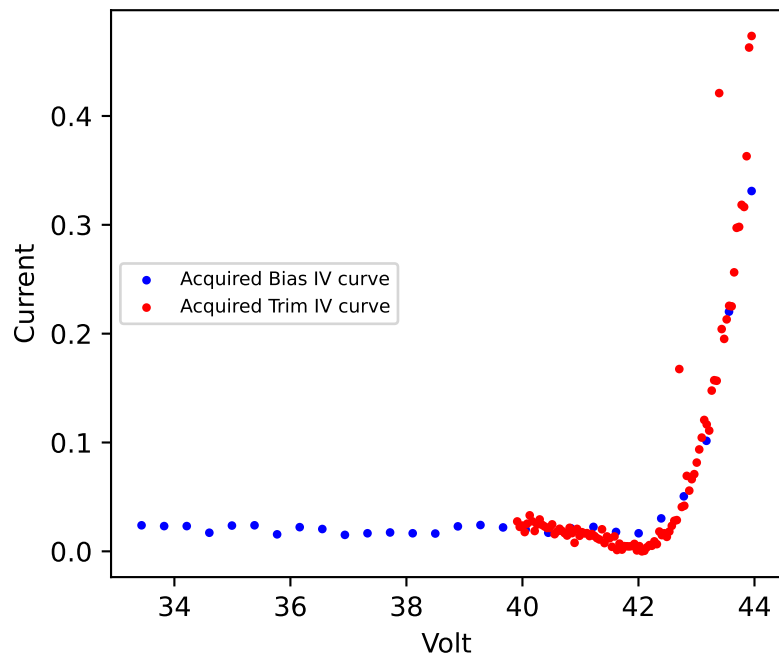
Bias REV IV curve



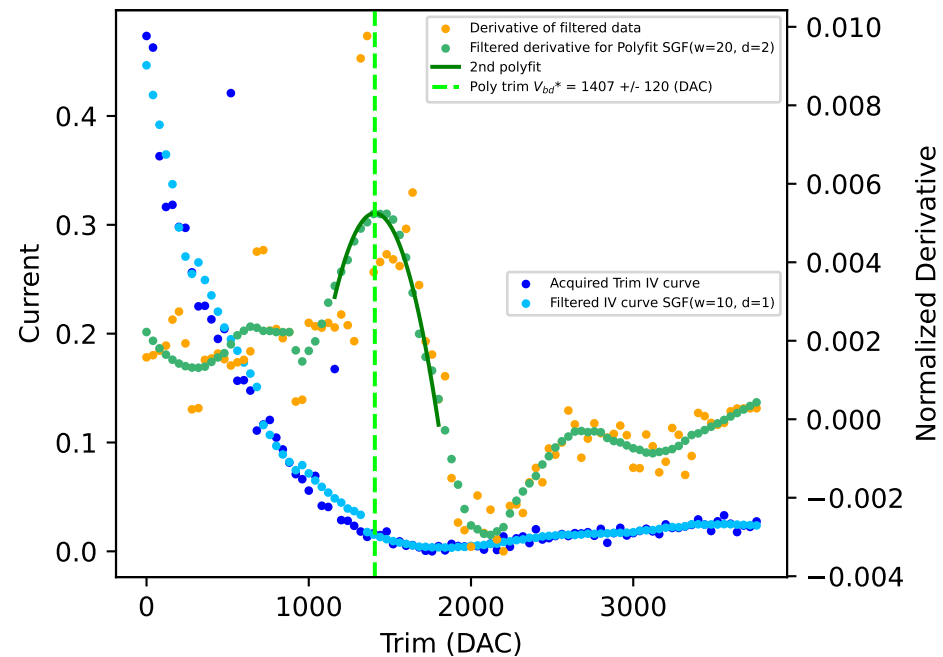
Bias DAC-VOLT conversion



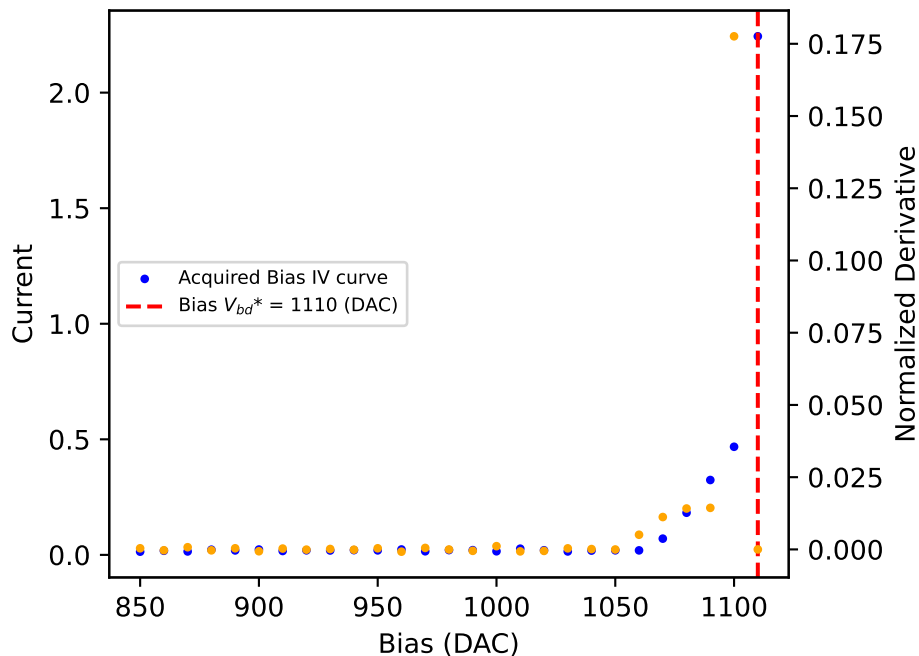
Bias + Trim REV IV curve



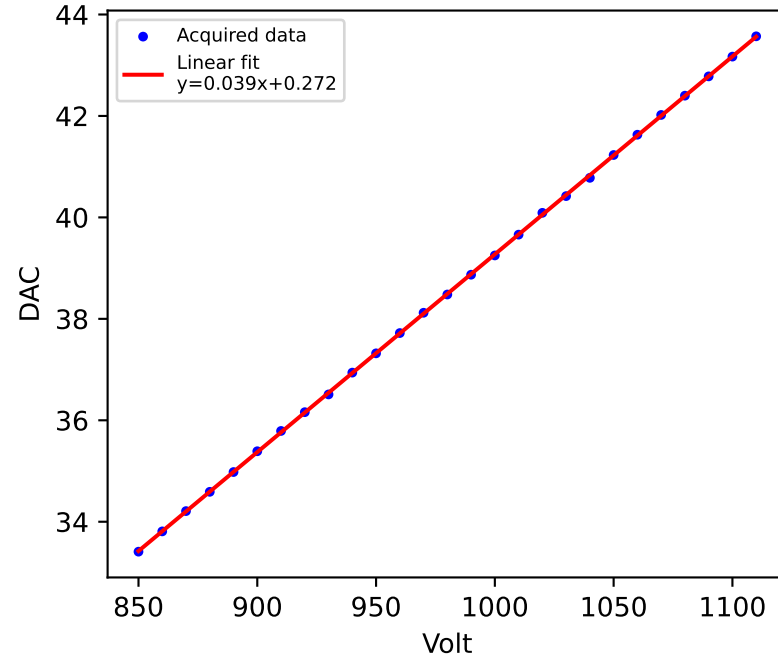
Trim REV IV curve



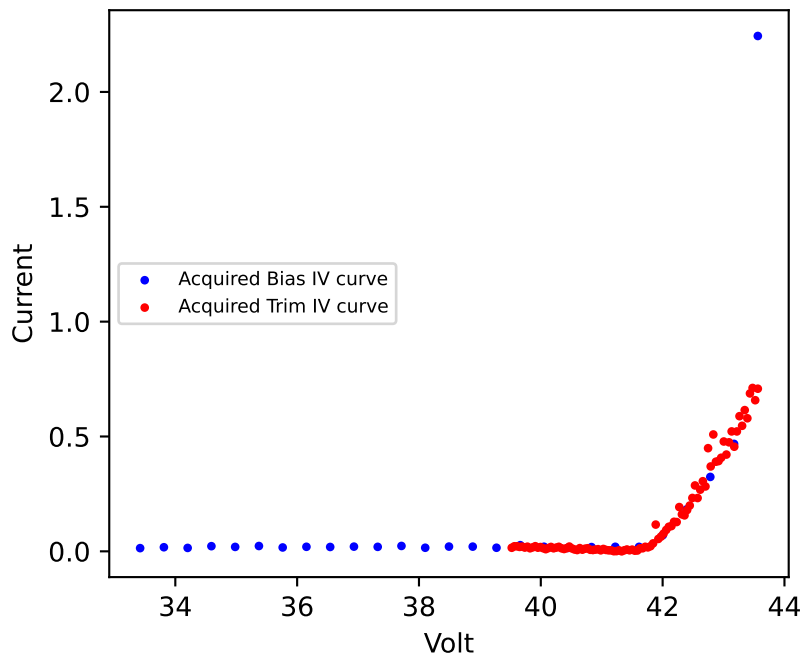
Bias REV IV curve



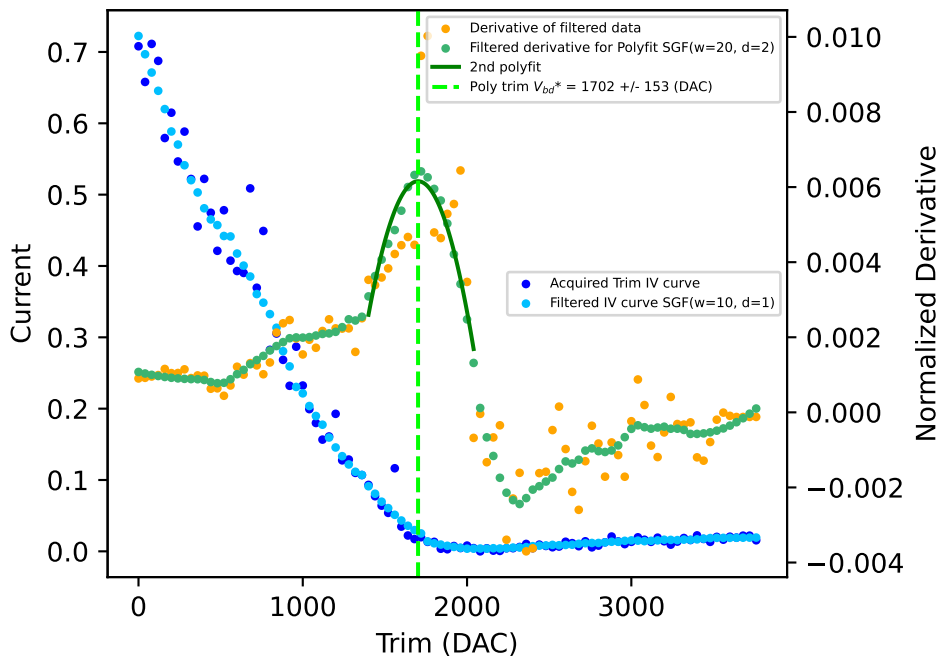
Bias DAC-VOLT conversion



Bias + Trim REV IV curve

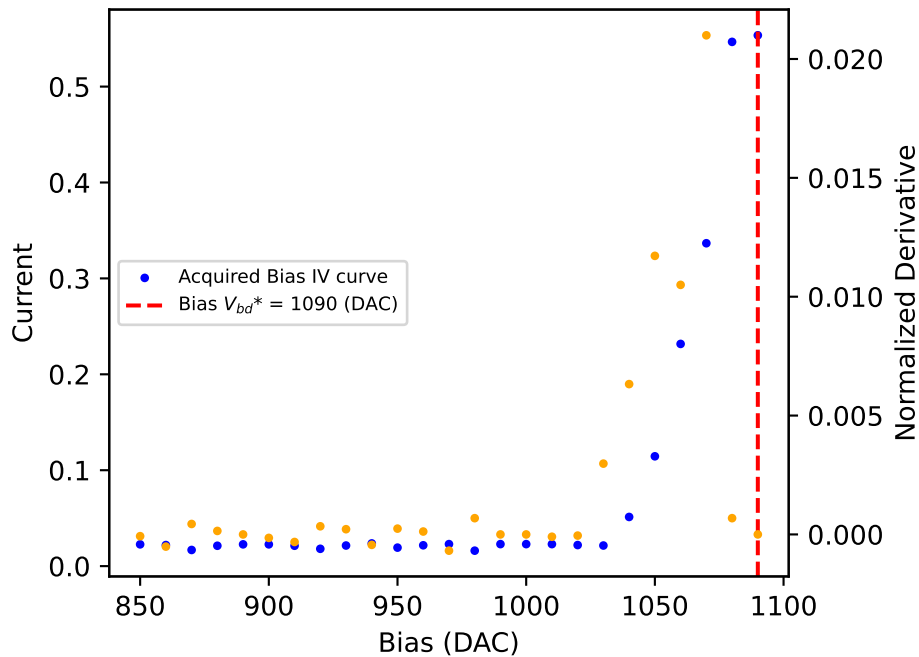


Trim REV IV curve

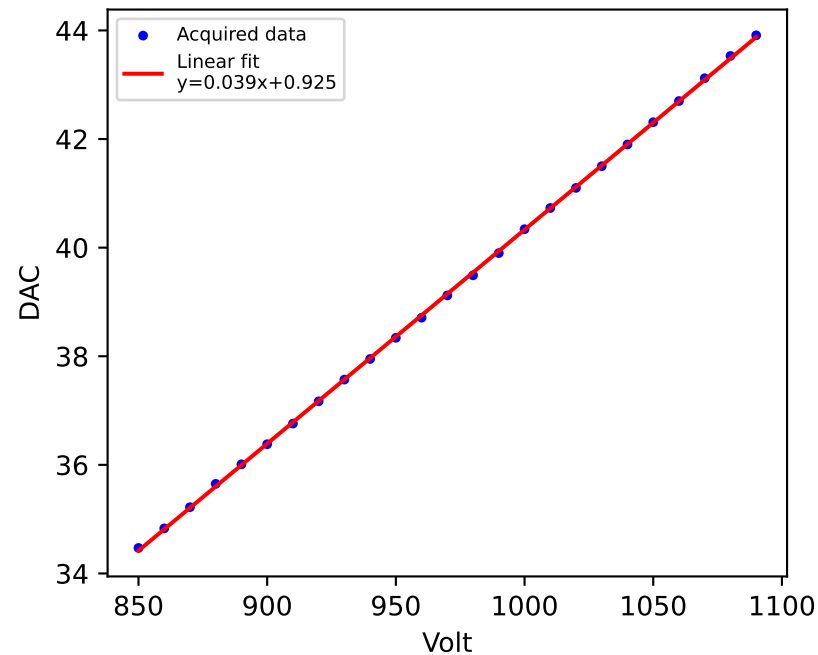




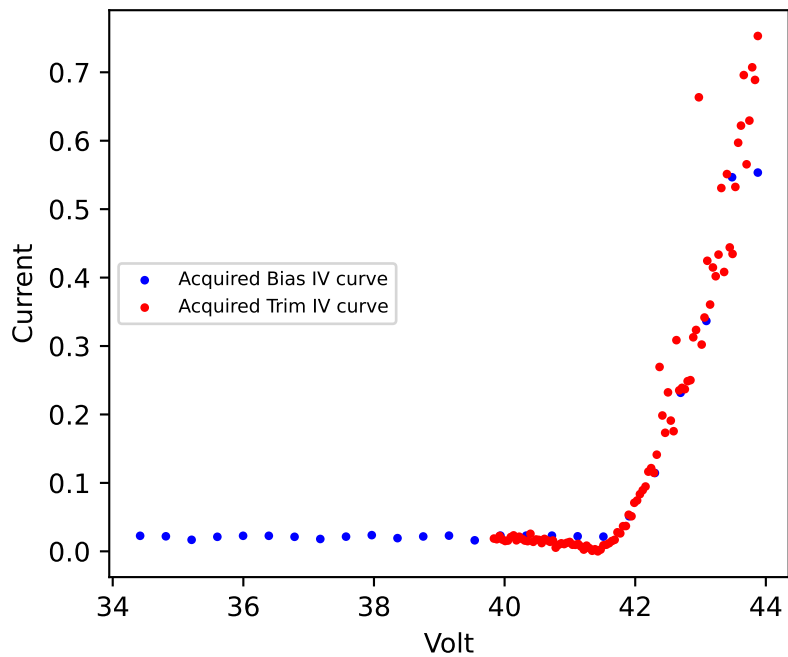
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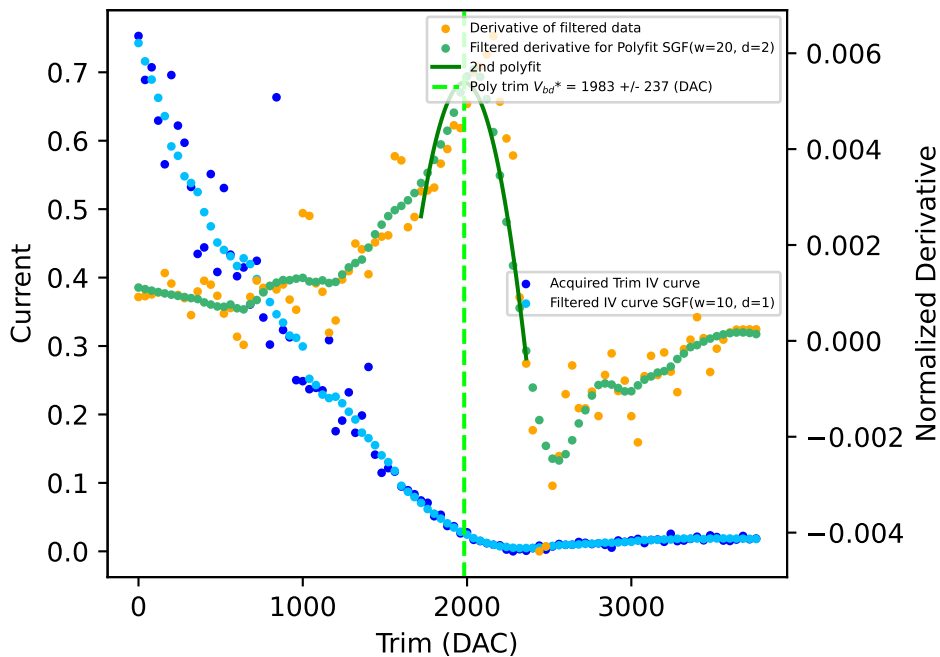
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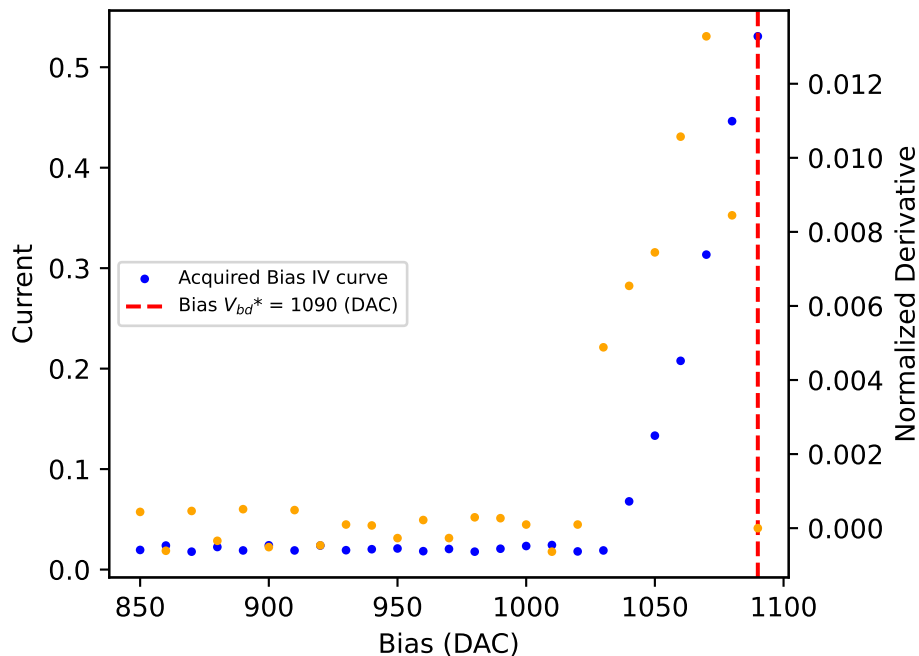
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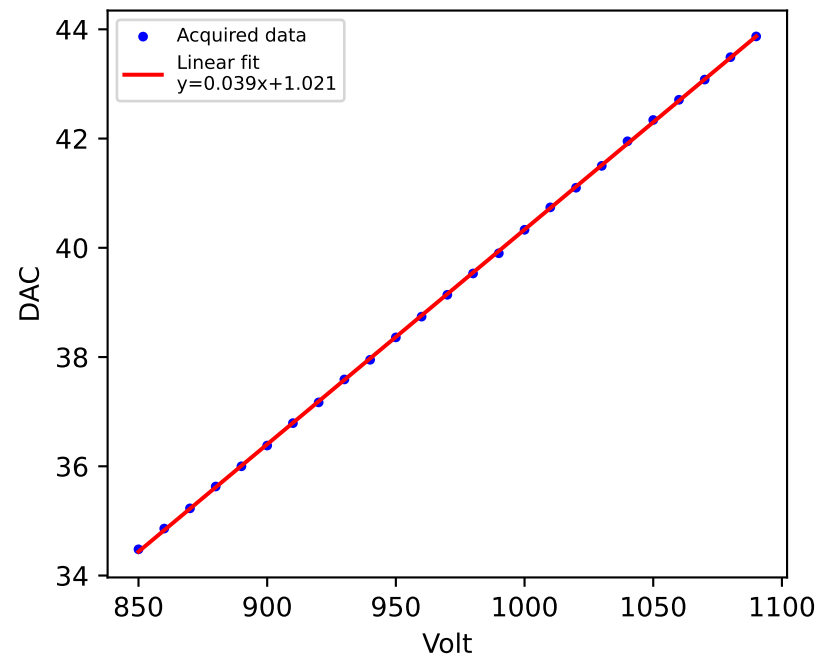
Trim REV IV curve



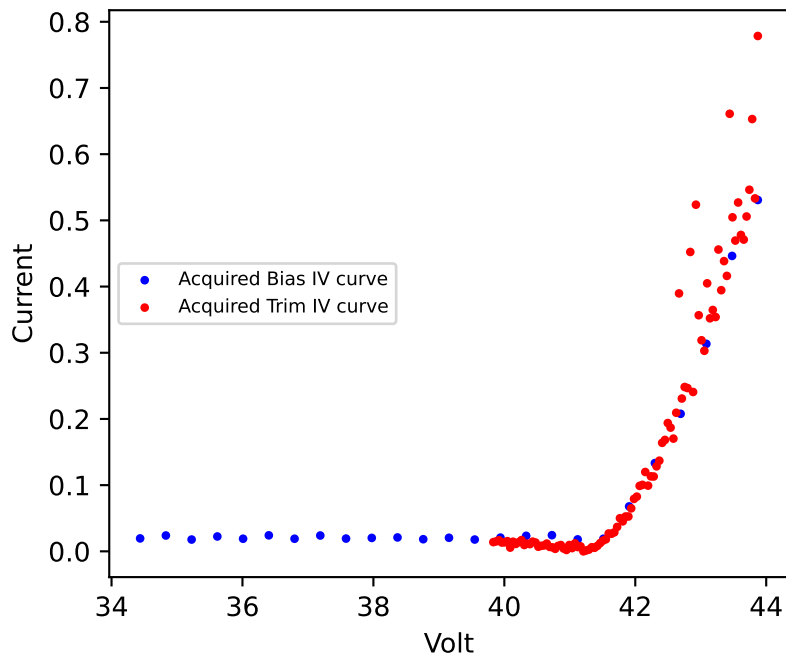
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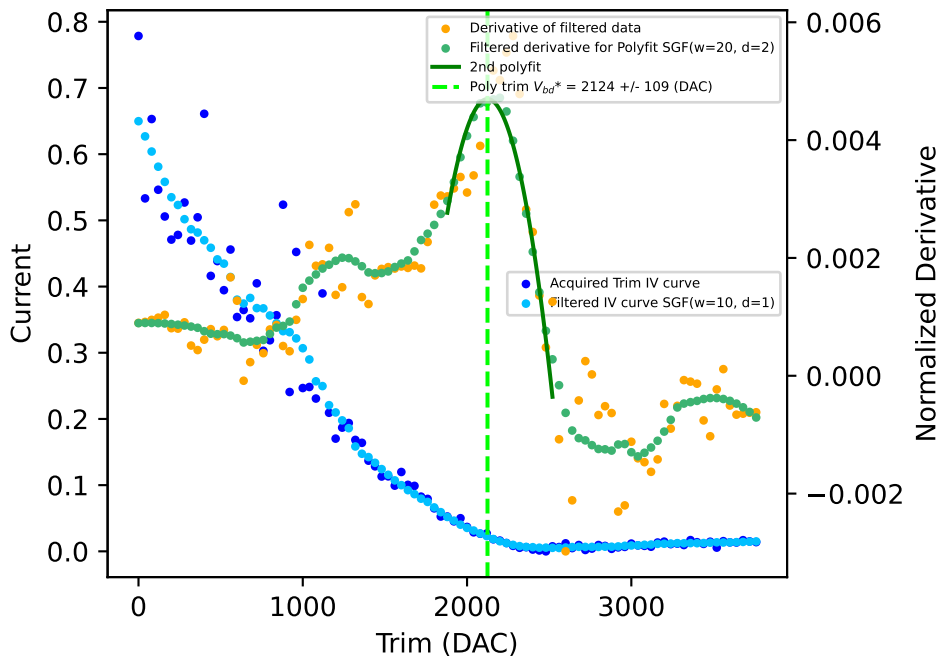
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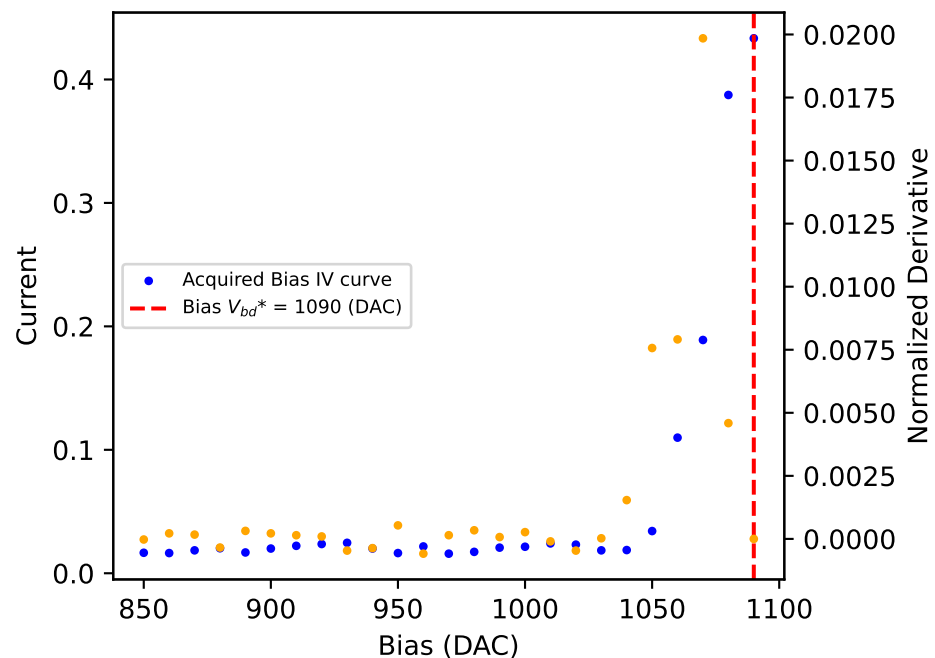
Bias + Trim REV IV curve



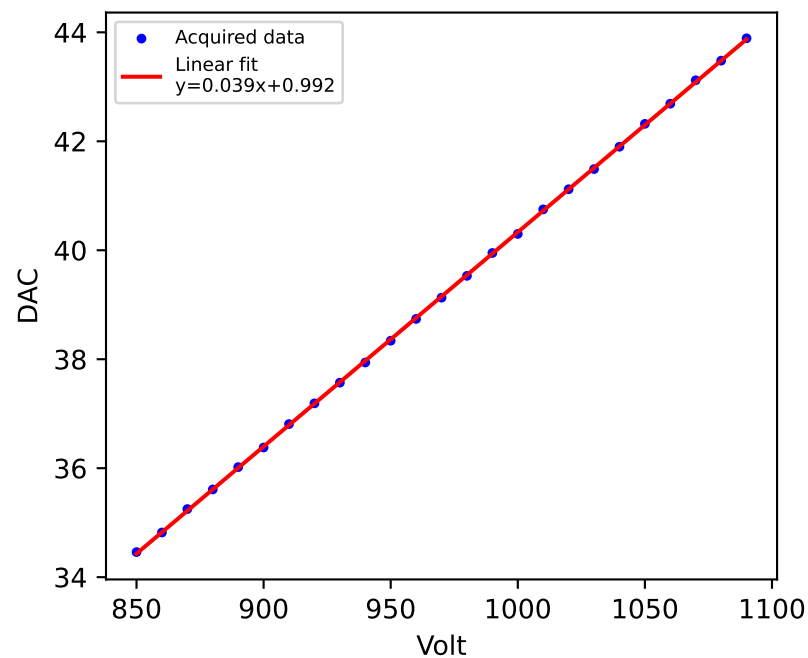
Trim REV IV curve



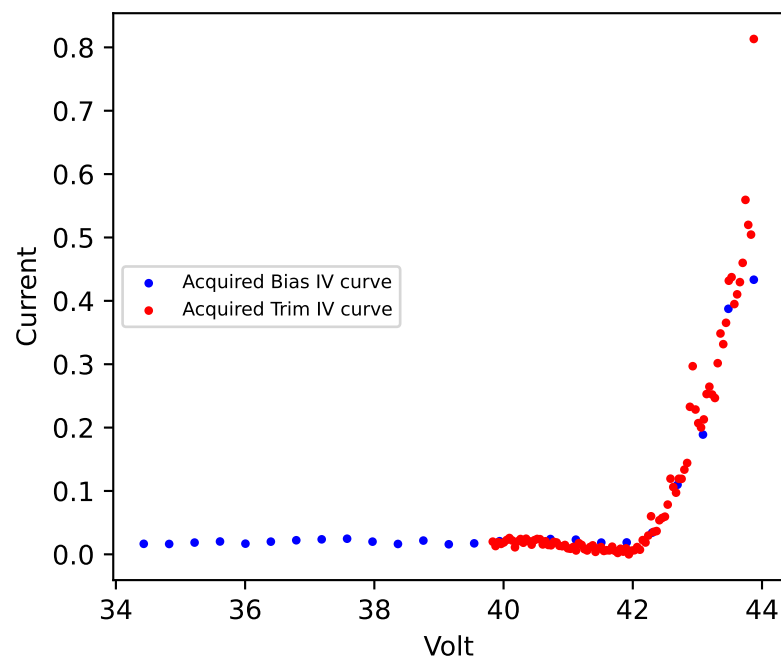
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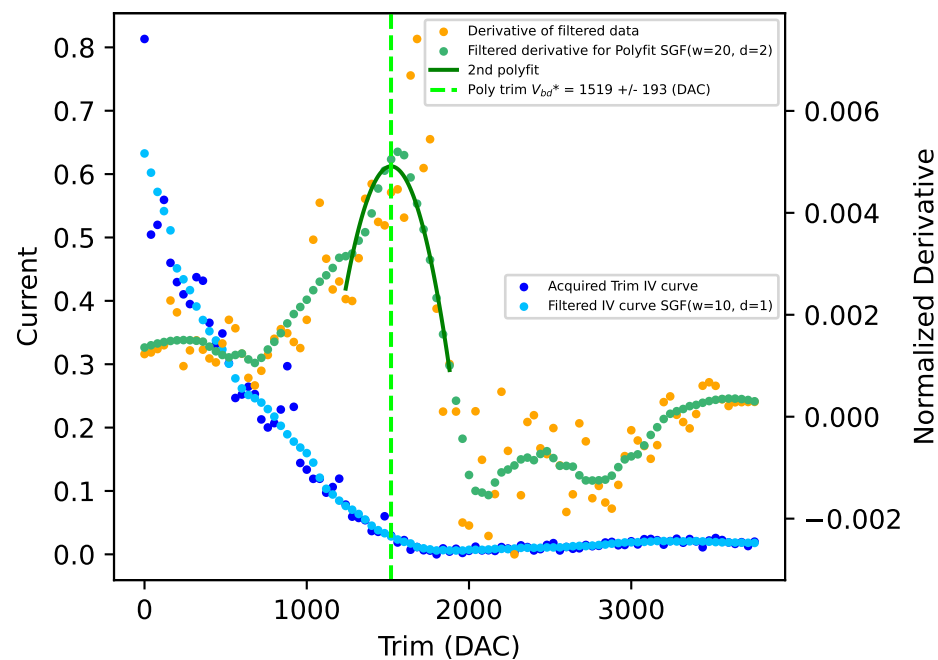
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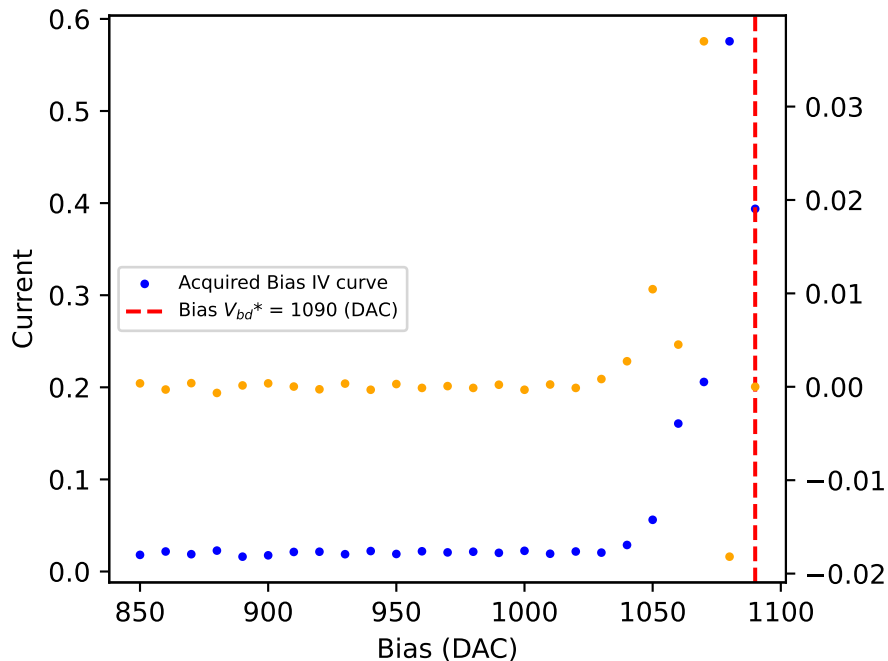
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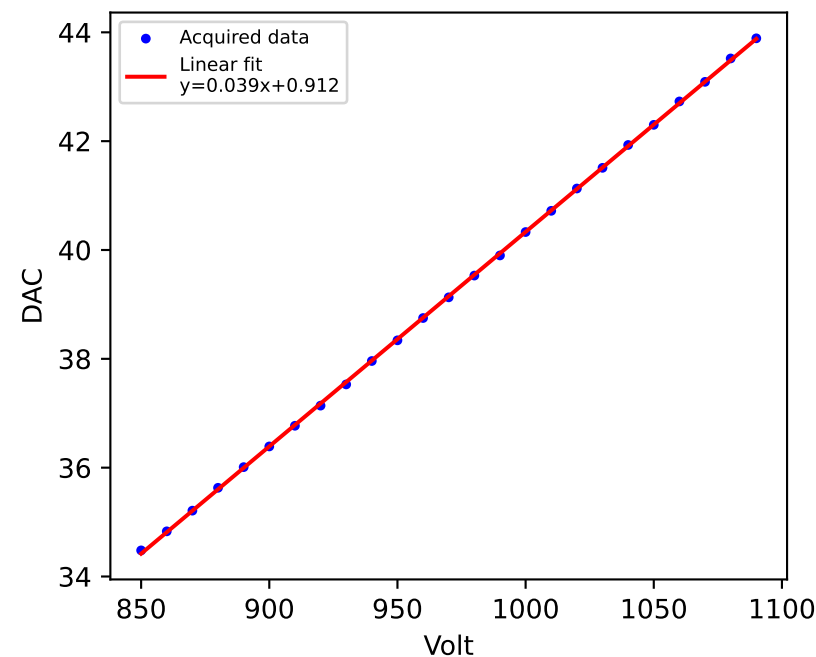
Trim REV IV curve



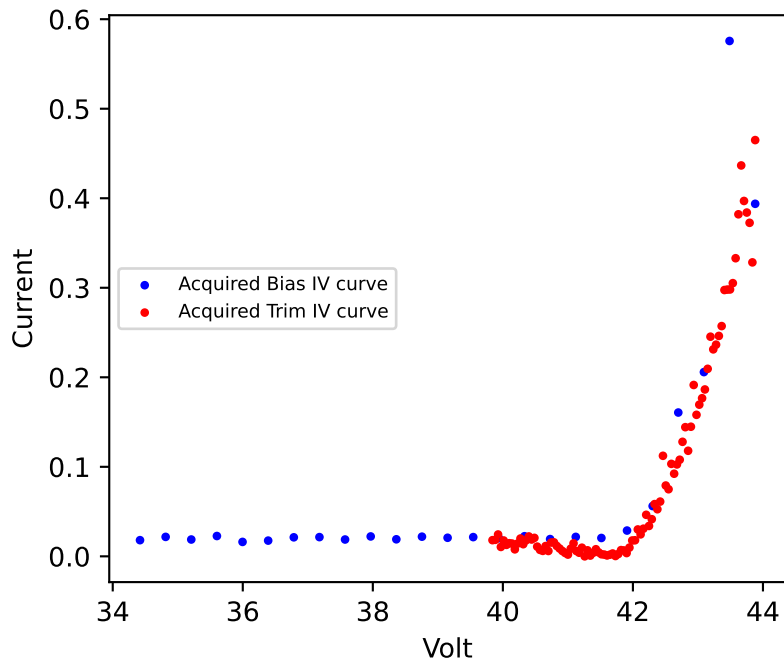
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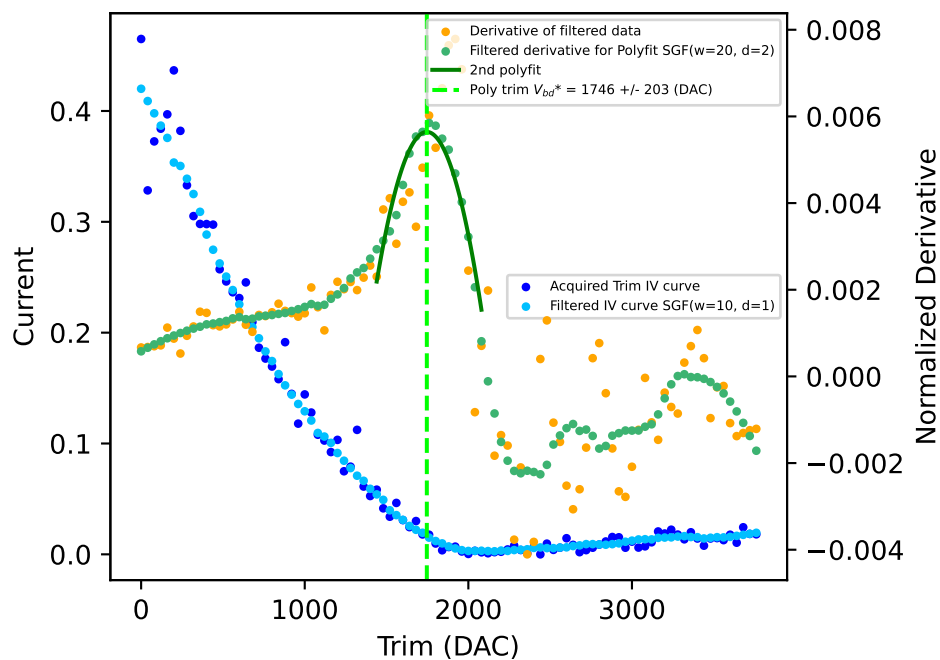
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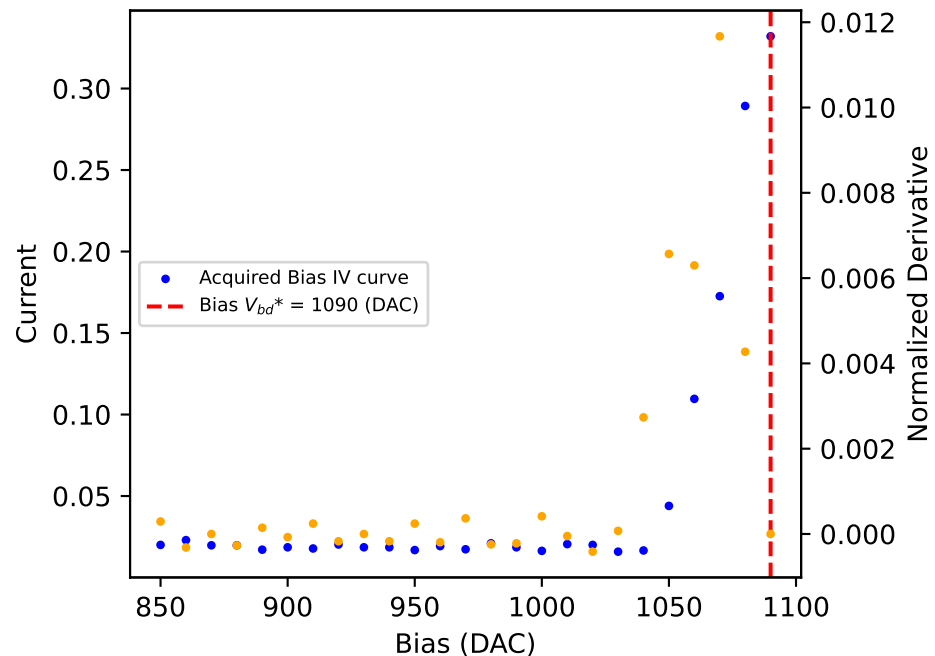
Bias + Trim REV IV curve



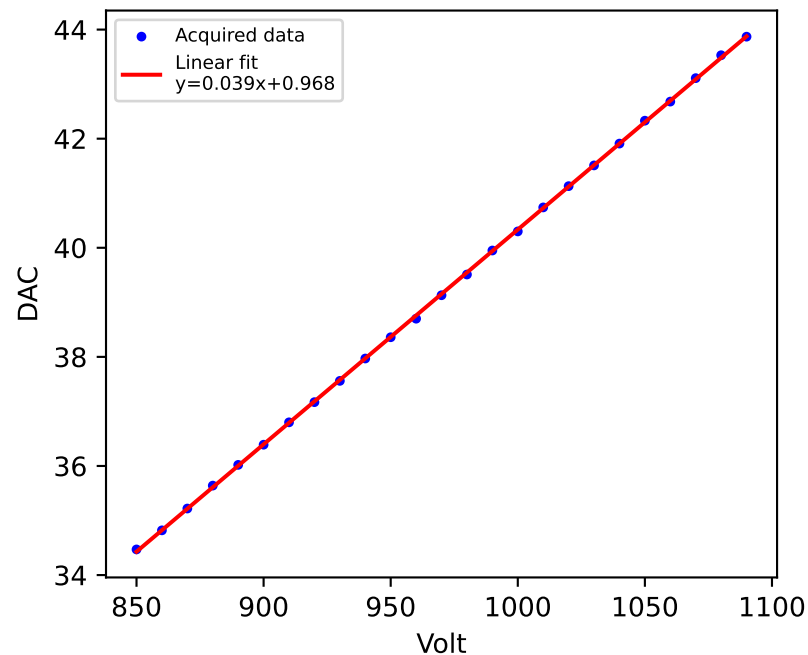
Trim REV IV curve



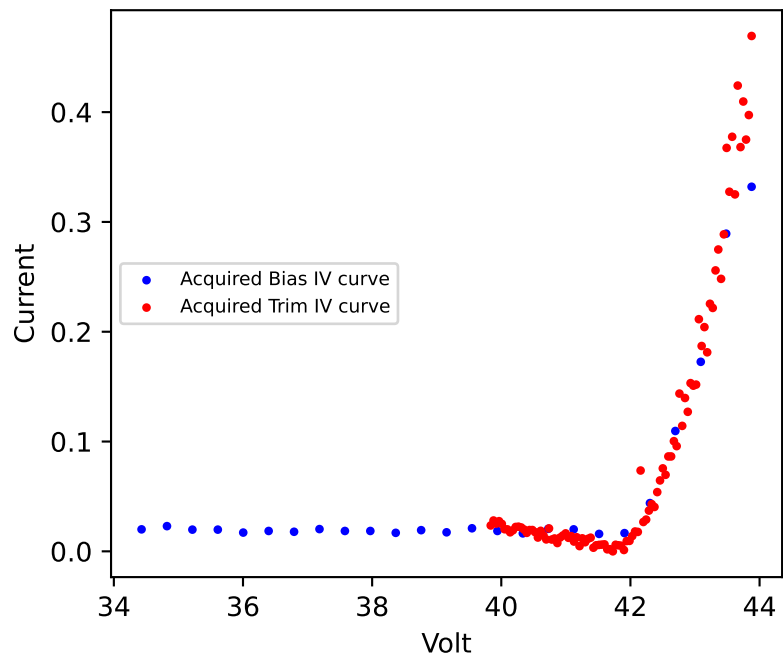
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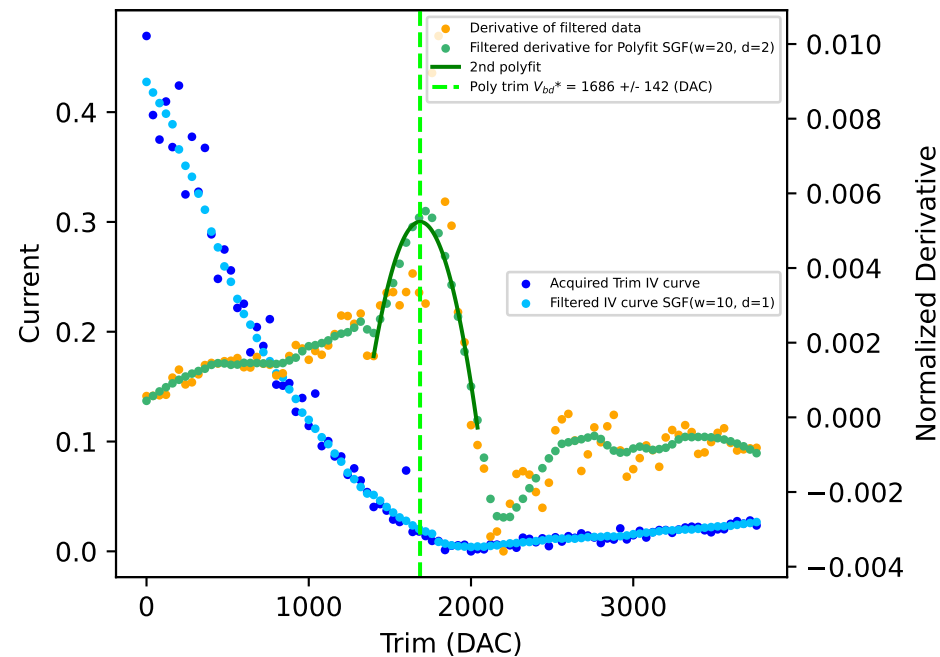
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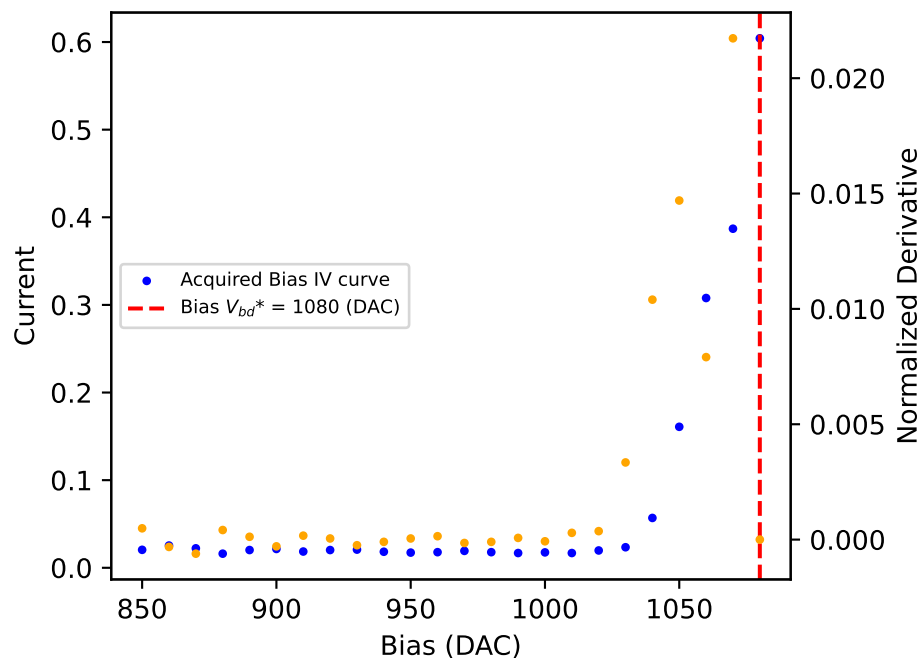
Bias + Trim REV IV curve



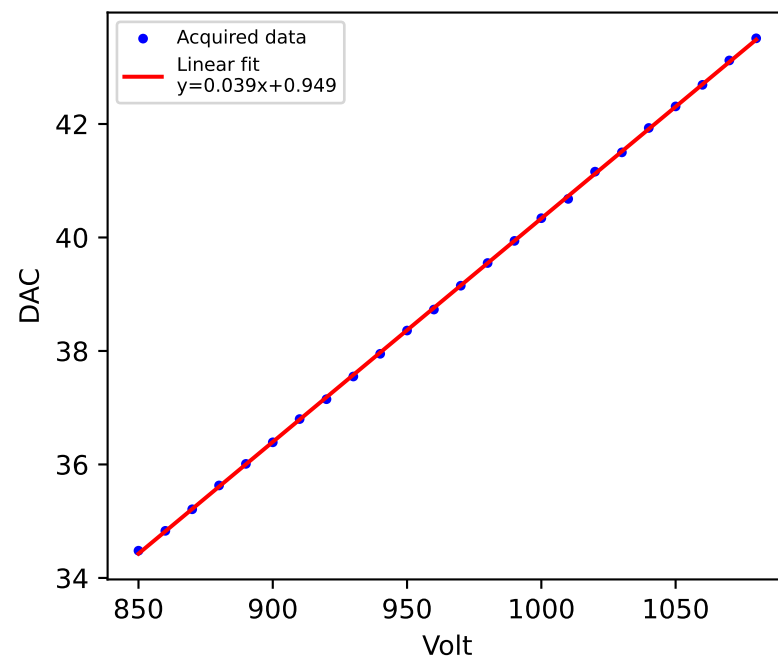
Trim REV IV curve



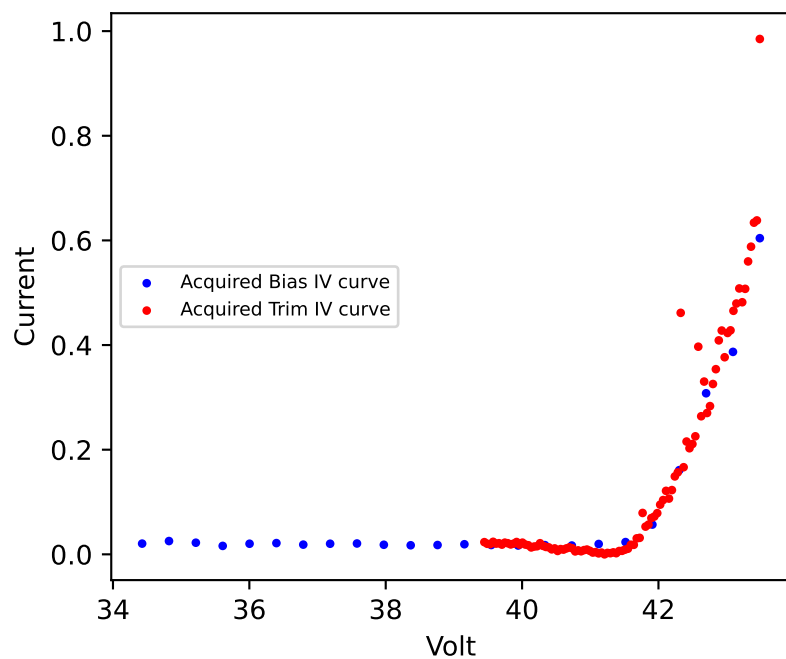
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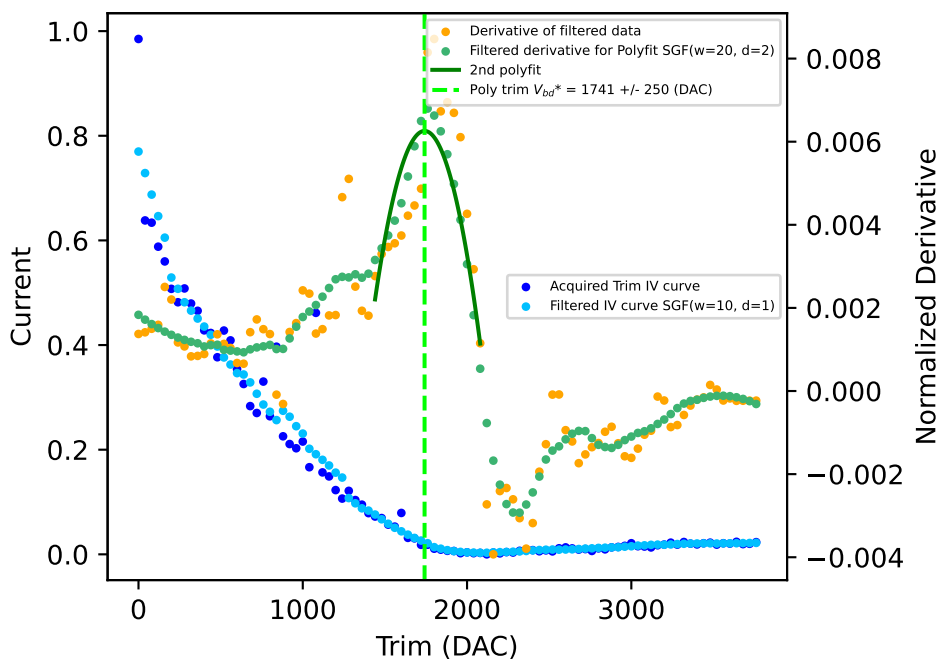
Bias DAC-VOLT conversion



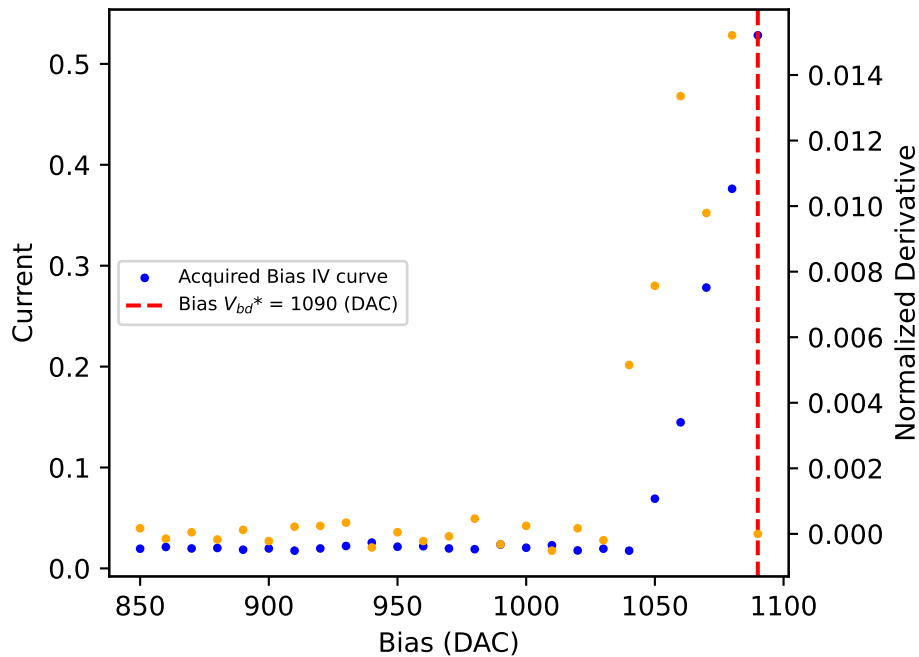
Bias + Trim REV IV curve



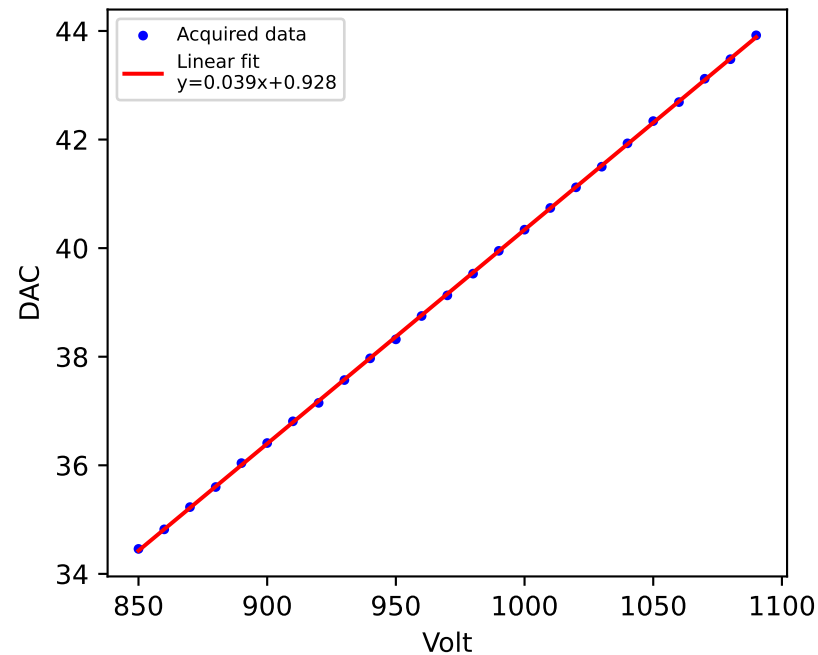
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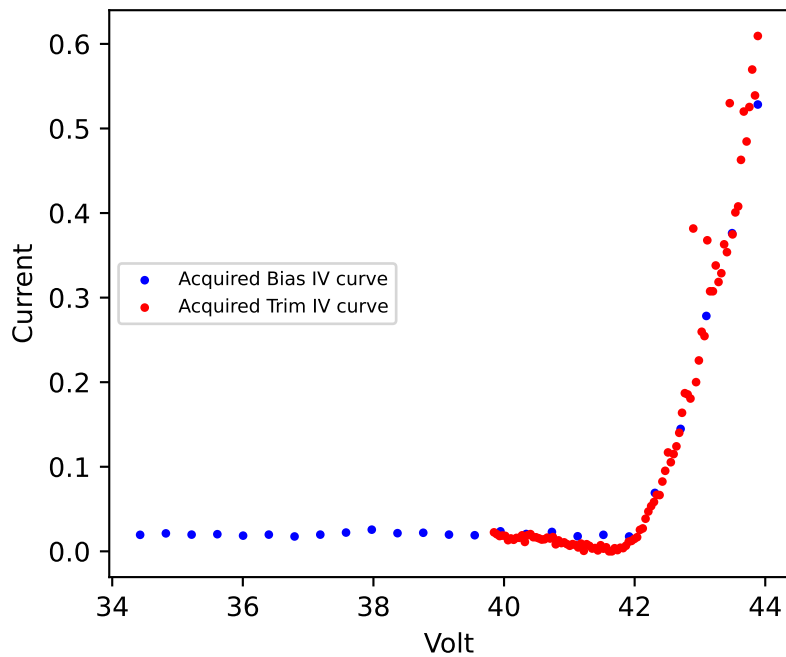
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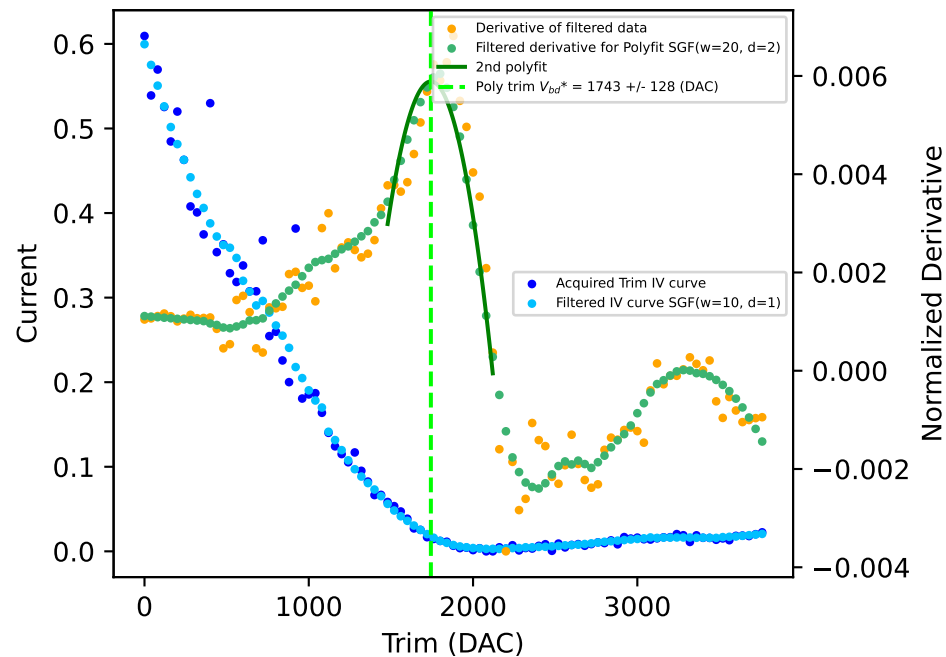
Bias DAC-VOLT conversion



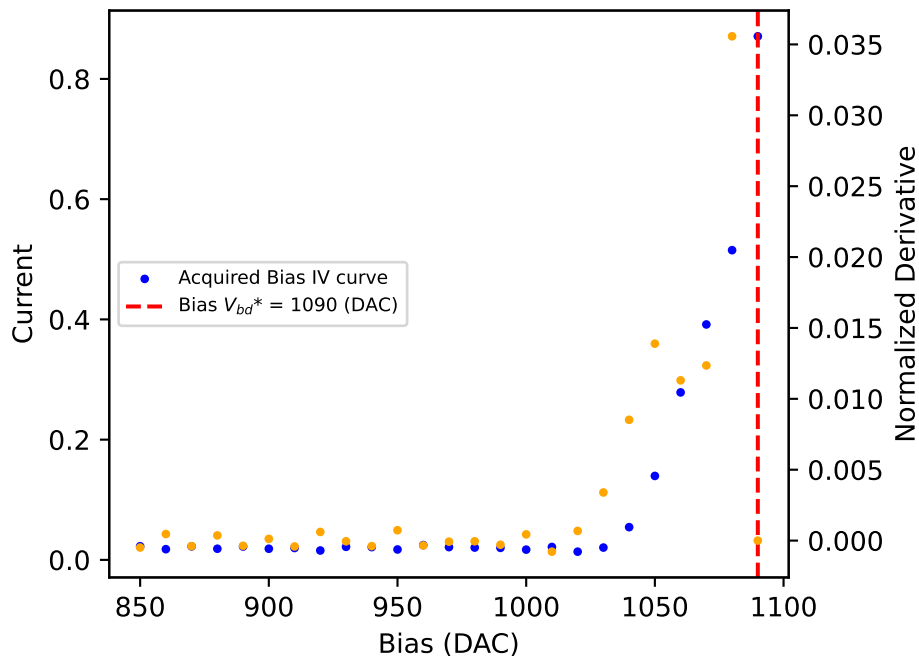
Bias + Trim REV IV curve



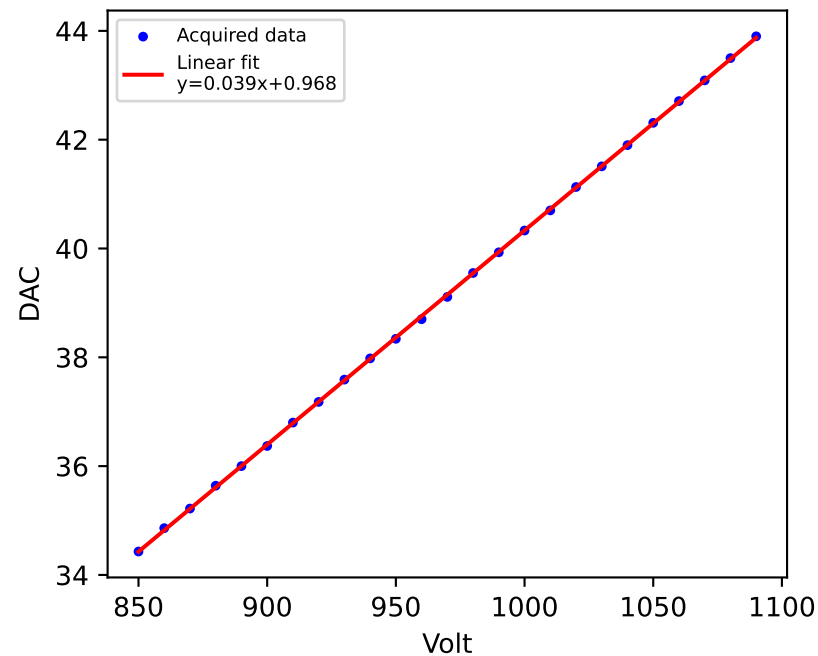
Trim REV IV curve



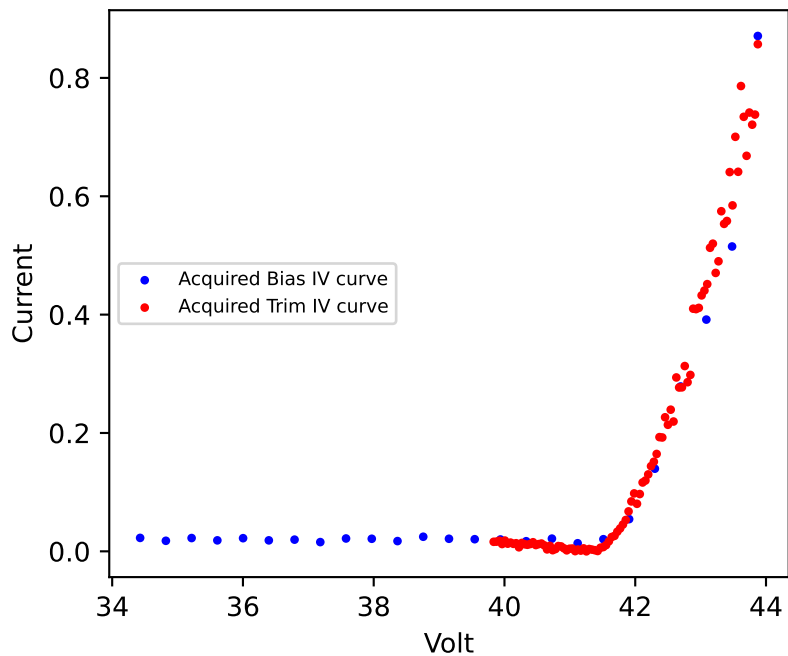
Bias REV IV curve



Bias DAC-VOLT conversion



Bias + Trim REV IV curve



Trim REV IV curve

