

# QUAD SOCKETS FE ASIC TESTER

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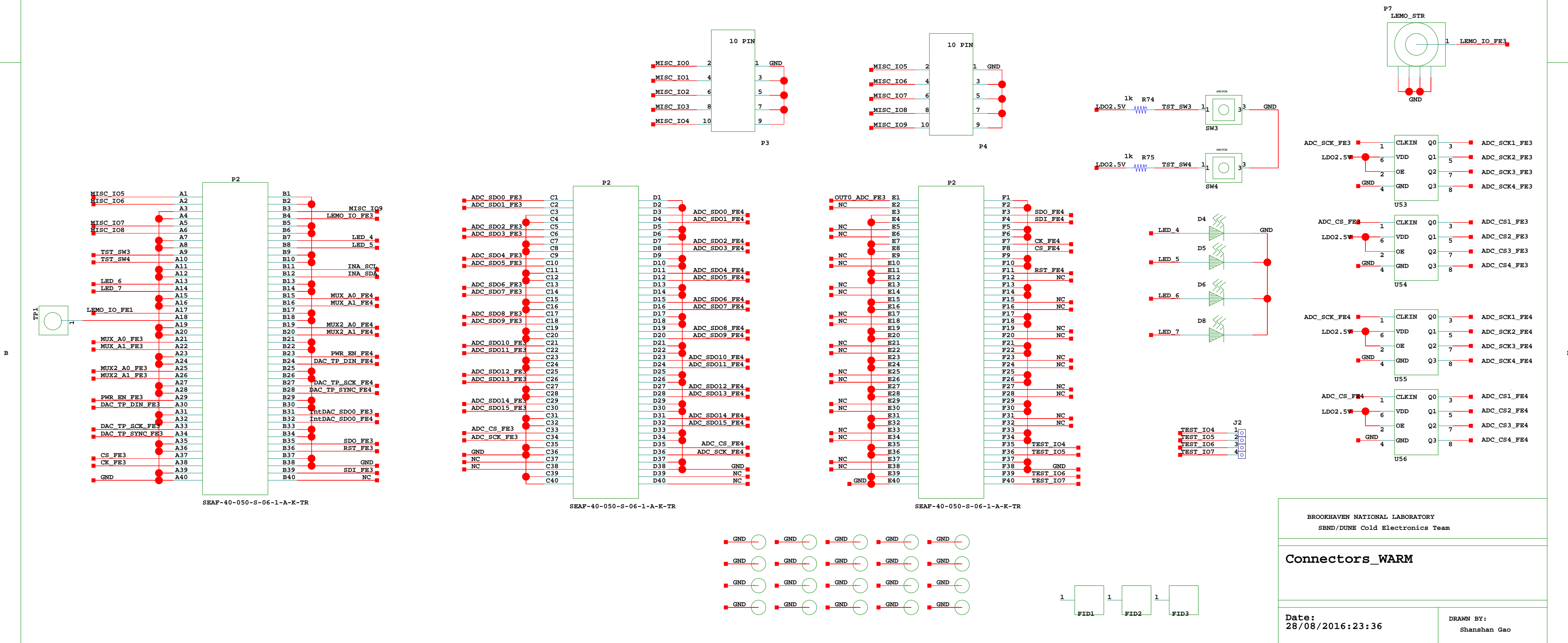
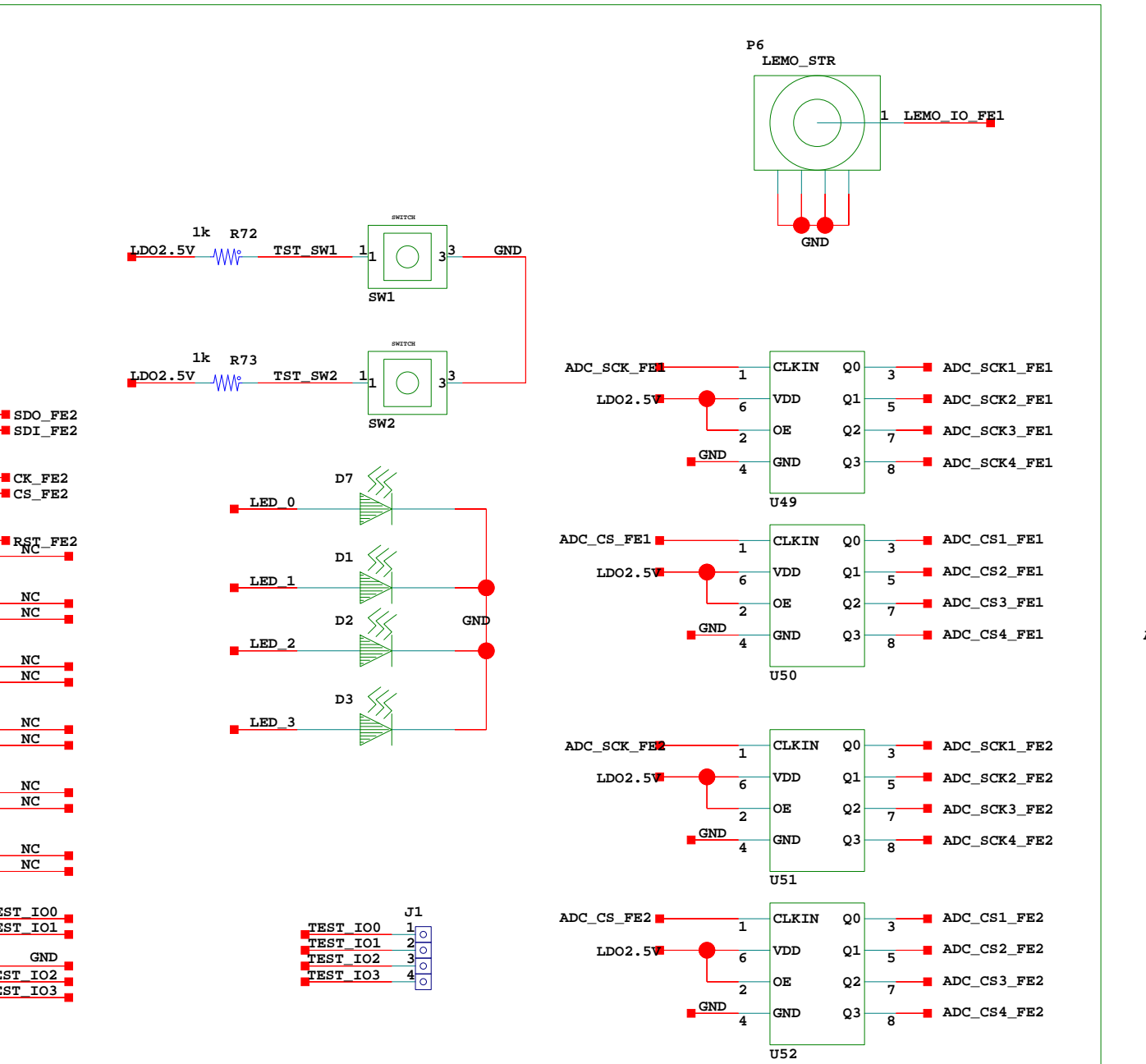
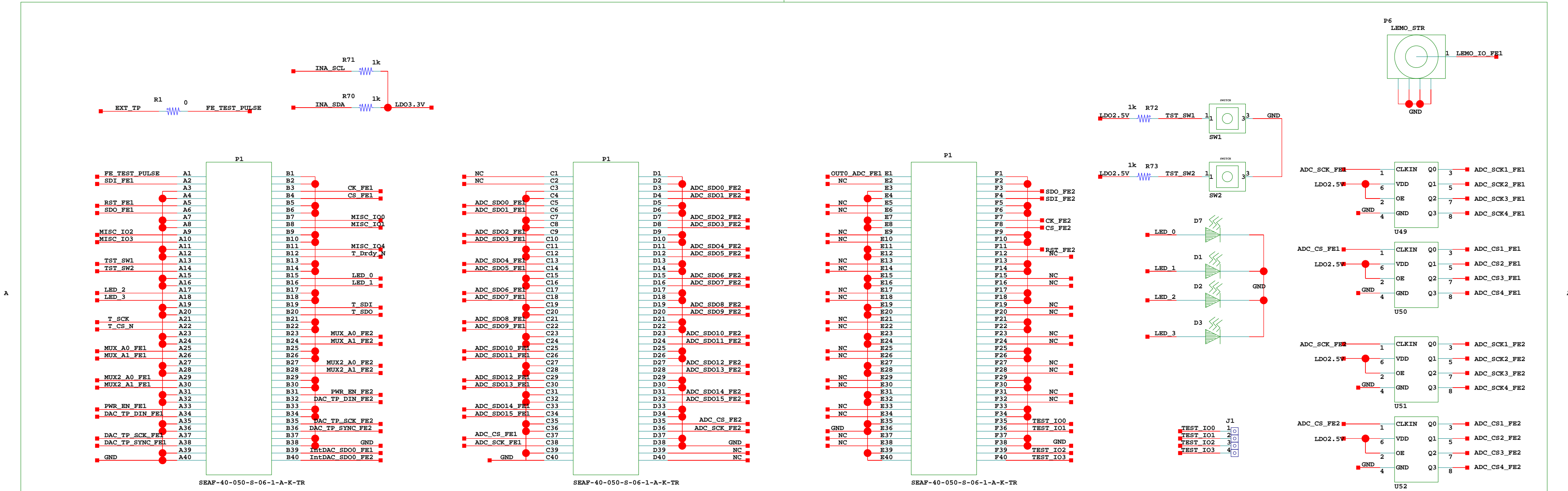
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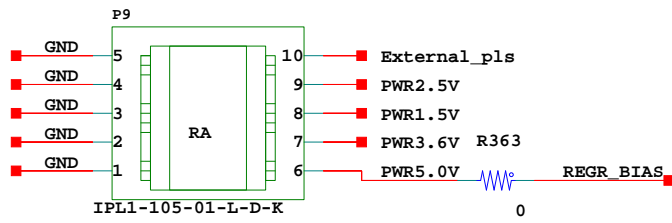
FE\_ASIC\_4\_SKT\_WARM: Circuit for FE ASIC 4 on warm side



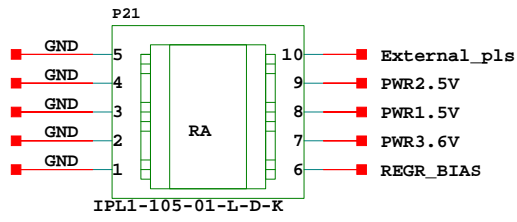
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**Connectors\_WARM**

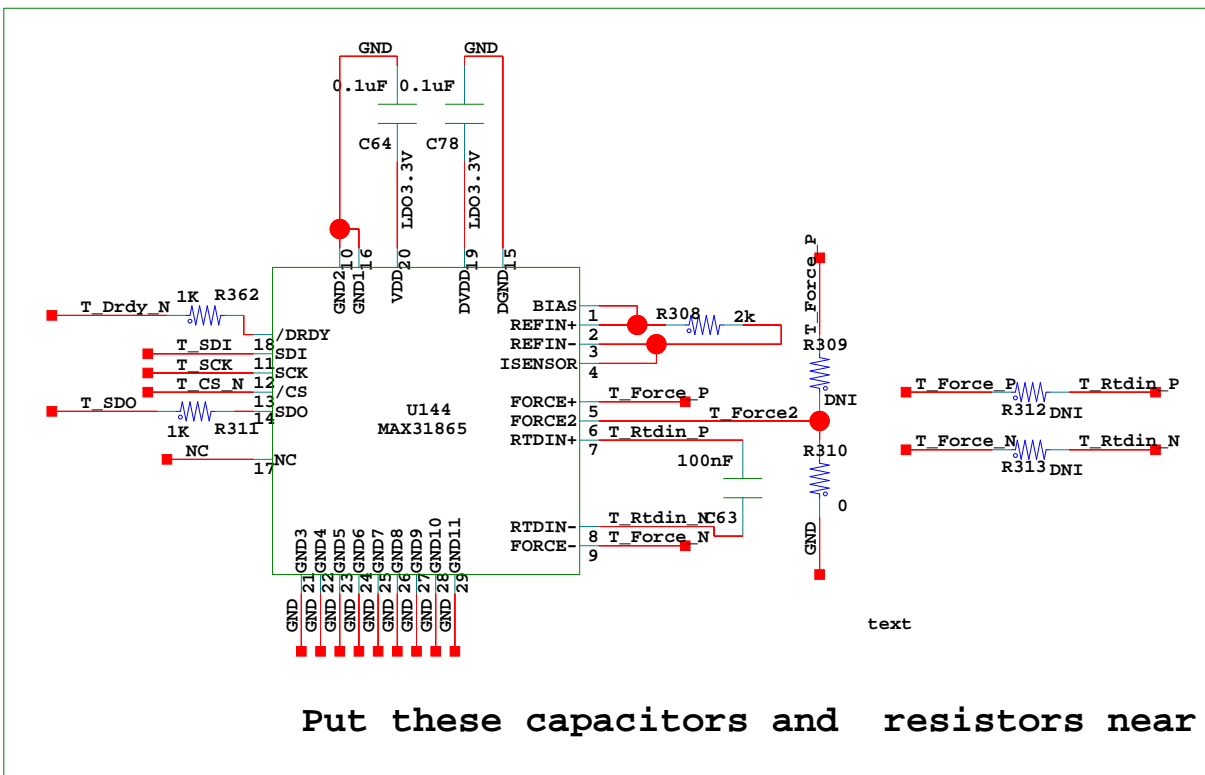
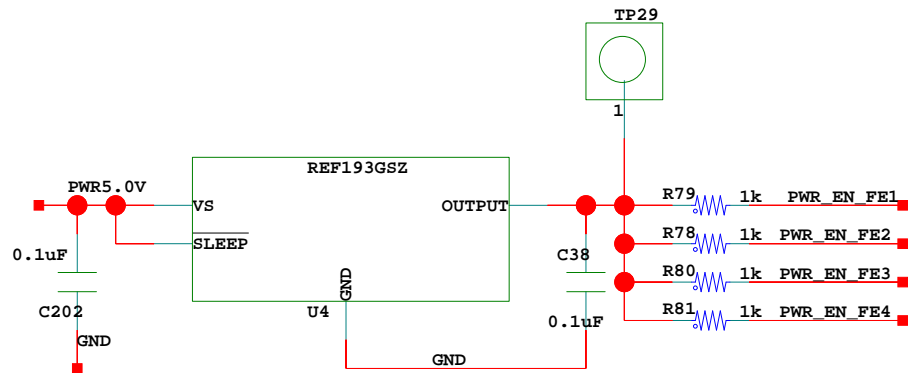
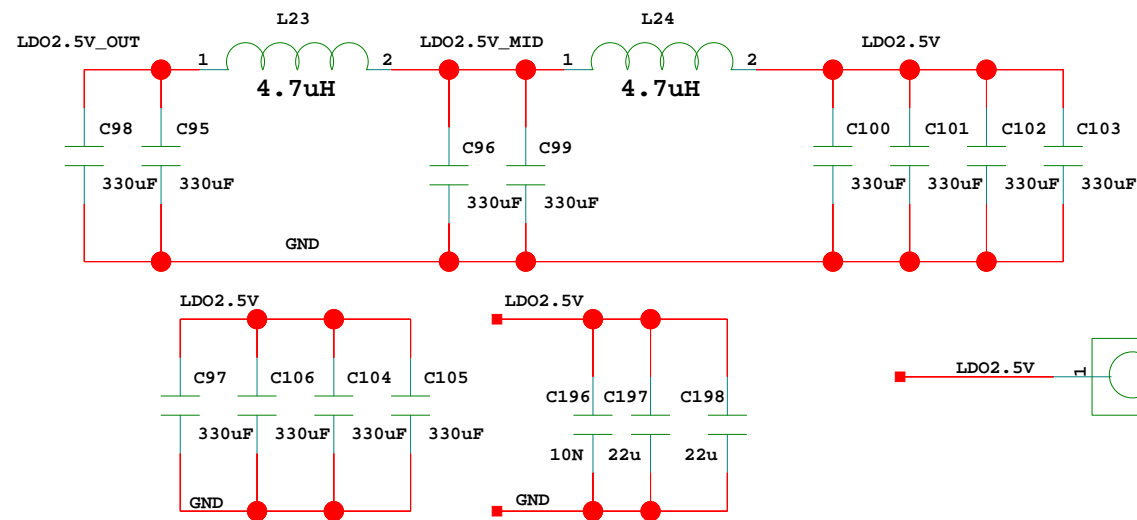
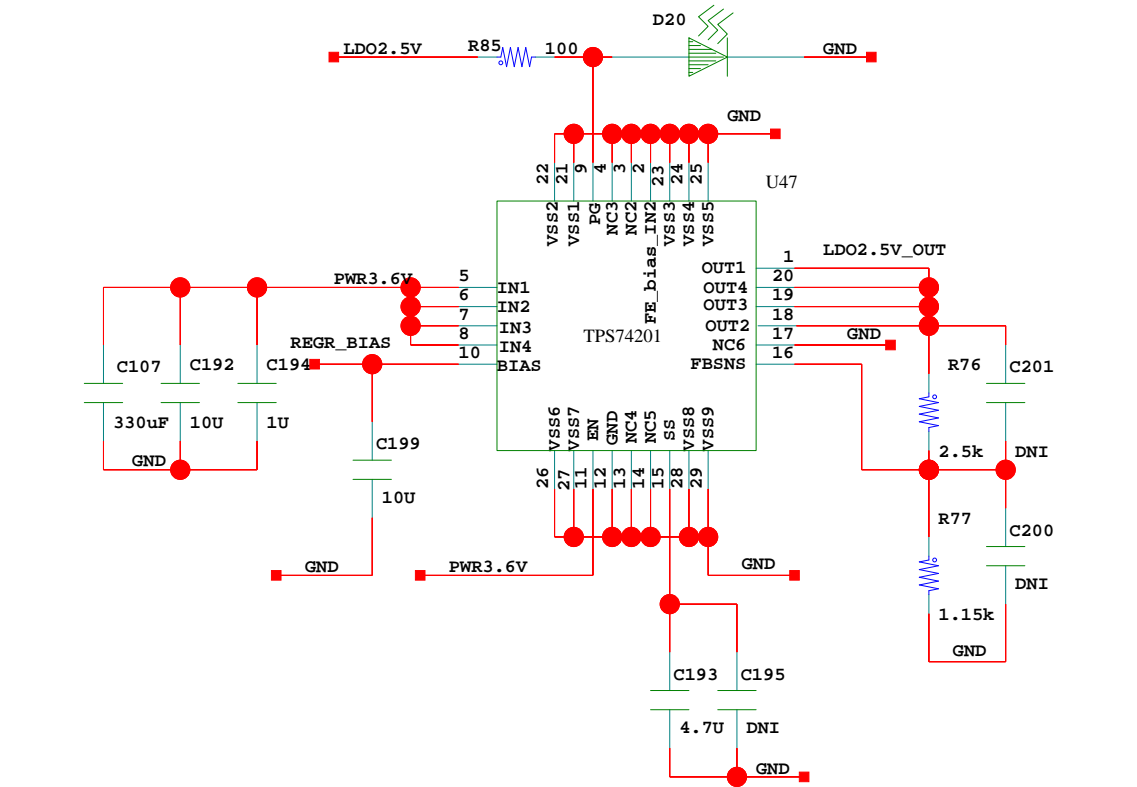
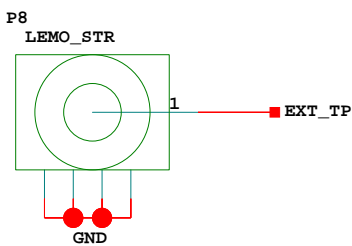
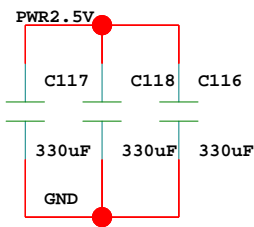
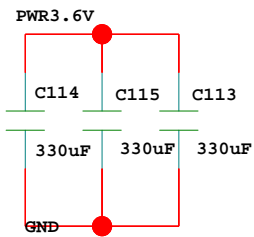
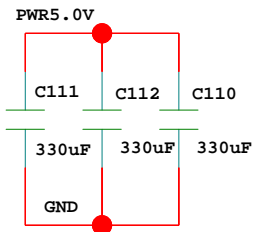
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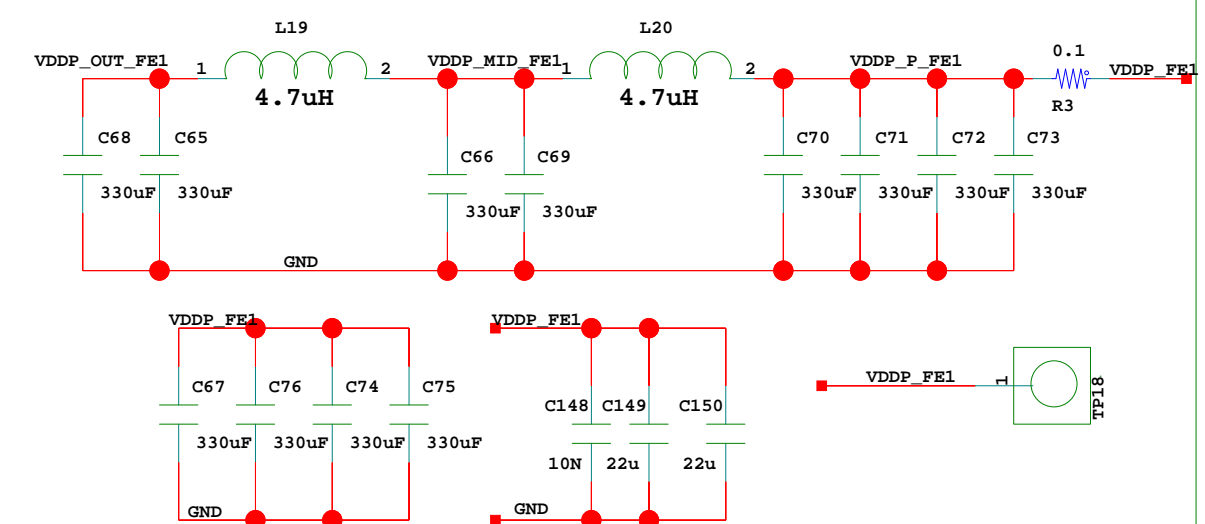
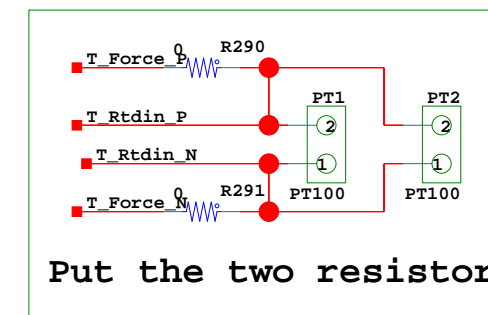


Put P9 on the bottom side for input

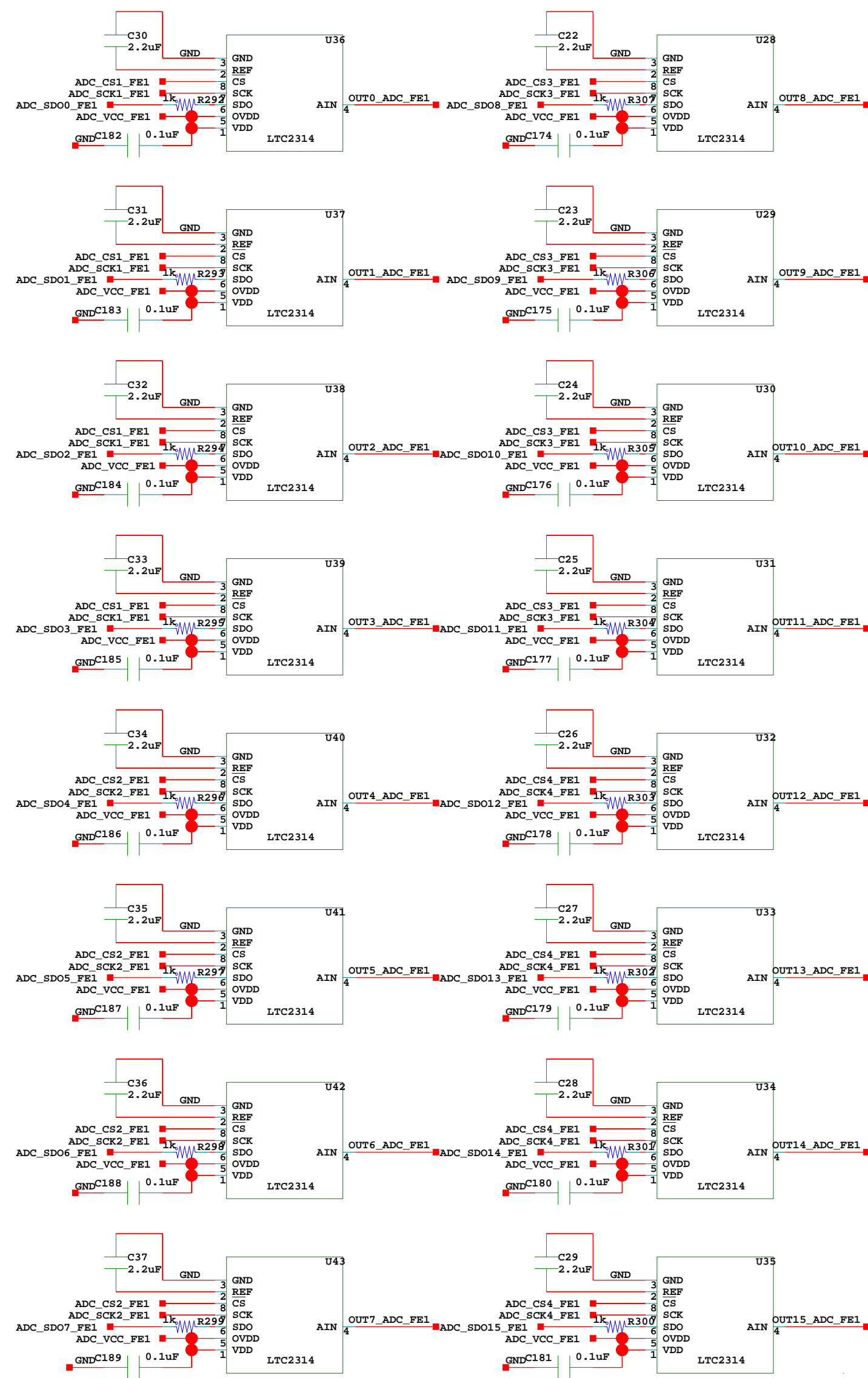
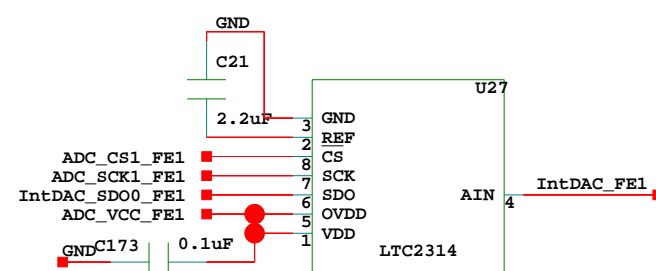


Put P21 on the top side for output to FM









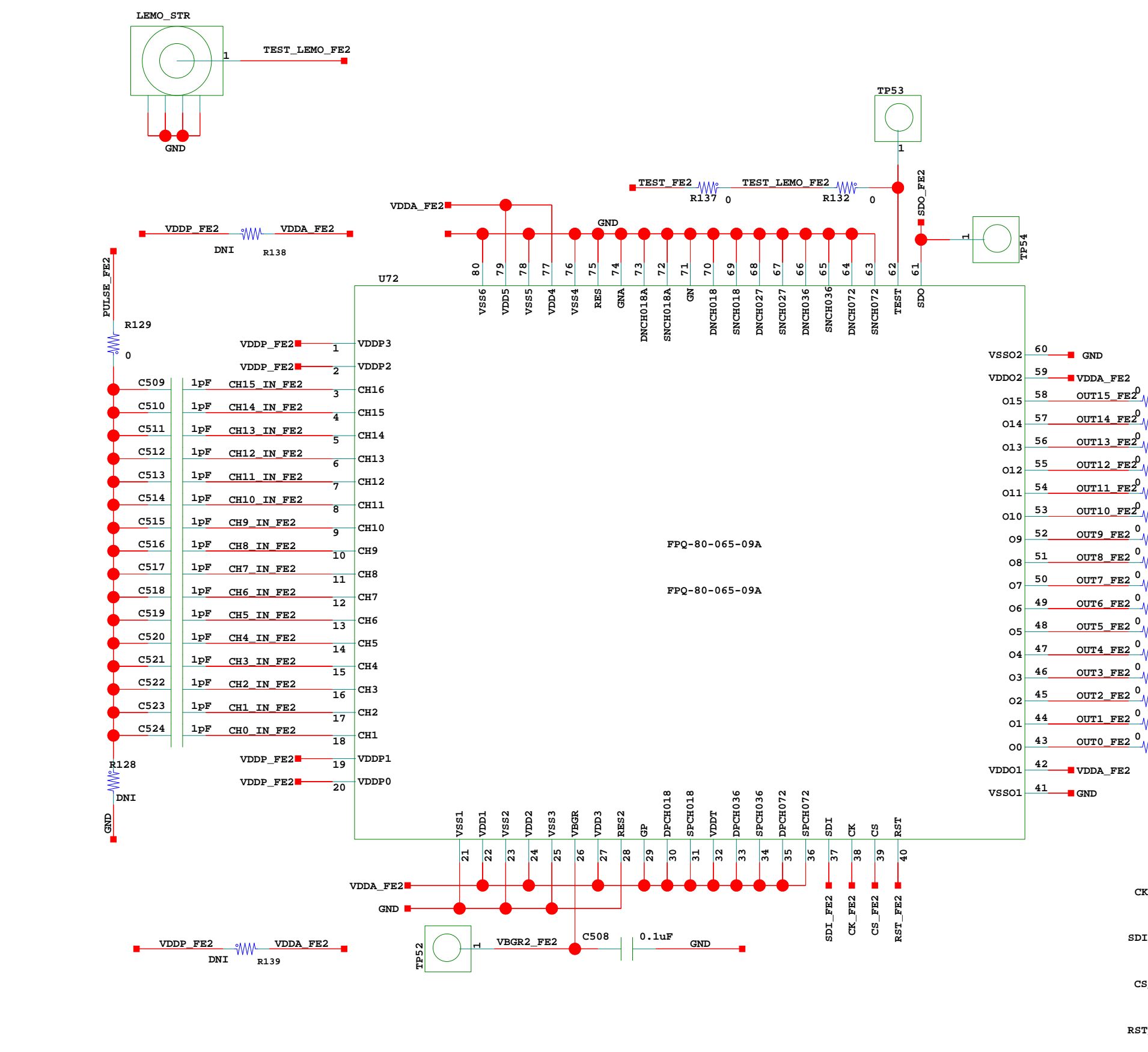
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## FE\_ASIC\_1\_ADC\_WARM

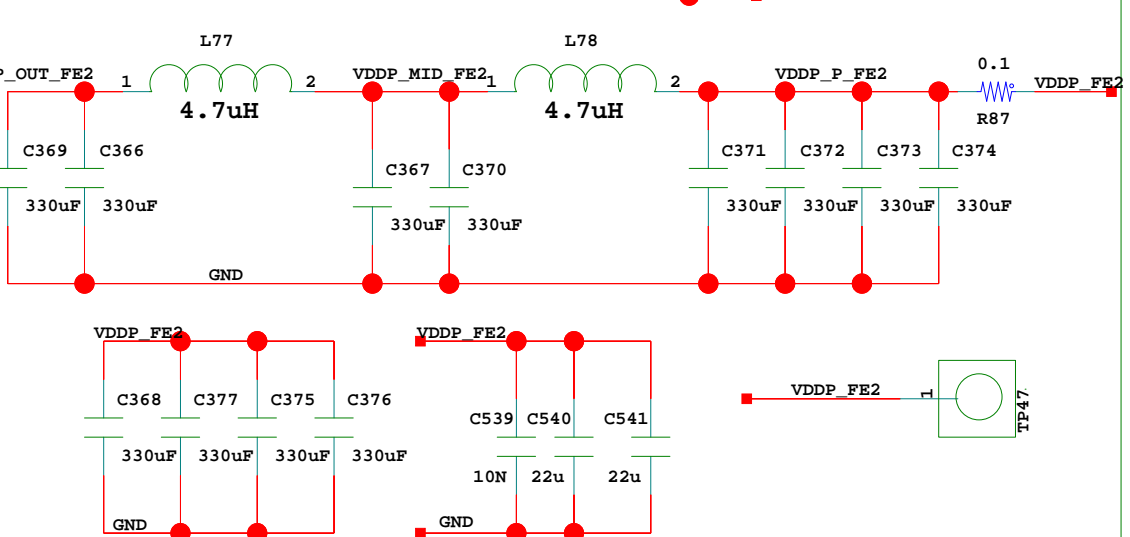
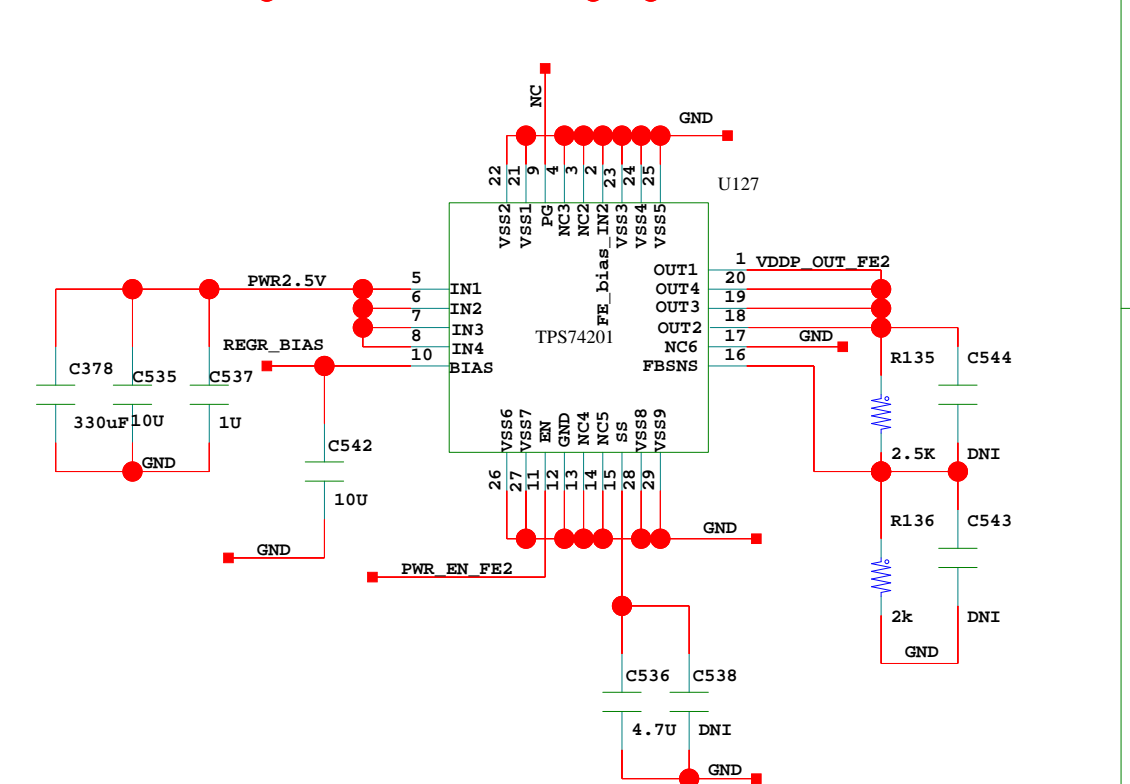
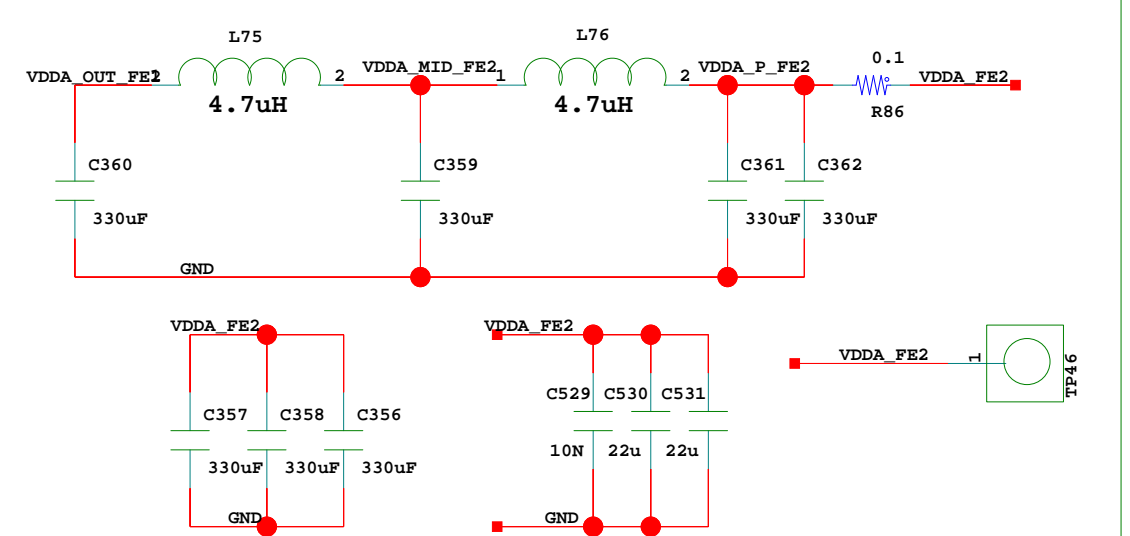
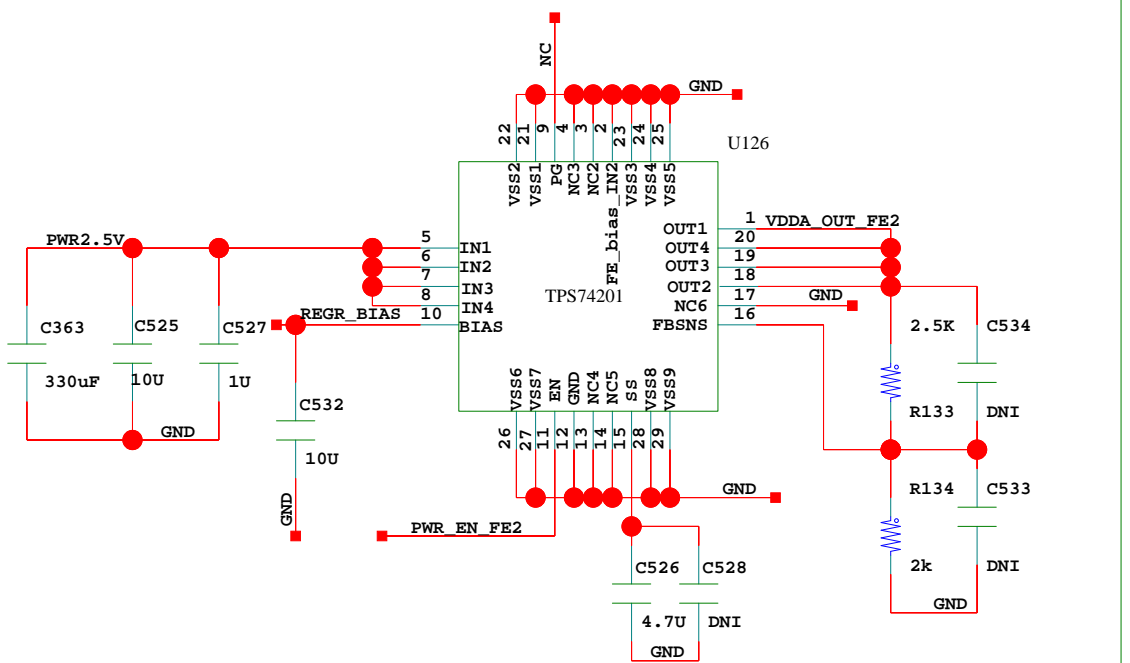
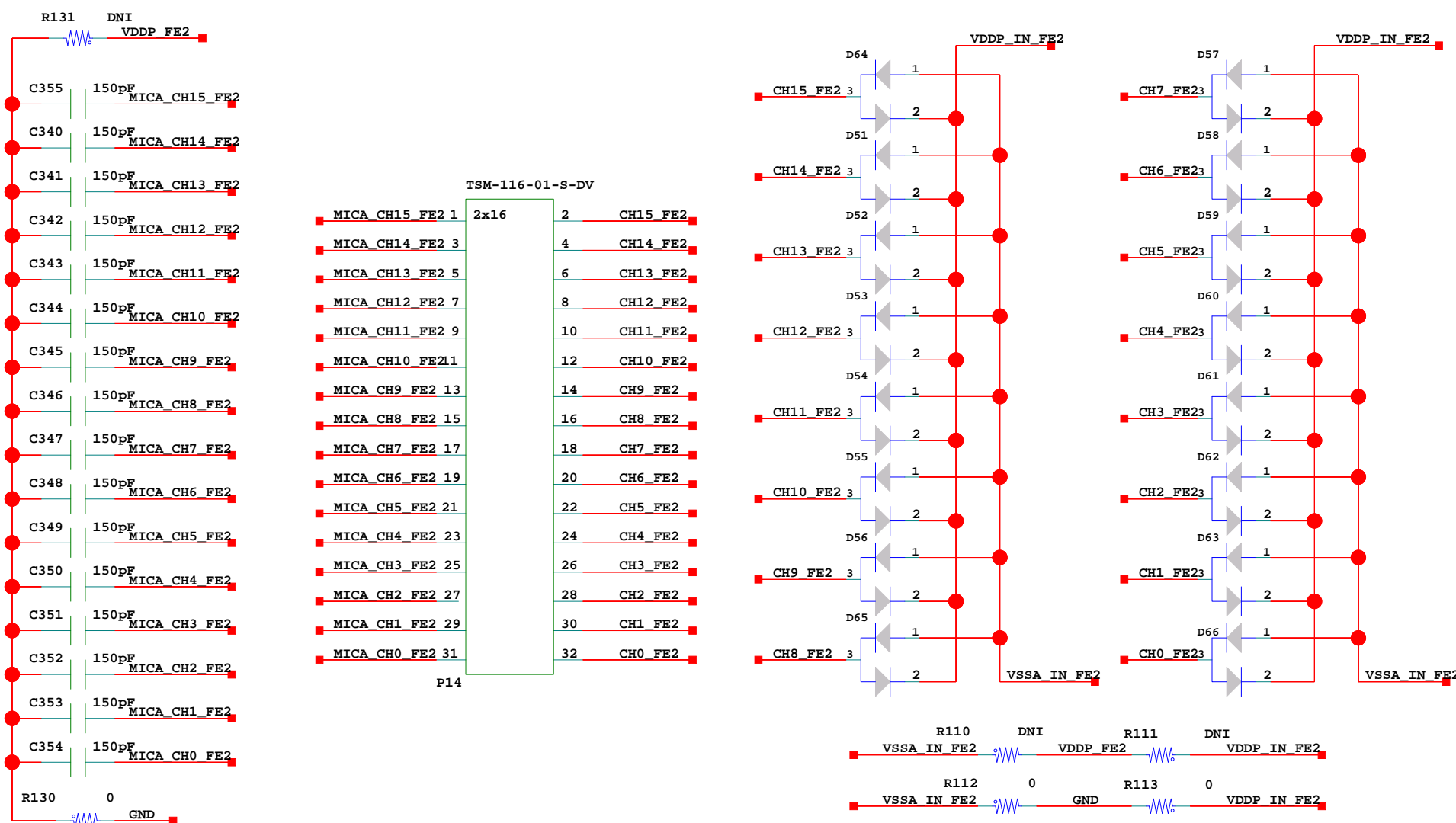
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Shanshan Gao

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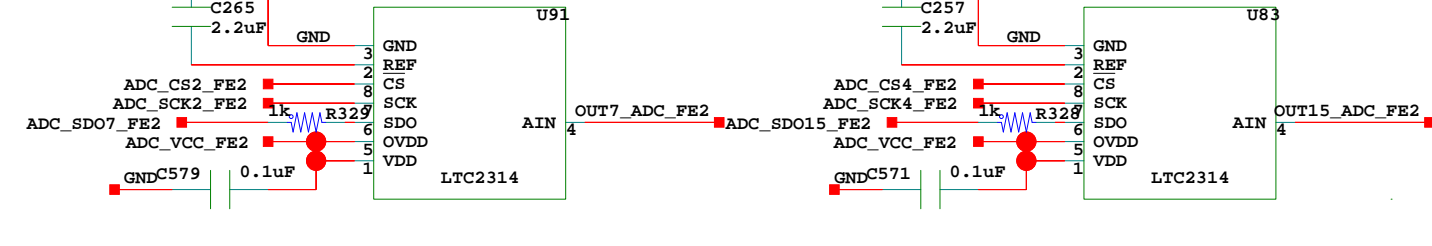
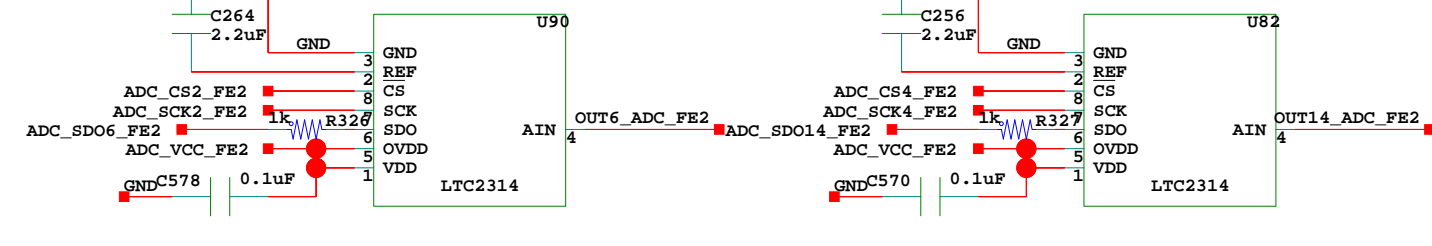
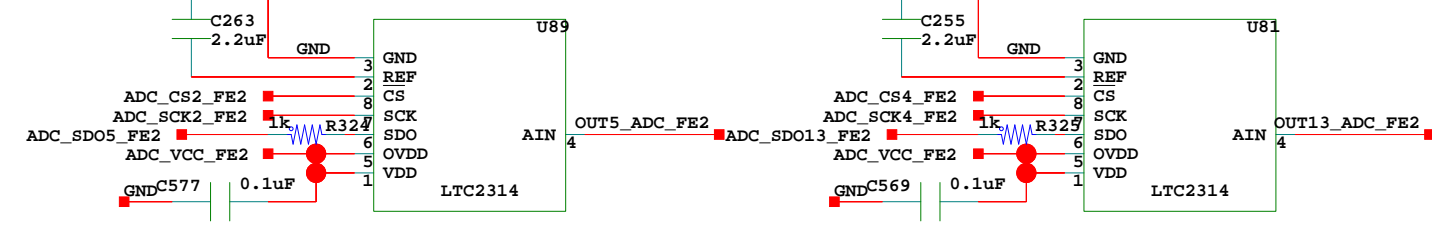
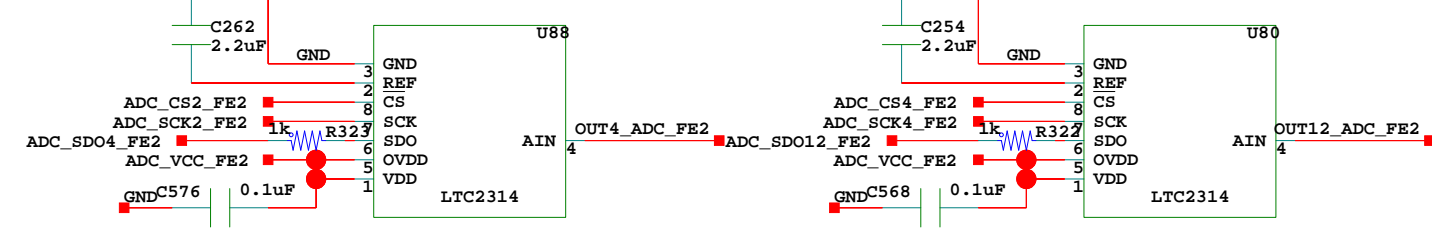
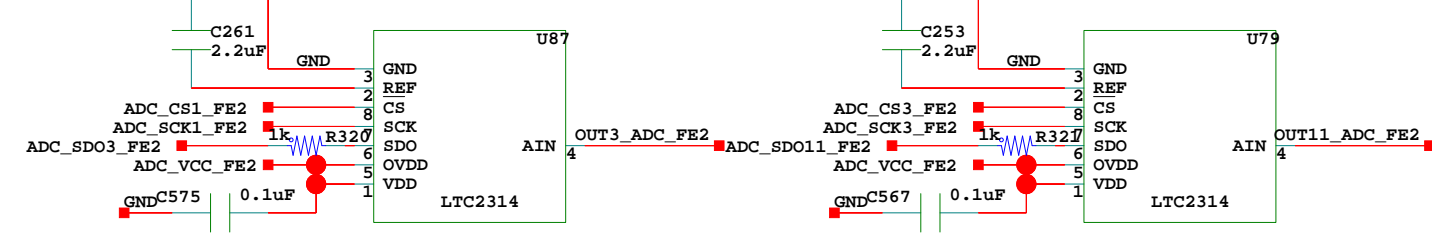
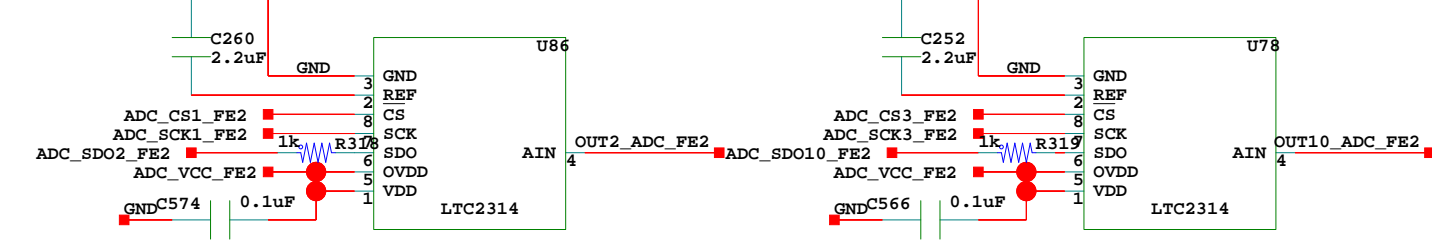
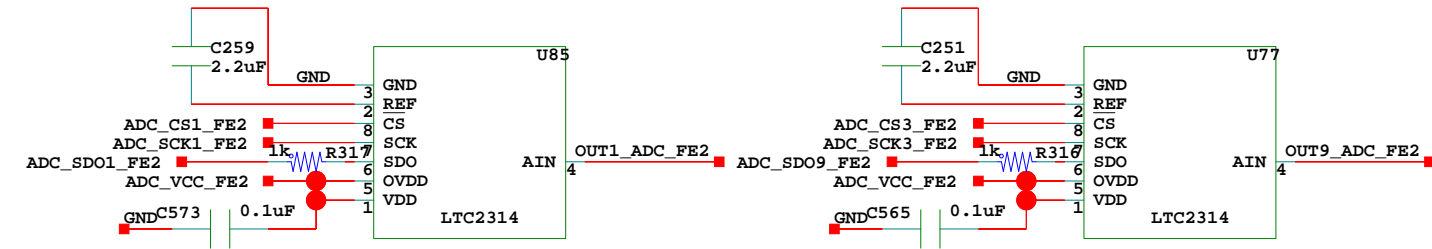
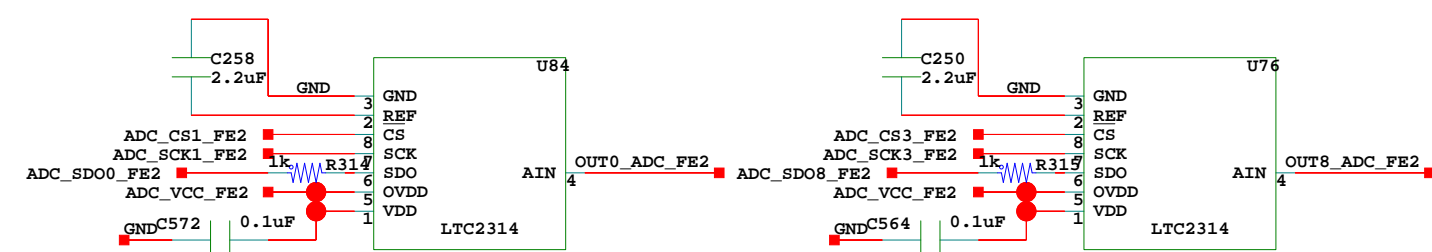
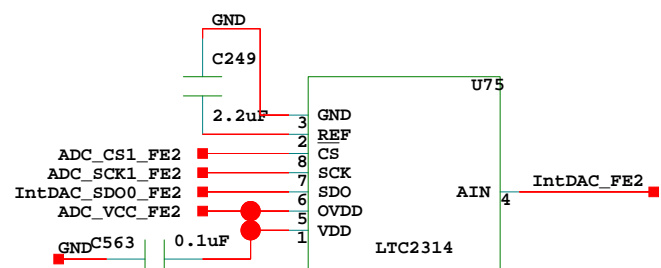
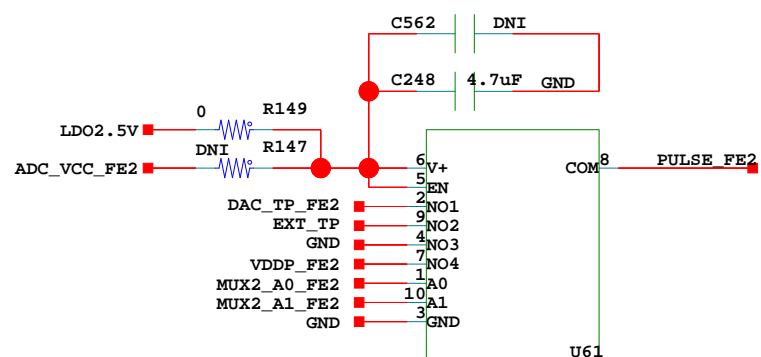
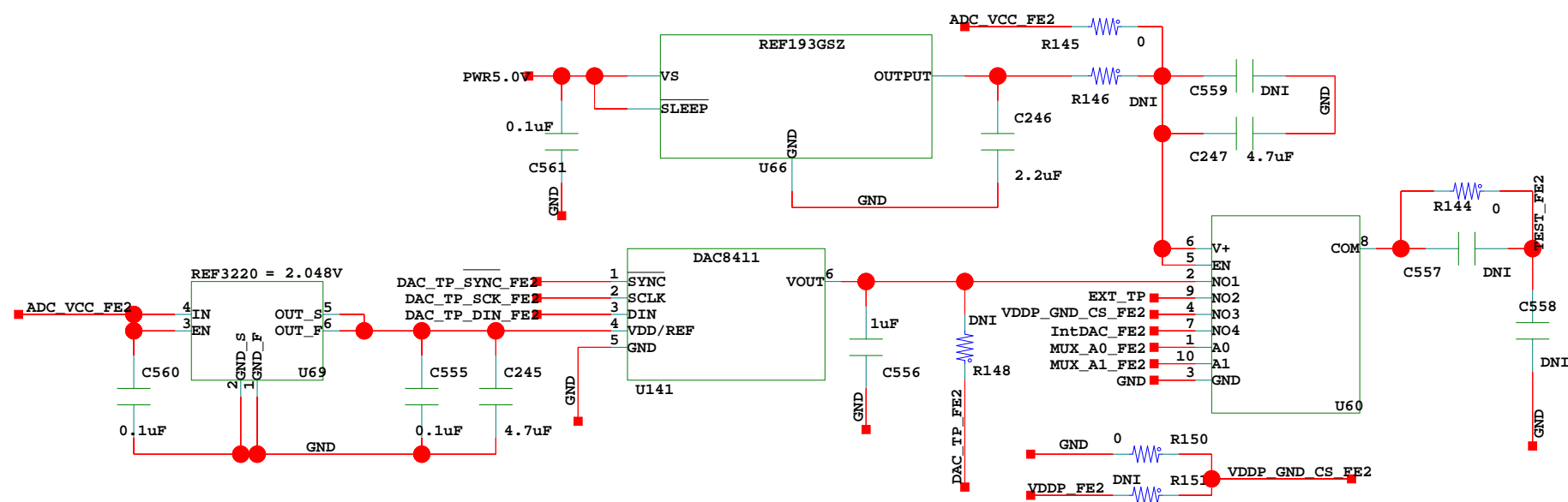
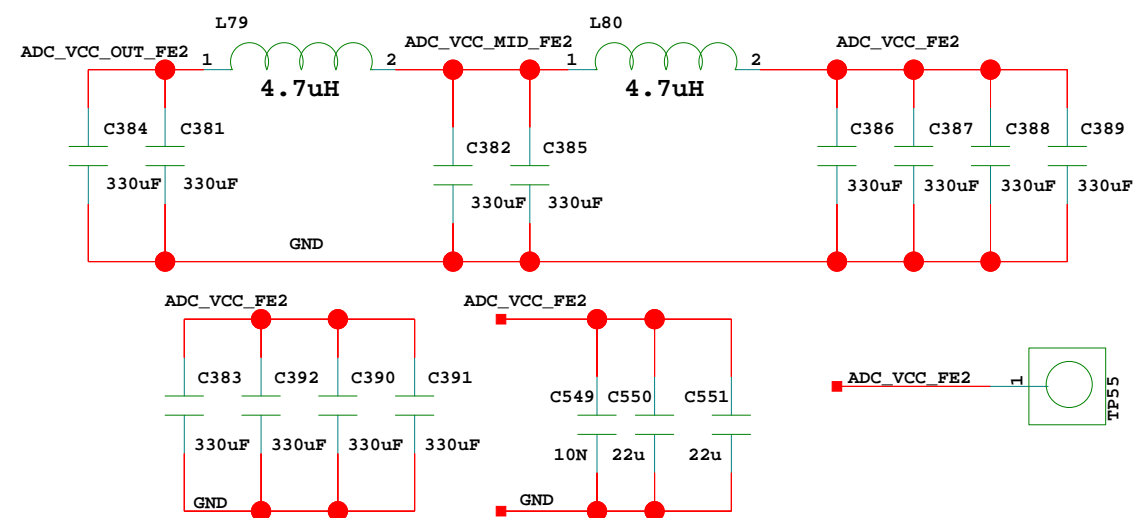
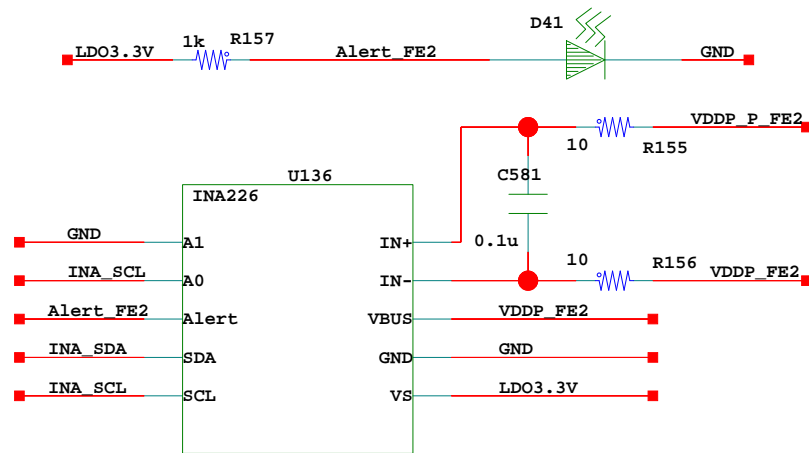
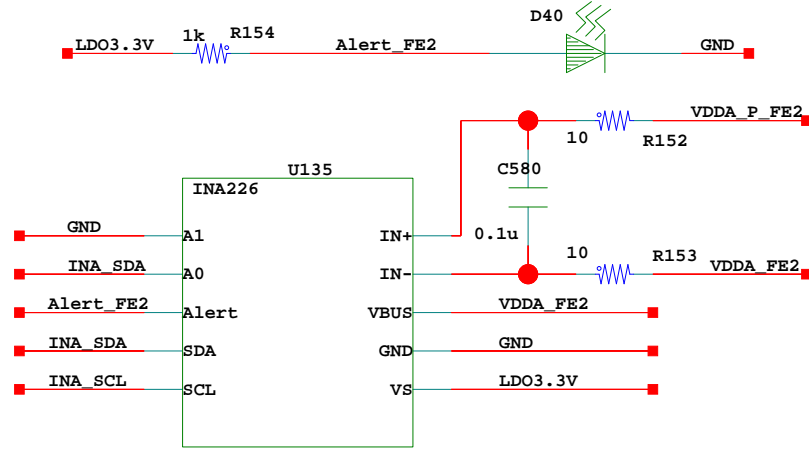
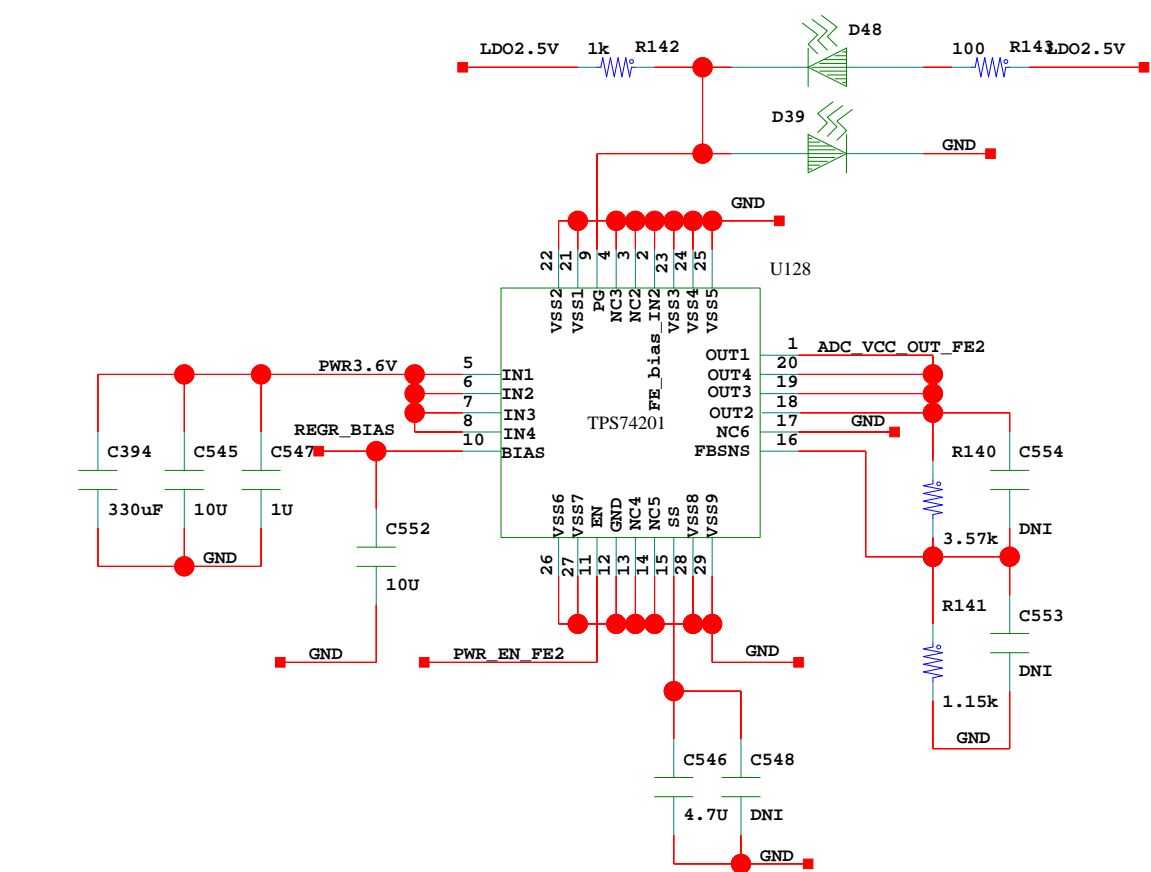
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FE\_ASIC\_2\_SKT\_COLD

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## FE ASIC\_2\_ADC\_WARM

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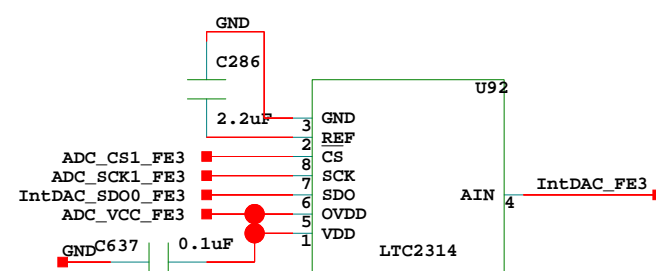


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## FE\_ASIC\_3\_SKT\_COLD

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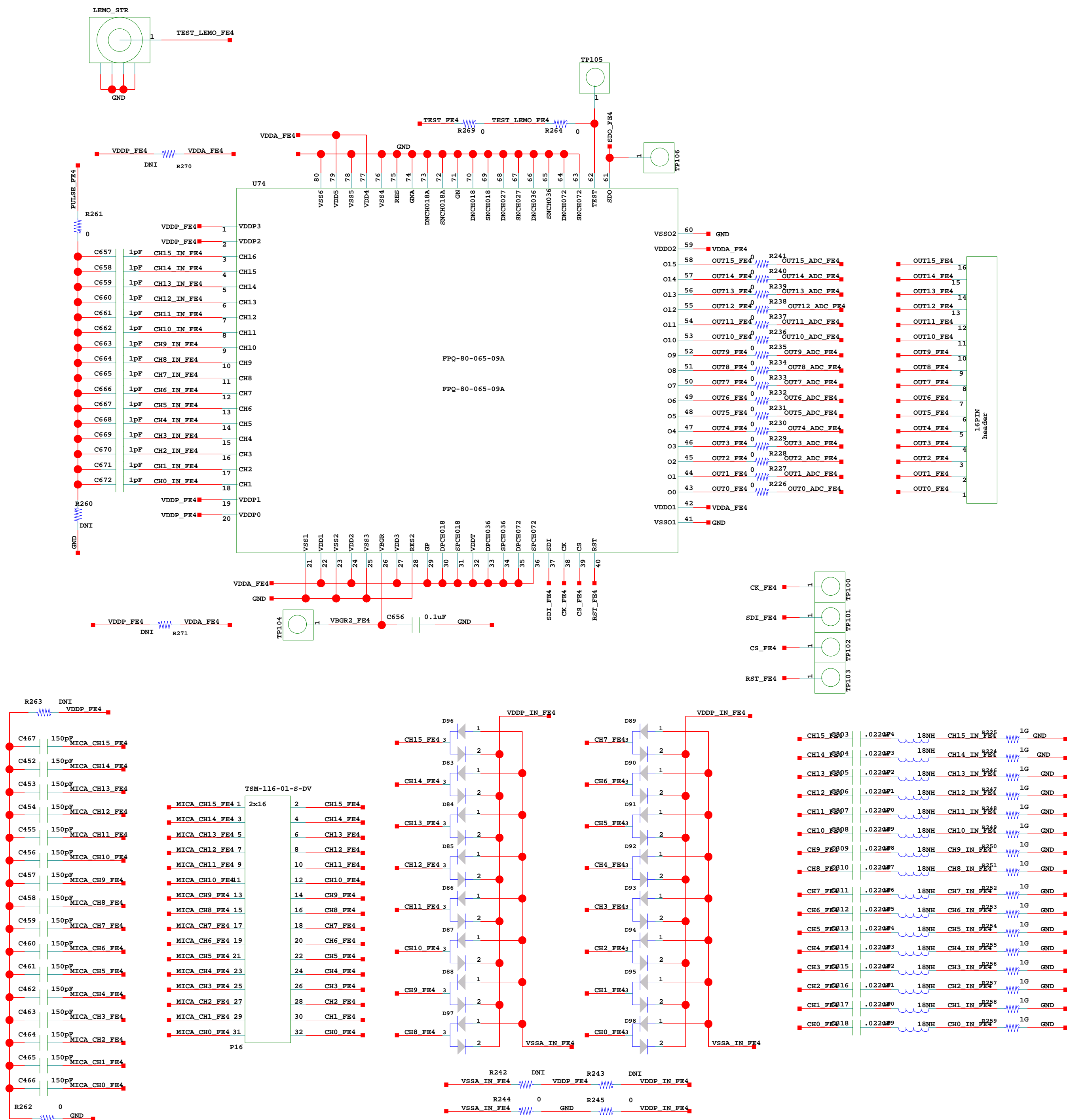
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## FE\_ASIC\_3\_ADC\_WARM

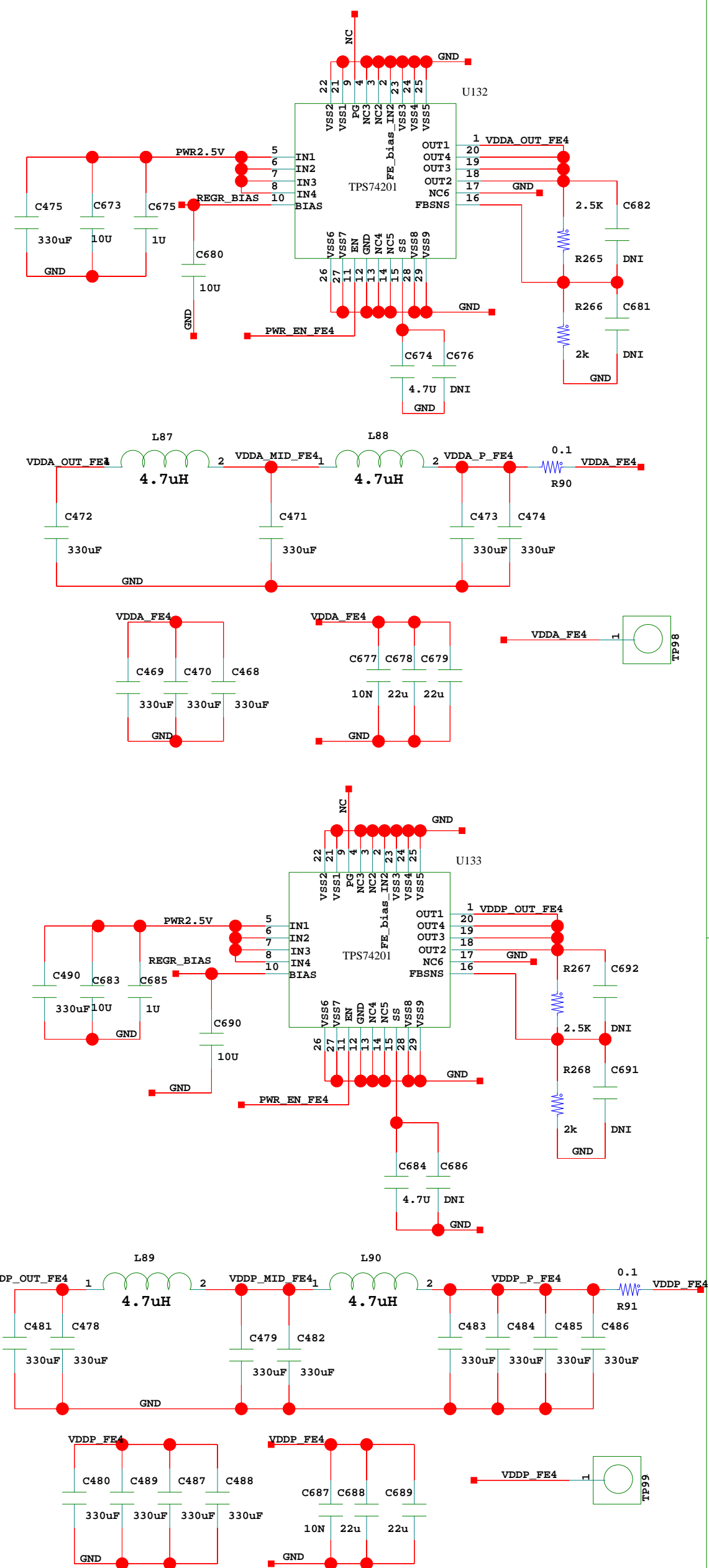
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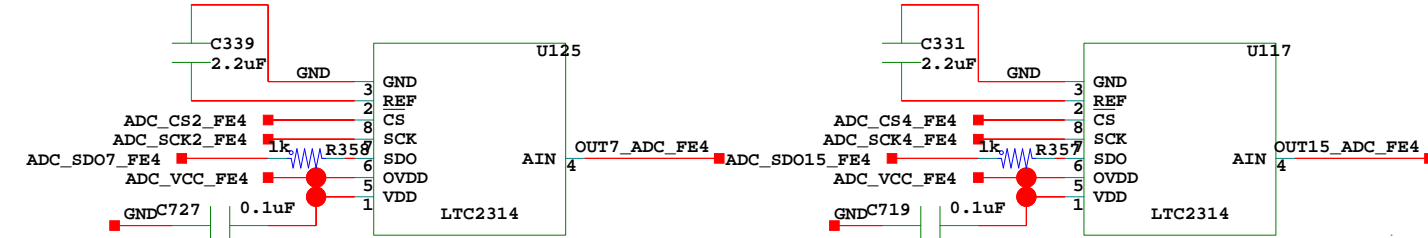
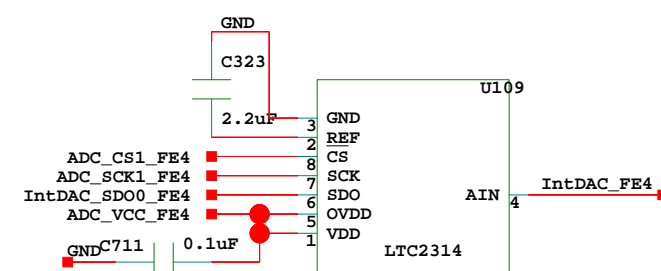
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## FE\_ASIC\_4\_SKT\_COLD

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## FE\_ASIC\_4\_ADC\_WARM

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