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4

QUAD SOCKETS ADC ASIC TESTER

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1

1

COMPANY			
BROOKHAVEN NATIONAL LABORATORY			
TITLE			
QUAD SOCKETS ADC ASIC TESTER			
SIZE	B	REV	SHEET 1 OF
DRAWN BY:	Jack Fried		DATE 09/03/2017:13:53

A

B

C

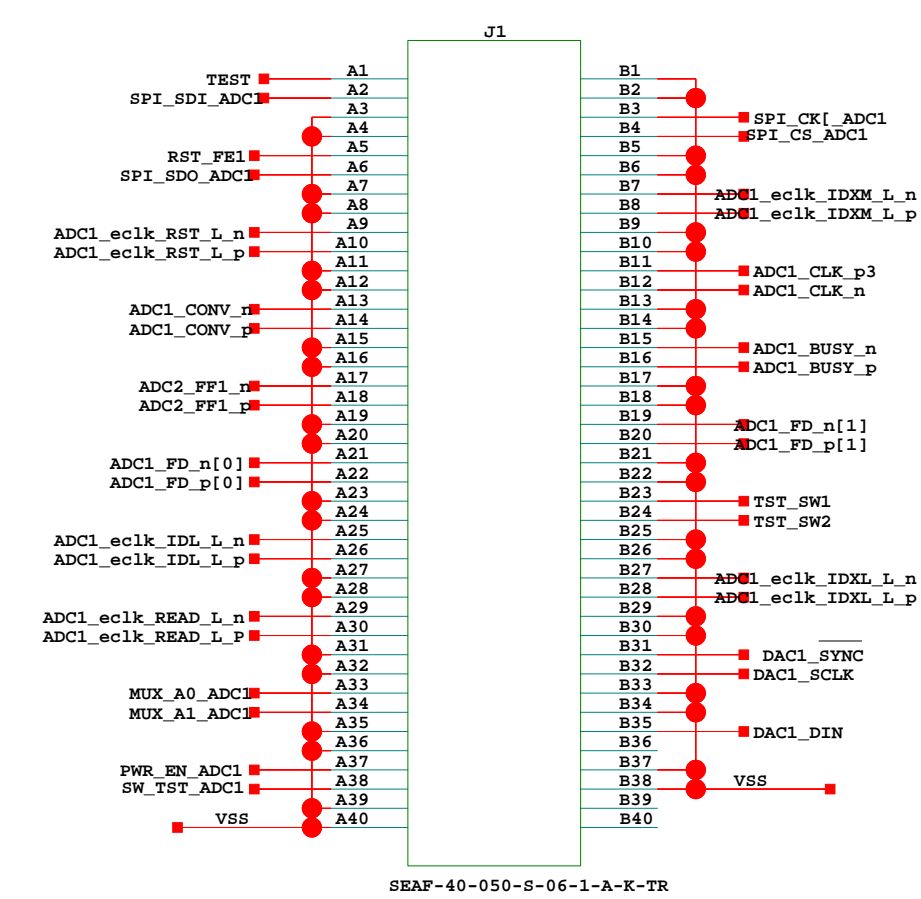
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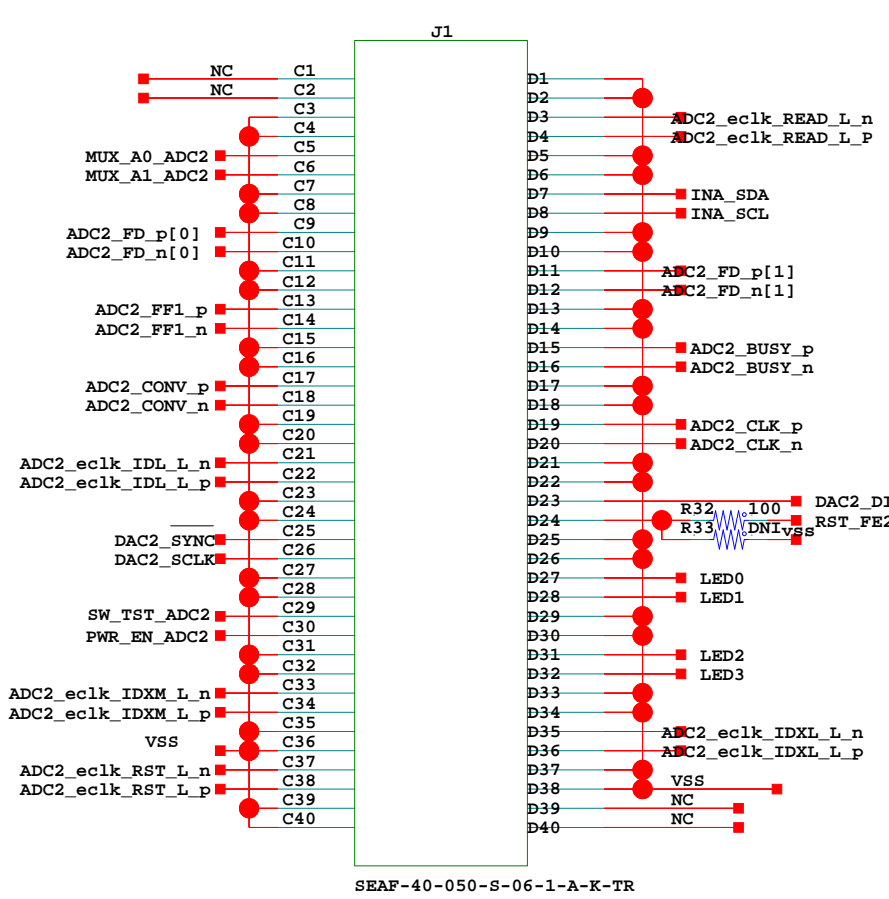
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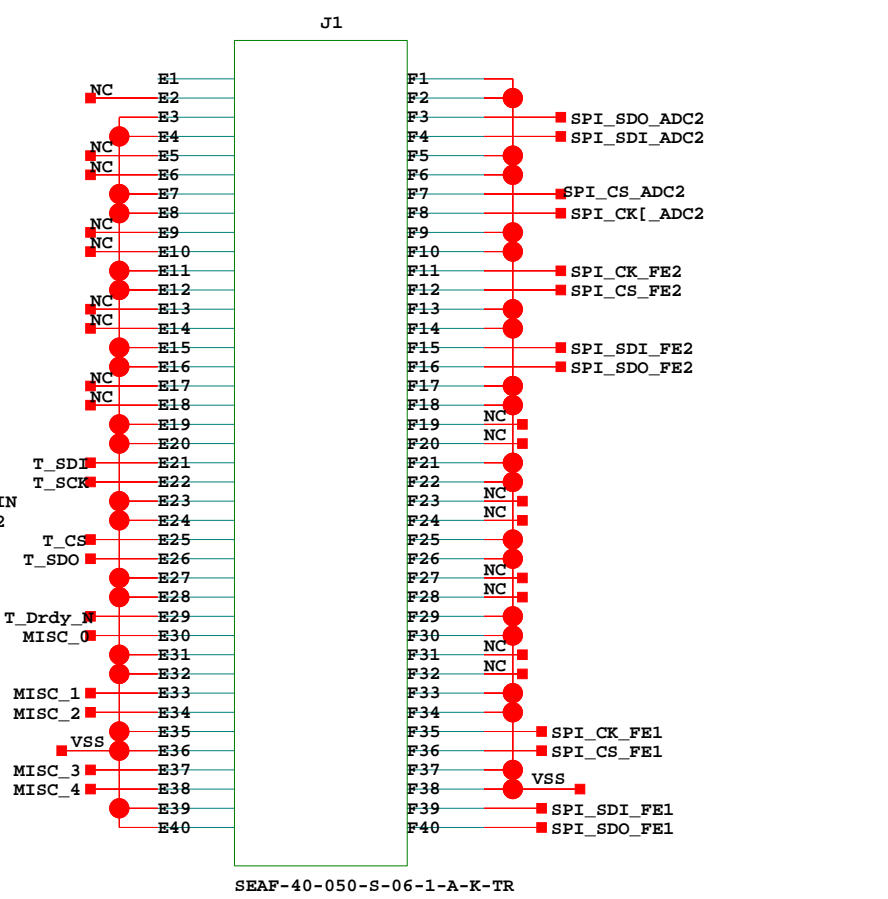
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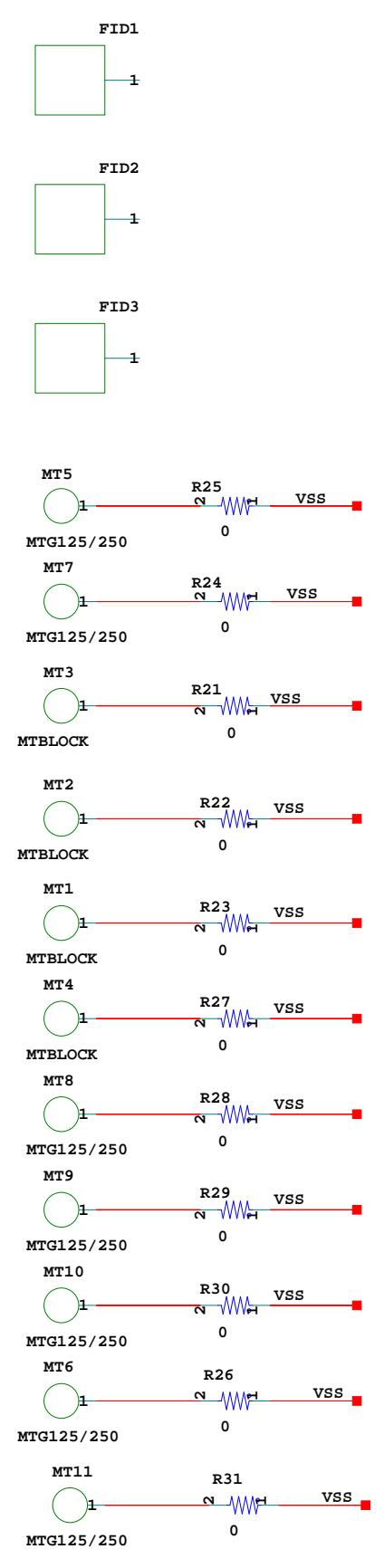
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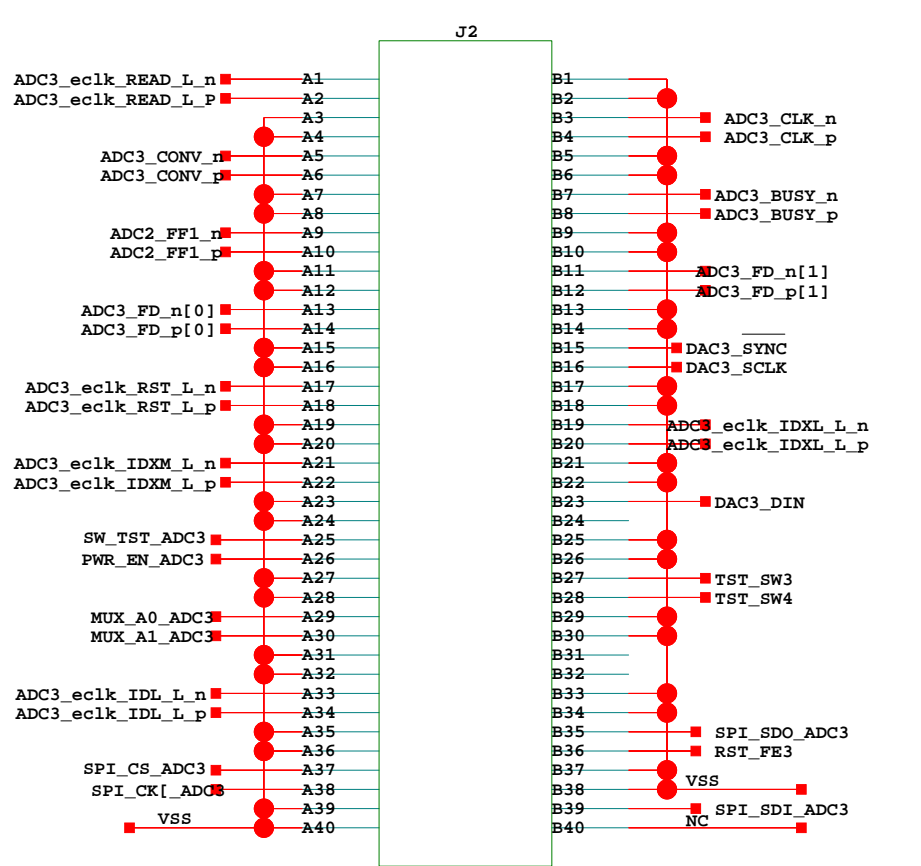


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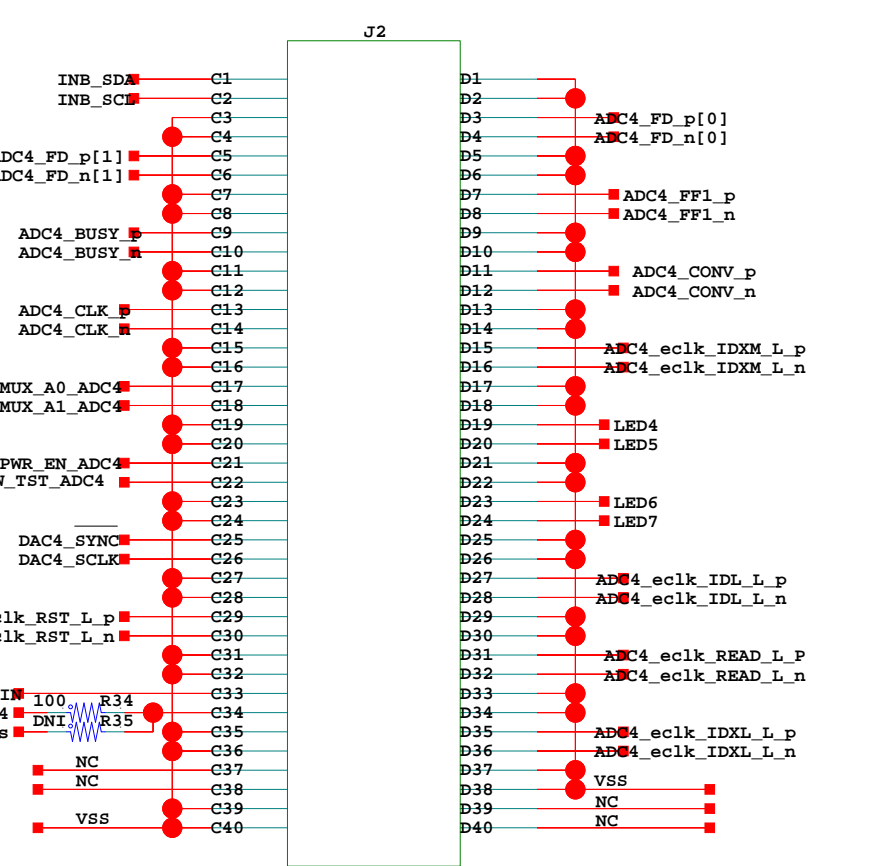


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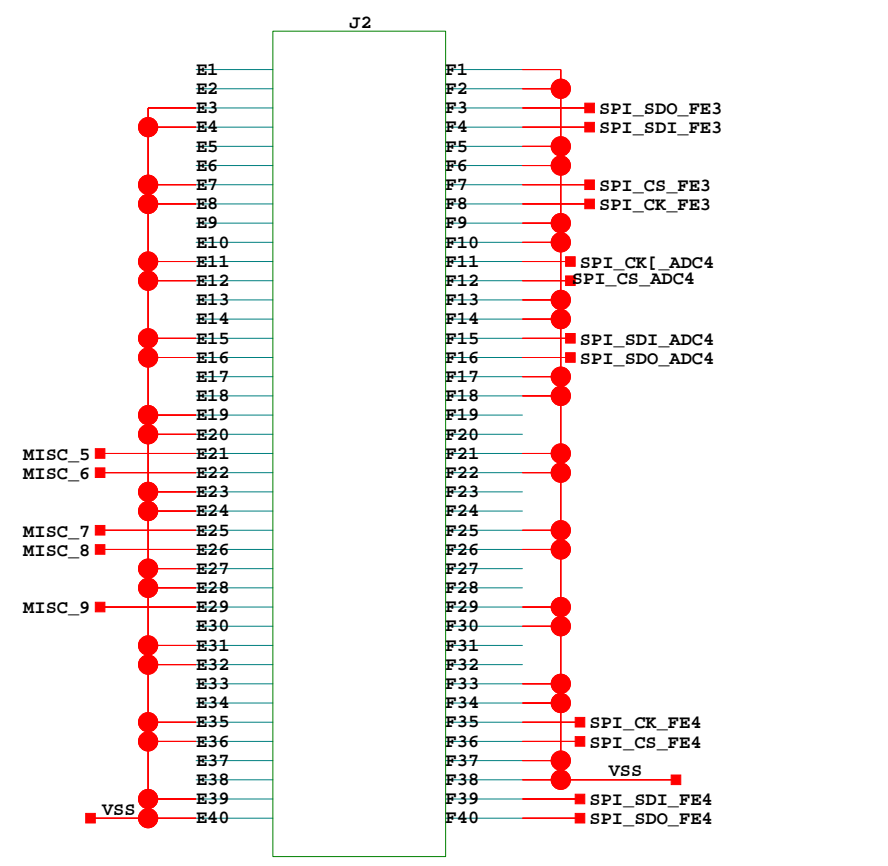
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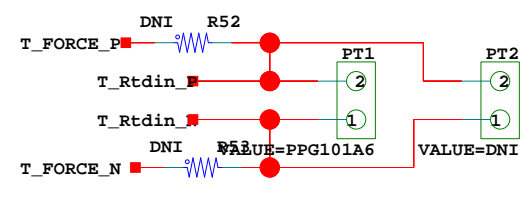
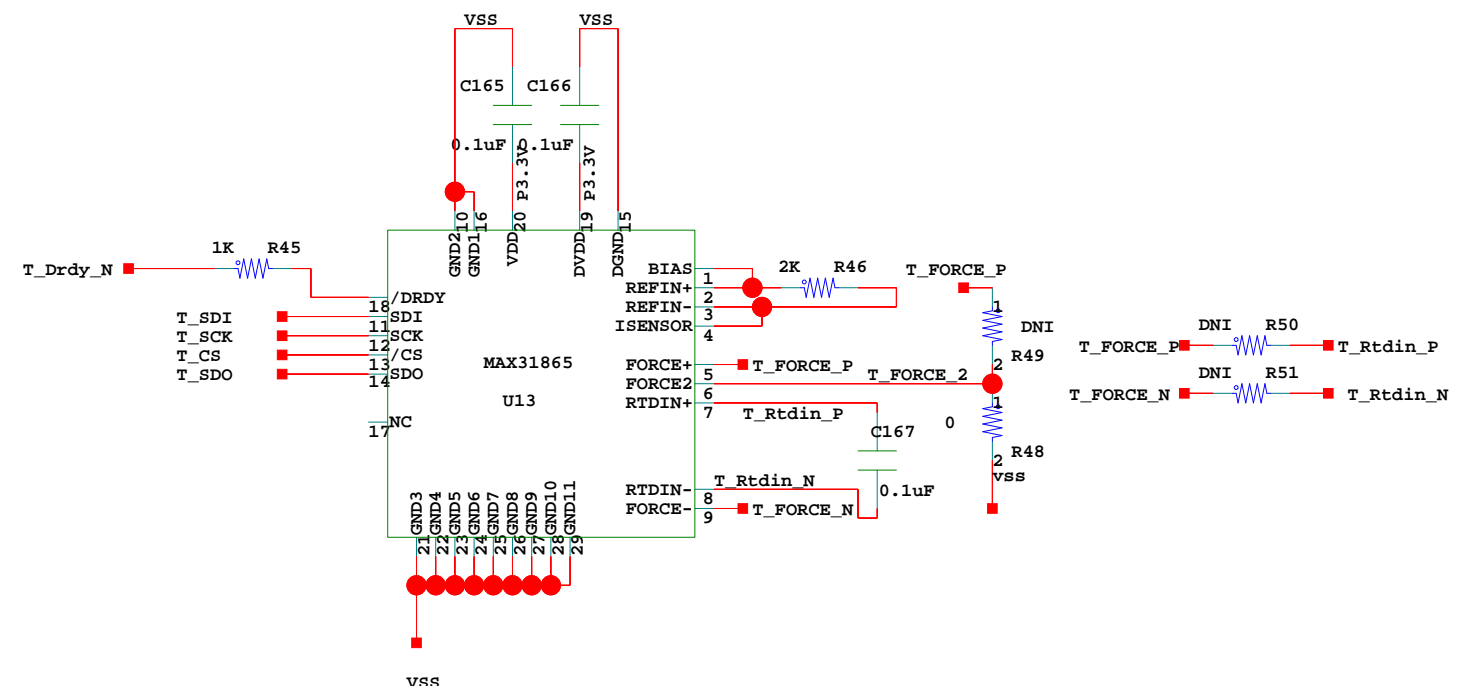
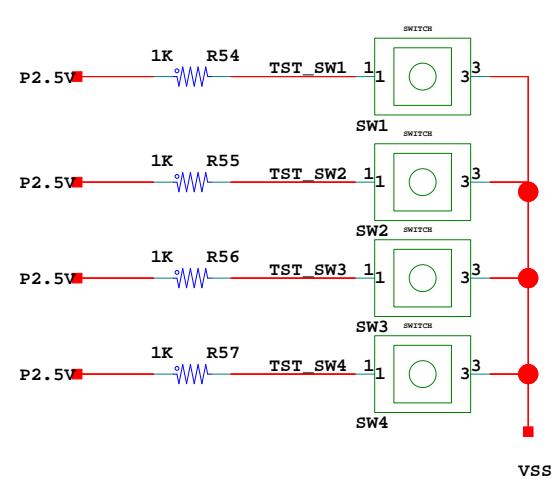
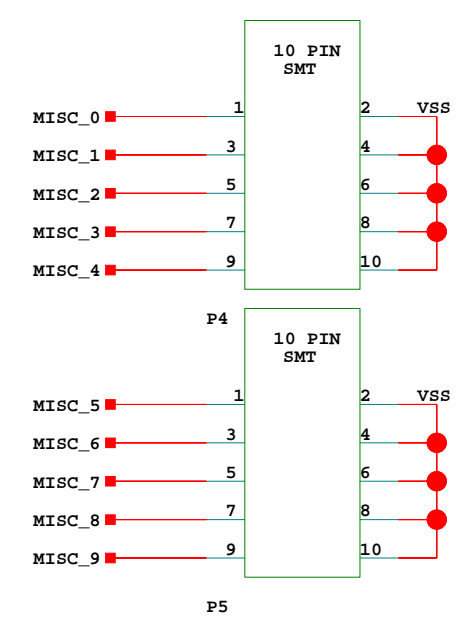
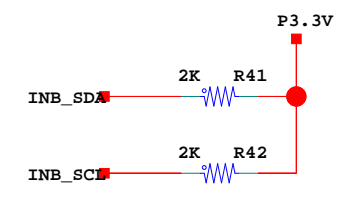
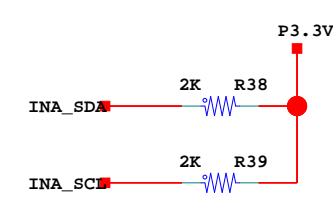
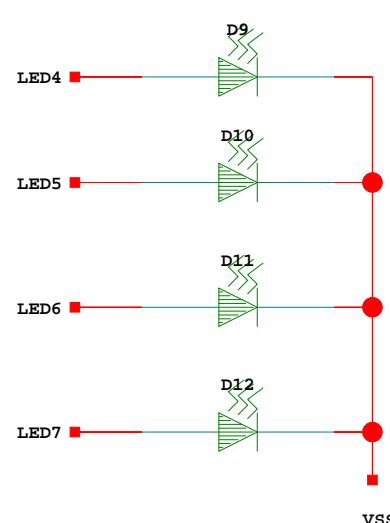
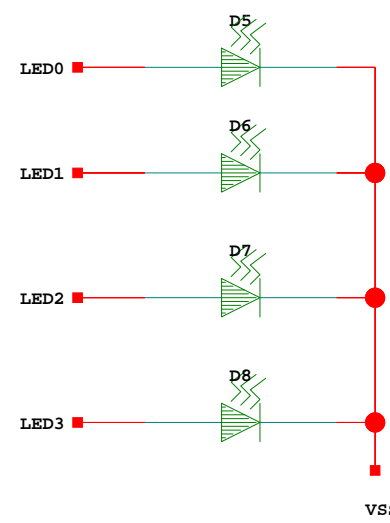
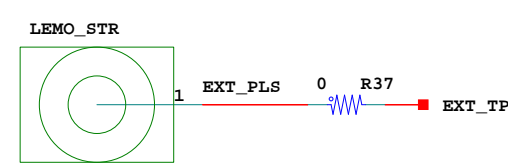
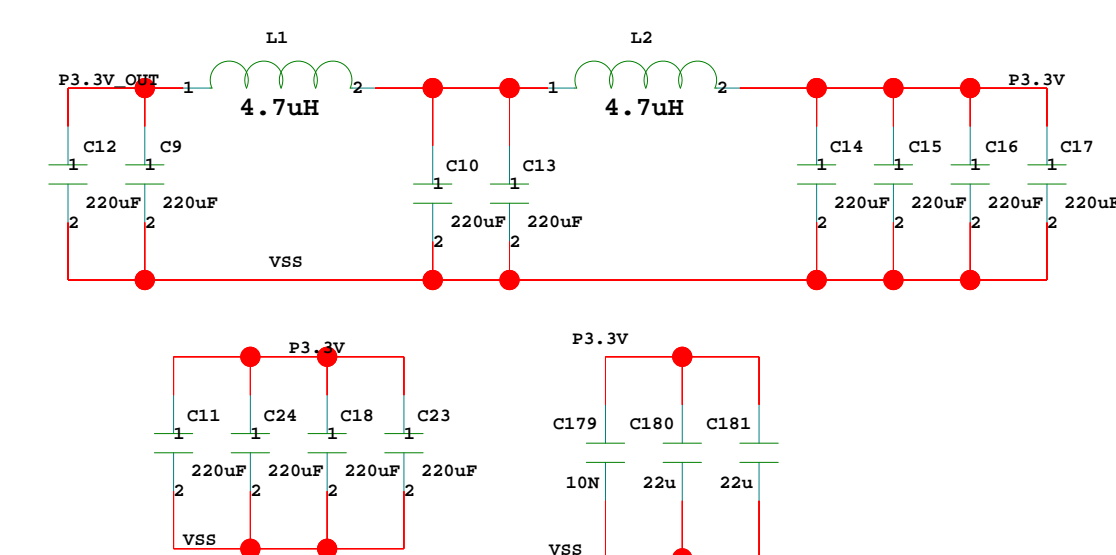
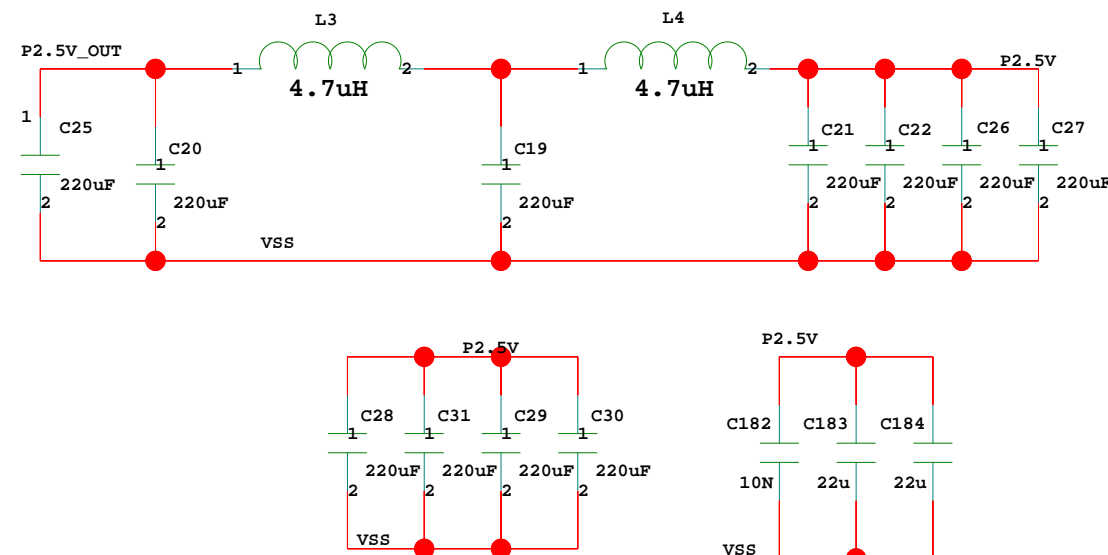
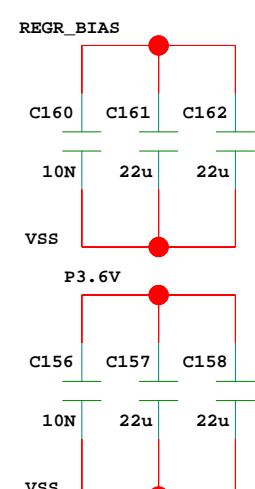
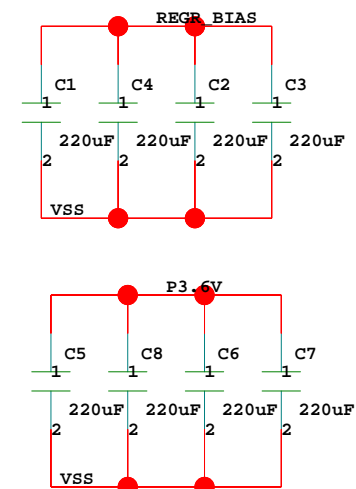
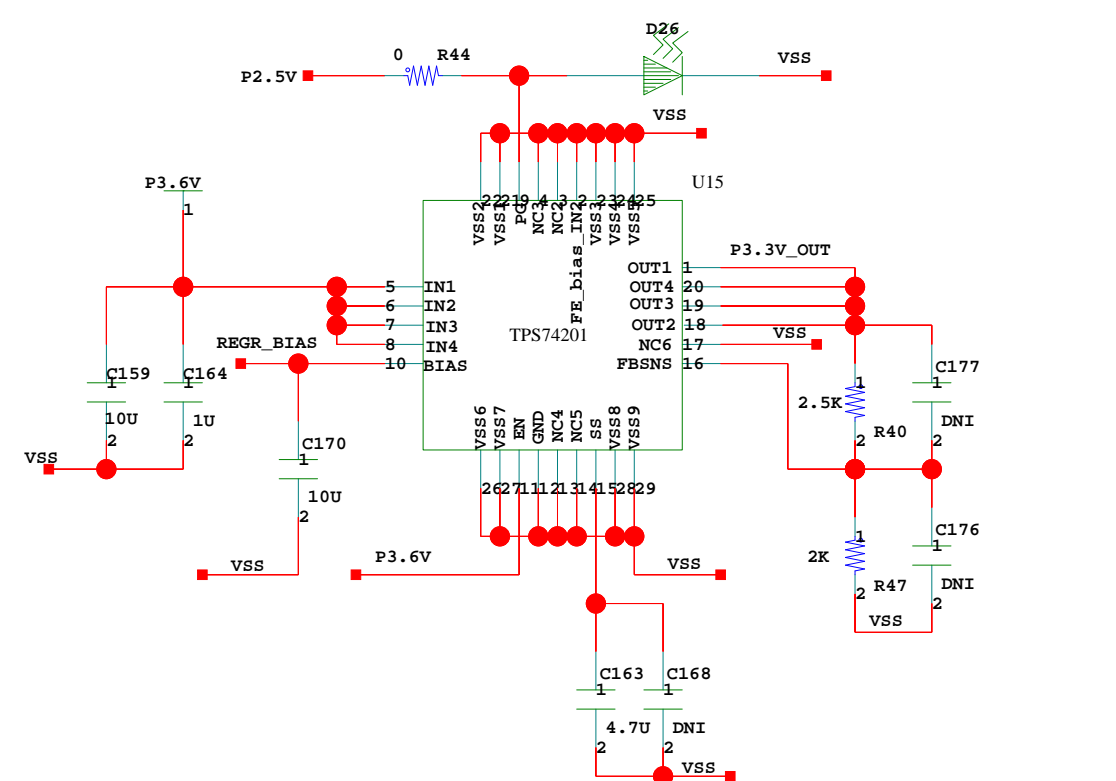
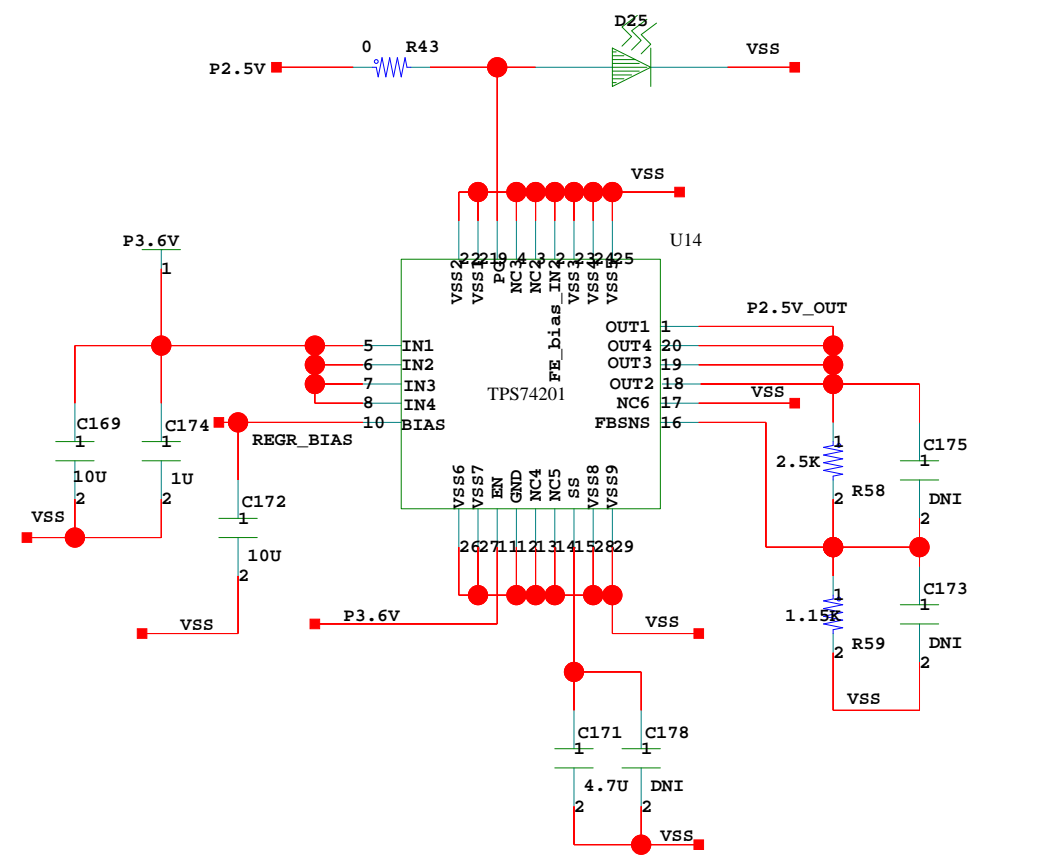
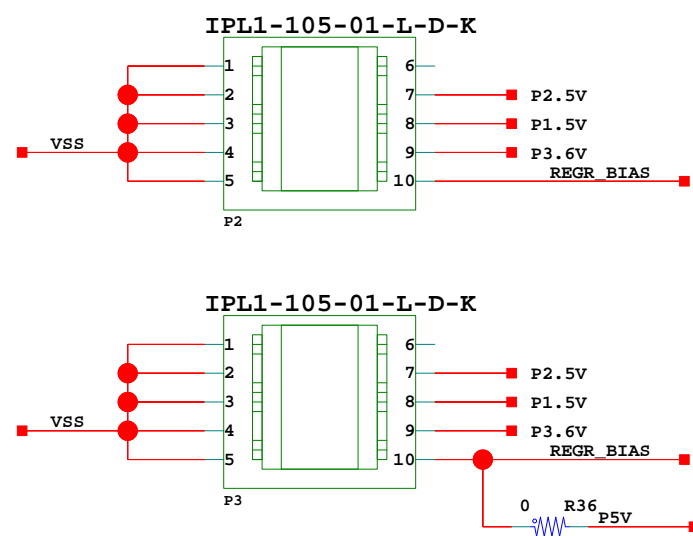


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ADC_CHIP_TESTER
FPGA Mezzanine Conector

Date: 09/03/2017:14:13
DRAWN BY: Jack Fried



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ADC_CHIP_TESTER

Board PWR and MISC

Date: 08/05/2017:11:35
DRAWN BY: Jack Fried

A

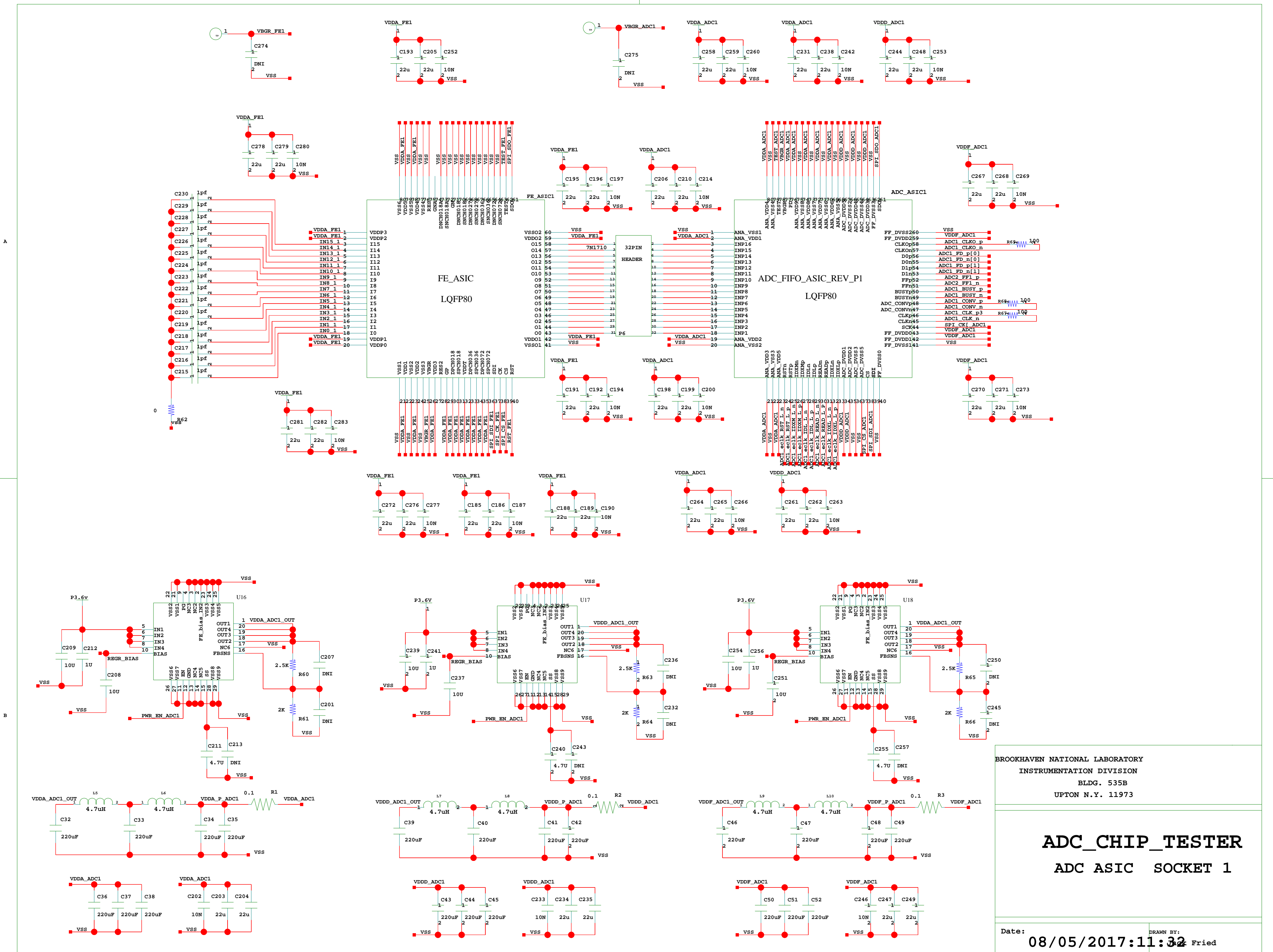
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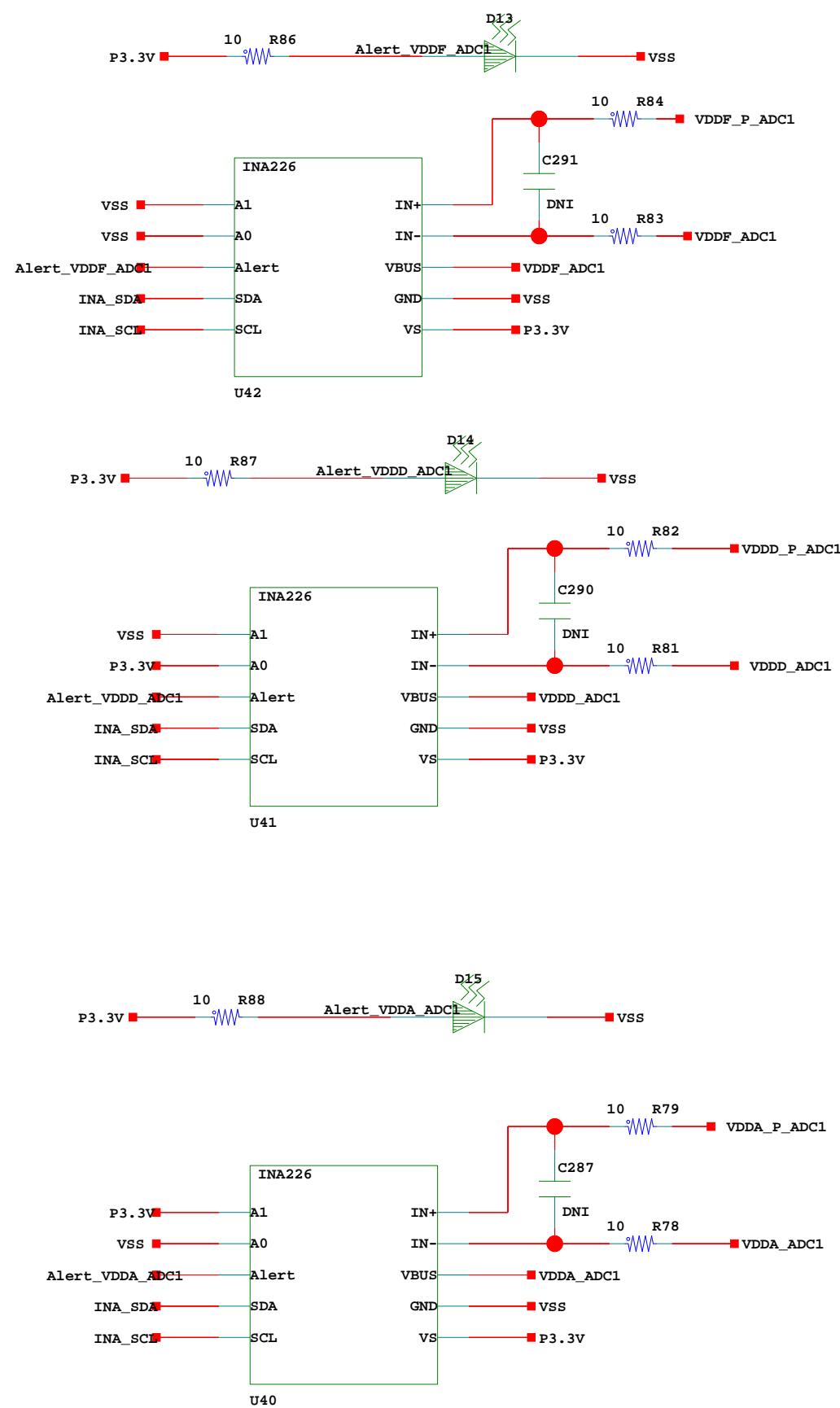
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A

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ADC_CHIP_TESTER

ADC ASIC 1 PWR Monitor and

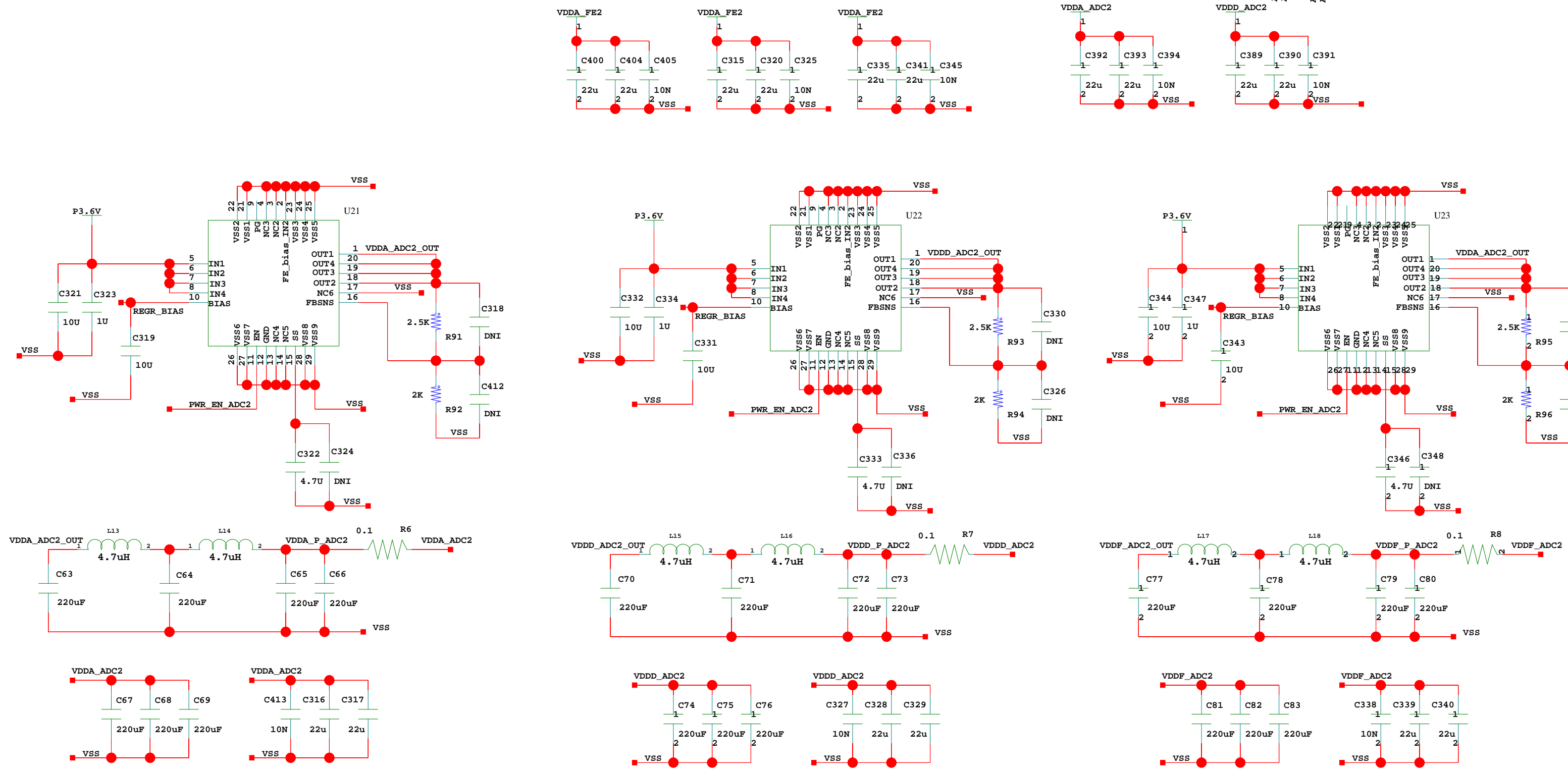
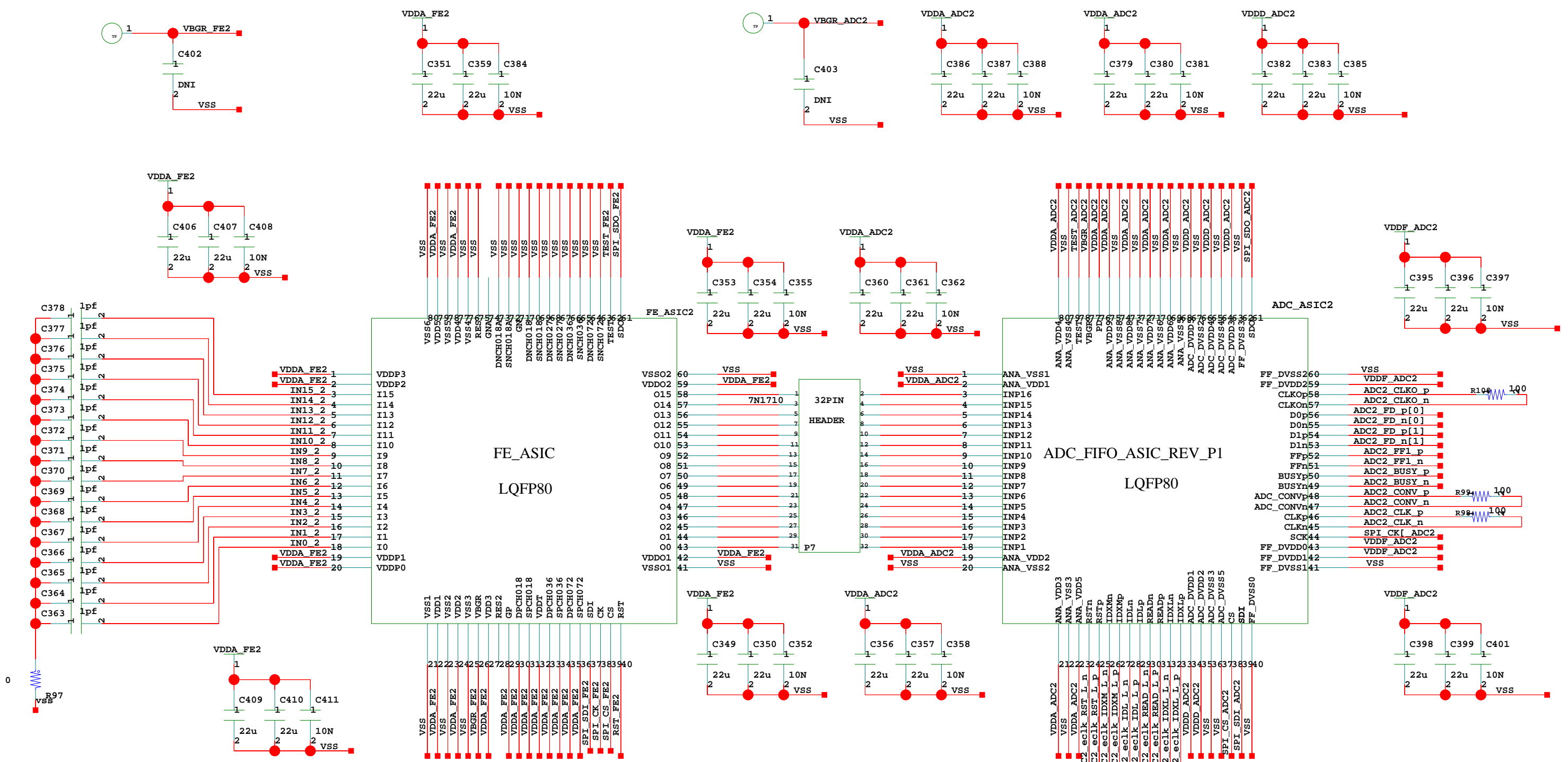
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A

B

A

B



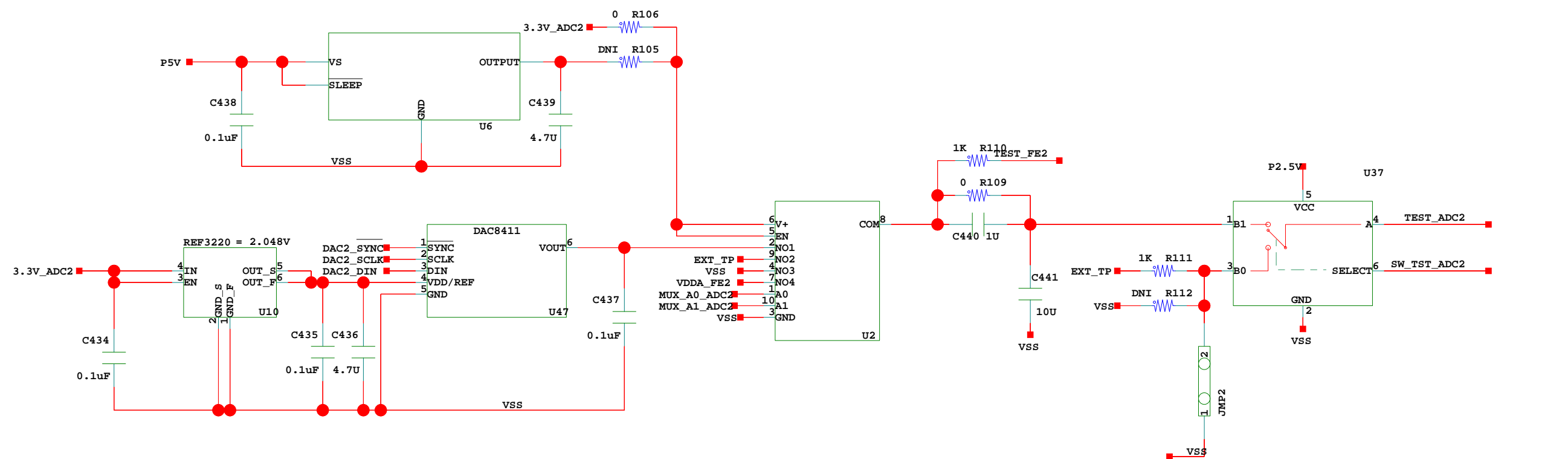
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UPTON N.Y. 11973

ADC_CHIP_TESTER

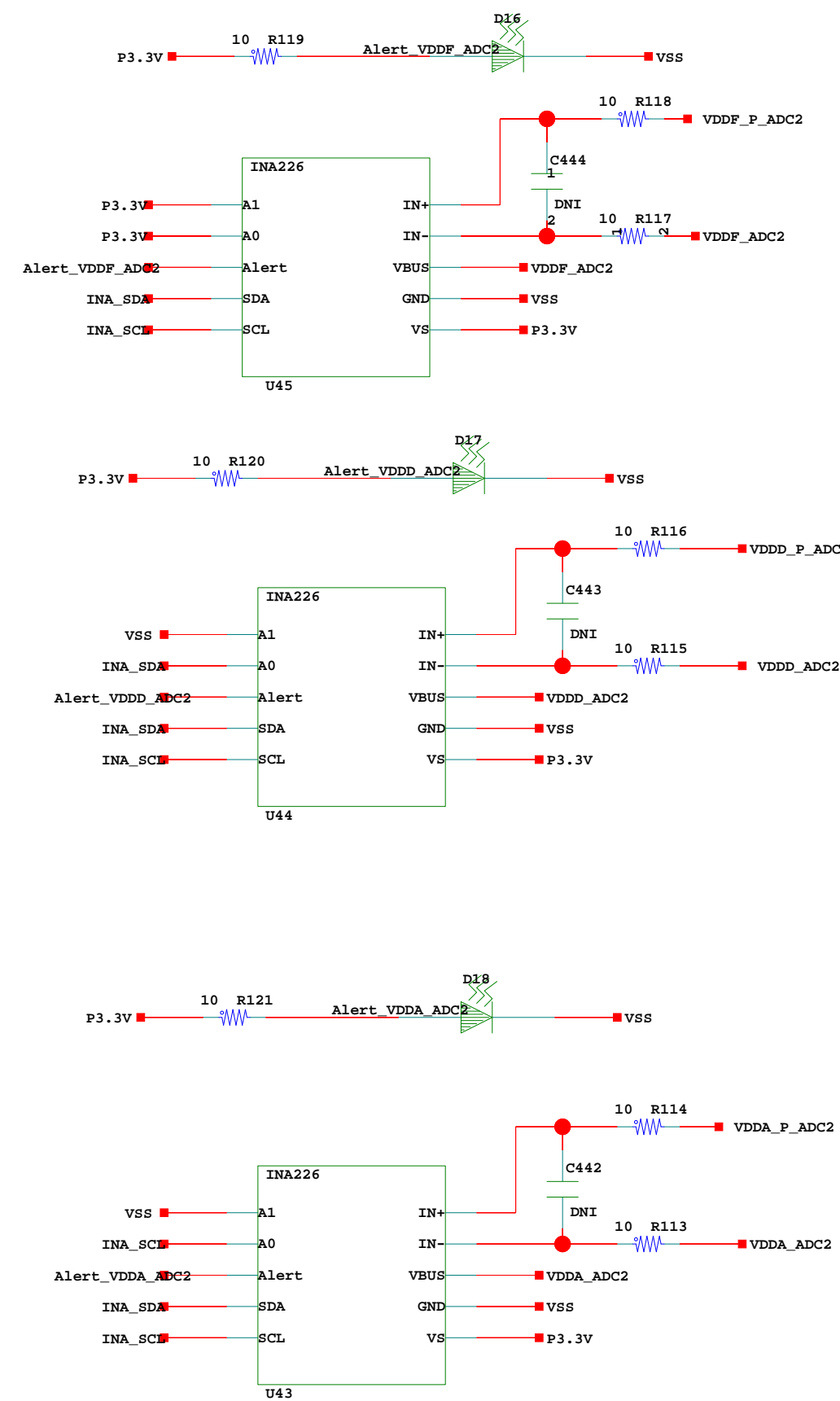
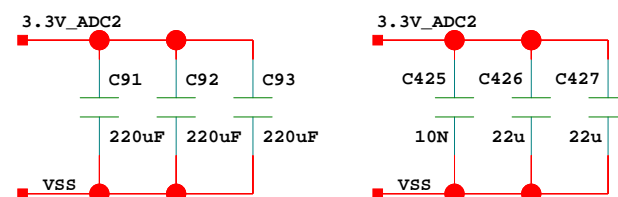
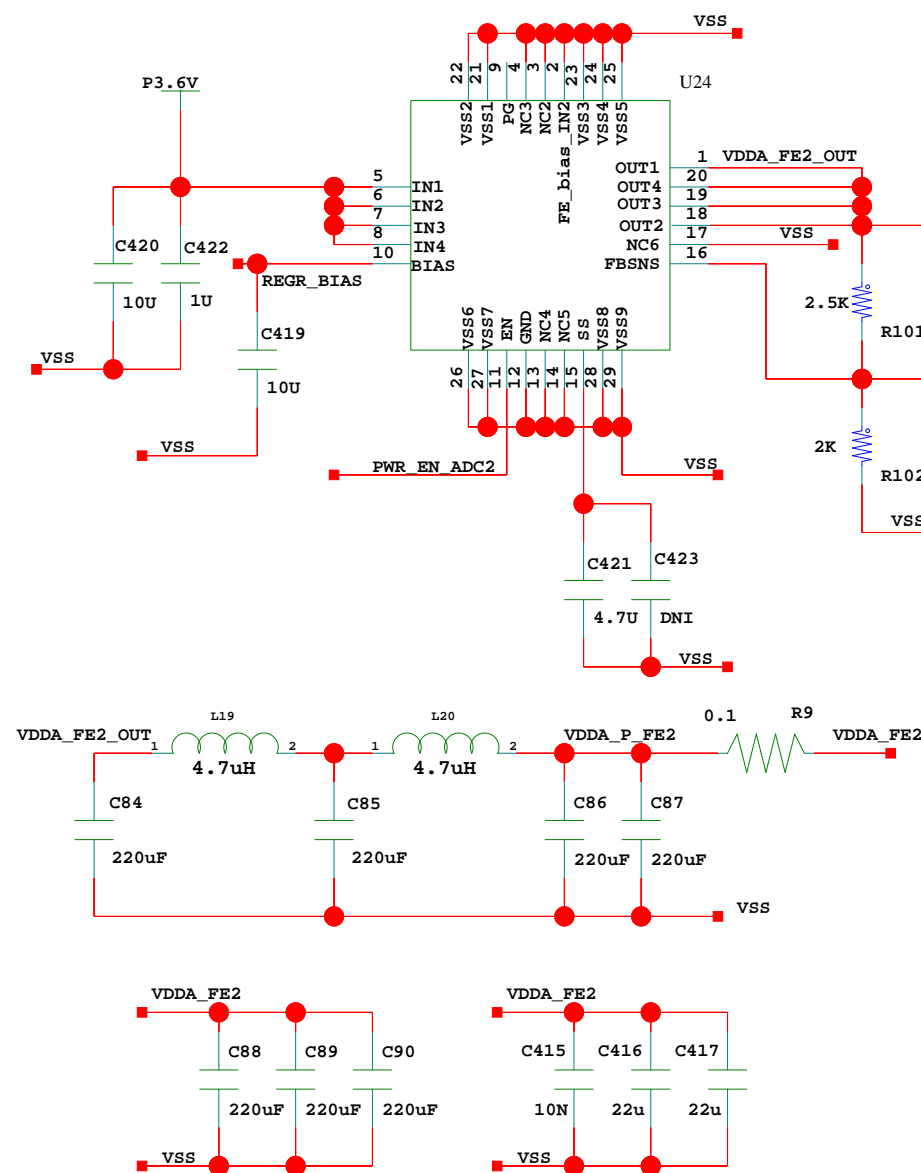
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Date: 08/05/2017:11:32
DRAWN BY: Jack Fried

A



B

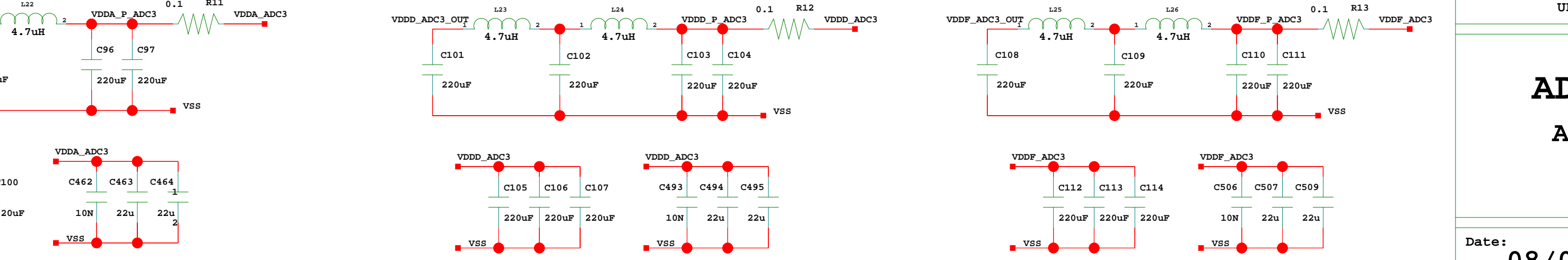


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ADC_CHIP_TESTER

ADC ASIC 2 PWR Monitor and

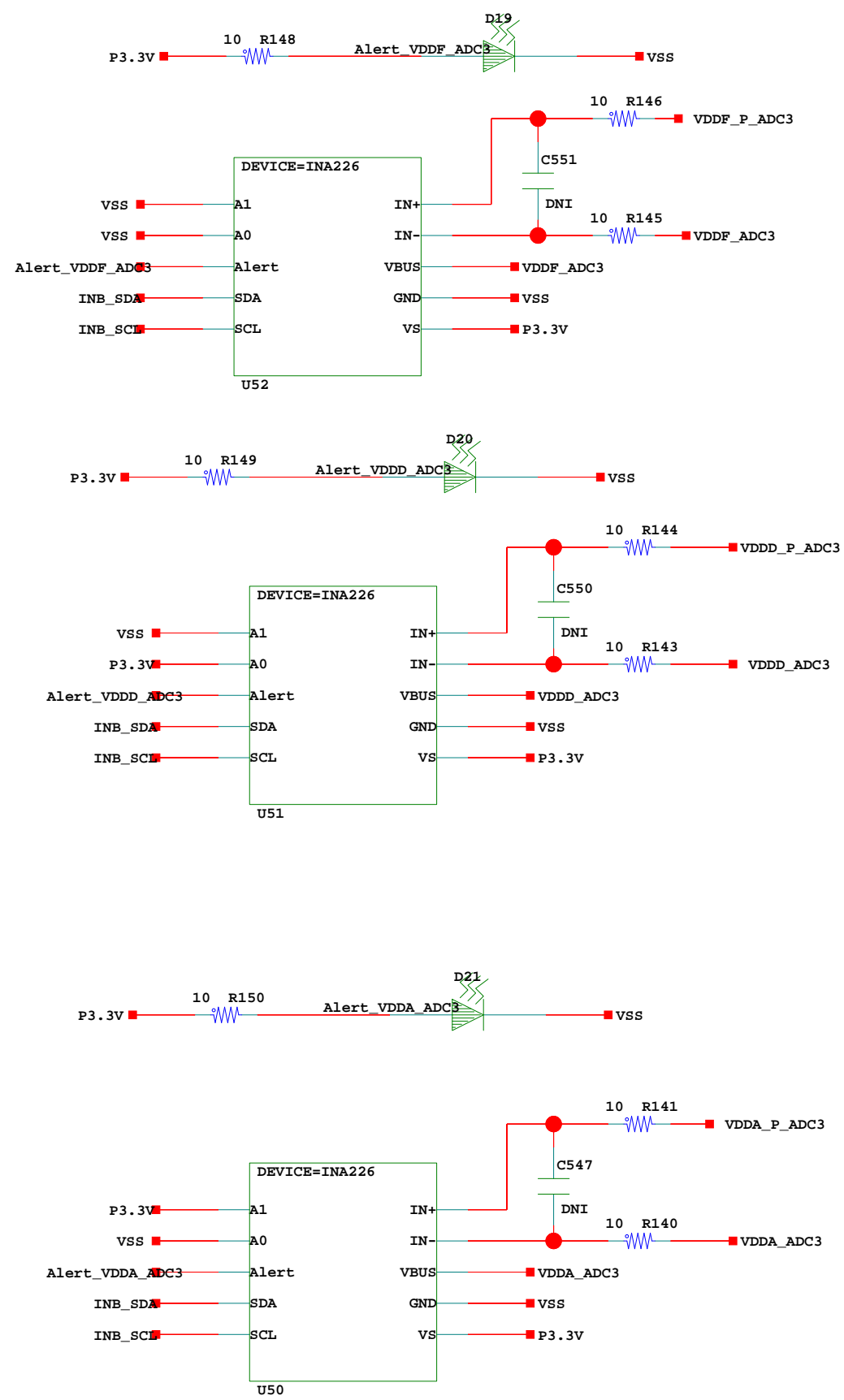
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ADC_CHIP_TESTER
ADC ASIC SOCKET 3
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Date: 08/05/2017:11:32 DRAWN BY: Jack Fried



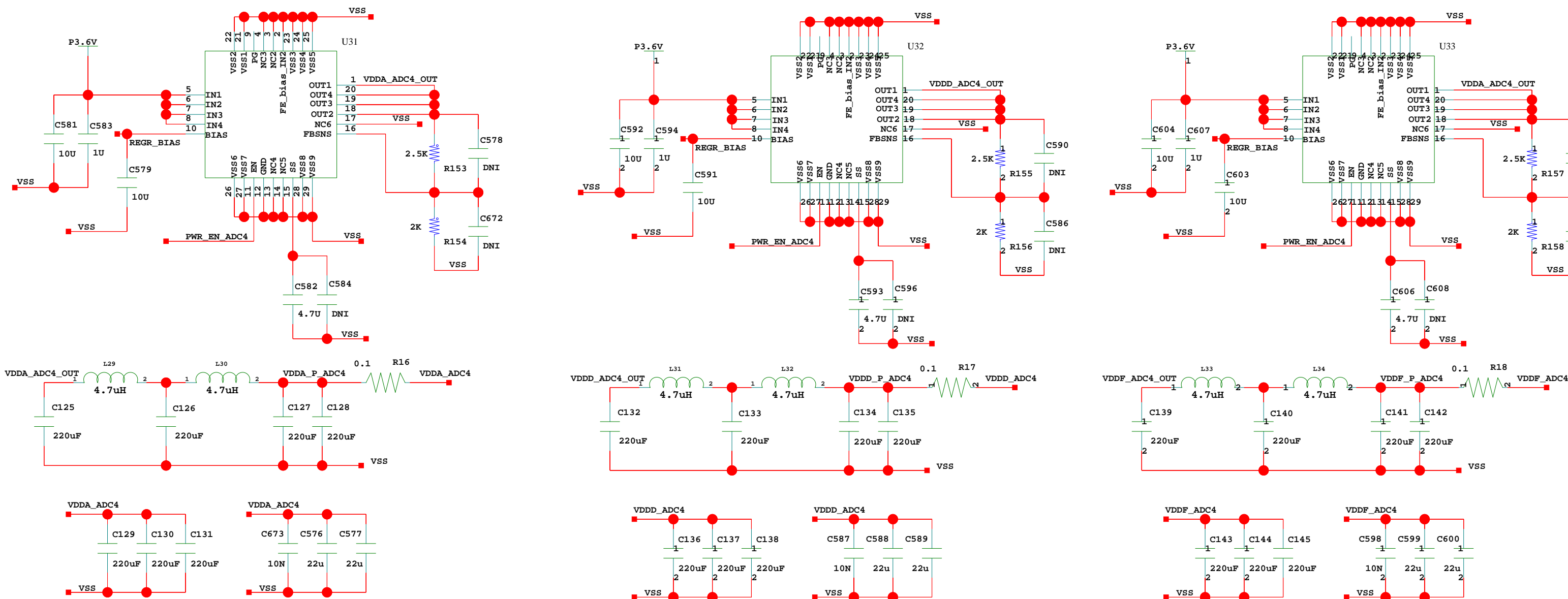
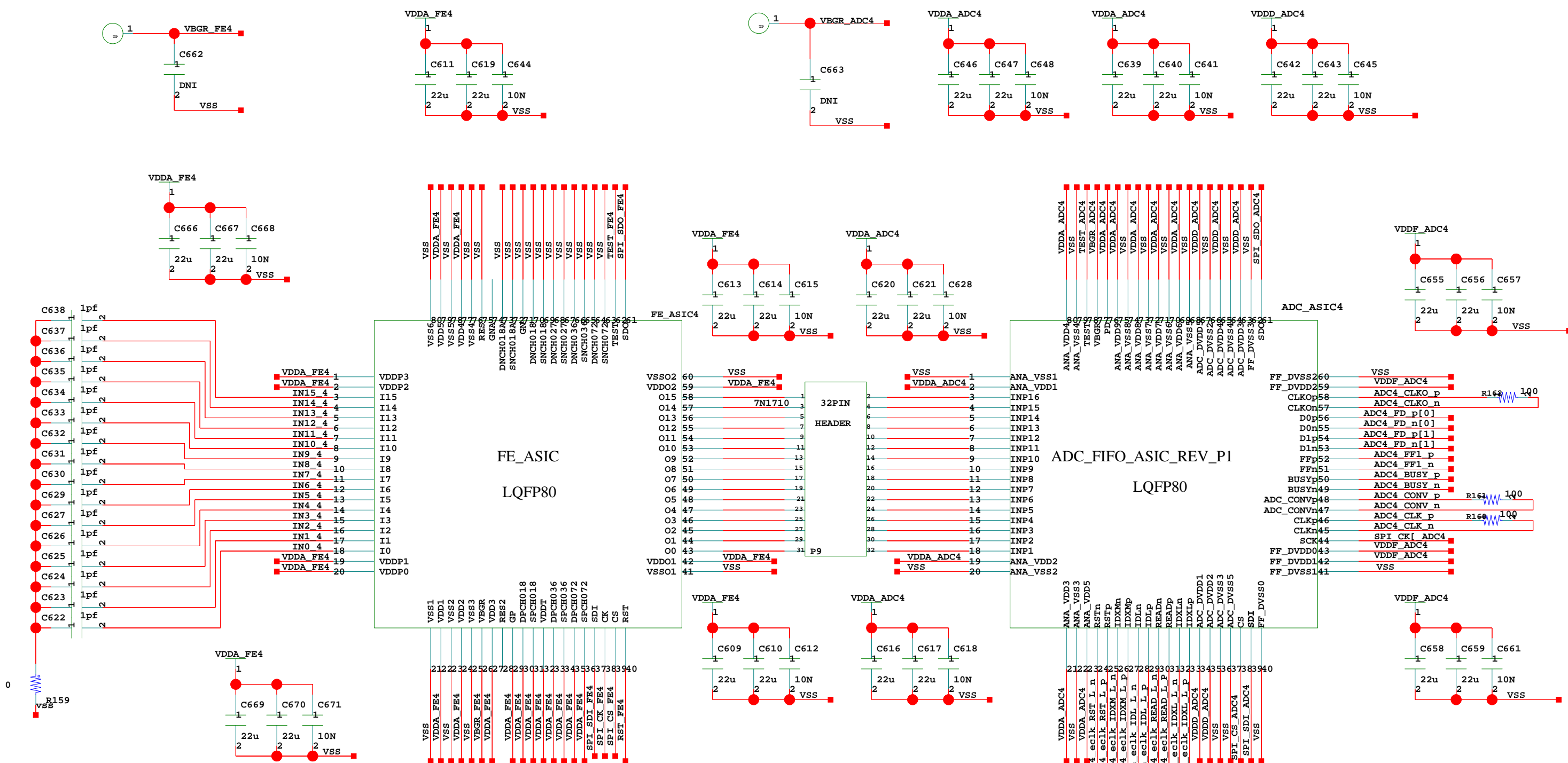
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ADC_CHIP_TESTER

ADC ASIC 3 PWR Monitor and

Date: 08/05/2017:11:38 DRAWN BY: Jack Fried

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: 38 Jack Fried

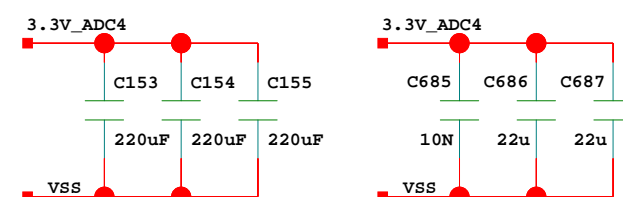
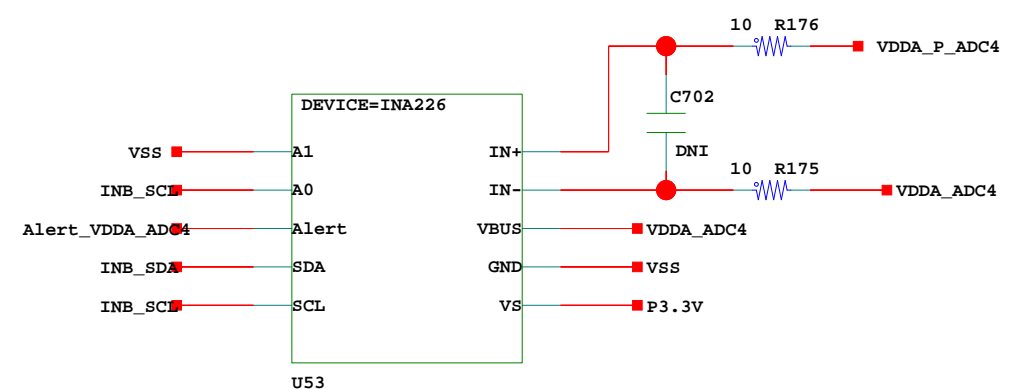


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ADC_CHIP_TESTER

ADC ASIC SOCKET 4

Date: 08/05/2017:11:32 DRAWN BY: Jack Fried



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ADC_CHIP_TESTER

ADC ASIC 4 PWR Monitor and

Date:	DRAWN BY:
08/05/2017:11:38	Jack Fried