

## Maximum Input 36V / 1A / Fixed Output 3.3V, 5V, 12V or 15V

### DESCRIPTION

The FDSM series of the MagI<sup>3</sup>C Power Module family is a fixed output voltage, fully integrated DC-DC power supply including the controller IC, inductor and capacitors all in one package.

For optimal performance the module is recommended for use with an external input capacitor, reducing design effort and complexity to a minimum.

The FDSM ensures fast time to market and low development costs.

It is pin compatible with the common 78xx linear regulator series. The high efficiency reduces the power dissipation and in many cases a heatsink and assembly parts are unnecessary.

24V to 3.3V conversion achieves up to 84% efficiency.  
 24V to 5V conversion achieves up to 88% efficiency.  
 24V to 12V conversion achieves up to 94% efficiency.  
 24V to 15V conversion achieves up to 96% efficiency.

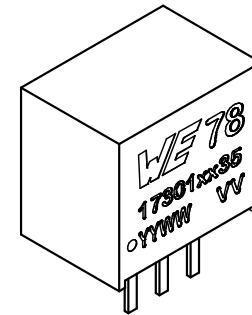
The standard THT (11.6 x 10.4 x 8mm) package allows for easy assembly.

### TYPICAL APPLICATIONS

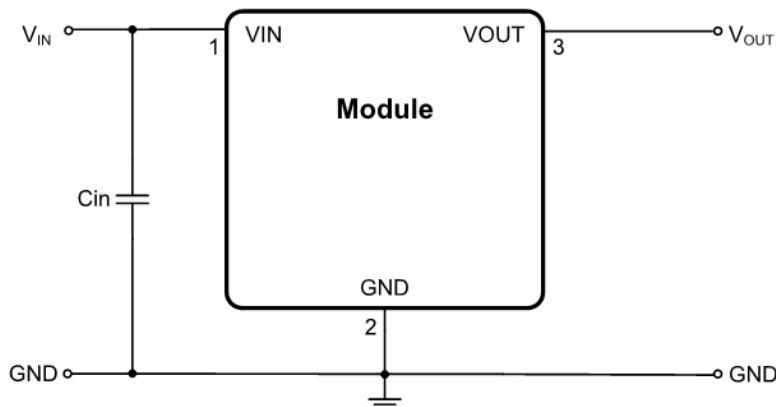
- Point-of-Load DC-DC applications
- Replacement for linear regulators
- Interface and microcontroller supplies
- General purpose

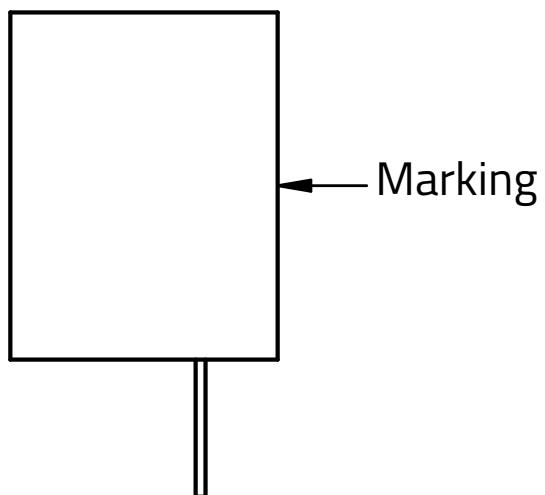
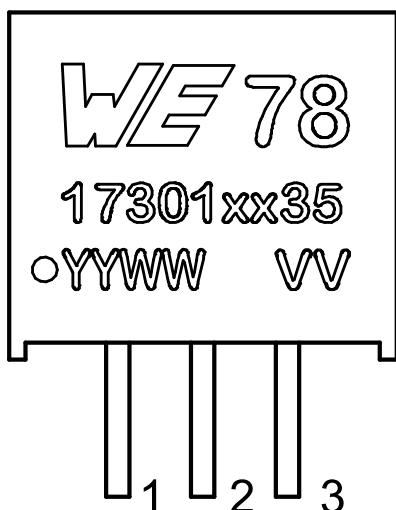
### FEATURES

- Peak efficiency up to 96%
- Current capability up to 1A
- Input voltage up to 36V
- Minimum input voltage / output voltage:  
 6 Vin / 3.3 Vout (173010335)  
 8 Vin / 5 Vout (173010535)  
 16 Vin / 12 Vout (173011235)  
 20 Vin / 15 Vout (173011535)
- No minimum load required
- Partially integrated input and output capacitors
- Integrated inductor
- Low output voltage ripple (<50mV<sub>pp</sub>)
- Current mode control
- Pulse skipping for high efficiency at light loads
- Internal soft-start
- Short circuit protection
- Cycle by cycle current limit
- Pin compatible with the FDSM power modules series
- Operating ambient temperature range: -40°C to 85°C
- RoHS & REACH compliant
- Case and potting material UL 94 Class V0 (flammability testing) certified
- Complies with EN55032 class B conducted and radiated emissions standard



### TYPICAL CIRCUIT DIAGRAM



**PINOUT (PACKAGE TYPE F)**

Type F package (pins are located at the front)

**MARKING DESCRIPTION**

MARKING	DESCRIPTION
WE	Würth Elektronik eiSos GmbH & Co. KG
78	Indicates compatibility with 78xx linear regulator
17301xx35	Order code
YY	Year
WW	Calendar week
VV	Output voltage (3.3V, 5V, 12V or 15V)

**PIN DESCRIPTION**

SYMBOL	NUMBER	TYPE	DESCRIPTION
VIN	1	Power	The supply input pin is a terminal for an input voltage source. It is recommended to use a 10µF/50V input capacitor.
GND	2	Power	Ground pin; reference for V <sub>IN</sub> and V <sub>OUT</sub> .
VOUT	3	Power	Regulated output voltage pin. There is no need for an external output capacitor.







**RoHS, REACH**

RoHS directive		Directive 2011/65/EU of the European Parliament and the Council of June 8th, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.
REACH directive		Directive 1907/2006/EU of the European Parliament and the Council of June 1st, 2007 regarding the Registration, Evaluation, Authorization and Restriction of Chemicals (REACH).

**RELIABILITY**

SYMBOL	PARAMETER	TEST CONDITIONS	TYP <sup>(3)</sup>	UNIT
MTBF	Mean time between failures	MIL-HDBK-217F, 25°C	2000 ·10 <sup>3</sup>	h

**PACKAGE SPECIFICATIONS**

ITEM	PARAMETER	TYP <sup>(3)</sup>	UNIT
Case	Black flame-retardant and heat-resistant plastic (UL94 V-0)	-	-
Potting material	Silicone, UL94V-0	-	-
Weight		1.8	g
Vibration	5g for 20 min	MIL-STD-202, Method 204	

**NOTES**

- (1) Min and Max are 100% production tested at 25° C. Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods.
- (2) The human body model is a 100pF capacitor discharged through a 1.5 kΩ resistor into each pin. Test method is per JESD-22-114.
- (3) Typical numbers are valid at 25° C ambient temperature and represent statistically the utmost probability assuming the Gaussian distribution.
- (4) Depending on load current, see derating diagram.

## TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply:  $V_{IN} = 24V$ ;  $V_{OUT} = 3.3V$  (173010335),  $V_{OUT} = 5V$  (173010535),  $V_{OUT} = 12V$  (173011235), and  $V_{OUT} = 15V$  (173011535);  $I_{OUT} = 1A^{(5)}$ ;  $T_{AMB} = 25^{\circ}C$ .

## RADIATED AND CONDUCTED EMISSIONS EN55032 (CISPR-32) CLASS B COMPLIANT

The 173010335, 173010535, 173011235 and 173011535 power modules are tested with a standard EMC configuration (1m wire between the module and the load) to give more realistic information about implementation in the applications. The test setup is based on CISPR16 with the limit values CISPR32.

Measured with module on an Evaluation Board 17800FDSM in a Fully Anechoic Room (FAR) at 3m antenna distance.

## TEST SETUP

Input wire length:

- Radiated Emission: 160cm (80cm Horizontal + 80cm Vertical)
- Conducted Emission: 80cm

Output wire length:

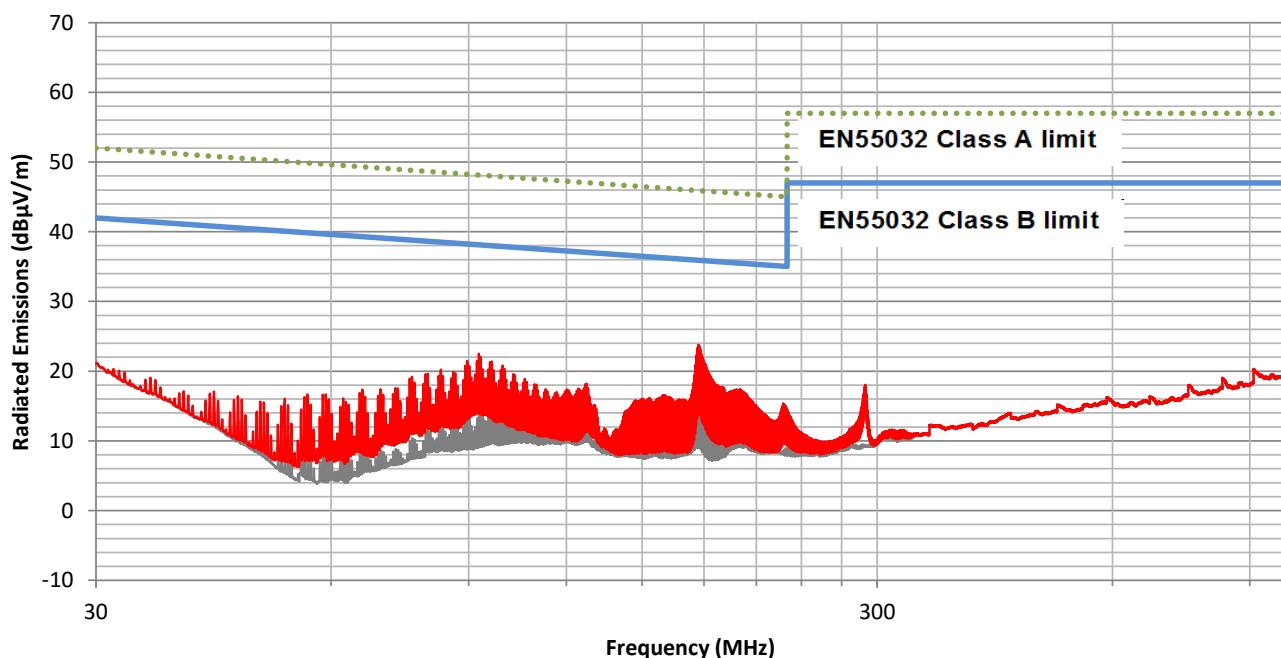
- Long wire (with input filter): 1m

## RADIATED AND CONDUCTED EMISSIONS - 173010335

## Radiated Emissions 173010335 (3m Antenna Distance)

 $V_{IN} = 24V$ ,  $V_{OUT} = 3.3V$ ,  $I_{LOAD} = 1A$  with input filter $C_F$  and  $C_{IN} = 4 \times 4.7\mu F$  (885012209048),  $L_F = 4.7\mu H$  (744773047)

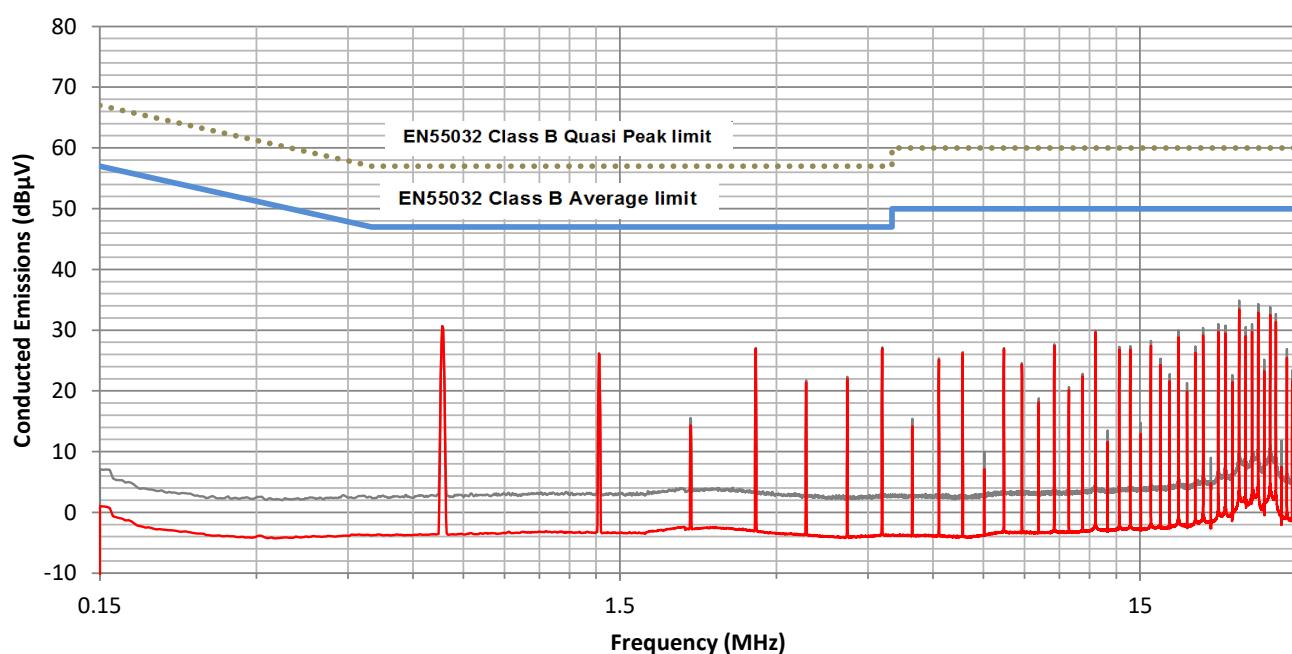
Horizontal      Vertical



## Conducted Emissions 173010335

 $V_{IN} = 24V$ ,  $V_{OUT} = 3.3V$ ,  $I_{LOAD} = 1A$  with input filter $C_F$  and  $C_{IN} = 4 \times 4.7\mu F$  (885012209048),  $L_F = 4.7\mu H$  (744773047)

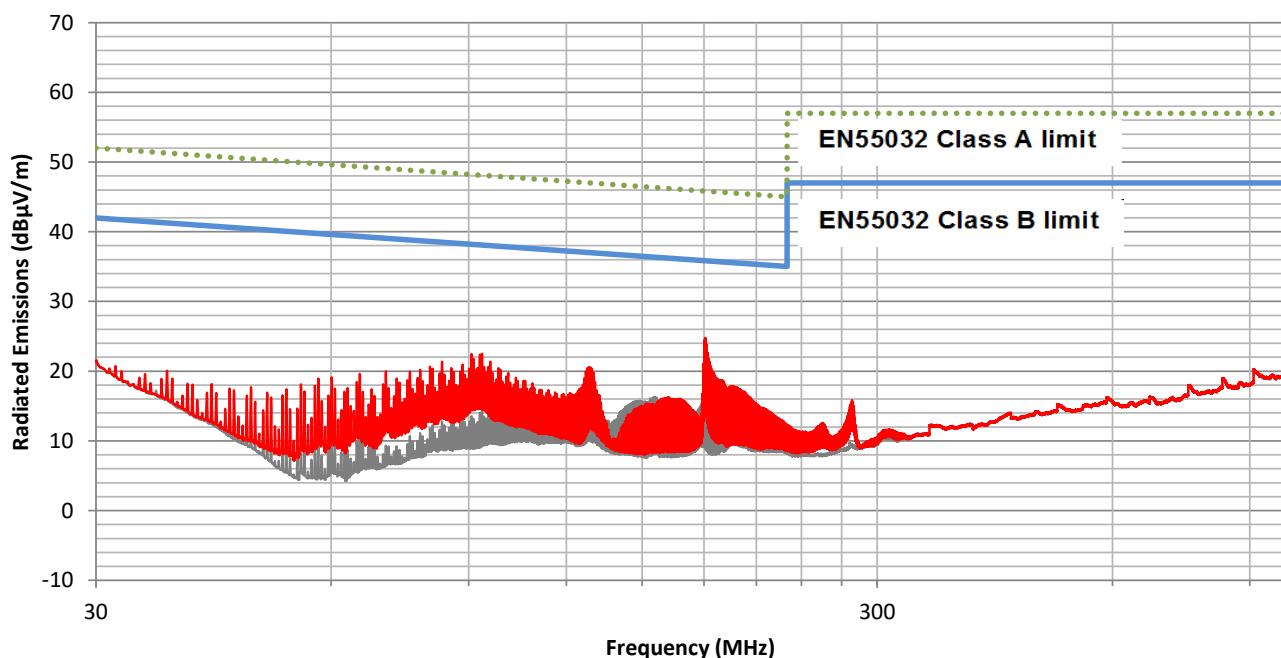
Average      Quasi peak



## RADIATED AND CONDUCTED EMISSIONS - 173010535

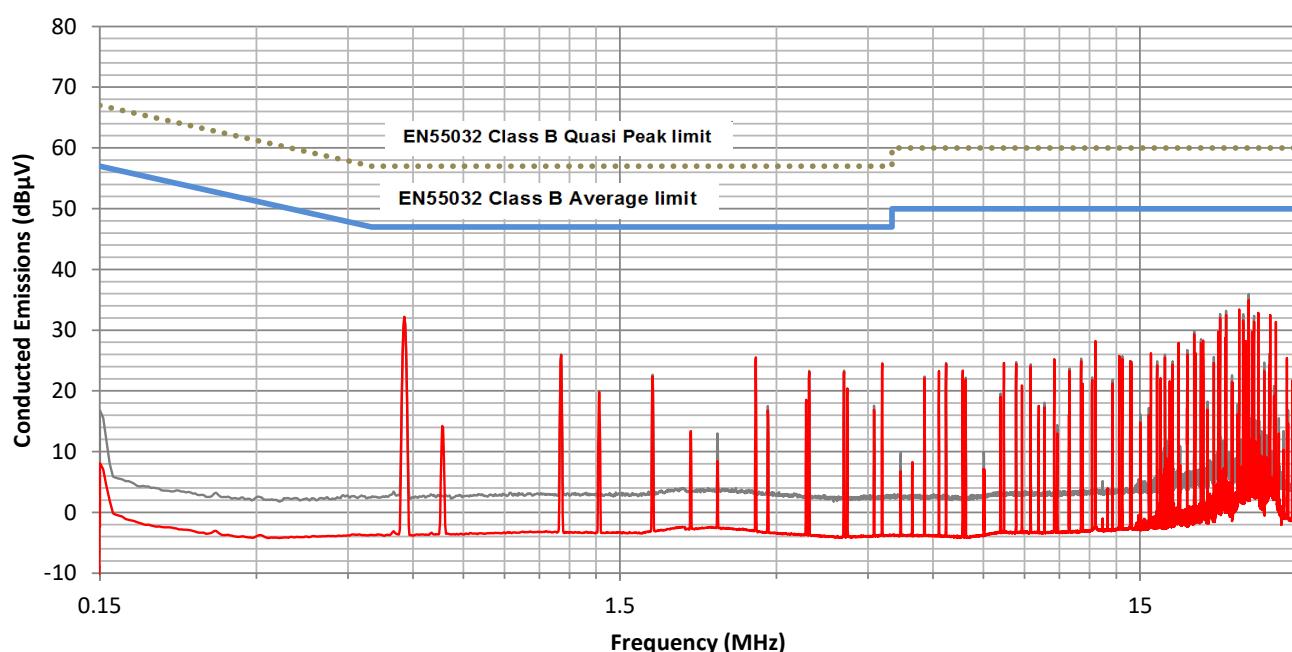
Radiated Emissions 173010535 (3m Antenna Distance)  
 $V_{IN} = 24V$ ,  $V_{OUT} = 5V$ ,  $I_{LOAD} = 1A$  with input filter  
 $C_F$  and  $C_{IN} = 4 \times 4.7\mu F$  (885012209048),  $L_F = 4.7\mu H$  (744773047)

Horizontal      Vertical



Conducted Emissions 173010535  
 $V_{IN} = 24V$ ,  $V_{OUT} = 5V$ ,  $I_{LOAD} = 1A$  with input filter  
 $C_F$  and  $C_{IN} = 4 \times 4.7\mu F$  (885012209048),  $L_F = 4.7\mu H$  (744773047)

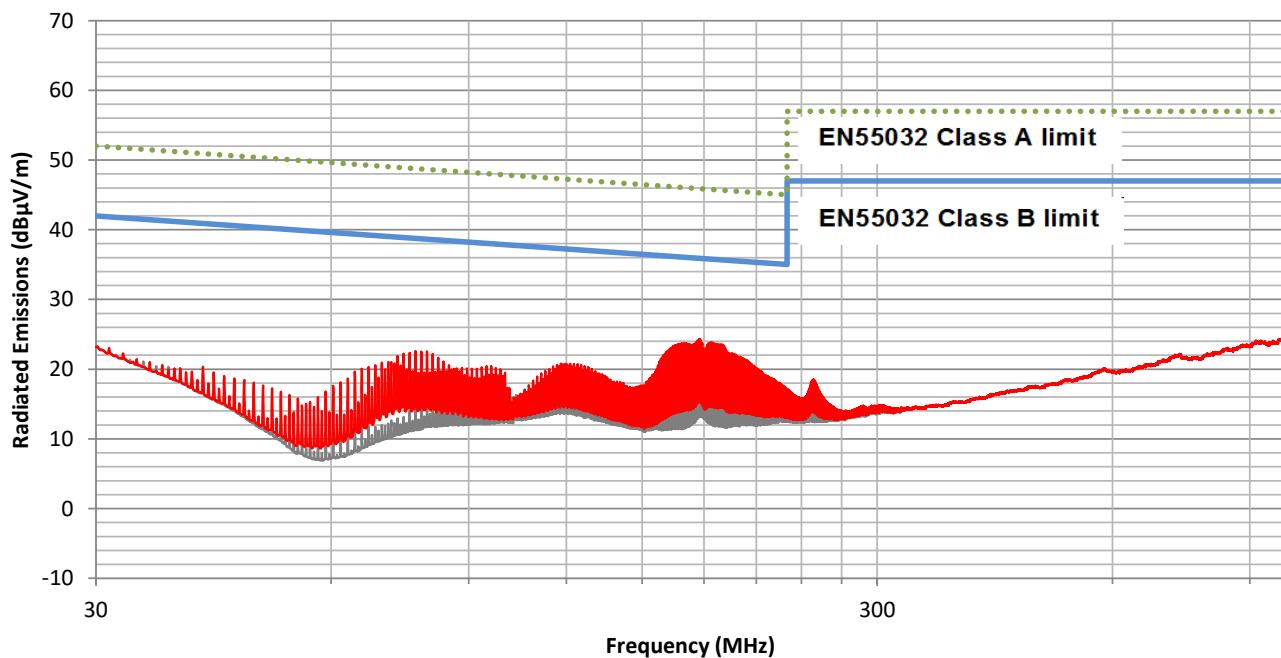
Average      Quasi peak



## RADIATED AND CONDUCTED EMISSIONS - 173011235

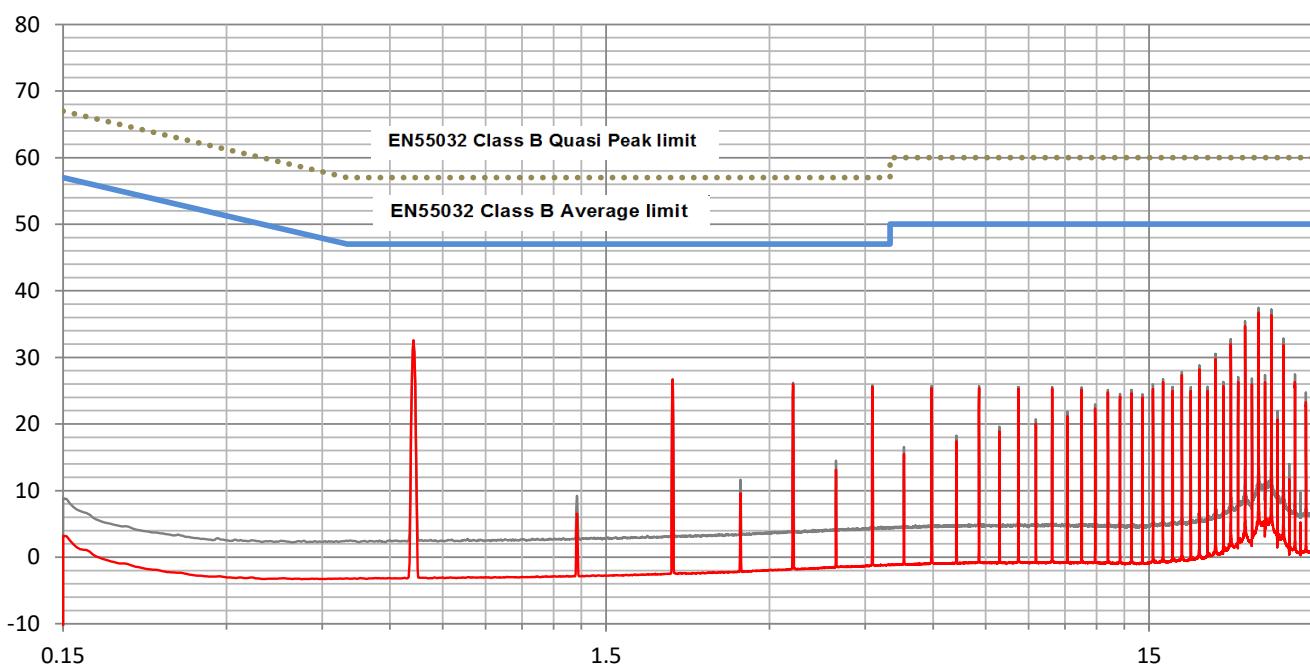
Radiated Emissions 173011235 (3m Antenna Distance)  
 $V_{IN} = 24V$ ,  $V_{OUT} = 12V$ ,  $I_{LOAD} = 1A$  with input filter  
 $C_F$  and  $C_{IN} = 4 \times 4.7\mu F$  (885012209048),  $L_F = 4.7\mu H$  (744773047)

Horizontal      Vertical



Conducted Emissions 173011235  
 $V_{IN} = 24V$ ,  $V_{OUT} = 12V$ ,  $I_{LOAD} = 1A$  with input filter  
 $C_F$  and  $C_{IN} = 4 \times 4.7\mu F$  (885012209048),  $L_F = 4.7\mu H$  (744773047)

Average      Quasi peak

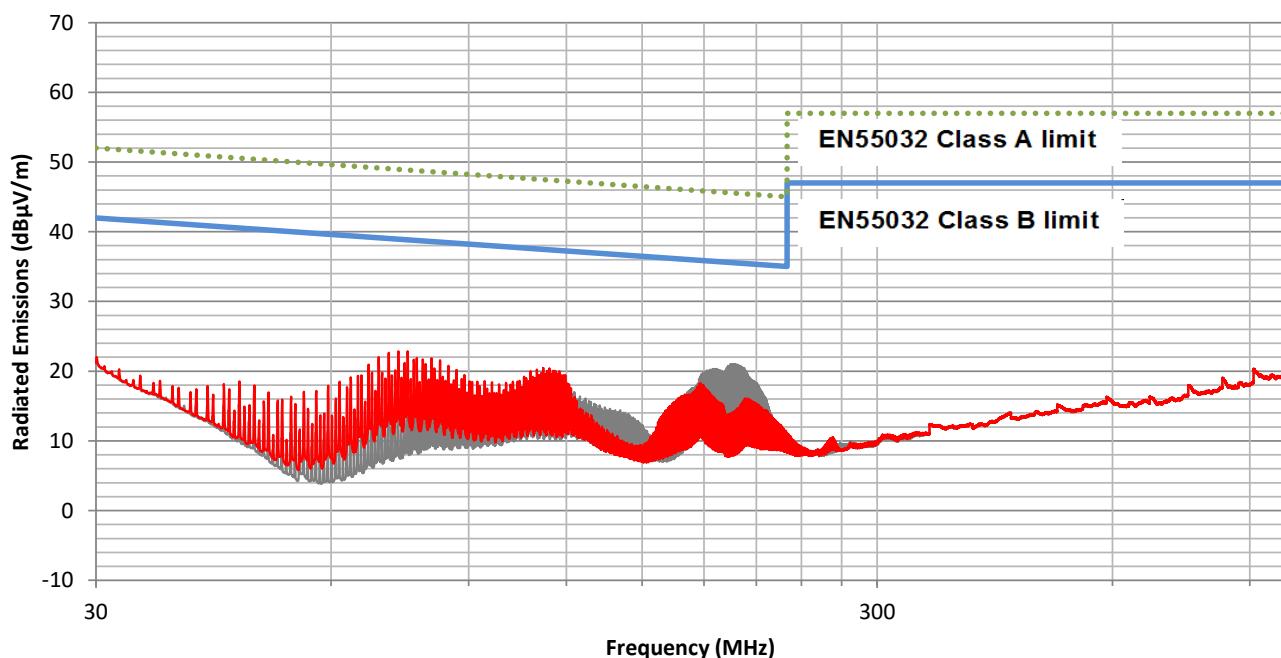


## RADIATED AND CONDUCTED EMISSIONS - 173011535

## Radiated Emissions 173011535 (3m Antenna Distance)

 $V_{IN} = 24V$ ,  $V_{OUT} = 15V$ ,  $I_{LOAD} = 1A$  with input filter $C_F$  and  $C_{IN} = 4 \times 4.7\mu F$  (885012209048),  $L_F = 4.7\mu H$  (744773047)

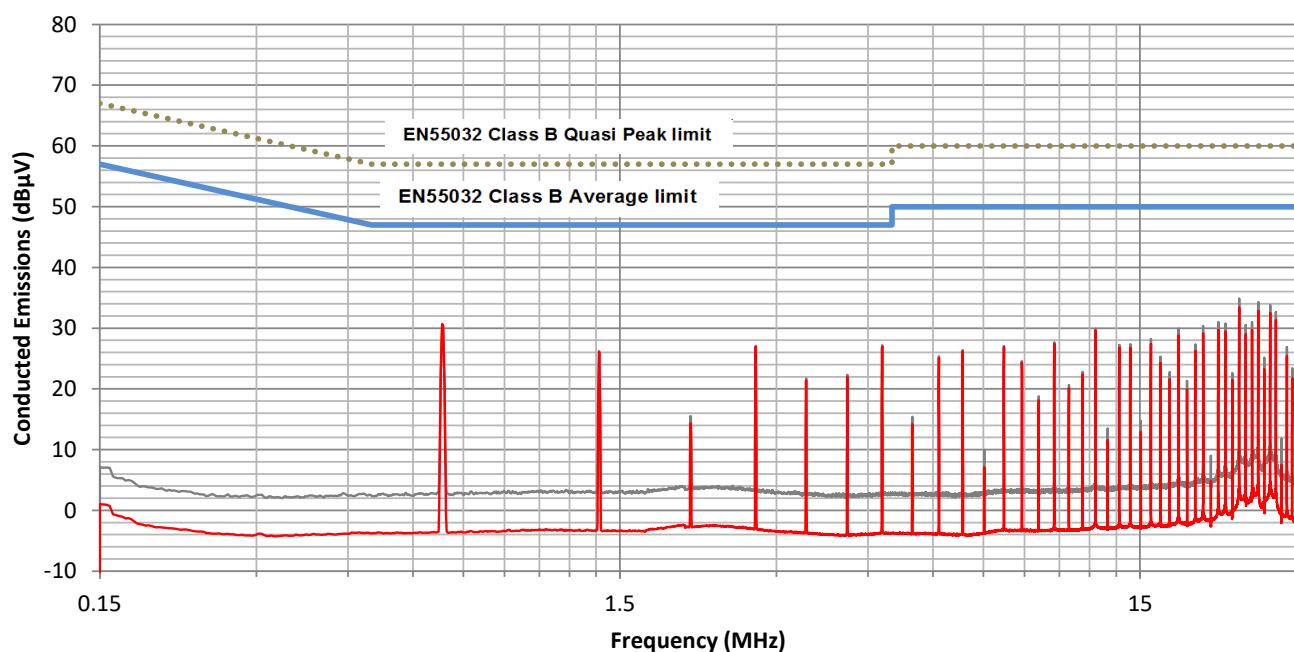
Horizontal      Vertical

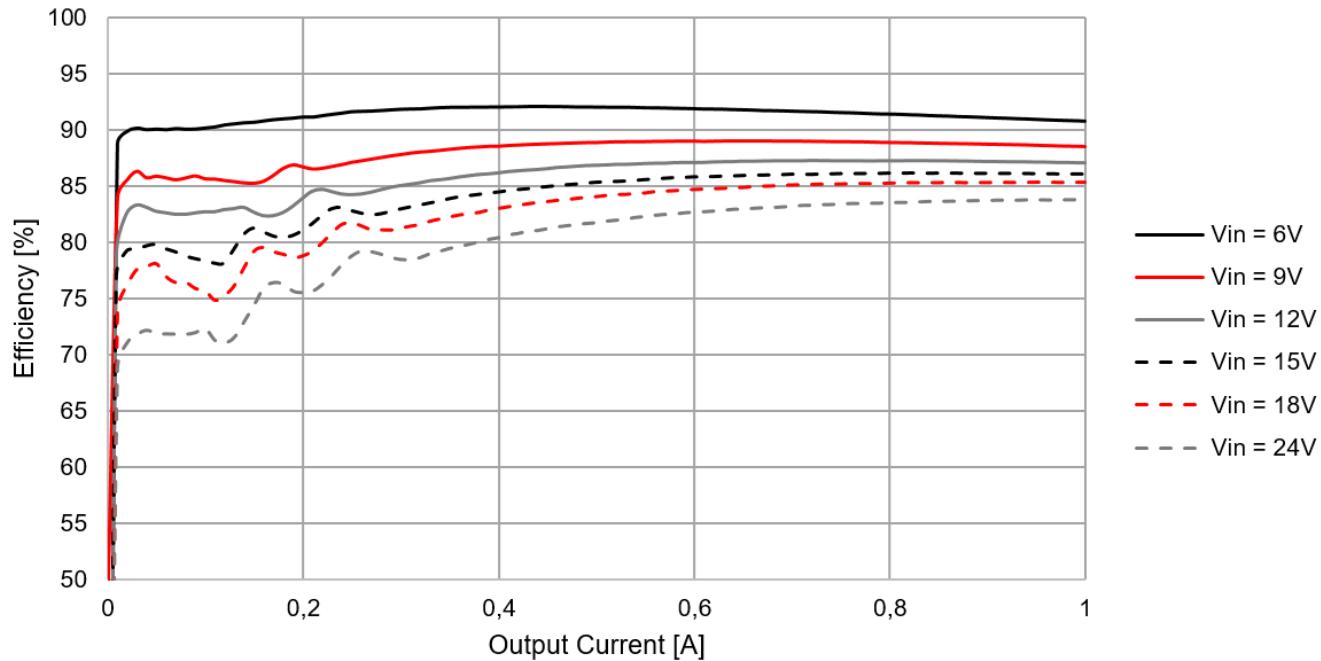
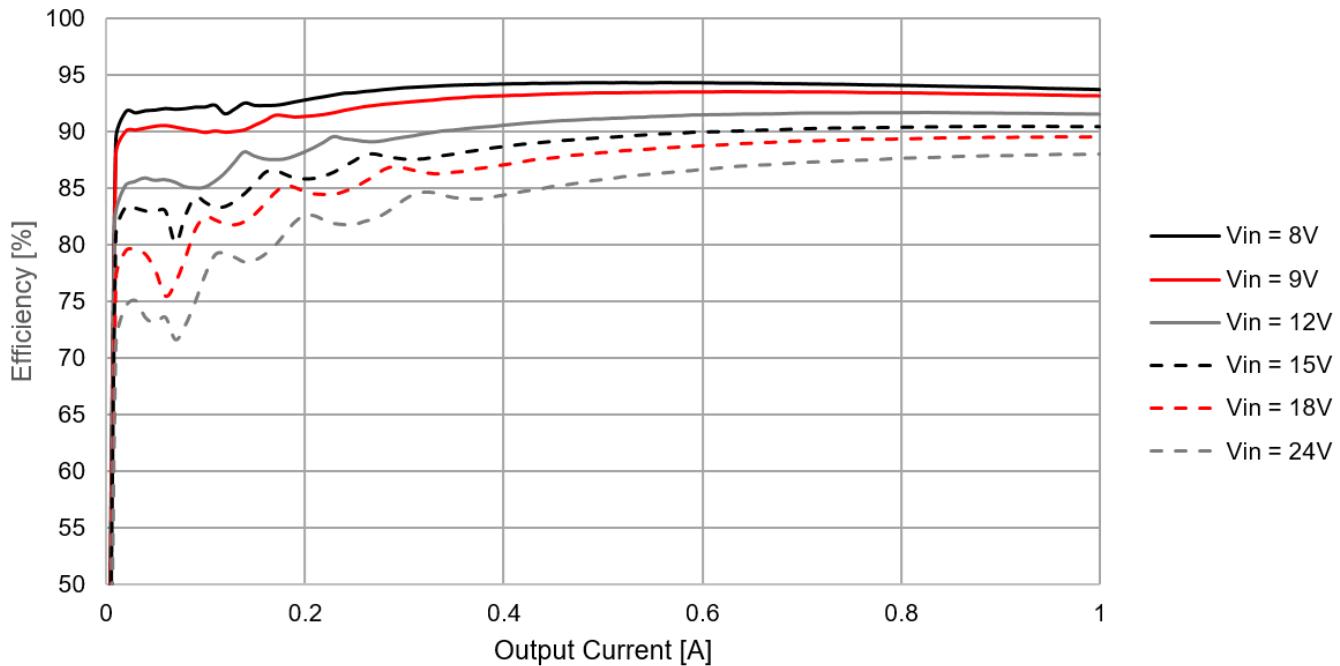


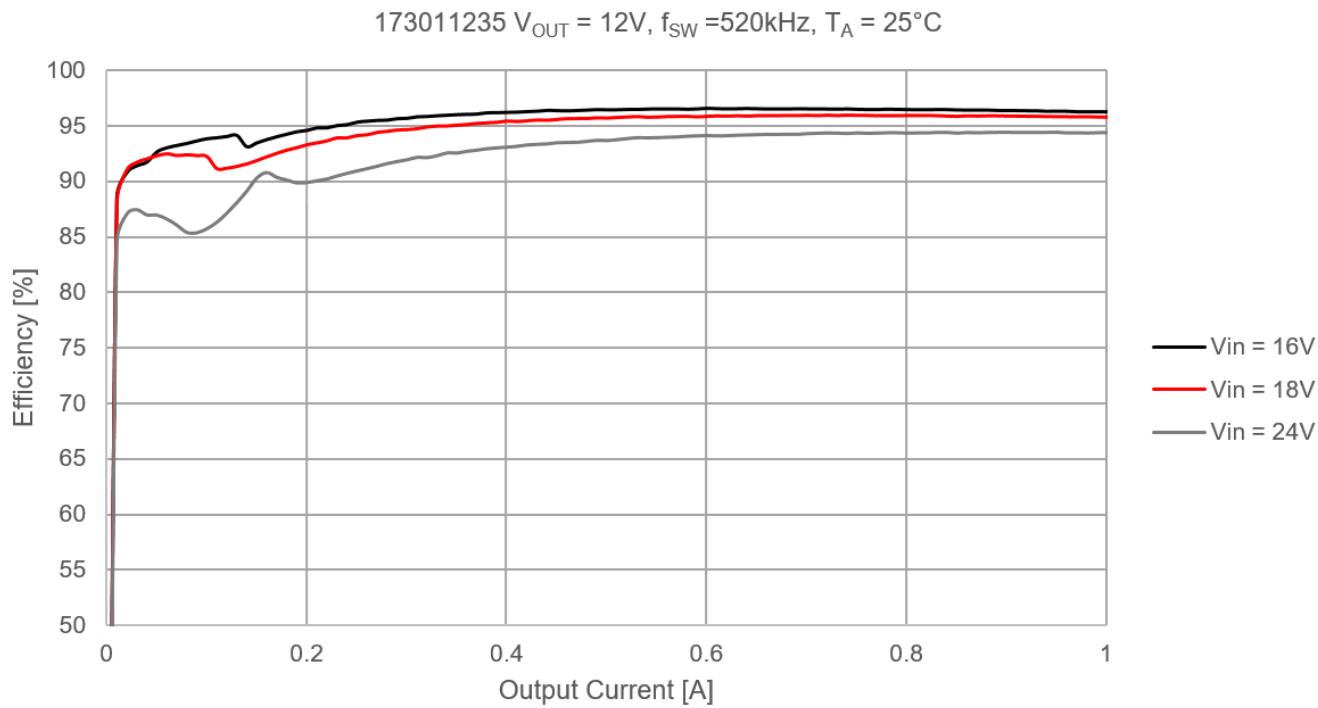
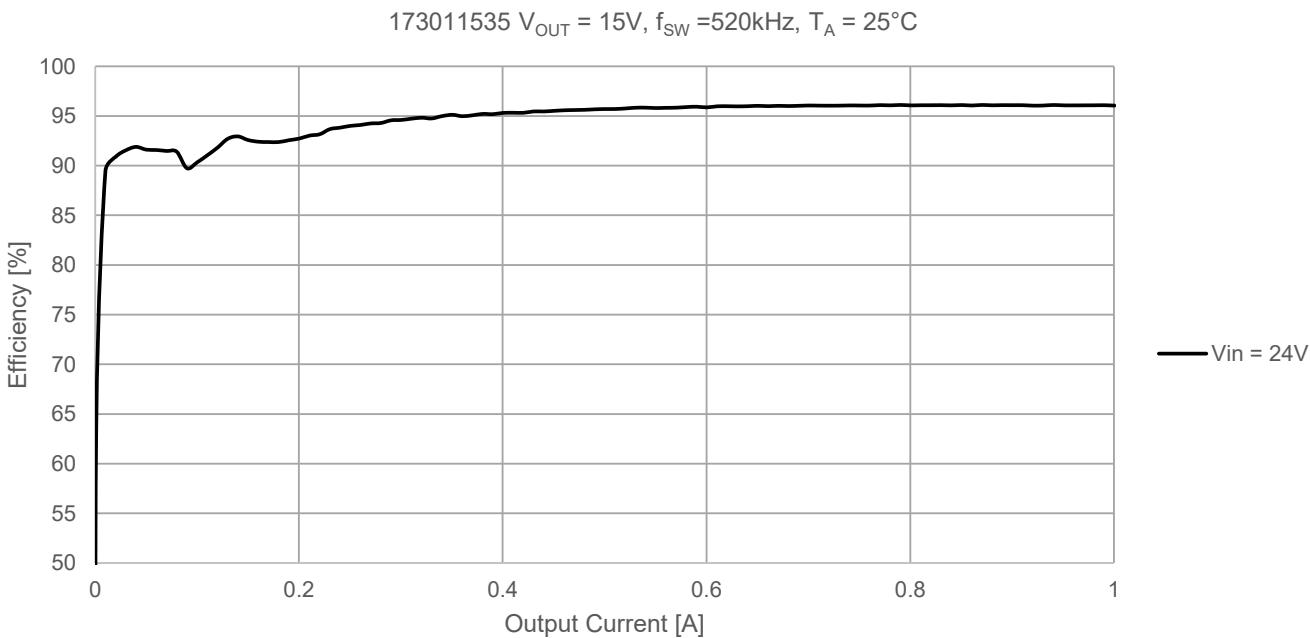
## Conducted Emissions 173011535

 $V_{IN} = 24V$ ,  $V_{OUT} = 15V$ ,  $I_{LOAD} = 1A$  with input filter $C_F$  and  $C_{IN} = 4 \times 4.7\mu F$  (885012209048),  $L_F = 4.7\mu H$  (744773047)

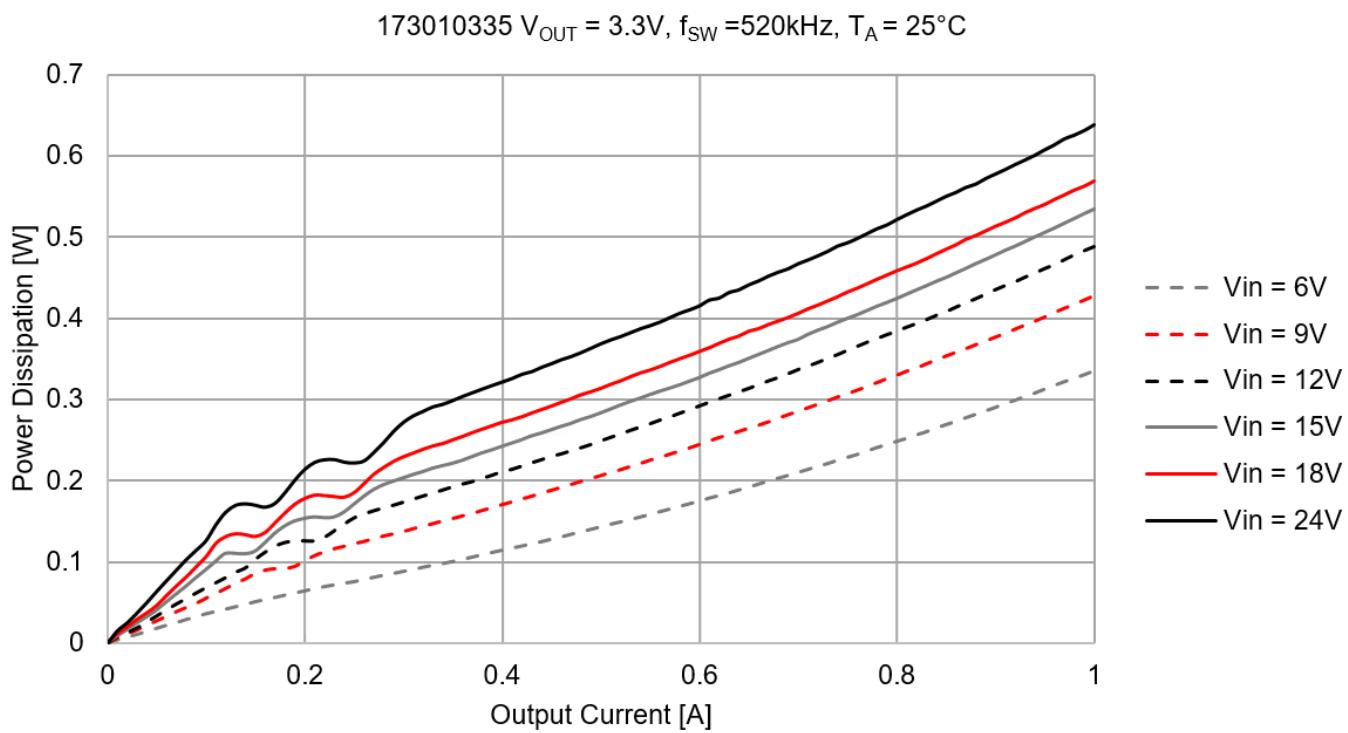
Average      Quasi peak



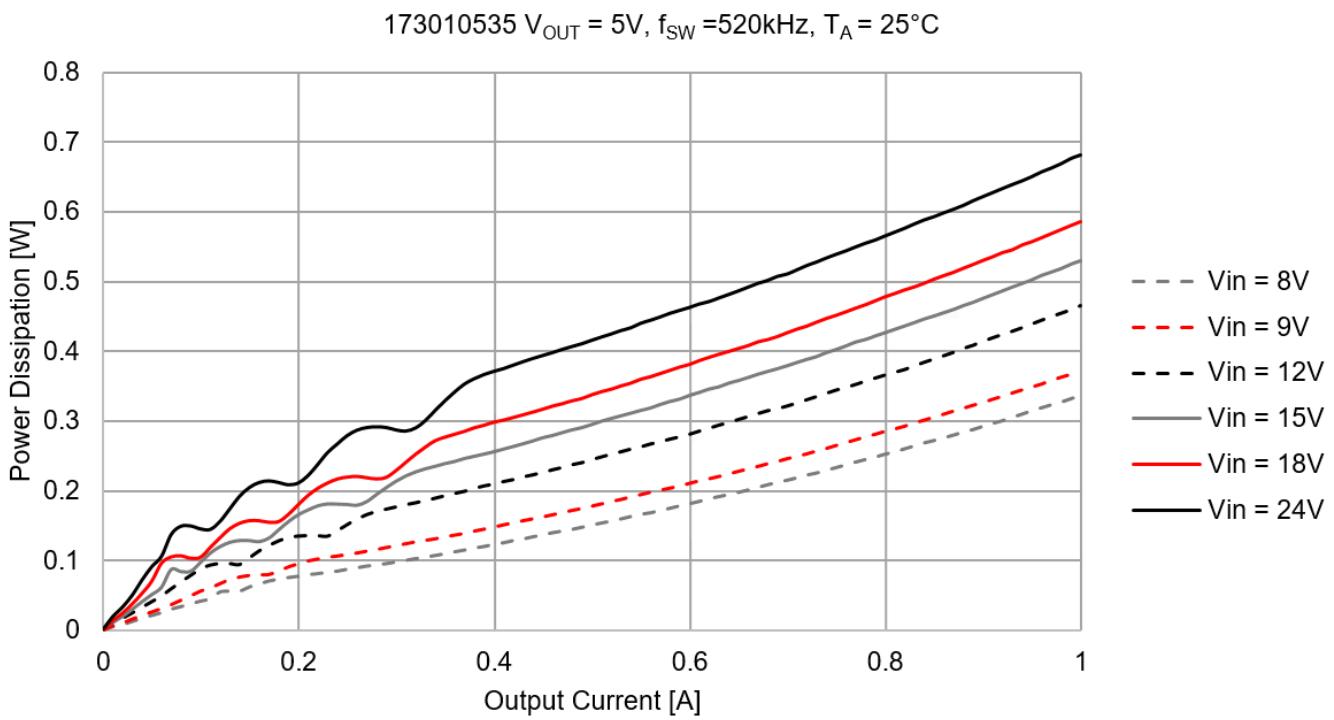
**EFFICIENCY - 173010335**173010335  $V_{OUT} = 3.3V$ ,  $f_{SW} = 520\text{kHz}$ ,  $T_A = 25^\circ\text{C}$ **EFFICIENCY - 173010535**173010535  $V_{OUT} = 5V$ ,  $f_{SW} = 520\text{kHz}$ ,  $T_A = 25^\circ\text{C}$ 

**EFFICIENCY - 173011235****EFFICIENCY - 173011535**

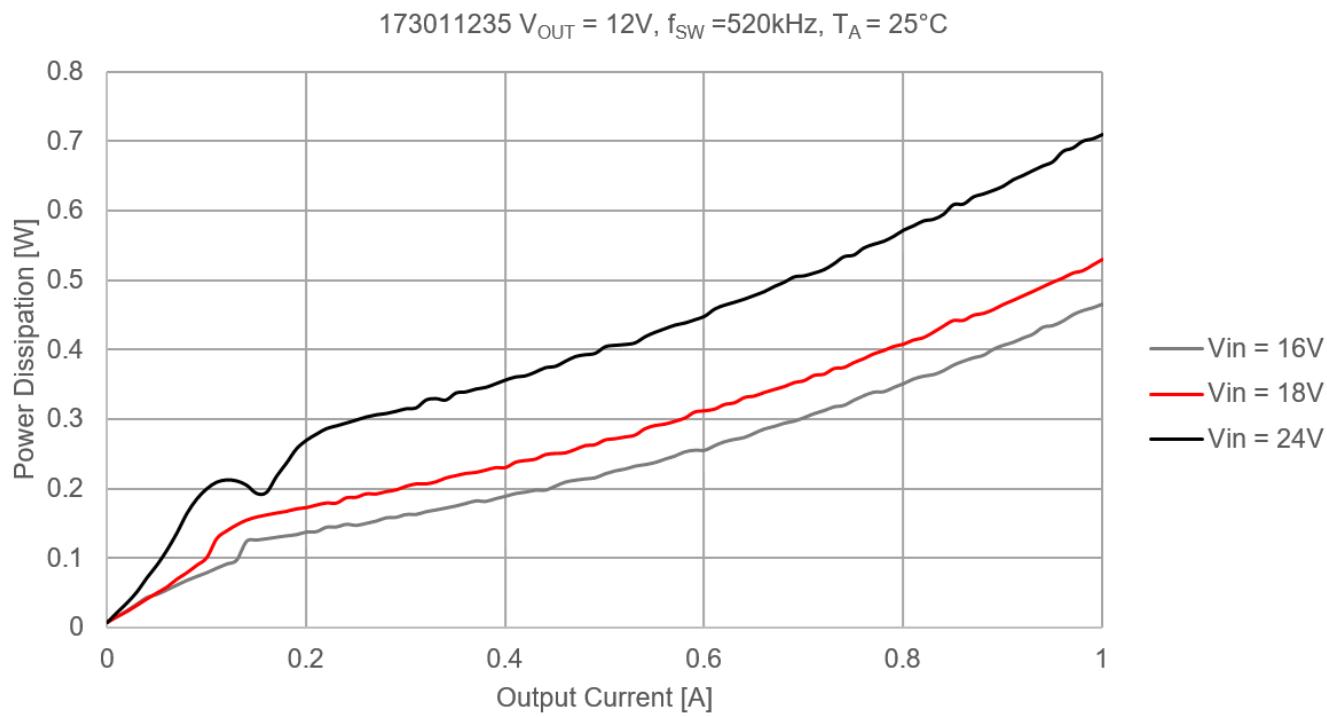
## POWER DISSIPATION - 173010335



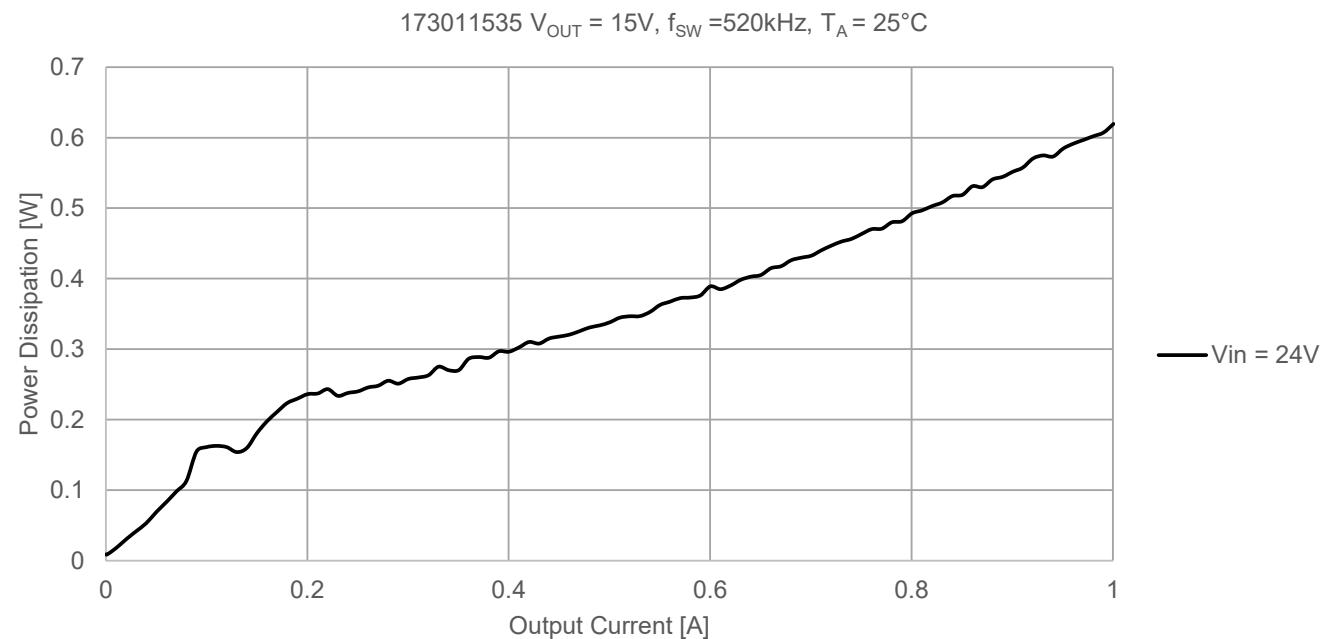
## POWER DISSIPATION - 173010535

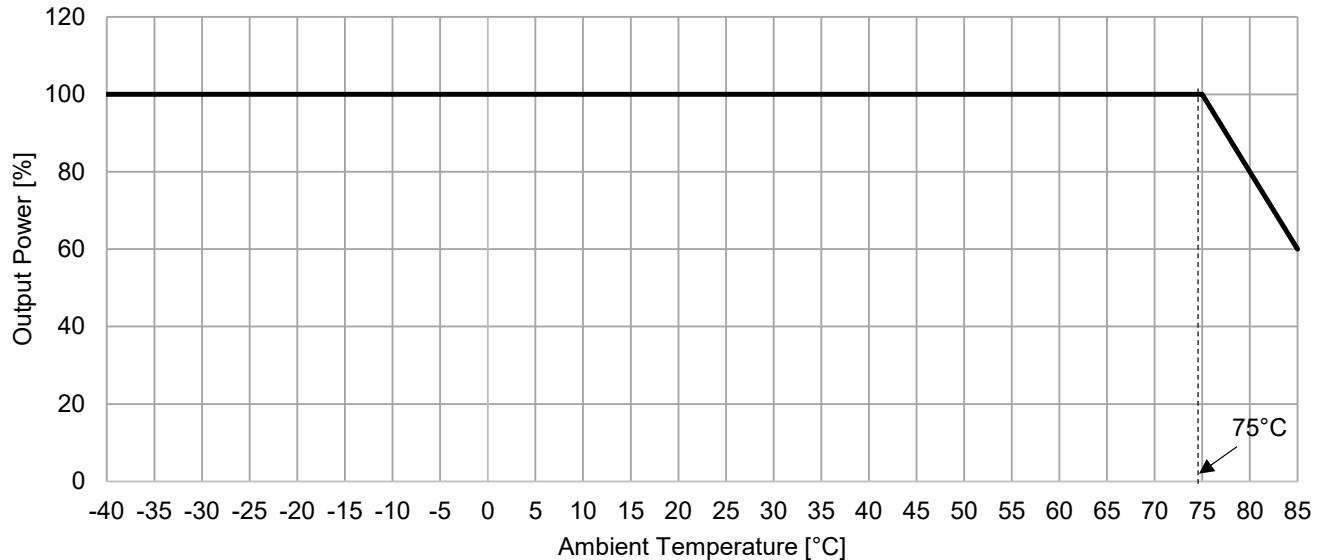
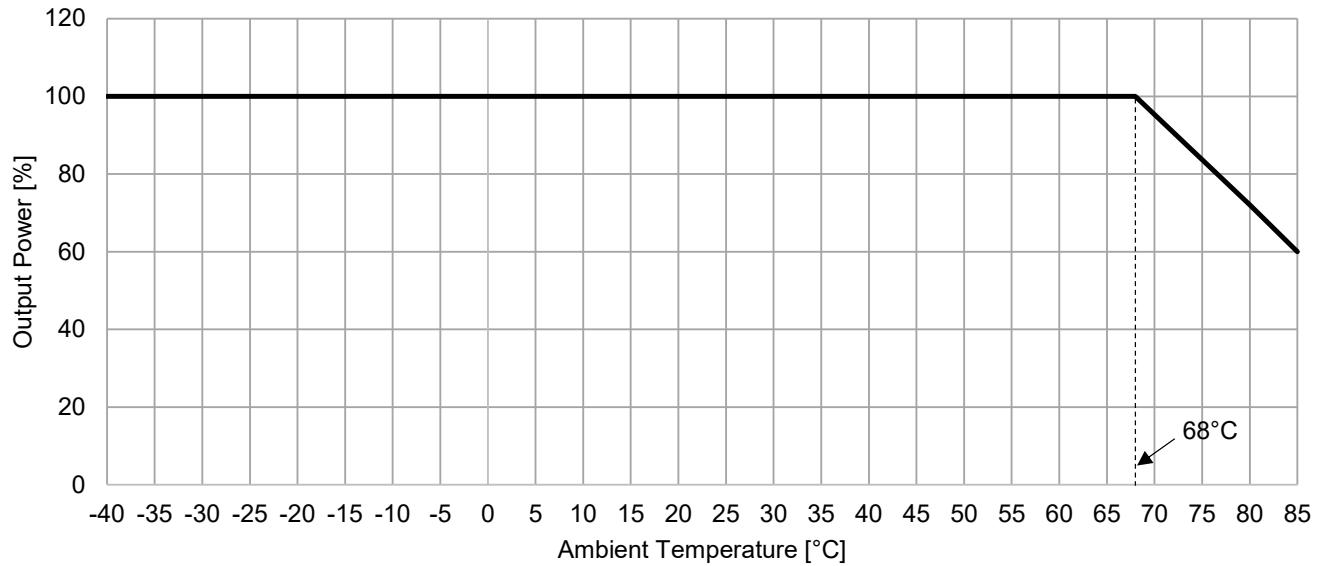


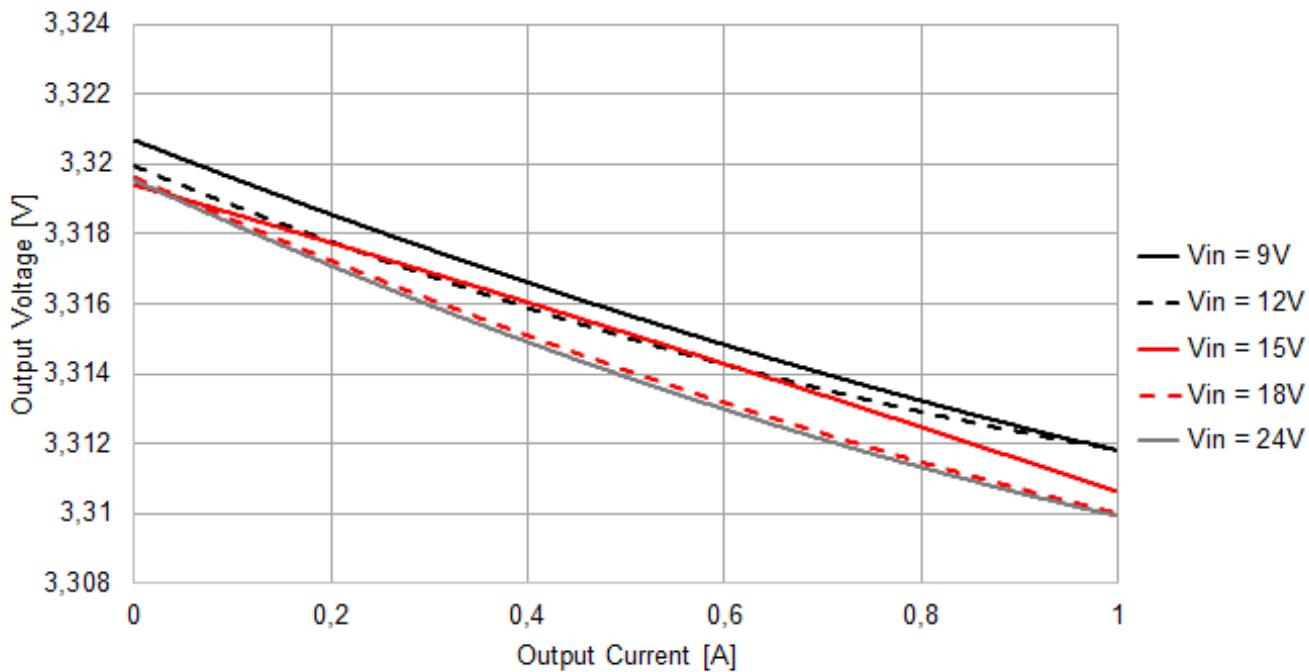
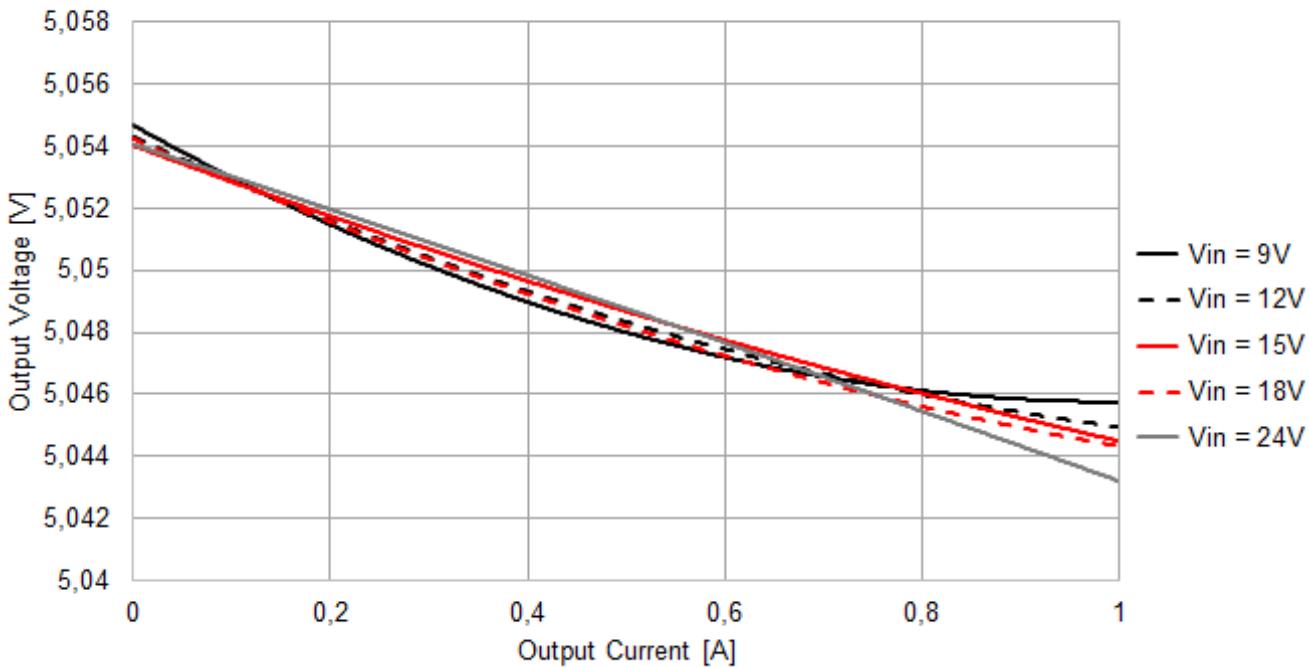
## POWER DISSIPATION - 173011235



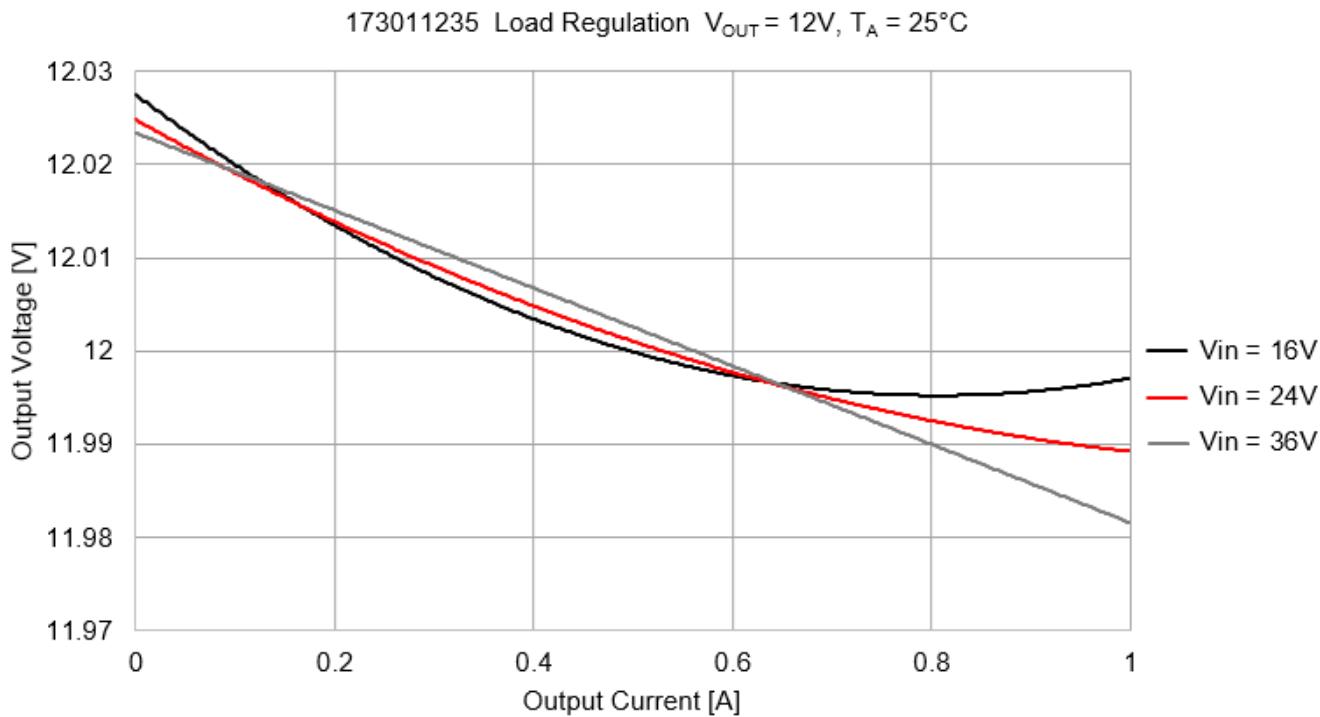
## POWER DISSIPATION - 173011535



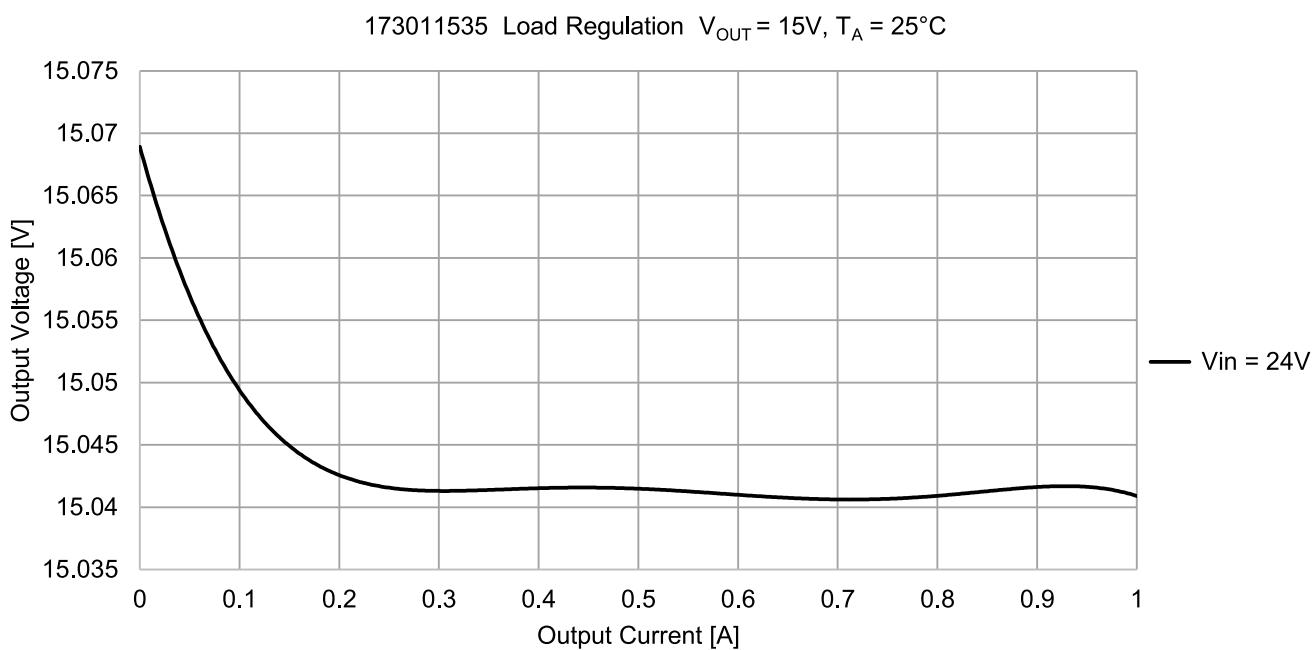
**THERMAL DERATING - 173010325, 173010535**
 173010335, 173010535 Output Power Thermal Derating  
 $V_{IN} = 24V, I_{OUT} = 1A$ 
**THERMAL DERATING - 173011225, 173011535**
 173011235, 173011535 Output Power Thermal Derating  
 $V_{IN} = 24V, I_{OUT} = 1A$ 


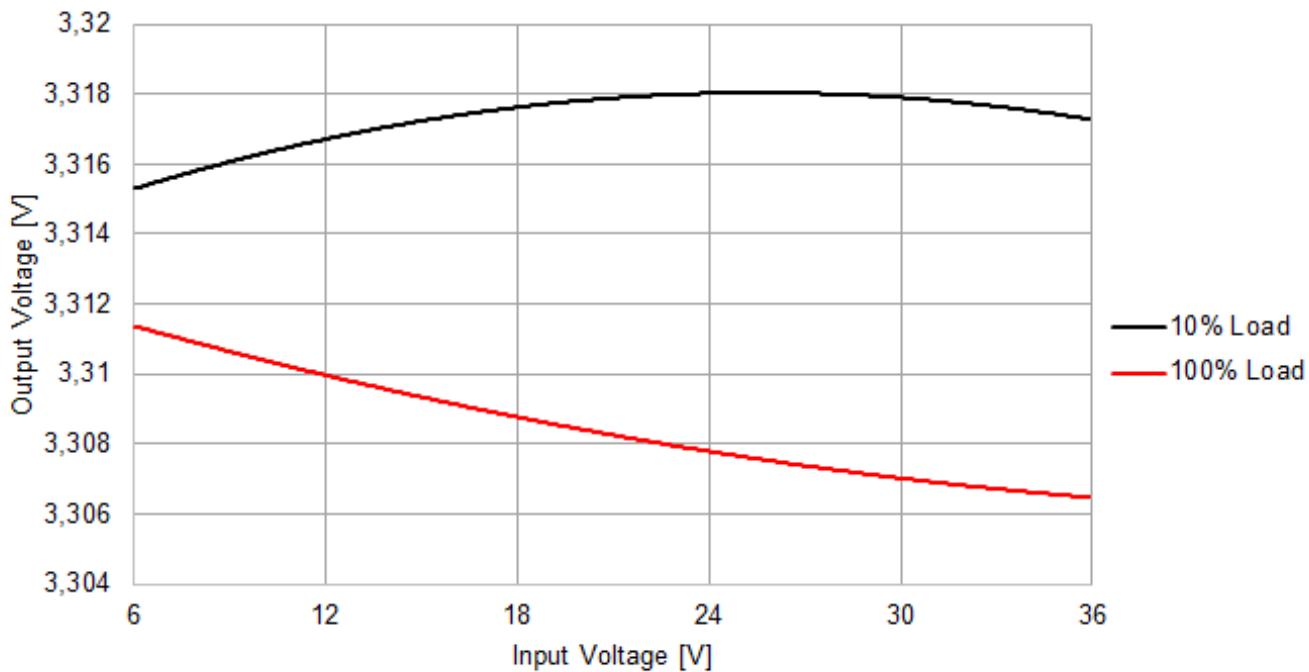
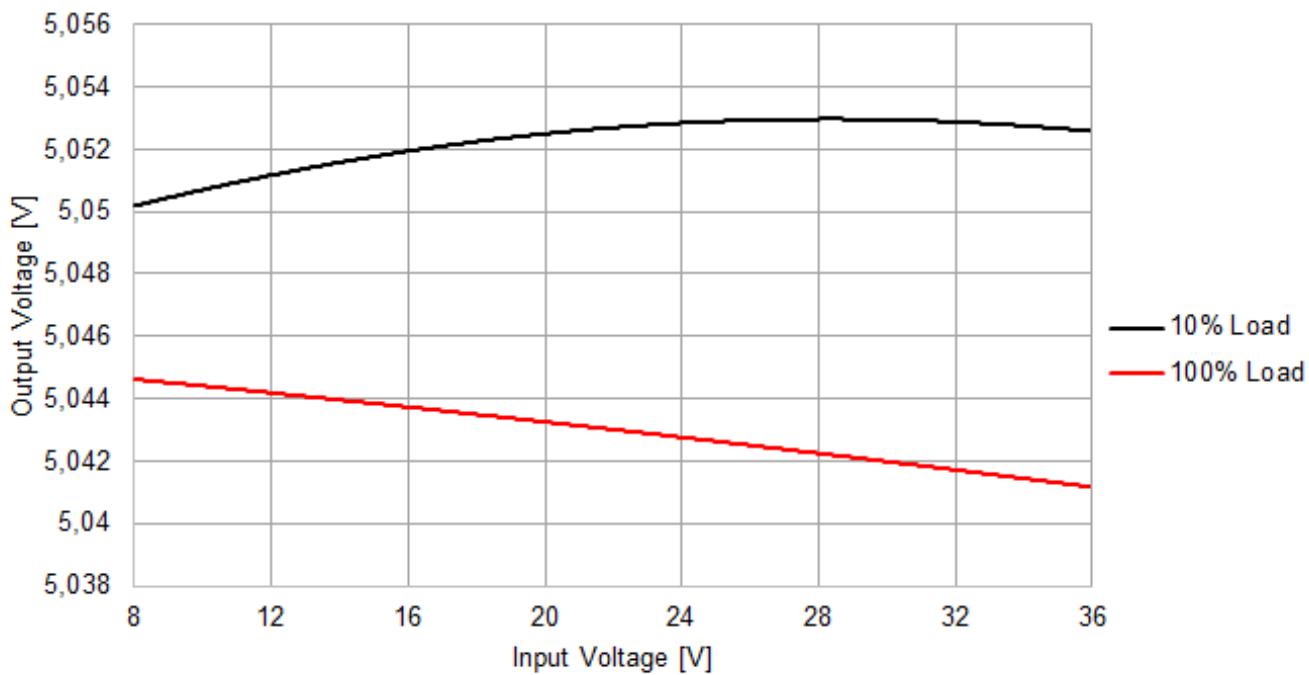
**LOAD REGULATION - 173010335**173010335 Load Regulation  $V_{OUT} = 3.3V$ ,  $T_A = 25^\circ C$ **LOAD REGULATION - 173010535**173010535 Load Regulation  $V_{OUT} = 5V$ ,  $T_A = 25^\circ C$ 

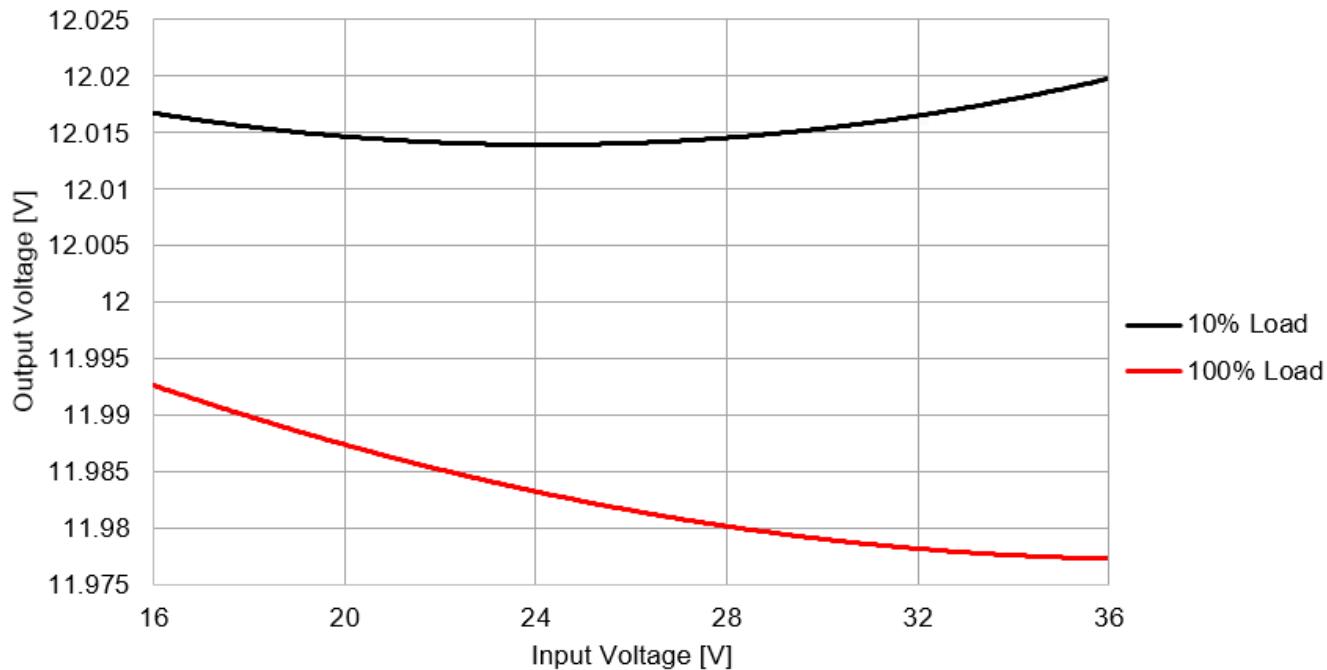
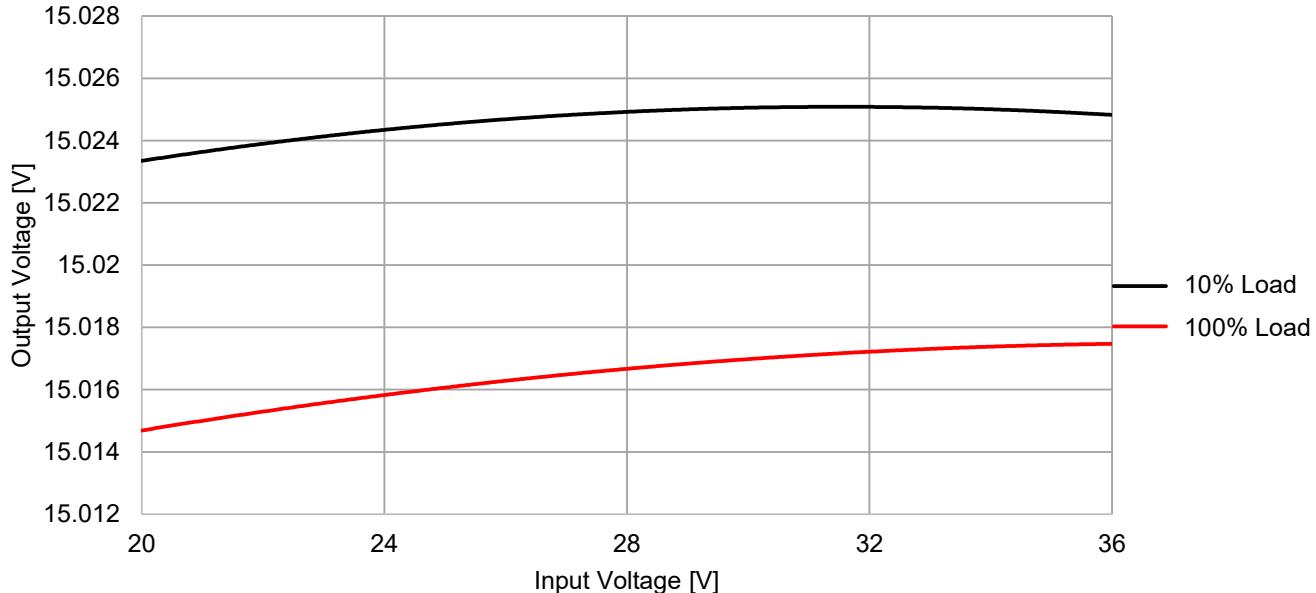
## LOAD REGULATION - 173011235

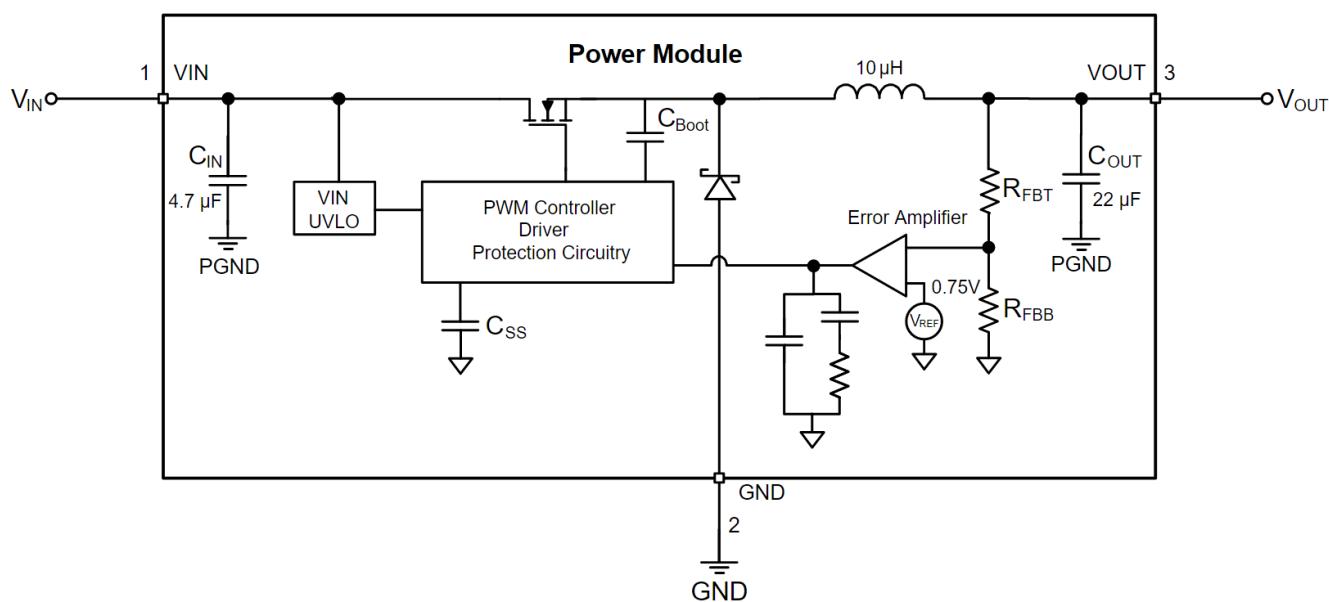


## LOAD REGULATION - 173011535



**LINE REGULATION - 173010335**173010335 Line Regulation  $V_{OUT} = 3.3V$ ,  $T_A = 25^\circ C$ **LINE REGULATION - 173010535**173010535 Line Regulation  $V_{OUT} = 5V$ ,  $T_A = 25^\circ C$ 

**LINE REGULATION - 173011235**173011235 Line Regulation  $V_{OUT} = 12V$ ,  $T_A = 25^\circ C$ **LINE REGULATION - 173011535**173011535 Line Regulation  $V_{OUT} = 15V$ ,  $T_A = 25^\circ C$ 

**BLOCK DIAGRAM****CIRCUIT DESCRIPTION**

The MagI<sup>3</sup>C power modules 173010335, 173010535, 173011235 and 173011535 are all based on a non-synchronous step-down regulator with integrated MOSFET, free-wheeling diode, power inductor, input and output capacitors. The control scheme is based on a current mode (CM) regulation loop.

The  $V_{OUT}$  of the regulator is divided with the internal feedback resistor network and fed into the error amplifier, which compares this signal with the internal 0.75V reference. The error amplifier controls the on-time of a fixed frequency pulse width generator, which drives the MOSFET.

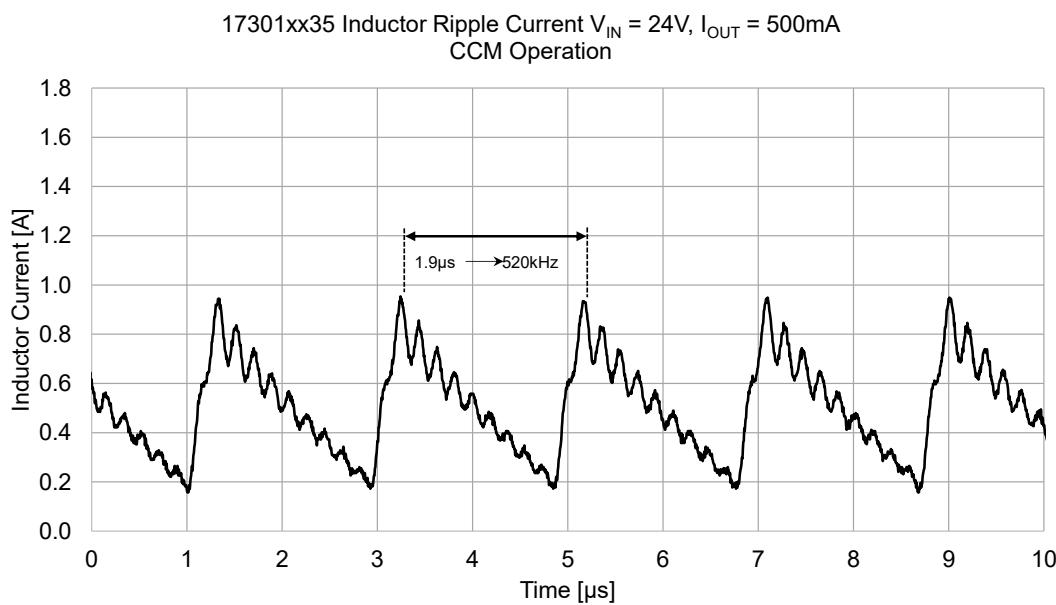
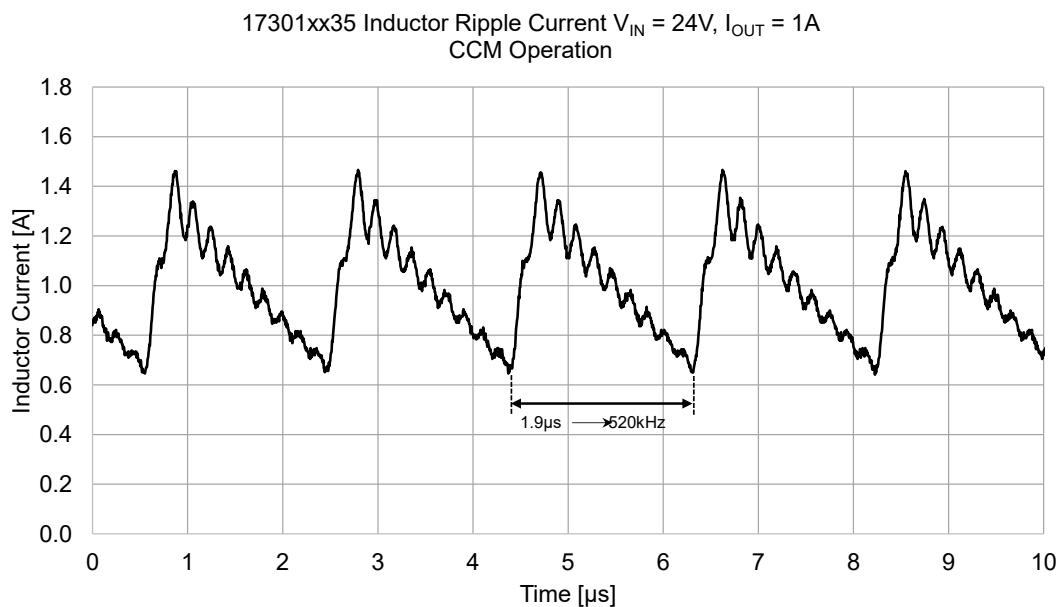
The current mode architecture features a constant frequency during load steps. Only the on-time is modulated. It is internally compensated and stable with low ESR output capacitors. No external compensation network is required. This architecture supports fast transient response and very small output voltage ripple values ( $<50\text{mV}_{\text{pp}}$ ) are achieved.

## LIGHT LOAD OPERATION

Under light load operation, the device switches from PWM mode to PFM mode. The load current where the transition between modes takes place can be estimated using the following formula:

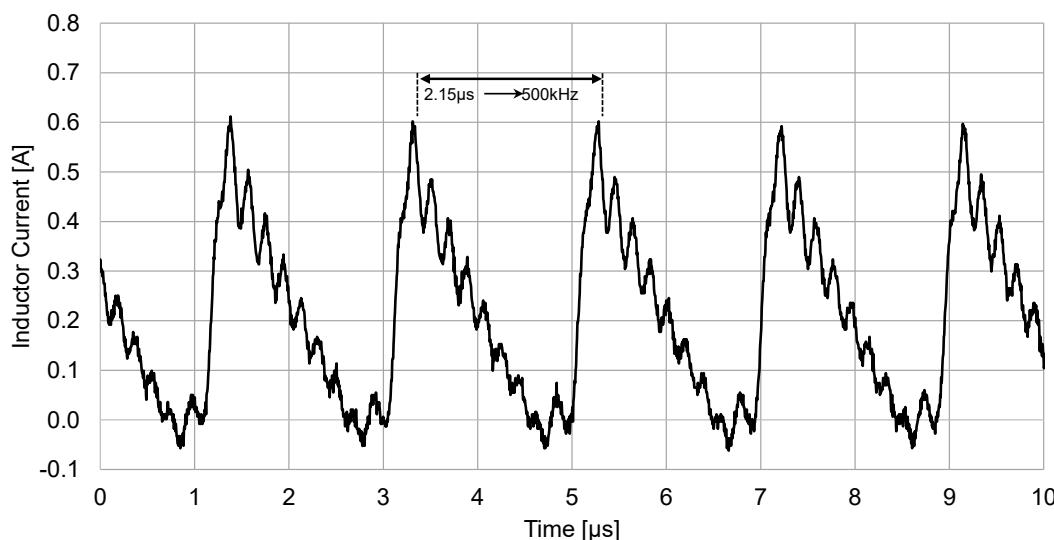
$$I_{\text{OUT}(\text{DCM})} = \frac{V_{\text{OUT}} \cdot (1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}})}{2 \cdot f_{\text{SW}} \cdot L} \quad (1)$$

The following figures show the device working in PWM mode with continuous conduction operation (CCM).

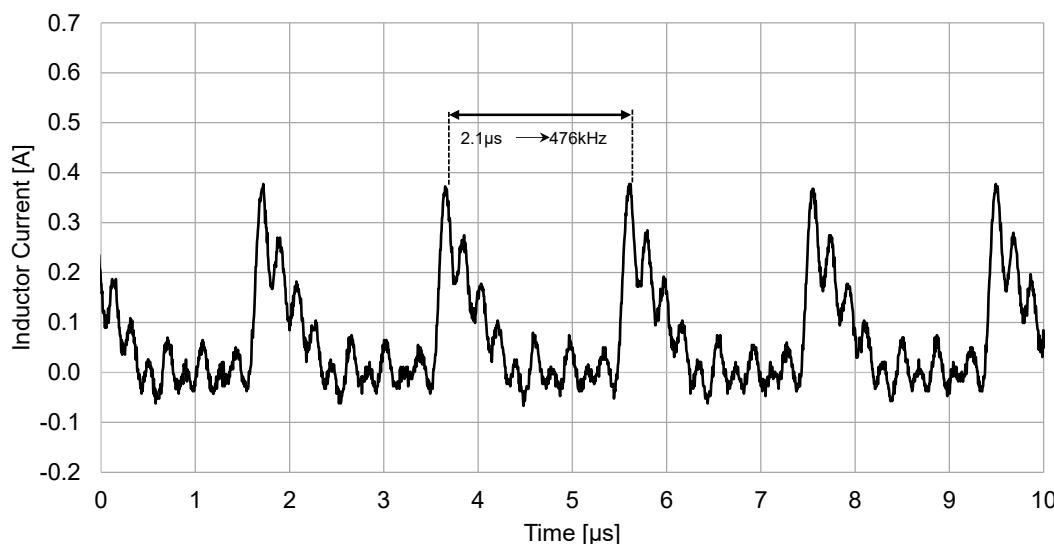


If the load current is further reduced, the device decreases the switching frequency in order to limit the energy transferred to the output (to both the output capacitor and load) maintaining regulation of the output voltage. The frequency reduction is shown in the figure below during PFM mode, with discontinuous operation (DCM).

17301xx35 Inductor Ripple Current  $V_{IN} = 24V$ ,  $I_{OUT} = 200mA$   
 DCM Operation

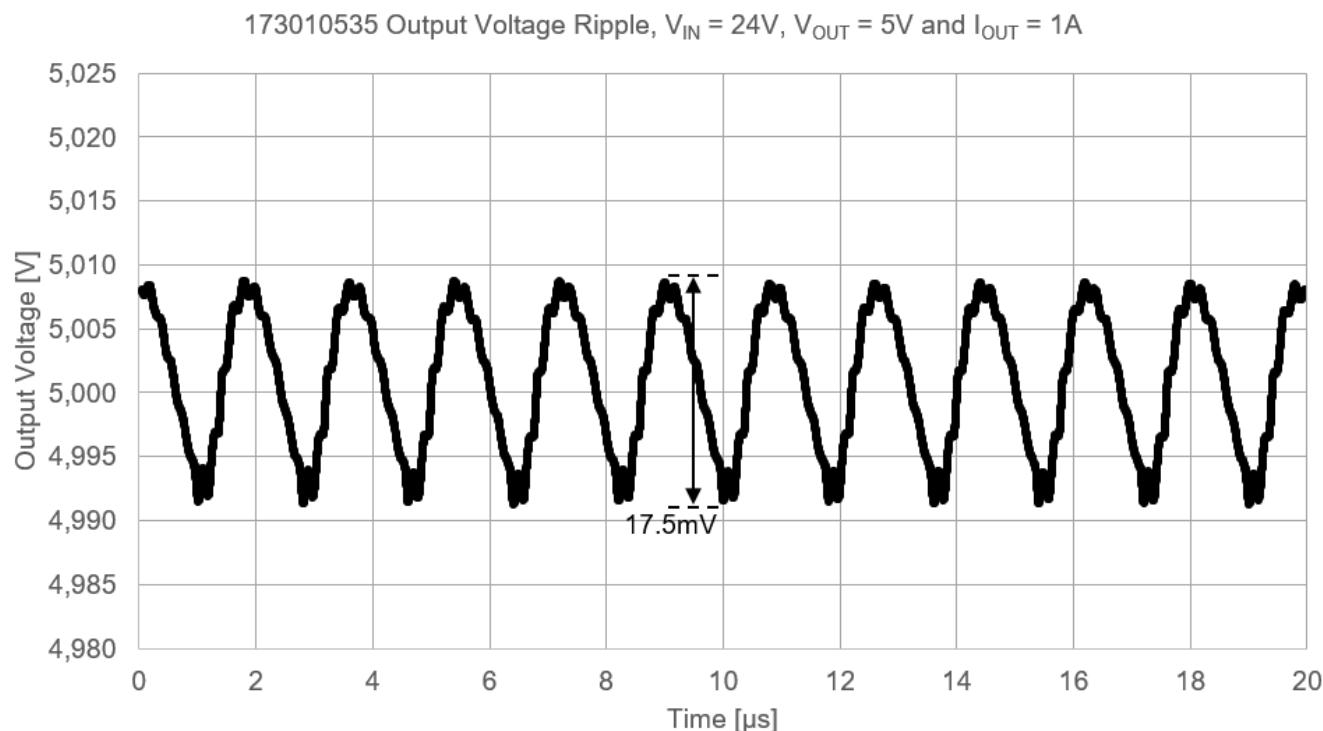
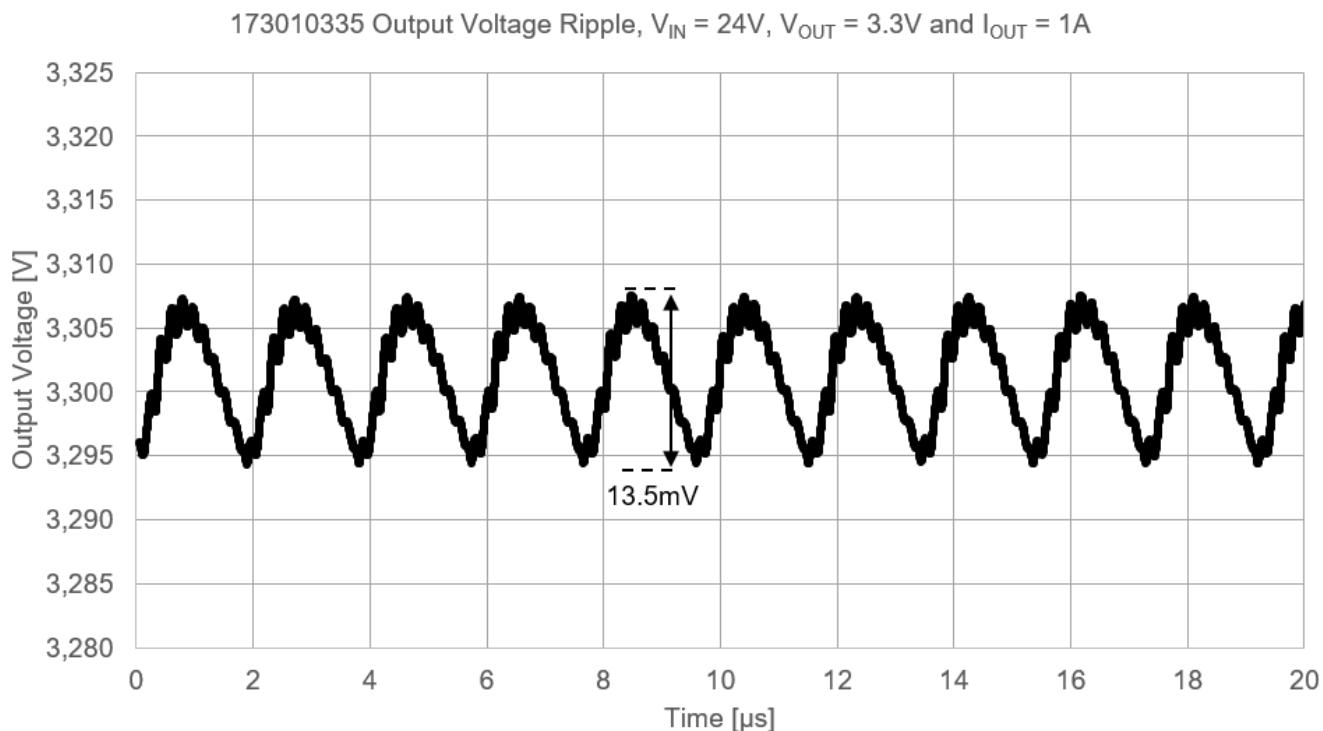


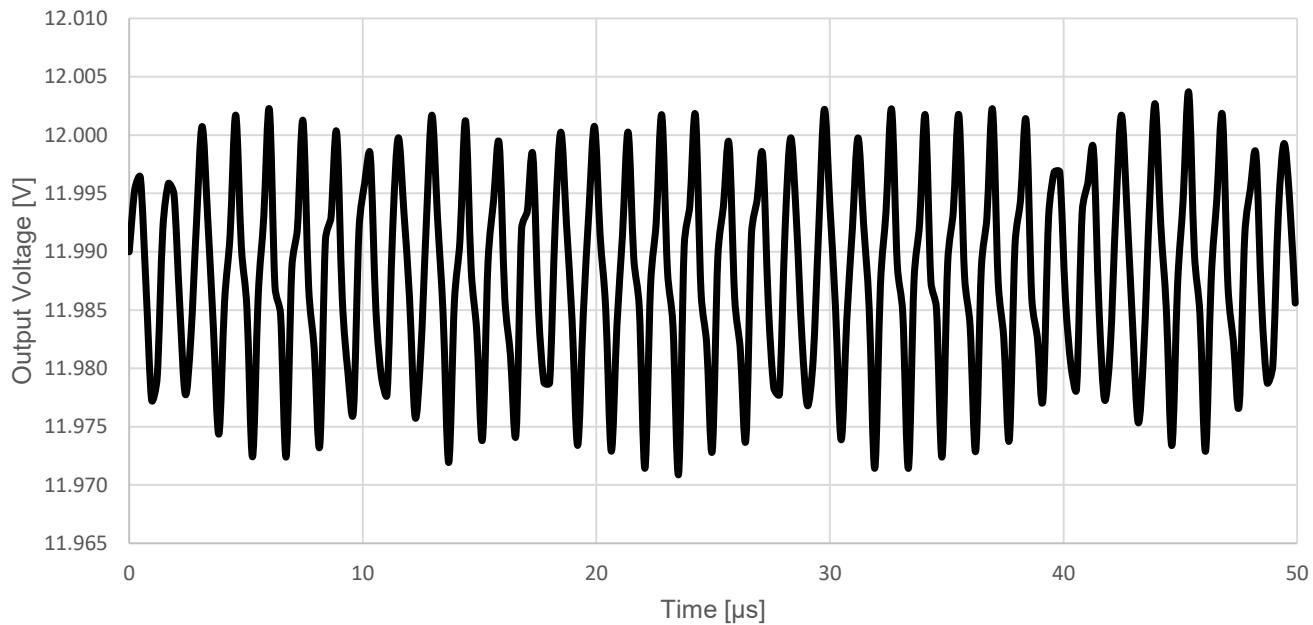
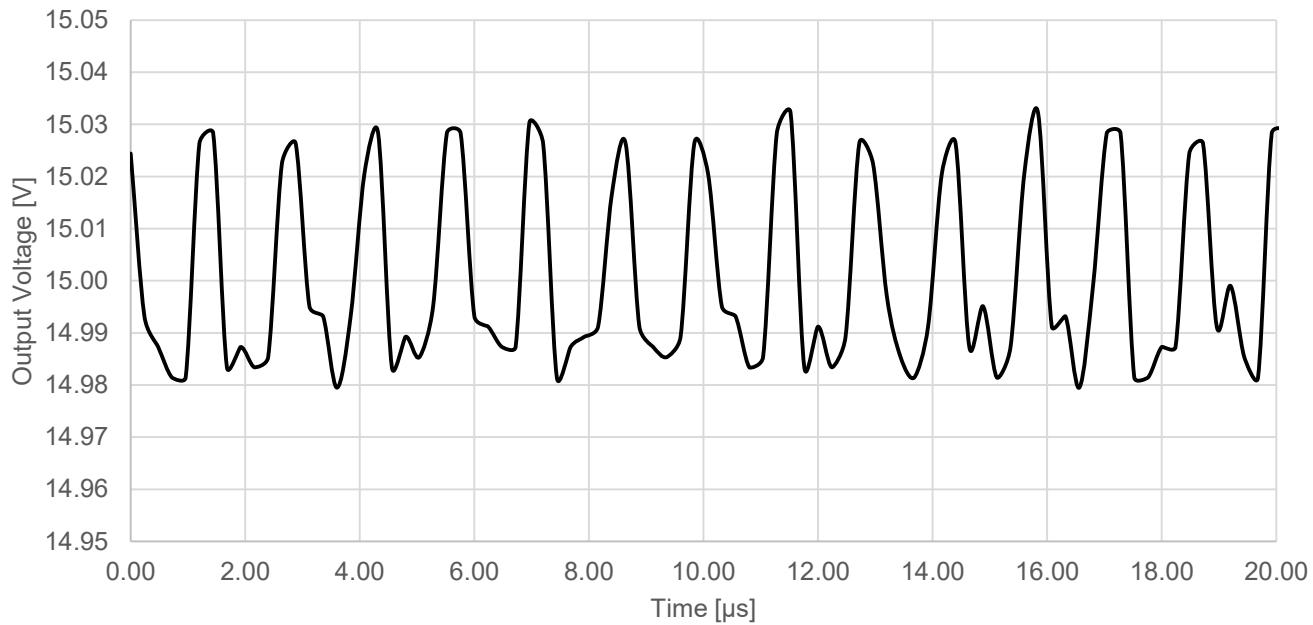
17301xx35 Inductor Ripple Current  $V_{IN} = 24V$ ,  $I_{OUT} = 50mA$   
 DCM Operation



## OUTPUT VOLTAGE RIPPLE

The output voltage ripple depends on several parameters. The figure below shows the  $V_{OUT}$  ripple at full load using a  $22\mu F$  MLCC output capacitor. An output voltage ripple of less than  $50mV_{pp}$  is measured under the conditions indicated.

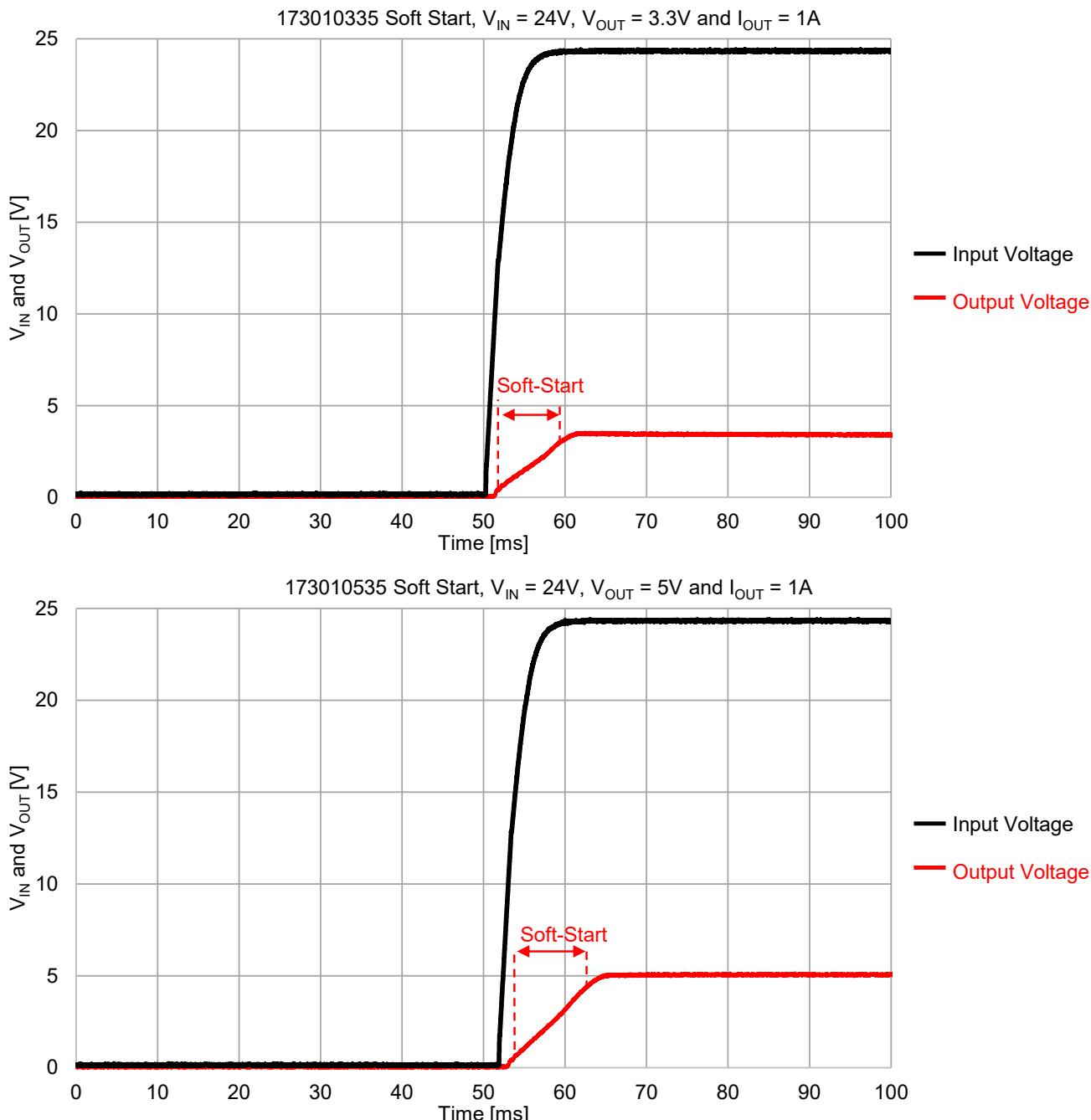


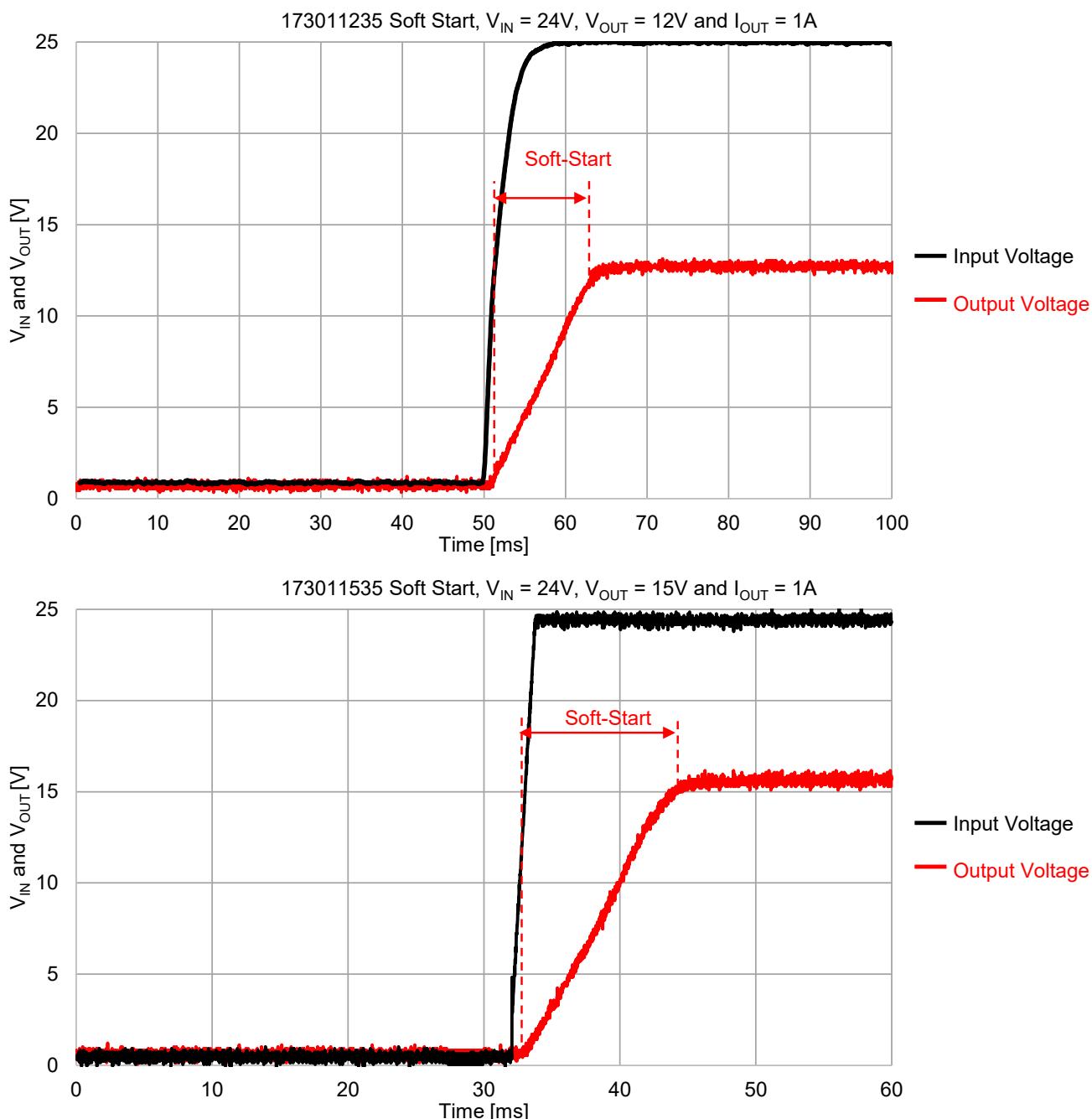
173011235 Output Voltage Ripple,  $V_{IN} = 24V$ ,  $V_{OUT} = 12V$  and  $I_{OUT} = 1A$ 173011535 Output Voltage Ripple,  $Vin = 24V$ ,  $Vout = 15V$  and  $Iout = 1A$ 

## PROTECTION FEATURES

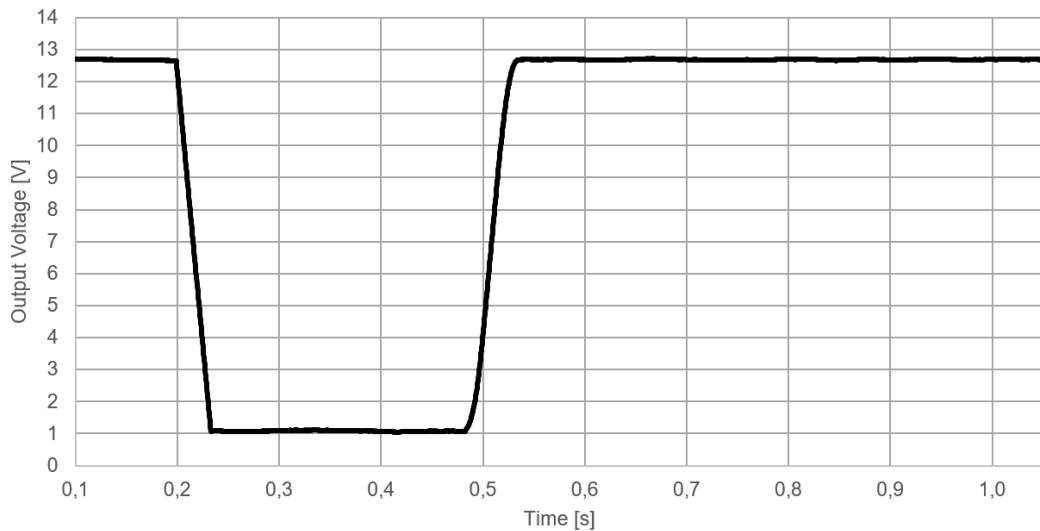
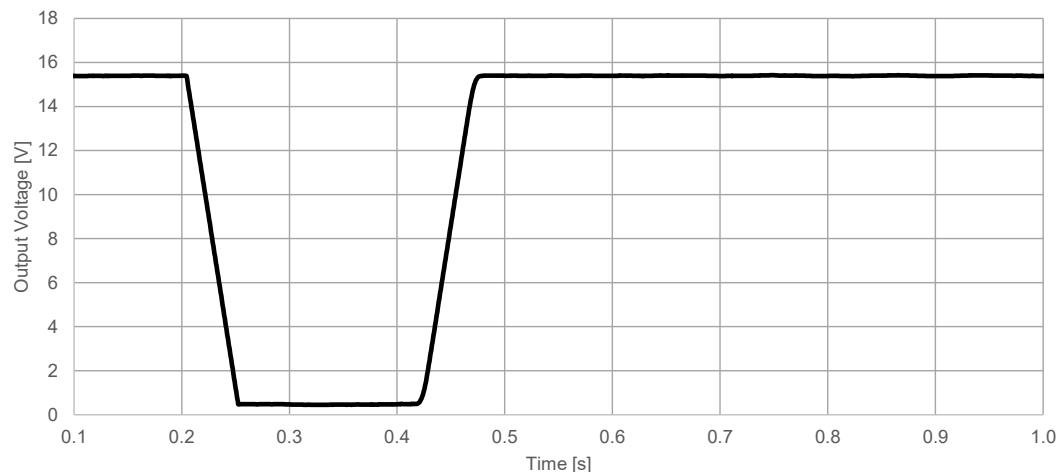
### Soft-Start

In order to prevent the output voltage from overshooting during start-up, a soft-start is internally set. The figures below show the start-up behavior of the power module with the 3.3V, 5V, 12V and 15V output voltage, respectively.



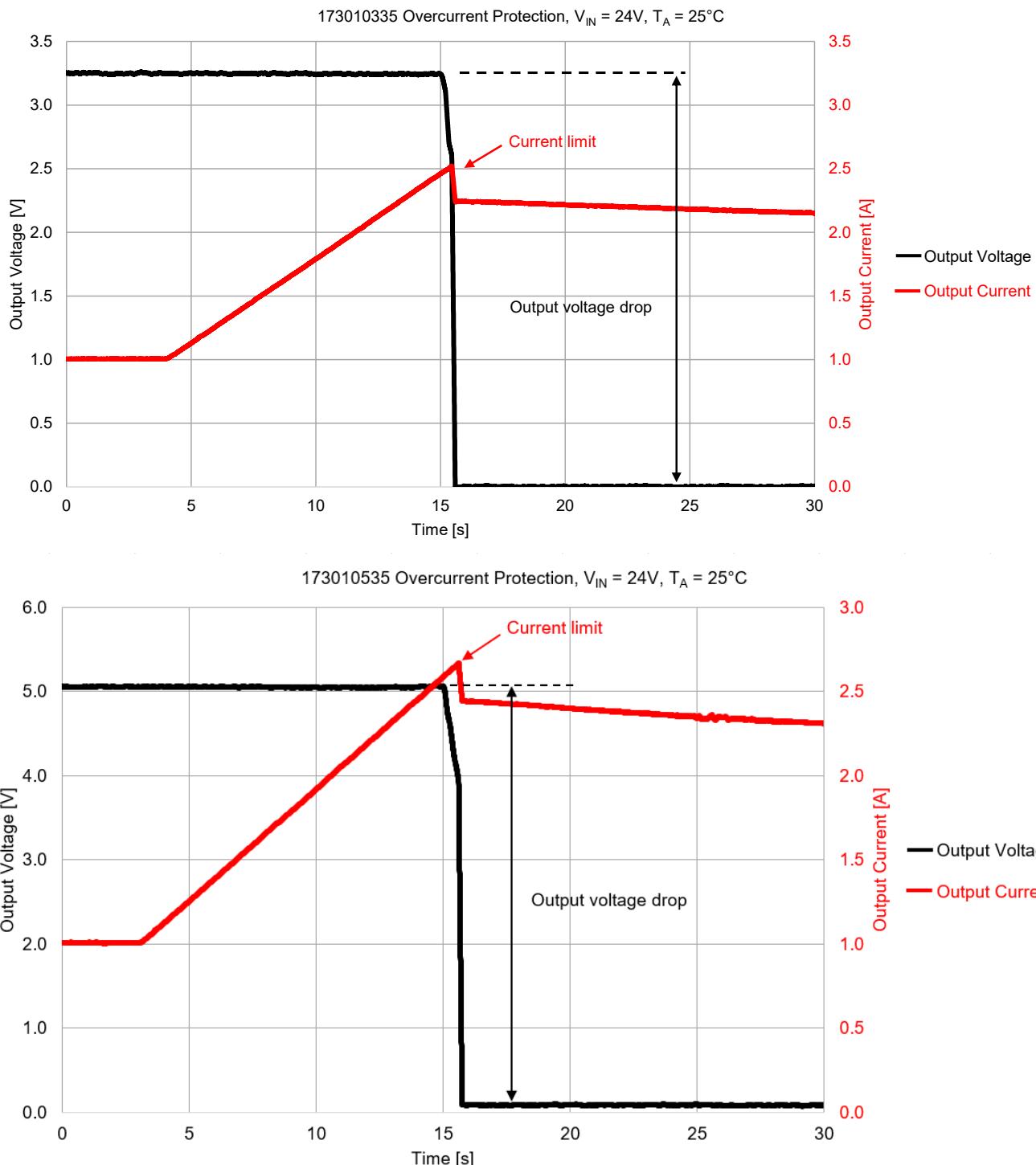


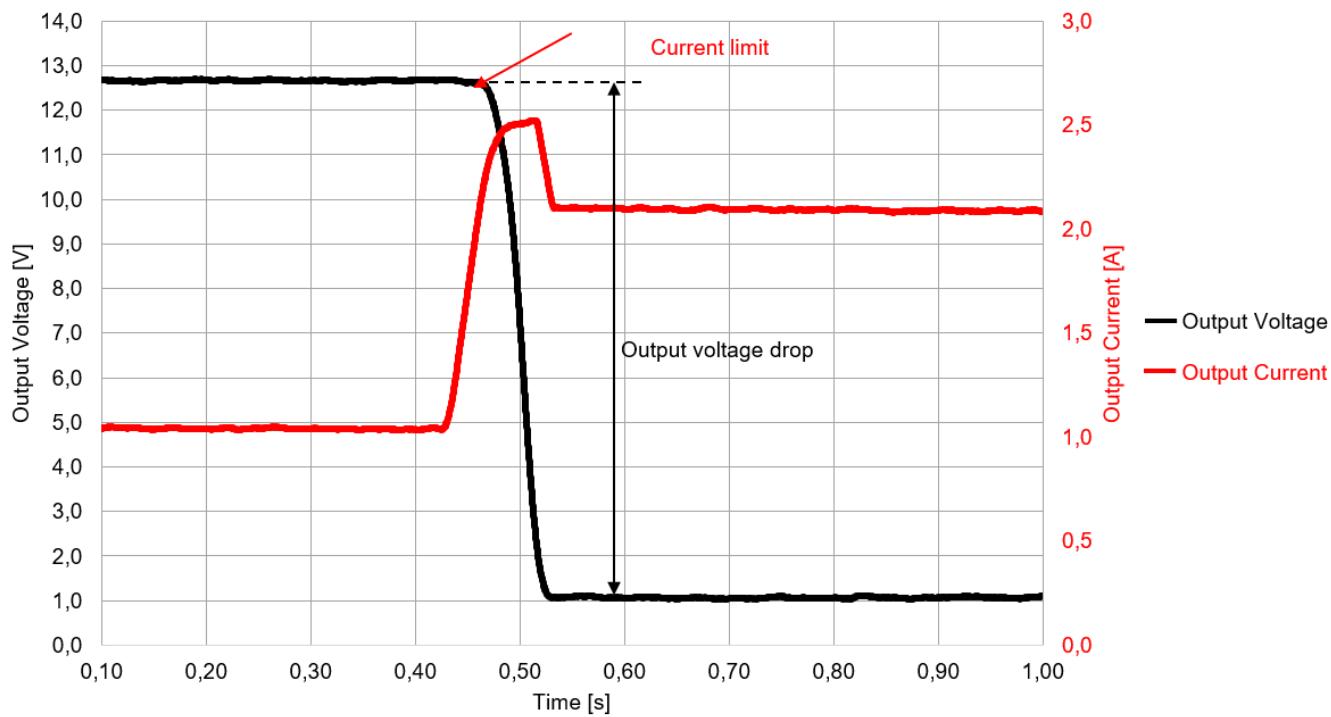
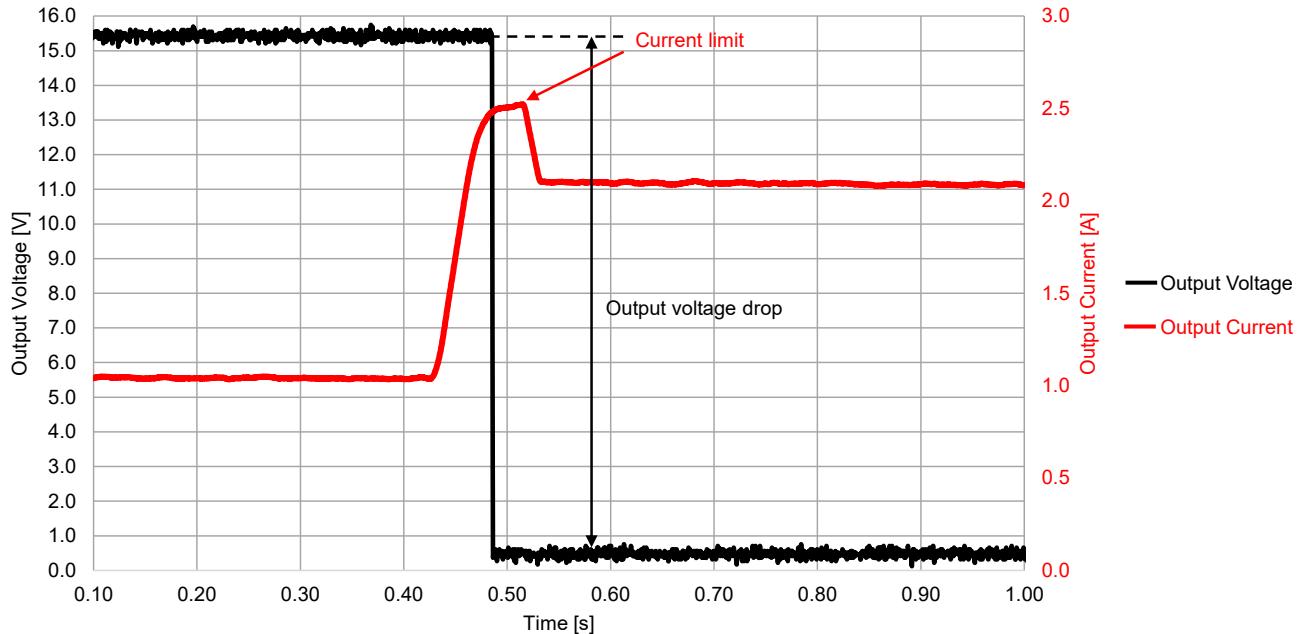


173011235 Short Circuit Protection,  $V_{IN} = 24V$ ,  $V_{OUT} = 12V$  and  $I_{OUT} = 1A$ 173011535 Short Circuit Protection,  $V_{IN} = 24V$ ,  $V_{OUT} = 15V$  and  $I_{OUT} = 1A$ 

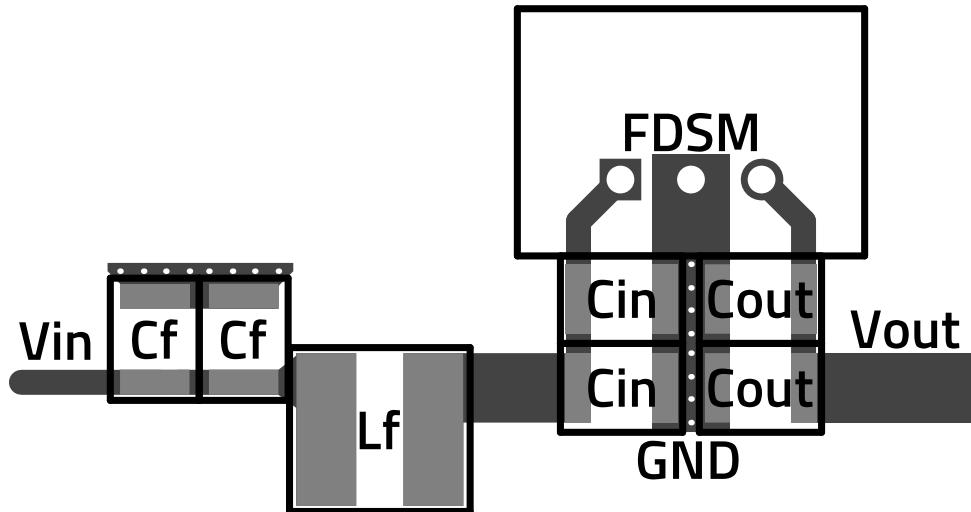
### Over Current Protection (OCP)

For protection against load faults, the power module incorporates cycle by cycle current monitoring. During an overcurrent condition the output current is limited and the output voltage drops. When the overcurrent condition is removed, the output voltage returns to the nominal voltage.



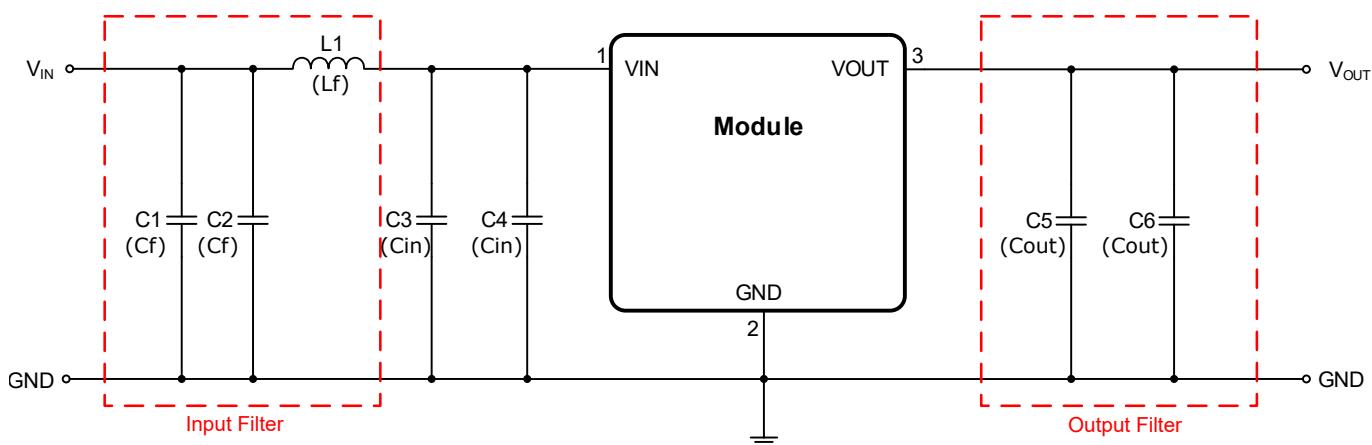
173011235 Overcurrent Protection,  $V_{IN} = 24V$ ,  $T_A = 25^\circ C$ 173011535 Overcurrent Protection,  $V_{IN} = 24V$ ,  $T_A = 25^\circ C$ 

## LAYOUT RECOMMENDATION



The layout above has been evaluated to provide the optimal performance in terms of transient response, efficiency, ripple and EMI. The design footprint can be reduced at the expense of performance in these parameters.  
The following recommendation should be followed when designing the layout:

1. The input and output capacitors should be placed as close as possible to the module pins.
2. The input filter should be placed close to the input capacitors of the module.

**Recommended External Circuit for Best Performance**


The 17301xx35 family integrates both the input and output capacitors. It is also recommended to use two 4.7µF input capacitors, C3 and C4, for high impedance input wires or traces and two 10µF output capacitor, C5 and C6, for applications where a low output voltage ripple is required.

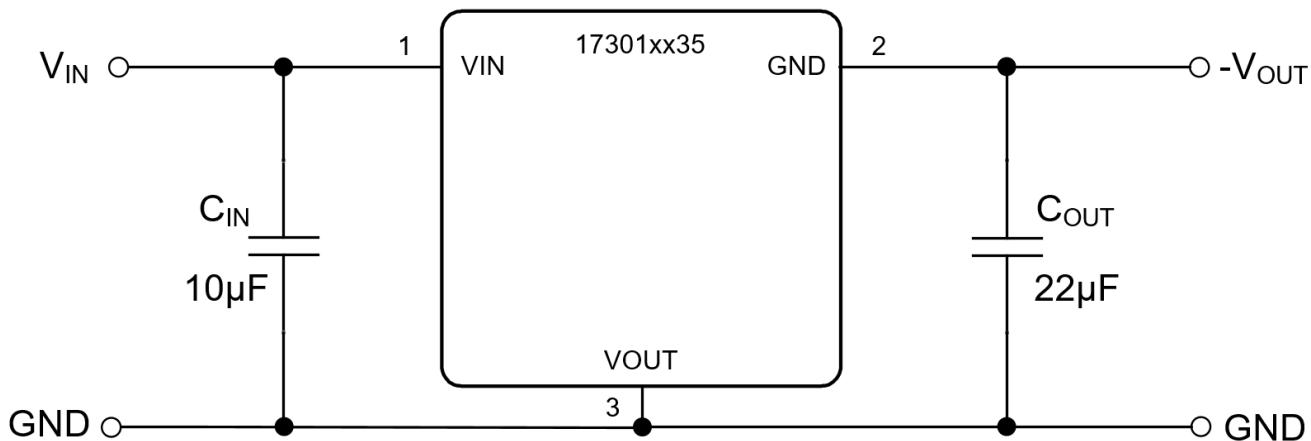
**Bill of Materials**

Symbol	Description	Filter	Quantity	Order Code	Manufacturer
U1	MagI <sup>3</sup> C Power Module (not mounted)		1	17301xx35	Würth Elektronik
L <sub>1</sub>	Filter inductor, 4.7µH, PD2 family, I <sub>SAT</sub> =2.46A, I <sub>R</sub> =1.82A	Input	1	744773047	Würth Elektronik
C <sub>1</sub> , C <sub>2</sub>	Ceramic chip capacitor 4.7µF/50V X5R, 1210	Input	2	885012209048	Würth Elektronik
C <sub>3</sub> , C <sub>4</sub>	Ceramic chip capacitor 4.7µF/50V X5R, 1210		2	885012209048	Würth Elektronik
C <sub>5</sub> , C <sub>6</sub>	Ceramic chip capacitor 22µF/25V X5R, 1210	Output	2	885012109014	Würth Elektronik

### Generating Negative Output Voltage

Many industrial applications require negative voltages. The 17301xx35 family can easily provide a negative voltage using the circuit shown below. The module VOUT pin is attached to the application ground and the module GND pin is used to provide the output voltage. For low output voltage ripple, it is recommended to use an additional 22 $\mu$ F external capacitor at the output of the module.

The 173010335 power module is not designed to be operated with a negative output voltage.



For additional information, please refer to the Application Note [ANS007b](#).

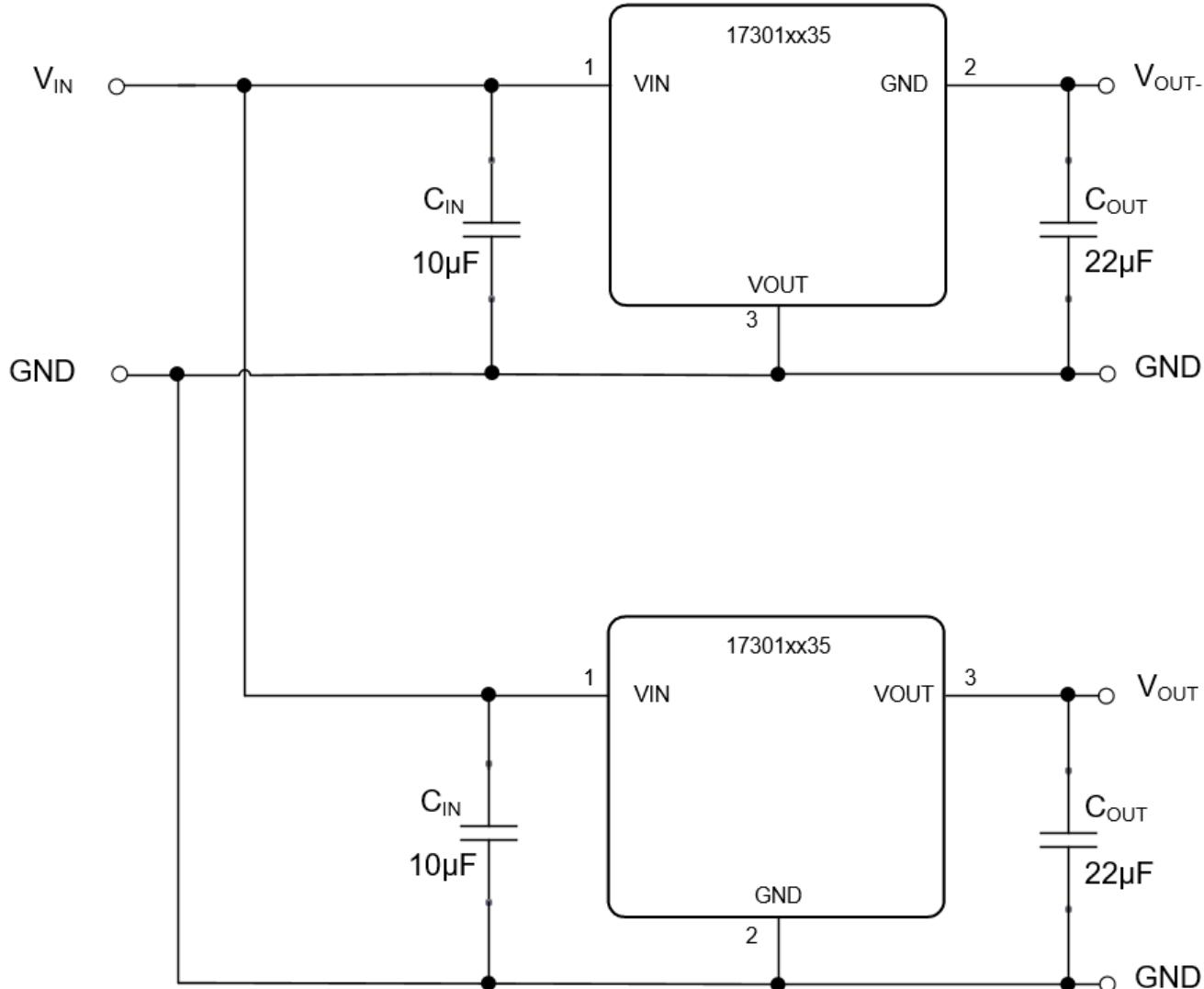
### Operating Conditions for Generating Negative Output Voltage:

Part Number	Minimum Vin (V)	Maximum Vin (V)	Maximum Iout (-mA)	Maximum Cout ( $\mu$ F)
173010535	8	27	500	330
173011235	8	20	300	
173011535	8	18	300	

### Generating Complementary Output Voltage

Another common requirement in industrial applications is to provide a complementary voltage (e.g.  $\pm 5V$ ). The circuit below shows how this target can be achieved simply combining one of the 17301xx35 family used in a standard configuration (delivering a positive output voltage) with the above mentioned solution for negative voltages. For low output voltage ripple, it is recommended to use an additional  $22\mu F$  external capacitor at the output of the module.

The 173010335 power module is not designed to be operated with a negative output voltage.

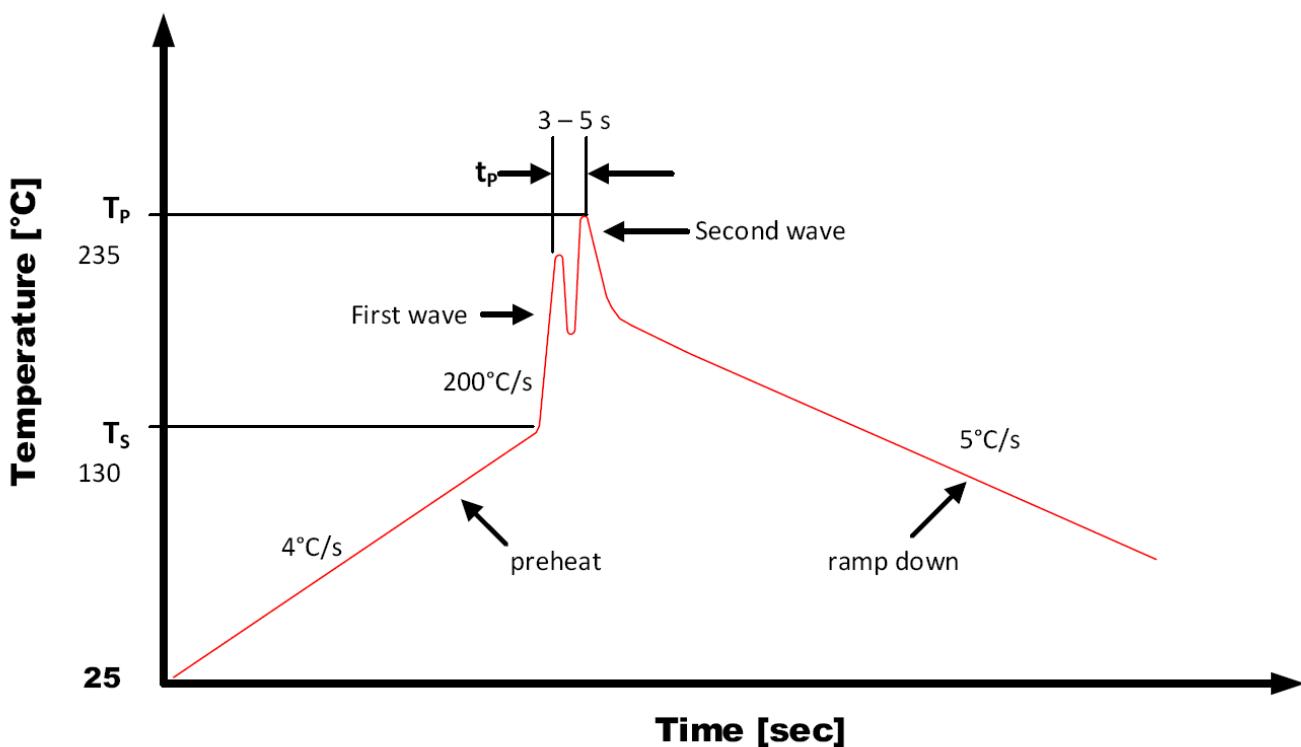


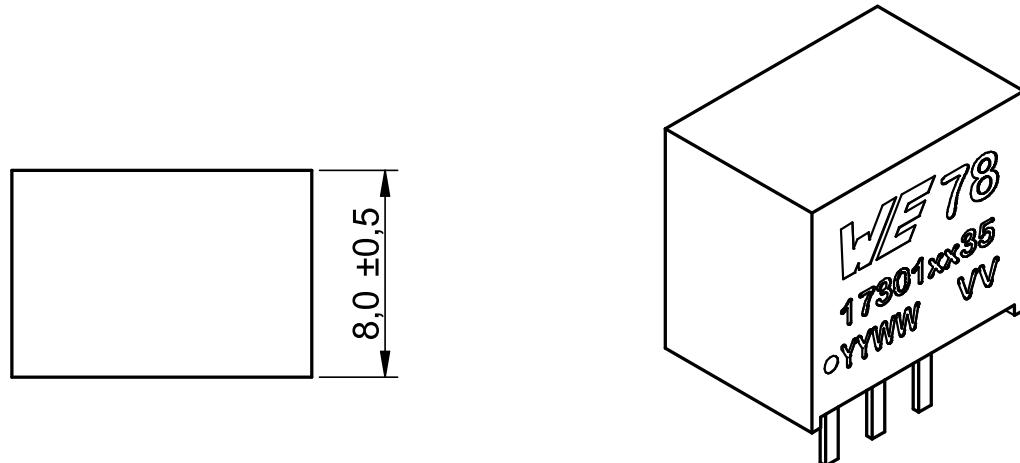
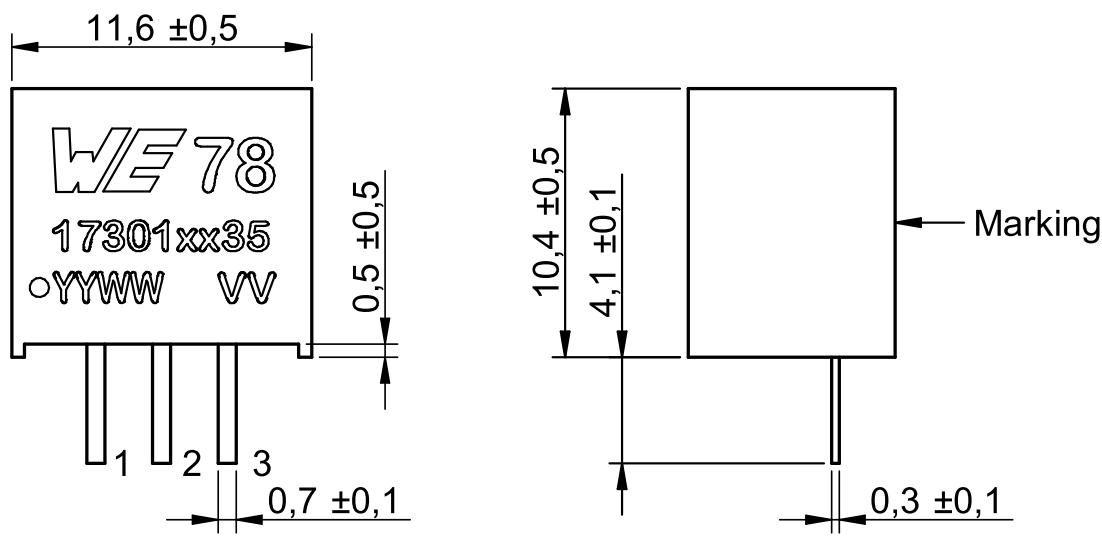
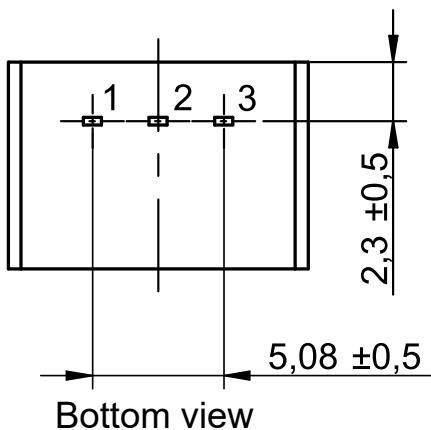
### Complementary Output Voltage

## WAVE SOLDER PROFILE

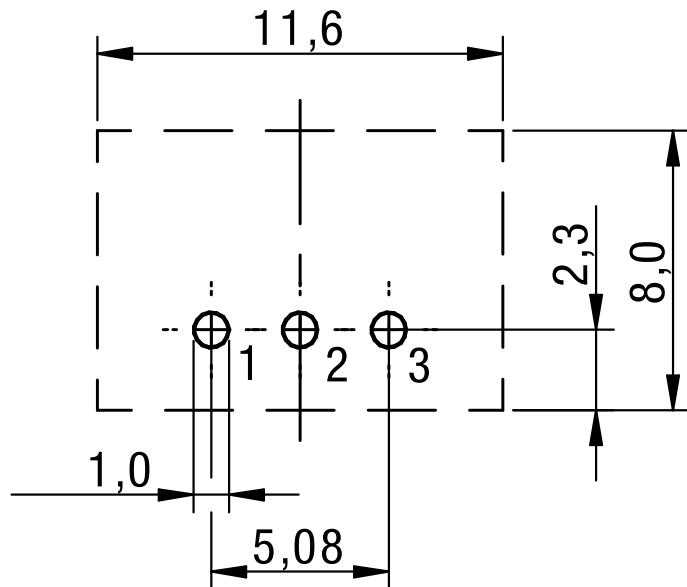
Profile Feature	Old standard (Pb)	New (Pb-free)
Time within peak temperature $t_p$	10s	10s
Average ramp-up rate	200°C/s	200°C/s
Final preheat temperature $T_s$	130°C/s	130°C/s
Peak temperature $T_p$	+235°C/s	+260°C/s
Ramp-down rate	-5°C/s	-5°C/s
Heating rate during preheat	4°C/s	4°C/s

Wave Solder Diagram:

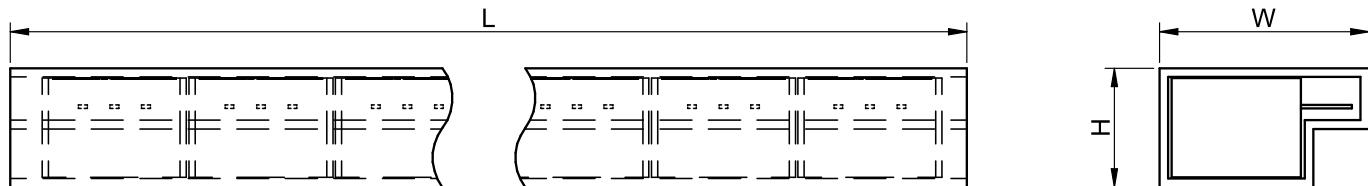


**PHYSICAL DIMENSIONS**

All dimensions in mm  
 Tolerances  $\pm 0,1$ mm unless otherwise indicated

**RECOMMENDED DRILL HOLES**

All dimensions in mm

**TUBE**

Dimension	L	W	H
Tolerance	typ.	typ.	typ.
Value	528mm	16.9mm	9.6mm

**DOCUMENT HISTORY**

Revision	Date	Description	Comment
1.0	November 2020	Initial data sheet release	
2.0	April 2021	Added 173011235 family member	
3.0	September 2021	Add 173011535 family member Updated data sheet information	
4.0	May 2022	PCN	<ul style="list-style-type: none"> <li>1. Corrected absolute maximum table parameters description for <math>V_{esd}</math>. (page 5)</li> <li>2. Updated front page drawing. (page 1)</li> <li>3. Updated pinout drawing to include pin 1 marking. (page 2)</li> <li>4. Updated physical dimensions drawings to include pin 1 marking. (page 38)</li> </ul>

## CAUTIONS AND WARNINGS

The following conditions apply to all goods within the product series of MagI<sup>3</sup>C of Würth Elektronik eiSos GmbH & Co. KG:

### General:

- This electronic component is designed and manufactured for use in general electronic equipment.
- Würth Elektronik must be asked for written approval (following the PPAP procedure) before incorporating the components into any equipment in fields such as military, aerospace, aviation, nuclear control, submarine, transportation (automotive control, train control, ship control), transportation signal, disaster prevention, medical, public information network, etc. where higher safety and reliability are especially required and/or if there is the possibility of direct damage or human injury.
- Electronic components that will be used in safety-critical or high-reliability applications, should be pre-evaluated by the customer.
- The component is designed and manufactured to be used within the datasheet specified values. If the usage and operation conditions specified in the datasheet are not met, the component may be damaged or dissolved.
- Do not drop or impact the components as material of the body, pins or termination may flake apart.
- Würth Elektronik products are qualified according to international standards, which are listed in each product reliability report. Würth Elektronik does not warrant any customer qualified product characteristics beyond Würth Elektronik's specifications, for its validity and sustainability over time.
- All technical specifications for standard products also apply to customer specific products.
- Customer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of Würth Elektronik eiSos GmbH & Co. KG components in its applications, notwithstanding any applications-related information or support that may be provided by Würth Elektronik eiSos GmbH & Co. KG. Customer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Customer will fully indemnify Würth Elektronik eiSos and its representatives against any damages arising out of the use of any Würth Elektronik eiSos GmbH & Co. KG components in safety-critical applications.

### Product specific:

#### Soldering:

- The solder profile must comply with the technical product specifications. All other profiles will void the warranty.
- All other soldering methods are at the customer's own risk.

#### Cleaning and Washing:

- Residual washing varnish agent that is used during the production to clean the application might change the characteristics of the body, pins or termination. The washing varnish agent may have a negative effect on the long term function of the component.
- Using a brush during the cleaning process may break the component. Therefore, we do not recommend using a brush during the PCB cleaning process.

#### Potting and Coating:

- If the component is potted in the customer application, the potting material might shrink or expand during and after hardening. Shrinking could lead to an incomplete seal, allowing contaminants into the component. Expansion could damage the components or parts of it. We recommend a manual inspection after potting to avoid these effects.
- Conformal coating may affect the product performance.

**Storage Conditions:**

- A storage of Würth Elektronik products for longer than 12 months is not recommended. Within other effects, the terminals may suffer degradation, resulting in bad solderability. Therefore, all products shall be used within the period of 12 months based on the day of shipment.
- Do not expose the components to direct sunlight.
- The storage conditions in the original packaging are defined according to DIN EN 61760-2.
- For a moisture sensitive component, the storage condition in the original packaging is defined according to IPC/JEDEC-J-STD-033. It is also recommended to return the component to the original moisture proof bag and reseal the moisture proof bag again.
- ESD prevention methods need to be followed for manual handling and processing by machinery.
- The storage conditions stated in the original packaging apply to the storage time and not to the transportation time of the components.

**Packaging:**

- The packaging specifications apply only to purchase orders comprising whole packaging units. If the ordered quantity exceeds or is lower than the specified packaging unit, packaging in accordance with the packaging specifications cannot be ensured.

**Handling:**

- Violation of the technical product specifications such as exceeding the absolute maximum ratings will void the warranty and also the conformance to regulatory requirements.
- The edge castellation is designed and made for prototyping, i.e. hand soldering purposes, only.
- The applicable country regulations and specific environmental regulations must be observed.
- Do not disassemble the component. Evidence of tampering will void the warranty.
- The temperature rise of the component must be taken into consideration. The operating temperature is comprised of ambient temperature and temperature rise of the component. The operating temperature of the component shall not exceed the maximum temperature specified.
- Direct mechanical impact to the component must be prevented as the material of the body, pins or termination could flake or, in the worst case, could break. As these devices are sensitive to electrostatic discharge, proper IC Handling Procedures must be followed.

These cautions and warnings comply with the state of the scientific and technical knowledge and are believed to be accurate and reliable. However, no responsibility is assumed for inaccuracies or incompleteness.



## IMPORTANT NOTES

### General Customer Responsibility

Some goods within the product range of Würth Elektronik eiSos GmbH & Co. KG contain statements regarding general suitability for certain application areas. These statements about suitability are based on our knowledge and experience of typical requirements concerning the areas, serve as general guidance and cannot be estimated as binding statements about the suitability for a customer application. The responsibility for the applicability and use in a particular customer design is always solely within the authority of the customer. Due to this fact it is up to the customer to evaluate, where appropriate to investigate and decide whether the device with the specific product characteristics described in the product specification is valid and suitable for the respective customer application or not. Accordingly, the customer is cautioned to verify that the datasheet is current before placing orders.

### Customer Responsibility Related to Specific, in Particular Safety-Relevant, Applications

It has to be clearly pointed out that the possibility of a malfunction of electronic components or failure before the end of the usual lifetime cannot be completely eliminated in the current state of the art, even if the products are operated within the range of the specifications. In certain customer applications requiring a very high level of safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health it must be ensured by most advanced technological aid of suitable design of the customer application that no injury or damage is caused to third parties in the event of malfunction or failure of an electronic component.

### Best Care and Attention

Any product-specific notes, warnings and cautions must be strictly observed. Any disregard will result in the loss of warranty.

### Customer Support for Product Specifications

Some products within the product range may contain substances which are subject to restrictions in certain jurisdictions in order to serve specific technical requirements. Necessary information is available on request. In this case the field sales engineer or the internal sales person in charge should be contacted who will be happy to support in this matter.

### Product R&D

Due to constant product improvement product specifications may change from time to time. As a standard reporting procedure of the Product Change Notification (PCN) according to the JEDEC-Standard we inform about minor and major changes. In case of further queries regarding the PCN, the field sales engineer or the internal sales person in charge should be contacted. The basic responsibility of the customer as per Section 1 and 2 remains unaffected.

### Product Life Cycle

Due to technical progress and economical evaluation we also reserve the right to discontinue production and delivery of products. As a standard reporting procedure of the Product Termination Notification (PTN) according to the JEDEC-Standard we will inform at an early stage about inevitable product discontinuance. According to this we cannot guarantee that all products within our product range will always be available. Therefore it needs to be verified with the field sales engineer or the internal sales person in charge about the current product availability expectancy before or when the product for application design-in disposal is considered. The approach named above does not apply in the case of individual agreements deviating from the foregoing for customer-specific products.

### Property Rights

All the rights for contractual products produced by Würth Elektronik eiSos GmbH & Co. KG on the basis of ideas, development contracts as well as models or templates that are subject to copyright, patent or commercial protection supplied to the customer will remain with Würth Elektronik eiSos GmbH & Co. KG. Würth Elektronik eiSos GmbH & Co. KG does not warrant or represent that any license, either expressed or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, application, or process in which Würth Elektronik eiSos GmbH & Co. KG components or services are used.

### General Terms and Conditions

Unless otherwise agreed in individual contracts, all orders are subject to the current version of the "General Terms and Conditions of Würth Elektronik eiSos Group", last version available at [www.we-online.com](http://www.we-online.com).