

Below are current APIs, and their supported format (need to be more general in the following version):

- **GV Set System Mode**
 - **SETUP** : yosysSETUP, abcSETUP, v3SETUP
 - **vrf**
- **GV Set Engine**
 - **yosys**
 - **abc**
 - **v3**
- **GV Read Design**
 - **yosys** : verilog, blif
 - **abc** : verilog, blif, aig
 - **v3** : verilog, aig
- **GV File2Aig**
 - **verilog2aig**
- **GV Formal Verify**
 - **bmc** (Bounded Model Checking)
 - **pdr** (Property Directed Reachability)
 - **itp** (Craig Interpolation)
- **GV Random Simulate**
 - **engine** : yosys
 - **input file** : verilog