

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC7SH14F, TC7SH14FU

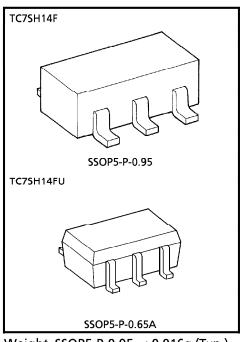
SCHMITT INVERTER

The TC7SH14 is an advanced high speed CMOS SCHMITT INVERTER fabricated with silicon gate CMOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation. Pin configuration and function are the same as the TC7SH14 but the inputs have hysteresis and with its schmitt trigger function, the TC7SH14 can be used as a line receivers which will receive slow input signals.

An input protection circuit ensures that 0V to 7V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5V to 3V system and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

FEATURES

- High Speed t_{pd} = 5.5ns (Typ.) at V_{CC} = 5V
- Low Power Dissipation \cdots I_{CC} = 2μ A (Max.) at Ta = 25°C
- High Noise Immunity ······· V_{NIH} = V_{NIL} = 28% V_{CC} (Min.)
- Power Down Protection is provided on all inputs.
- Balanced Propagation Delays ····· t_{pLH}≒t_{pHL}
- Wide Operation Voltage Range ··· V_{CC} (opr) = 2V~5.5V

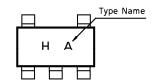


Weight SSOP5-P-0.95 : 0.016g (Typ.) SSOP5-P-0.65A : 0.006g (Typ.)

MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	Vcc	-0.5~7.0	V
DC Input Voltage	V _{IN}	-0.5~7.0	٧
DC Output Voltage	Vout	-0.5~V _{CC} +0.5	٧
Input Diode Current	IK	- 20	mA
Output Diode Current	loк	± 20	mA
DC Output Current	IOUT	± 25	mA
DC V _{CC} / Ground Current	Icc	± 50	mA
Power Dissipation	PD	200	mW
Storage Temperature	T _{stg}	-65∼150	°C
Lead Temperature (10 s)	TL	260	°C

MARKING



TRUTH TABLE

Α	Υ
L	Н
Н	L

961001EBA2

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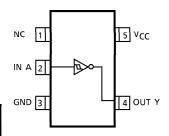


TC7SH14F/FU

LOGIC DIAGRAM

IN A -OUT Y

PIN ASSIGNMENT (TOP VIEW)



RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	2.0~5.5	V
Input Voltage	VIN	0~5.5	V
Output Voltage	VOUT	0~V _{CC}	V
Operating Temperature	T _{opr}	- 40∼85	°C
Input Rise and Fall Time	dt/dv	$0 \sim 100 \text{ (V}_{CC} = 3.3 \pm 0.3 \text{V)}$	ns / V
	at/dv	$0\sim20 \ (V_{CC} = 5 \pm 0.5 V)$	115 / V

DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC SYMBO		L TEST CONDITION			Ta = 25°C			Ta = -40~85°C		UNIT
CHARACTERISTIC	SYMBOL	IEST	TEST CONDITION		MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
Desitive Threshold				3.0	_	_	2.20	<u> </u>	2.20	
Positive Threshold	V _P			4.5	_	_	3.15	_	3.15	V
Voltage				5.5	_	_	3.85	_	3.85	
No motive Threehold				3.0	0.90	_	_	0.90	_	
Negative Threshold Voltage	V_{N}			4.5	1.35	_	_	1.35	_	V
Voltage				6.0	1.65	_	_	1.65	_	
				3.0	0.30	_	1.20	0.30	1.20	
Hysteresis Voltage	VH			4.5	0.40	_	1.40	0.40	1.40	V
				5.5	0.50	_	1.60	0.50	1.60	
	V _{ОН}		I _{OH} = -50μA	2.0	1.9	2.0	_	1.9	_	V
High-Level Output Voltage		V _{IN} = V _{IL}		3.0	2.9	3.0	_	2.9	_	
				4.5	4.4	4.5		4.4	. —	
Catput Voltage			$I_{OH} = -4mA$	3.0	2.58	_	_	2.48	_	
			$I_{OH} = -8mA$	4.5	3.94			3.80	_	
	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 50μA	2.0	_	0.0	0.1	_	0.1	
Low-Level Output Voltage				3.0	_	0.0	0.1	-	0.1	_v
				4.5	_	0.0	0.1	_	0.1	
Cutput voltage			$I_{OL} = 4mA$	3.0	_	_	0.36	_	0.44	
			I _{OL} = 8mA	4.5	_	_	0.36	_	0.44	.
Input Leakage Current	I _{IN}	V _{IN} = 5.5V or GND		0~ 5.5			±0.1		± 1.0	μΑ
Quiescent Supply Current	ICC	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ

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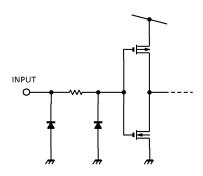
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AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3ns$)

CHARACTERISTIC SYMBOL		TEST CONDITION			Ta = 25°C			Ta = -40~85°C		UNIT
CHARACTERISTIC STIVIBUL	STIVIDUL		VCC (V)	C _L (pF)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
1		t _{DLH}	3.3 ± 0.3	15	_	8.3	12.8	1.0	15.0	ns
	t _{pLH}			50	_	10.8	16.3	1.0	18.5	
	tpHL		5.0 ± 0.5	15	_	5.5	8.6	1.0	10.0	
	<u> </u>	·		50	_	7.0	10.6	1.0	12.0	
Input Capacitance	CIN				_	4	10	_	10	pF
Power Dissipation	Coo	/NL	ote 1)			21				рF
Capacitance	C _{PD}	(IV	ote 1)		_	21	_	_	_	25

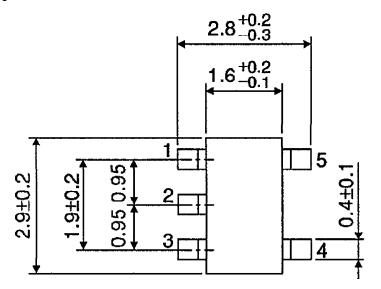
(Note 1): C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: $|C_{C}(opr)| = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

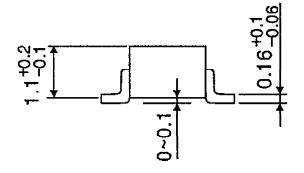
INPUT EQUIVALENT CIRCUIT



OUTLINE DRAWING SSOP5-P-0.95

Unit: mm



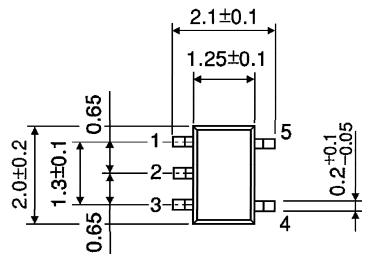


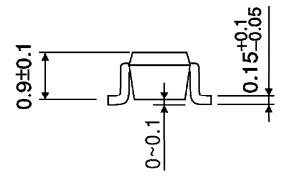
Weight: 0.016g (Typ.)

Unit: mm

OUTLINE DRAWING

SSOP5-P-0.65A





Weight: 0.006g (Typ.)